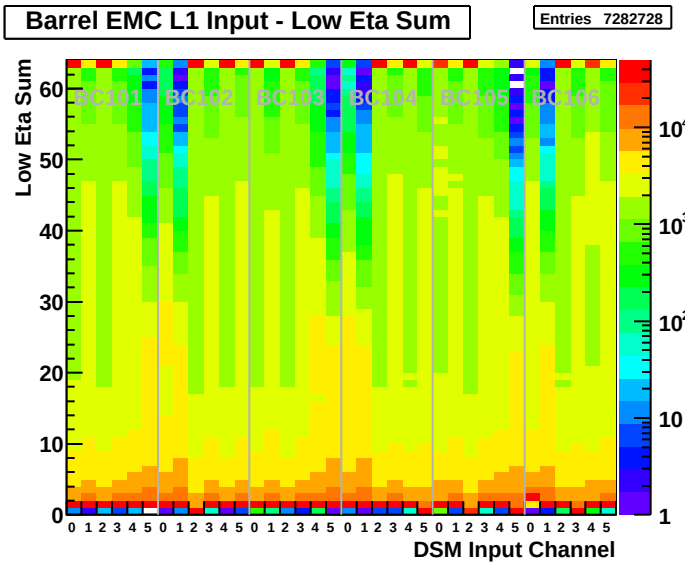
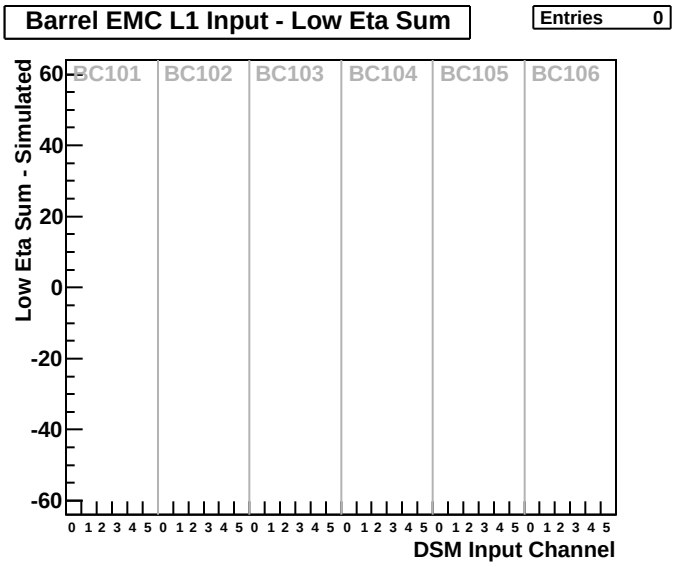
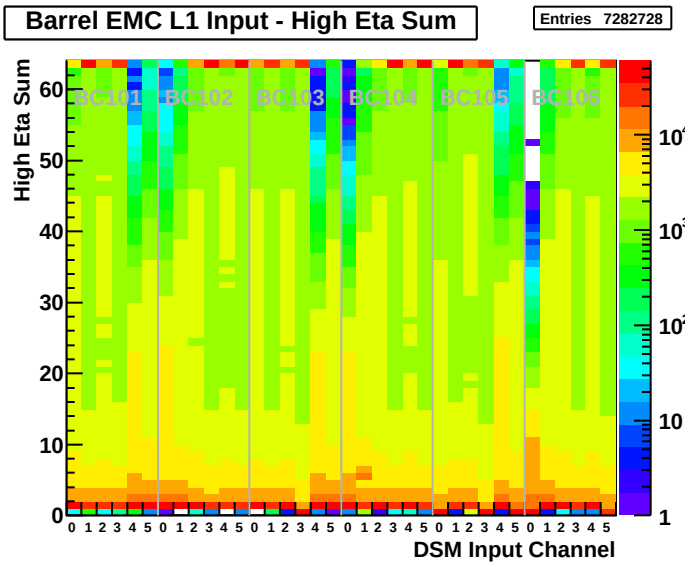
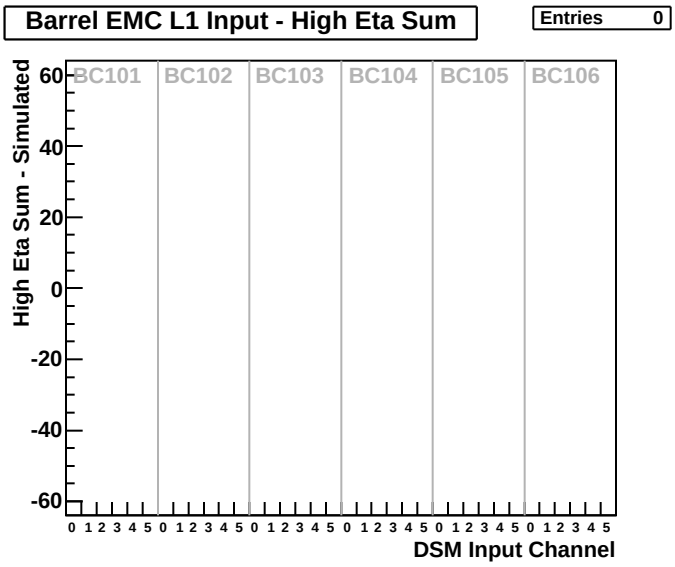
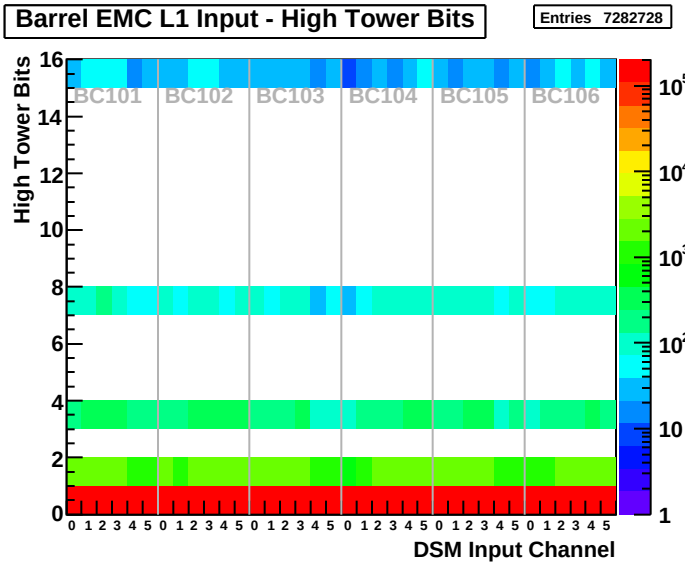
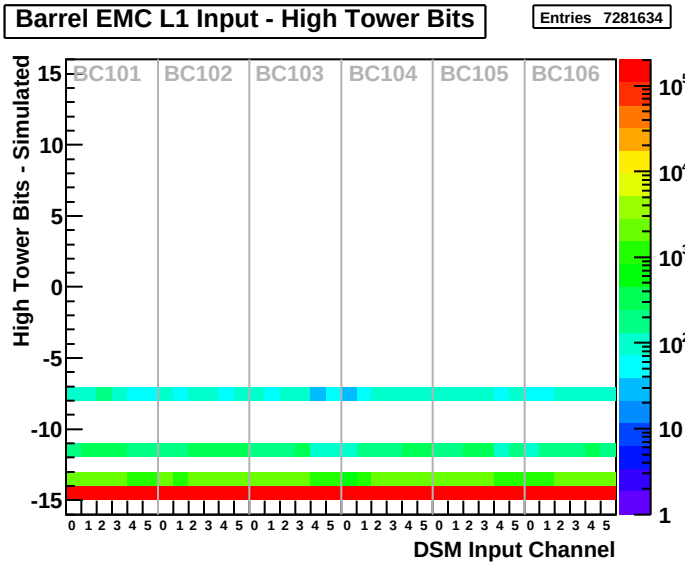

Barrel EMC L1 Input - Low Eta Sum
Entries 7282728
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

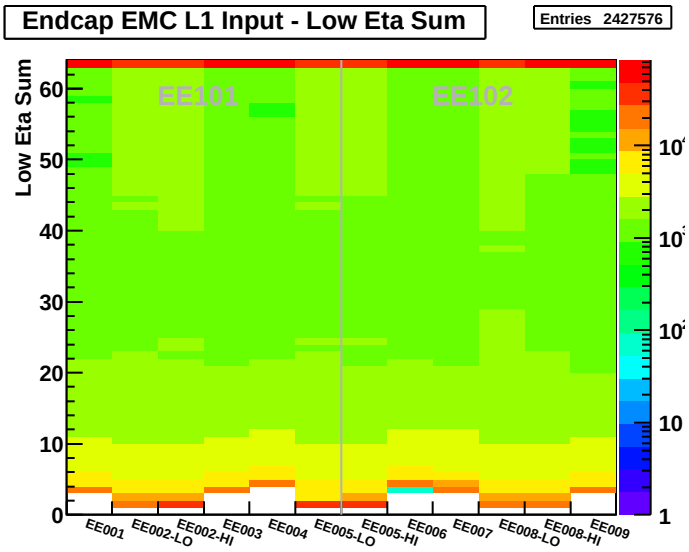

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

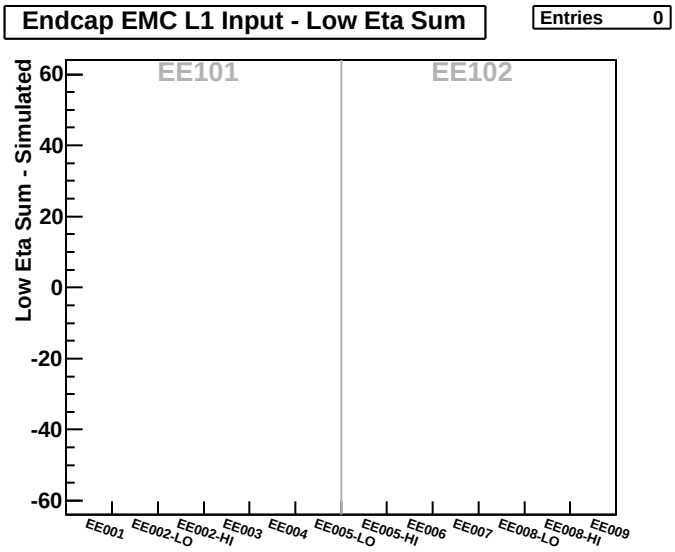

Barrel EMC L1 Input - High Eta Sum
Entries 7282728
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

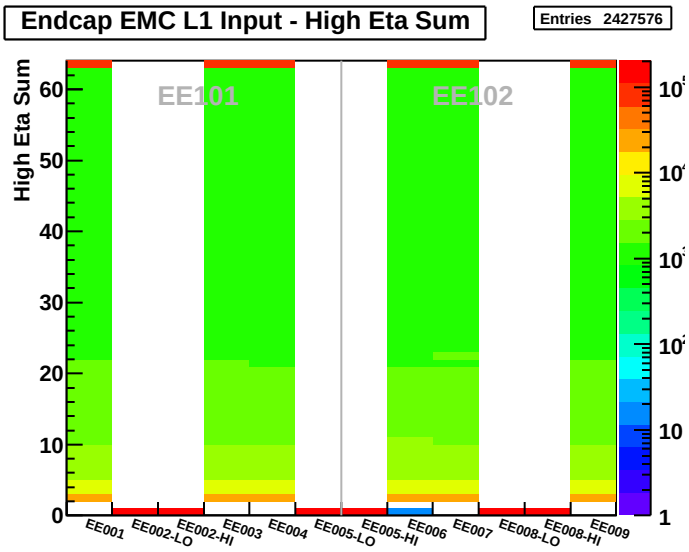

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

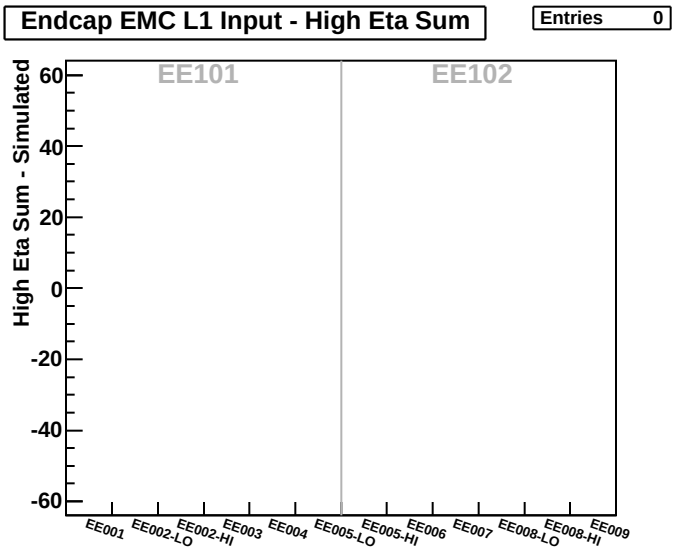

Barrel EMC L1 Input - High Tower Bits
Entries 7282728
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

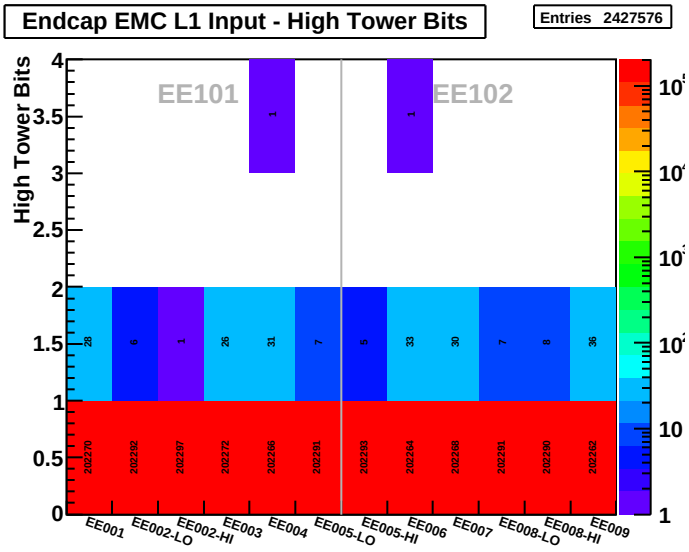

Barrel EMC L1 Input - High Tower Bits
Entries 7281634
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

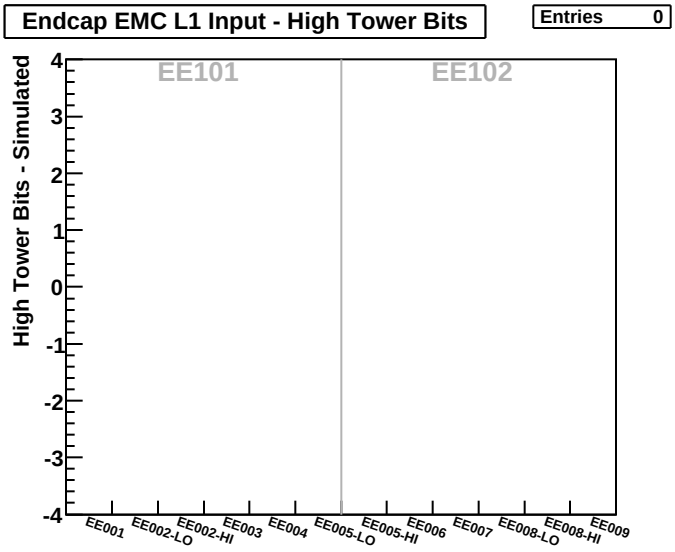

Endcap EMC L1 Input - Low Eta Sum
Entries 2427576
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

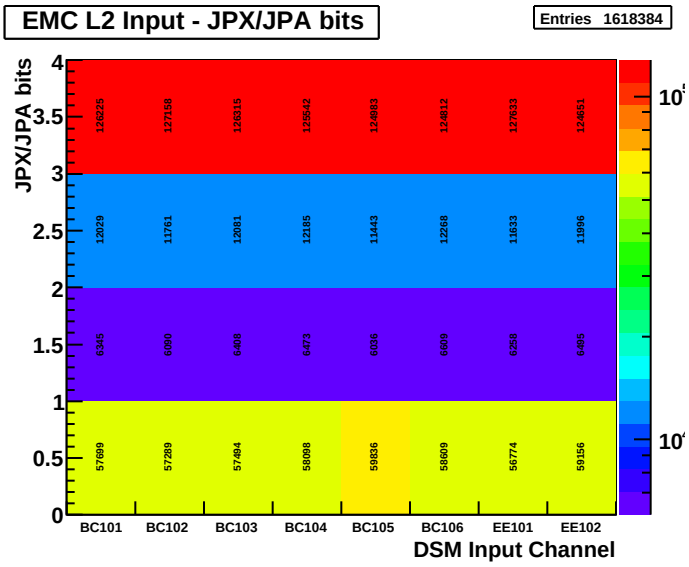

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

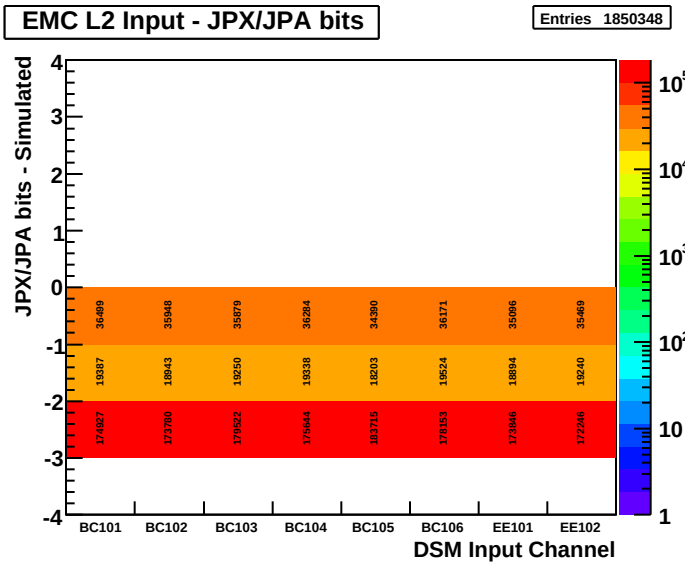

Endcap EMC L1 Input - High Eta Sum
Entries 2427576
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

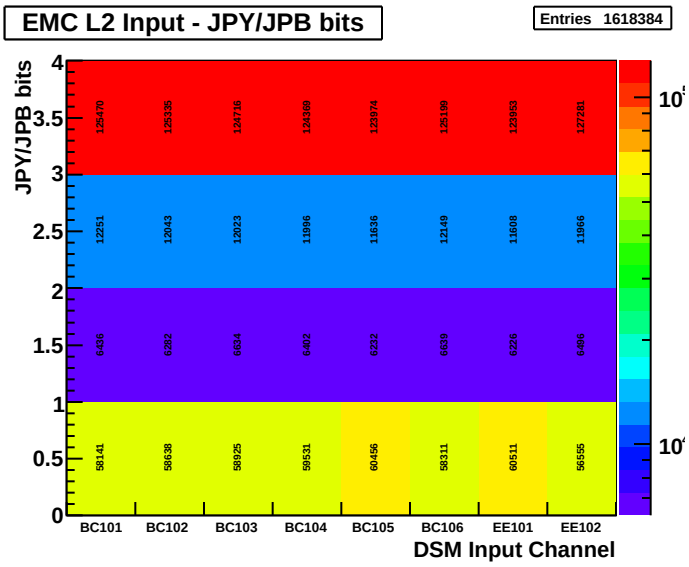

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

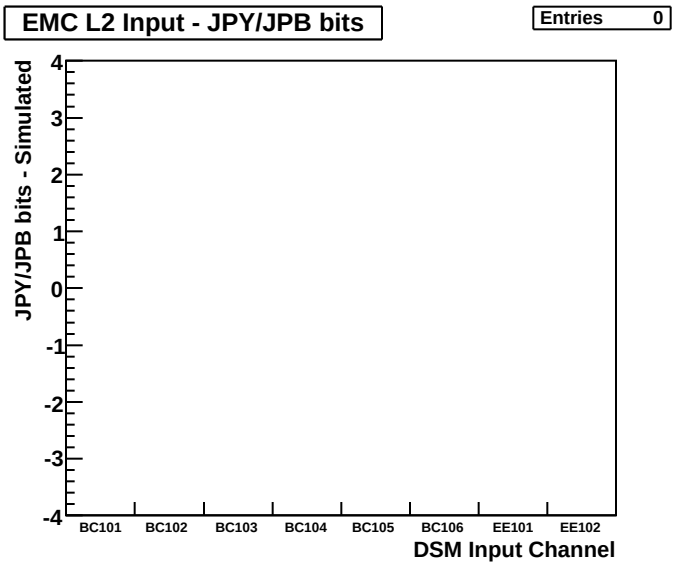

Endcap EMC L1 Input - High Tower Bits
Entries 2427576
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

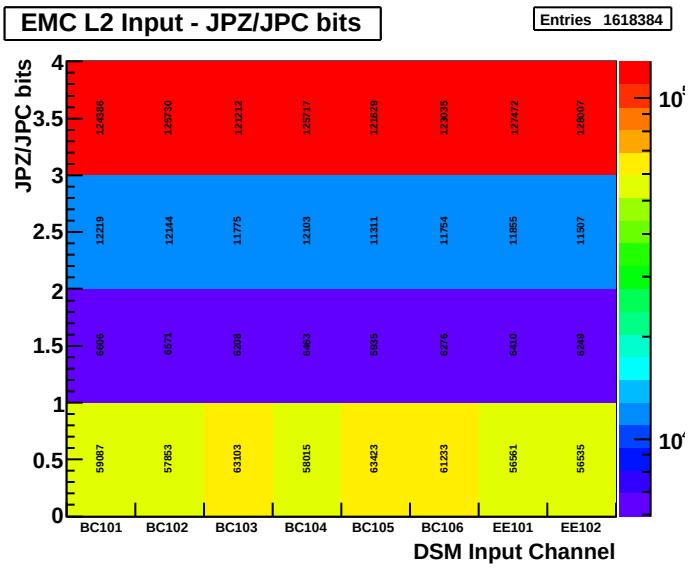

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

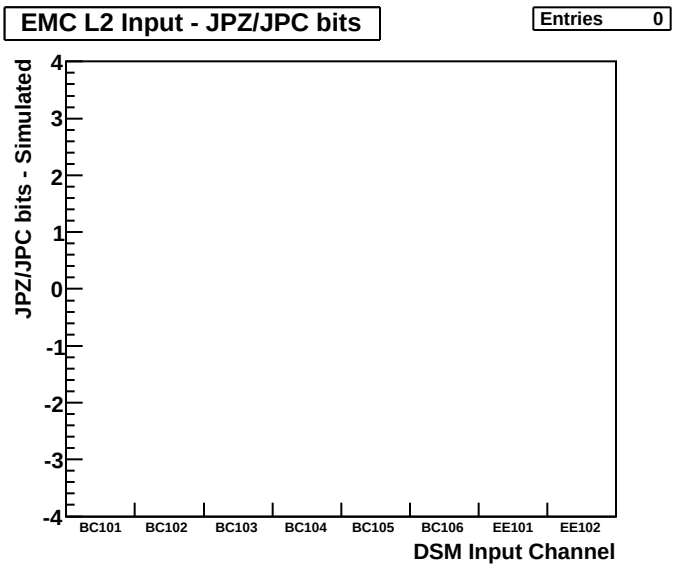

EMC L2 Input - JPX/JPA bits
Entries 1618384
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
126225 127158 126315 125542 124983 124812 127533 124651
12029 11761 12081 12185 11443 12268 11633 11996
6345 6080 6408 6473 6036 6609 6258 6495
57699 57289 57494 58098 59836 58609 56774 59156

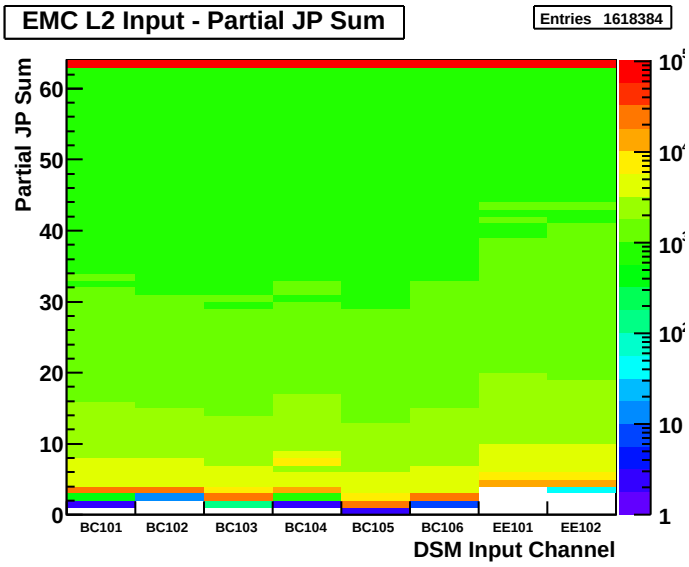

EMC L2 Input - JPX/JPA bits
Entries 1850348
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
35499 35948 35879 36284 34390 36171 35096 35469
19387 18943 19250 19338 18203 19524 18894 19240
174927 173760 179522 175644 183715 178153 173846 172246

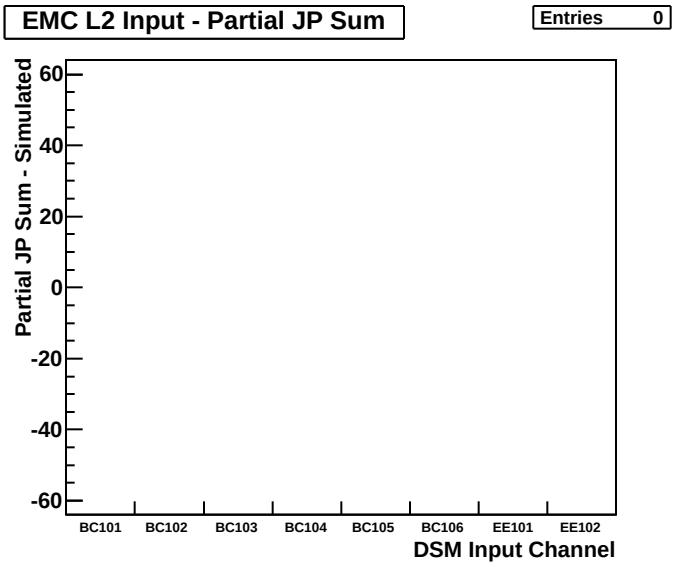

EMC L2 Input - JPY/JPB bits
Entries 1618384
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
125470 125335 124716 124389 123974 125199 123953 127281
12251 12043 12023 11996 11636 12149 11608 11966
6436 6282 6534 6402 6232 6639 6226 6496
58141 58638 58925 59531 60456 58311 60511 56555

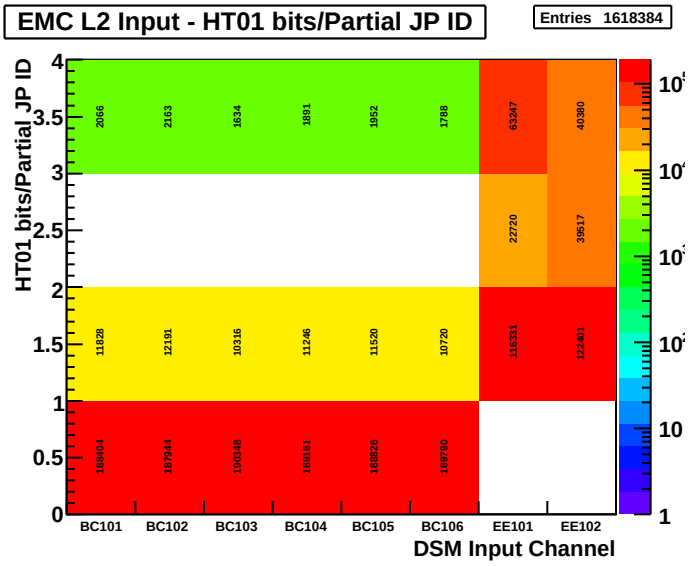

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

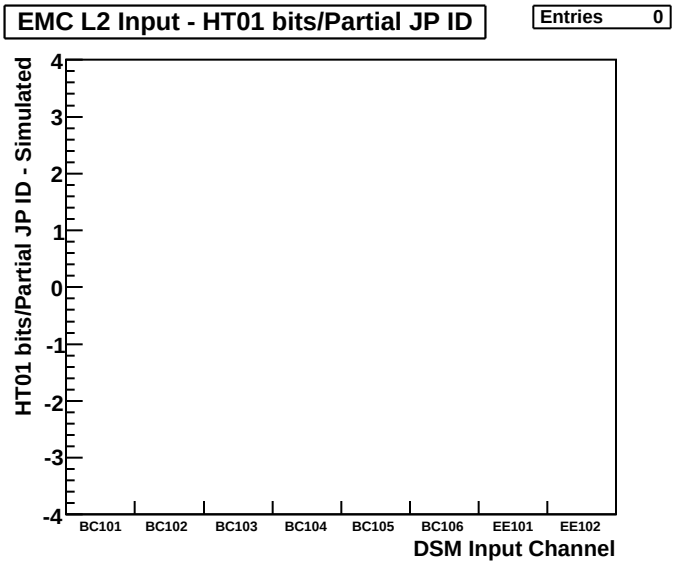

EMC L2 Input - JPZ/JPC bits
Entries 1618384
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
124386 125730 122212 125717 121629 123035 122472 128007
12219 12144 11775 12103 11311 11754 11855 11507
6606 6571 6208 6463 5935 6276 6410 6249
59087 57853 63103 58015 63423 61233 56561 56535

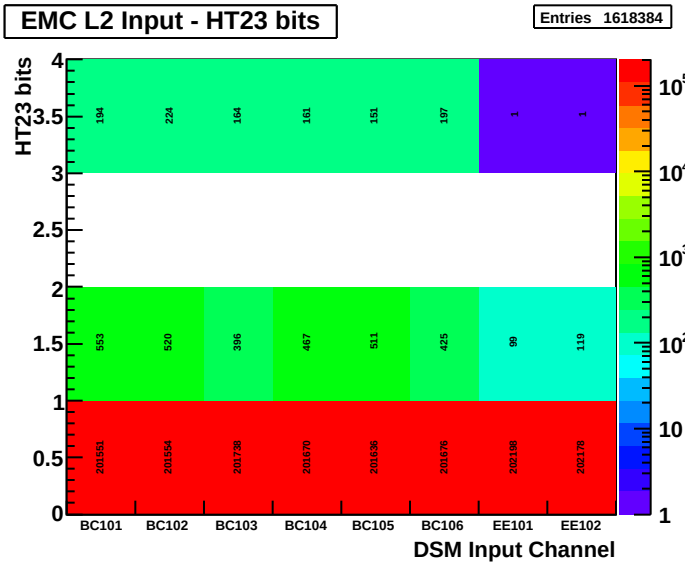

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

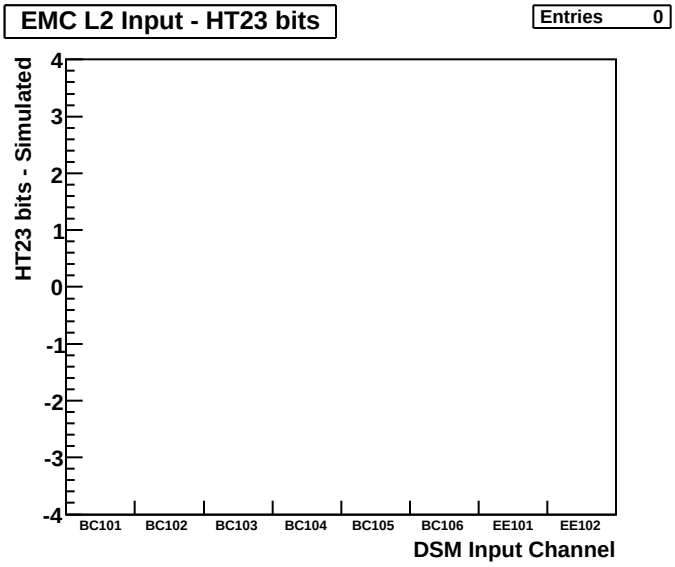

EMC L2 Input - Partial JP Sum
Entries 1618384
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

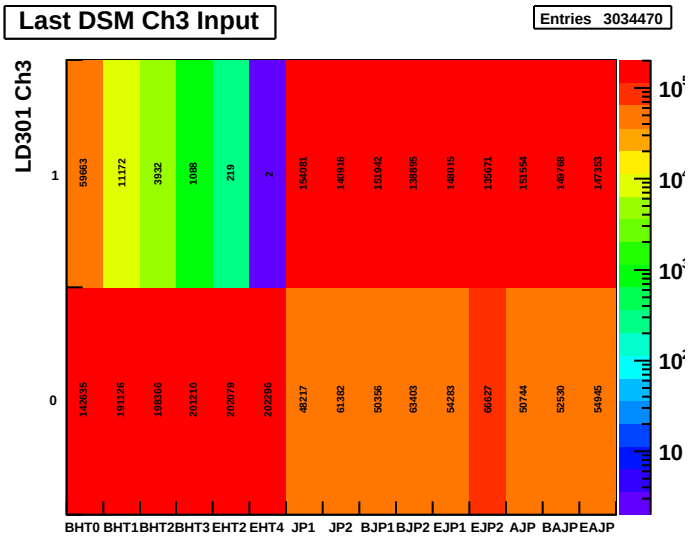

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

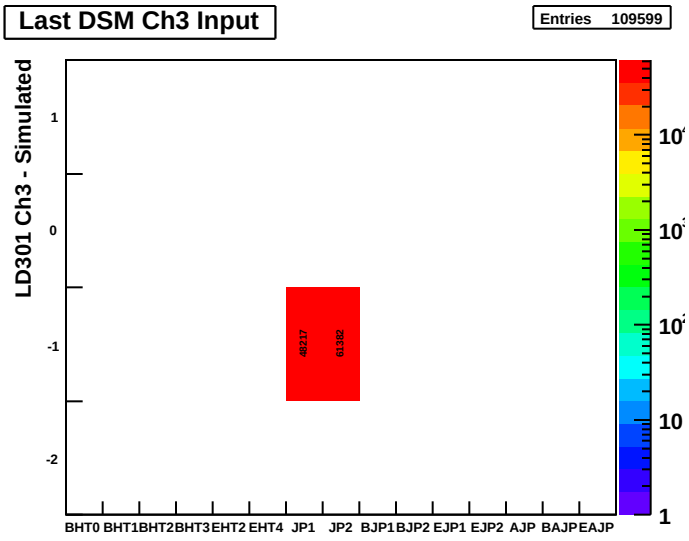

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1618384
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

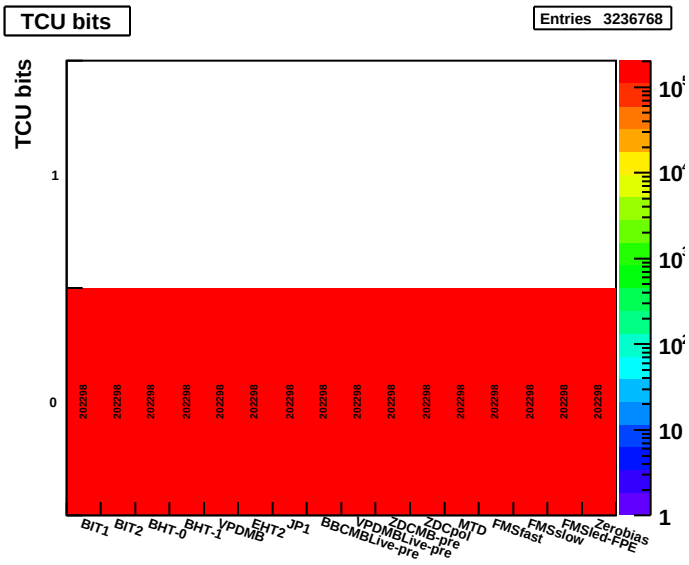

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

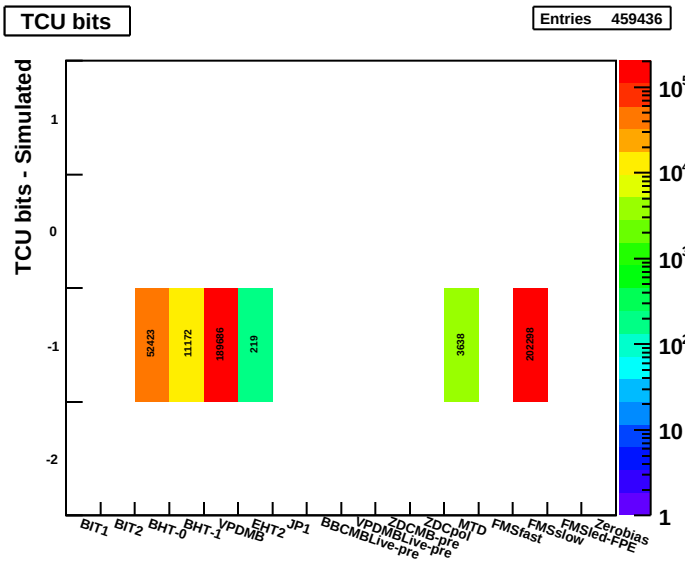

EMC L2 Input - HT23 bits
Entries 1618384
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

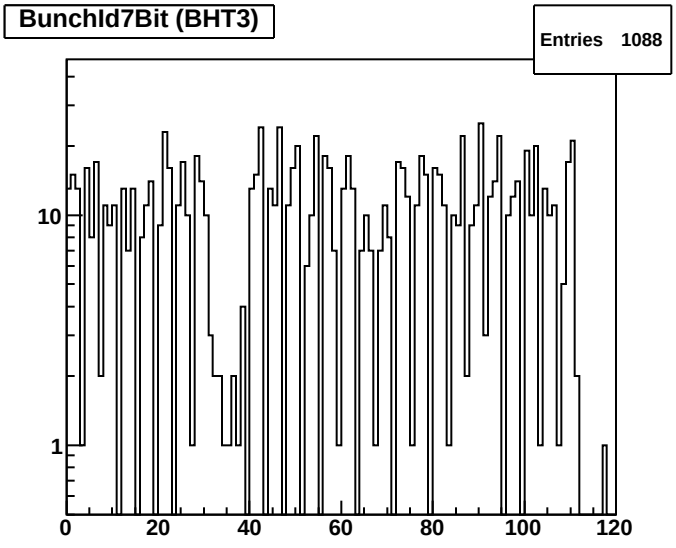

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

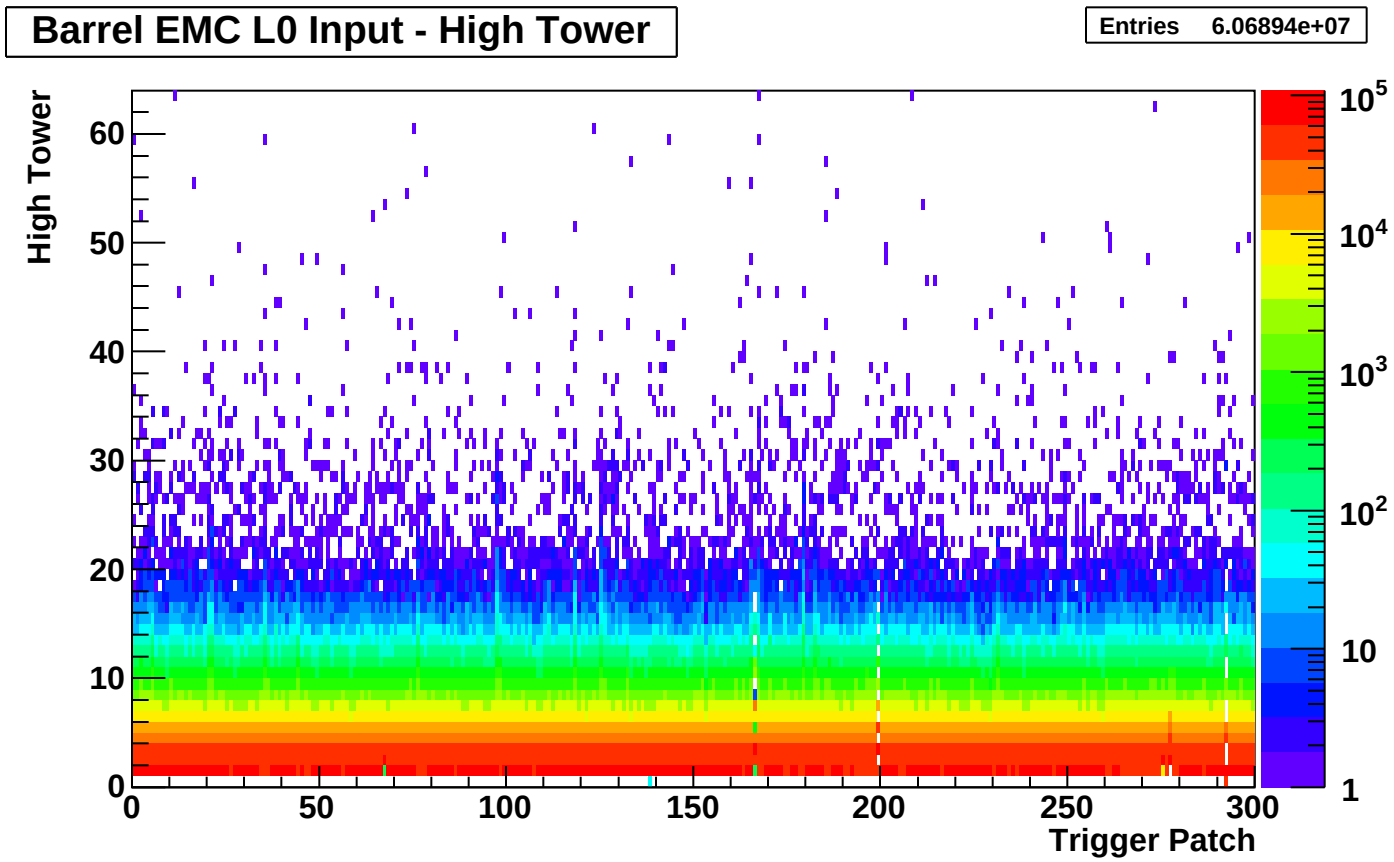

Last DSM Ch3 Input
Entries 3034470
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

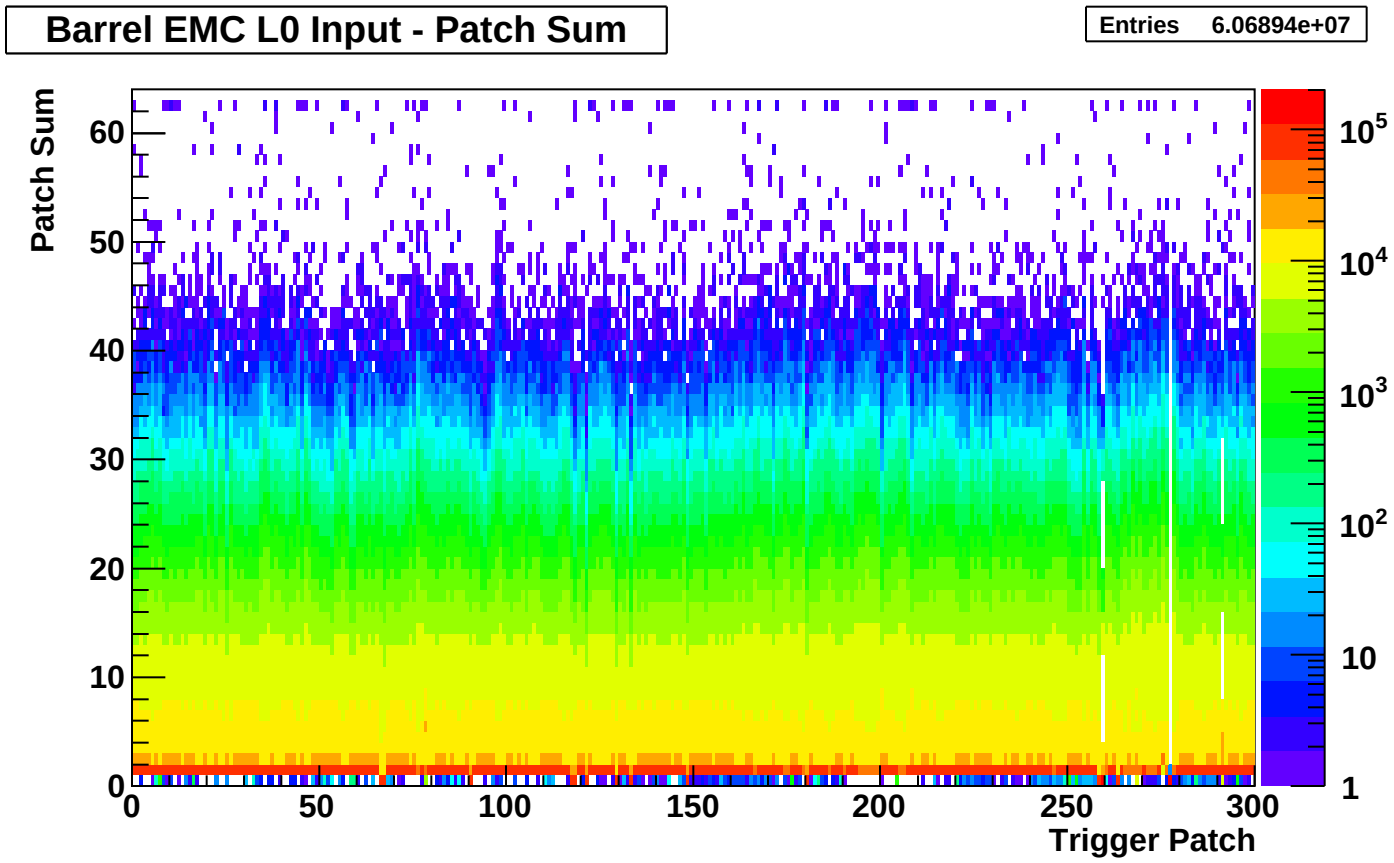

Last DSM Ch3 Input
Entries 109599
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

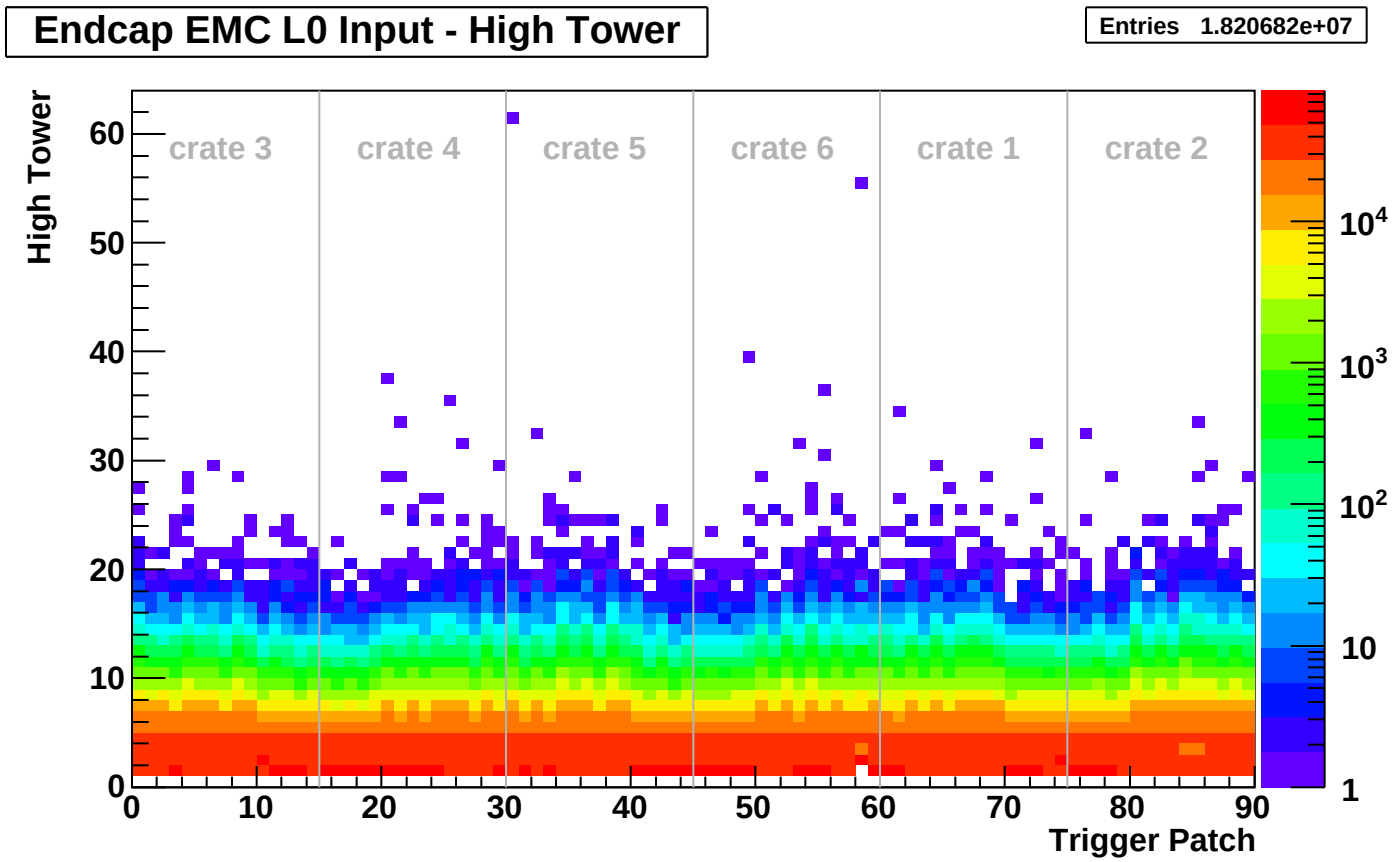

TCU bits
Entries 3236768
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

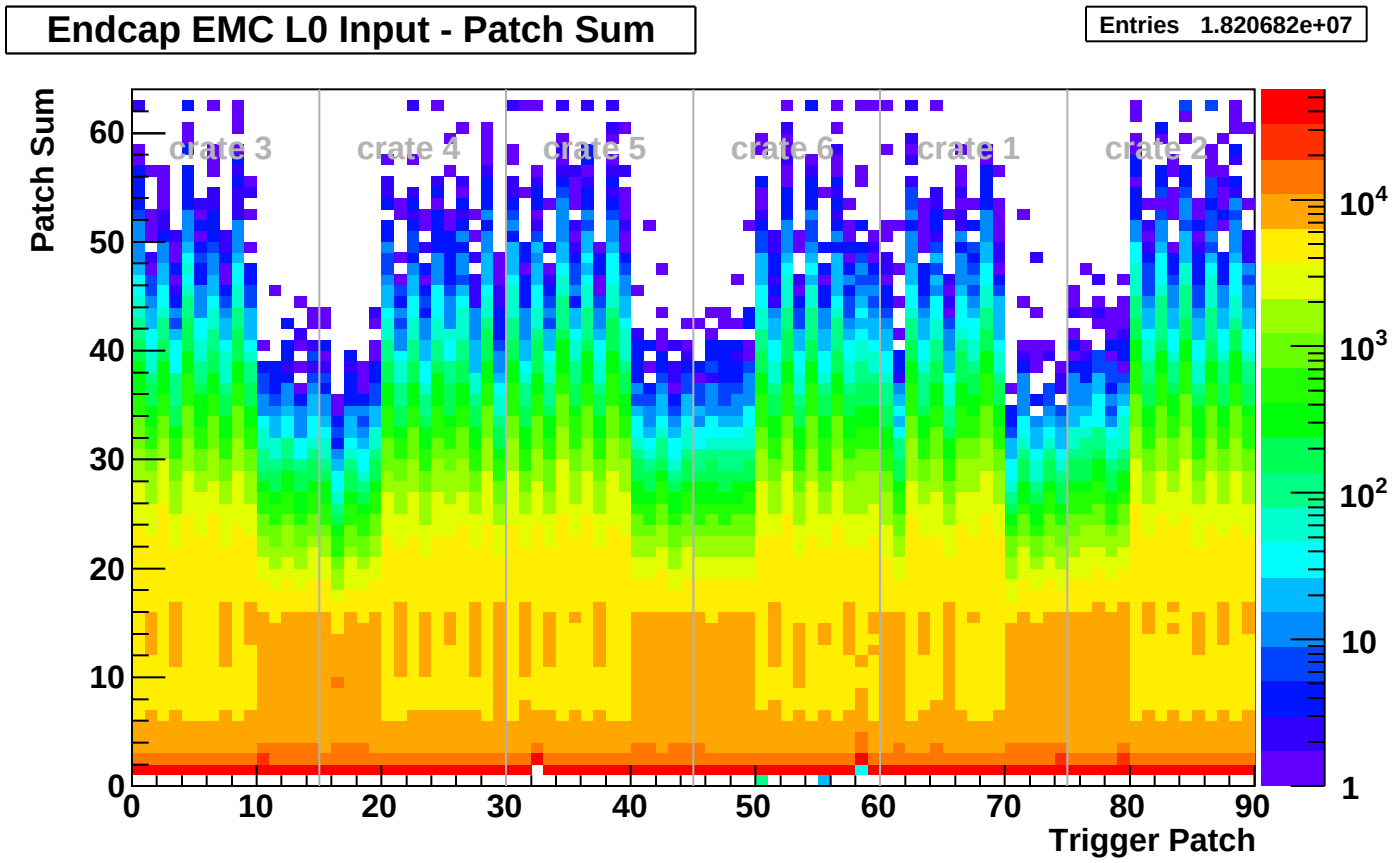

TCU bits
Entries 459436
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

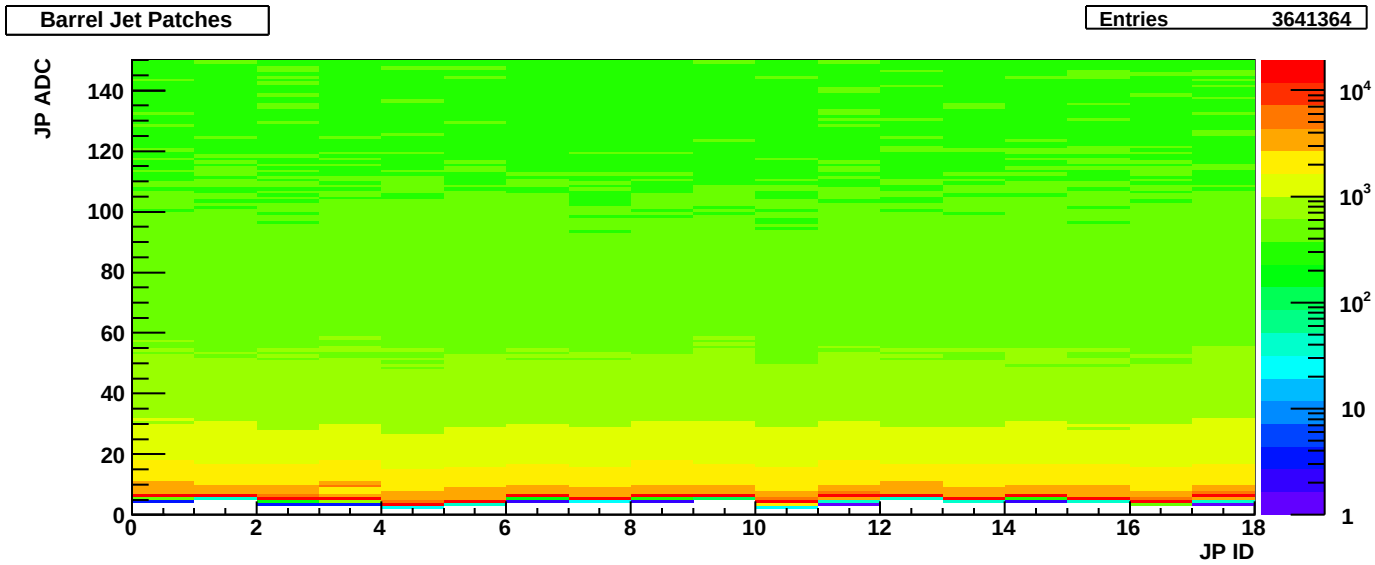

BunchId7Bit (BHT3)
Entries 1088

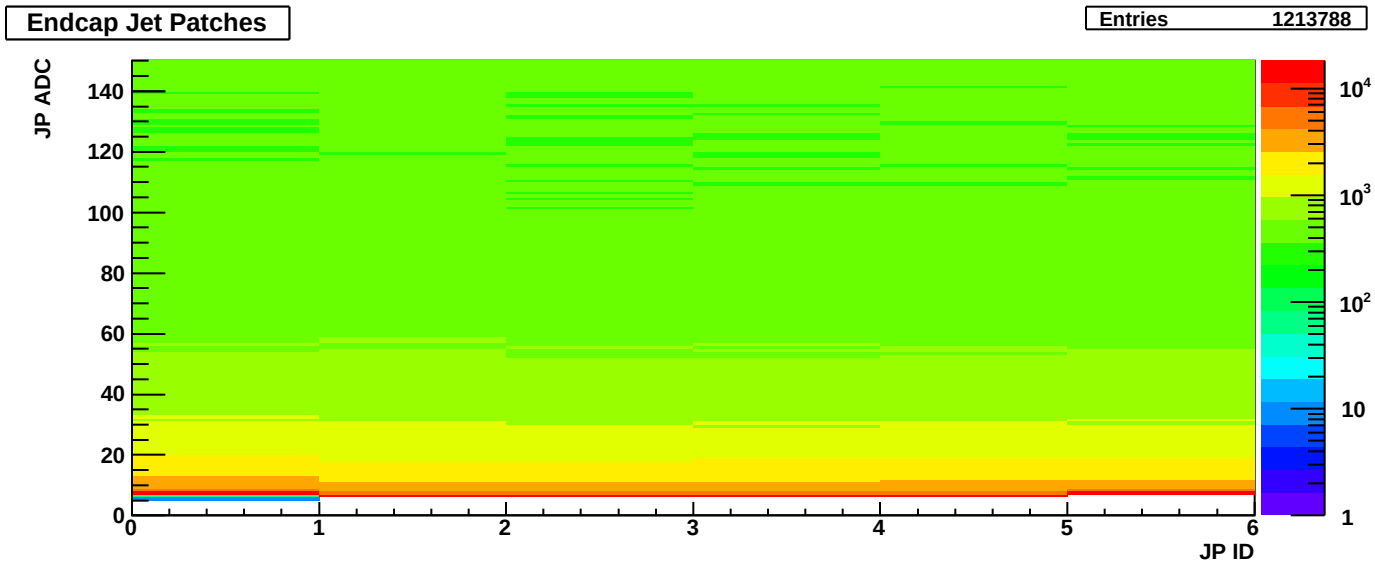

Barrel EMC L0 Input - High Tower
Entries 6.06894e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

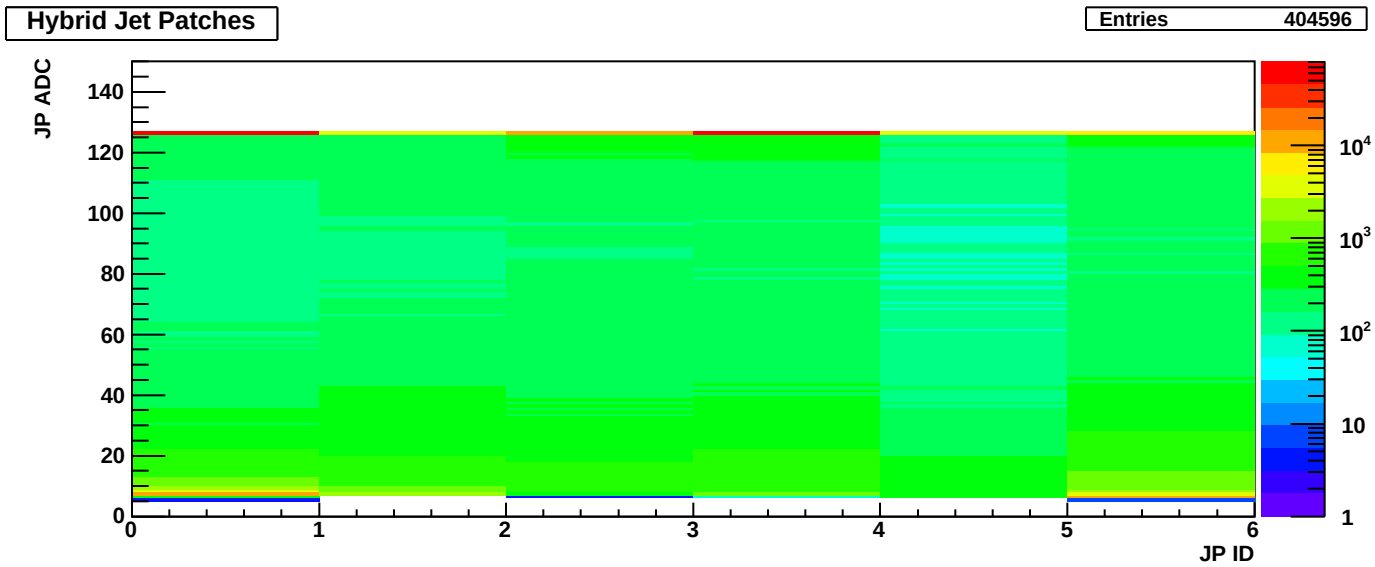

Barrel EMC L0 Input - Patch Sum
Entries 6.06894e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 1.820682e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch


Endcap EMC L0 Input - Patch Sum
Entries 1.820682e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

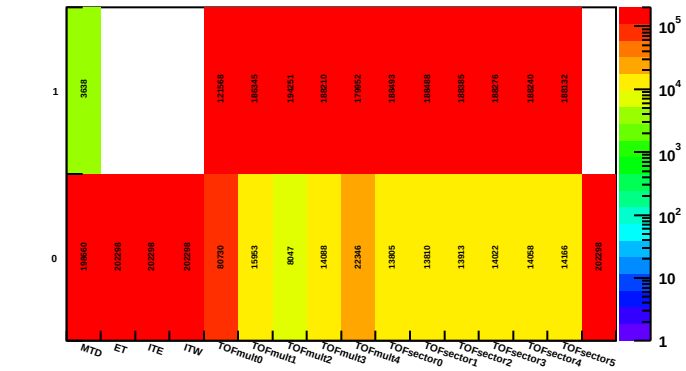

Barrel Jet Patches
Entries 3641364
JP ADC
JP ID


Endcap Jet Patches
Entries 1213788
JP ADC
JP ID


Hybrid Jet Patches
Entries 404596
JP ADC
JP ID

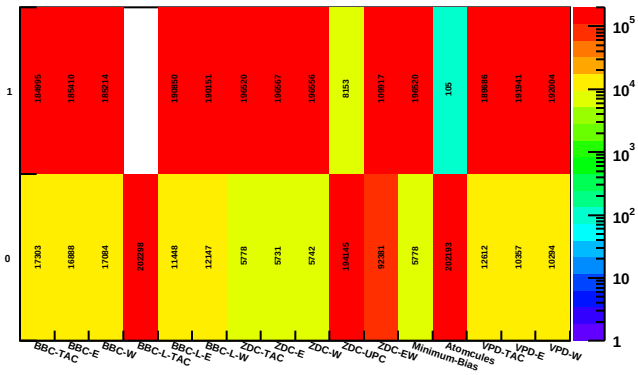
TF201 0-15 (ch0)

Entries 3236768



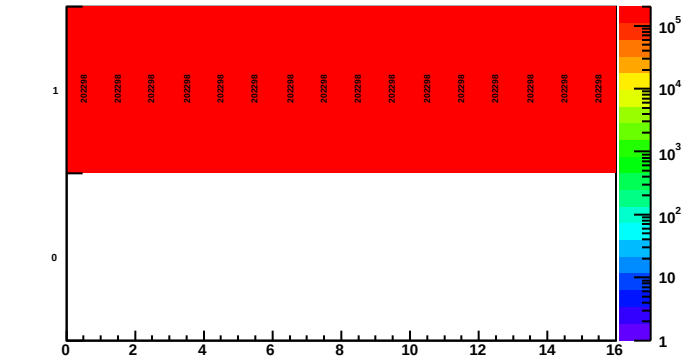
VT201 0-15 (ch1)

Entries 3236768



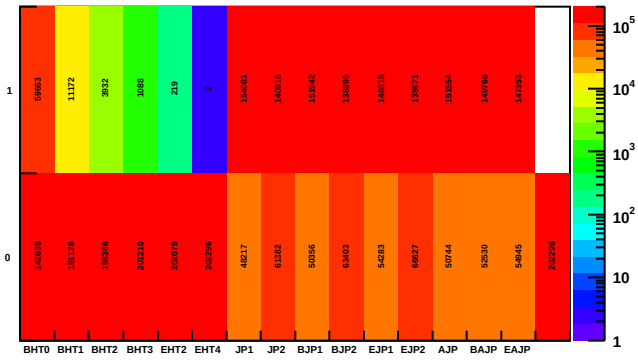
Unused (ch2)

Entries 3236768



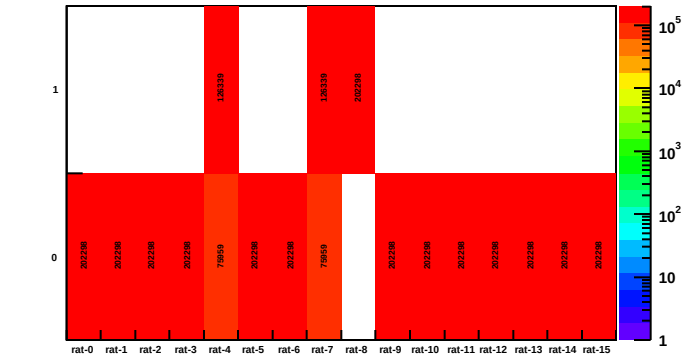
EM201 0-15 (ch3)

Entries 3236768



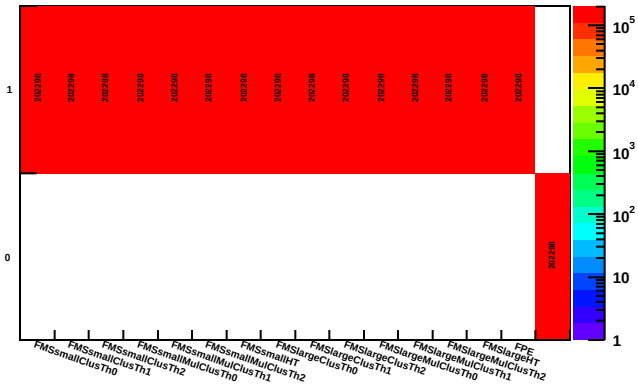
RAT board (ch4)

Entries 3236768



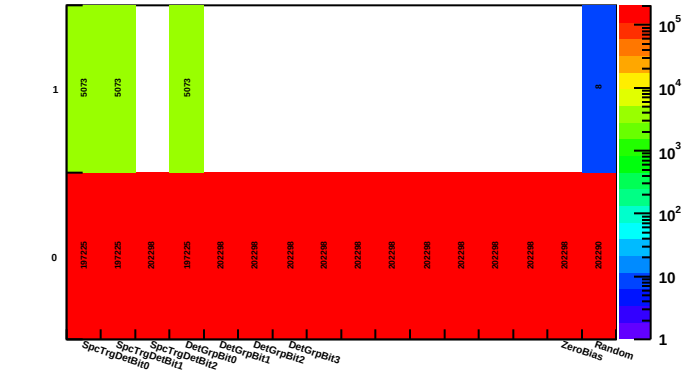
FP201 0-15 (ch5)

Entries 3236768



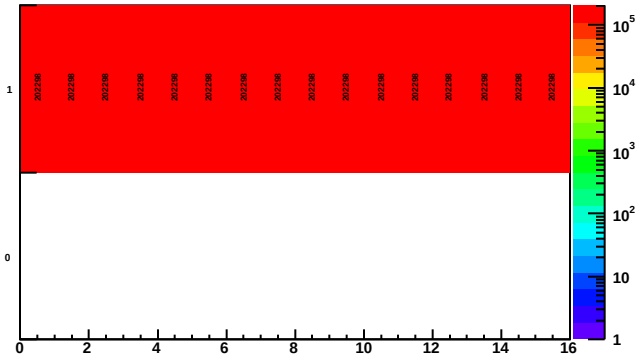
ST201 0-15 (ch6)

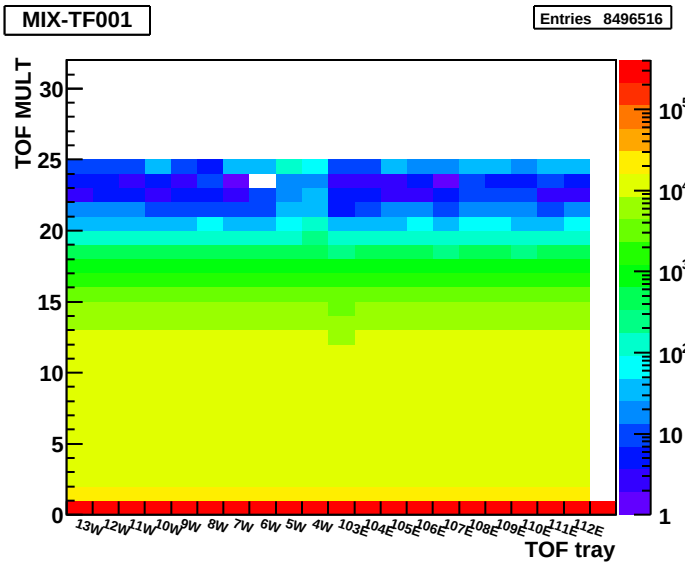
Entries 3236768

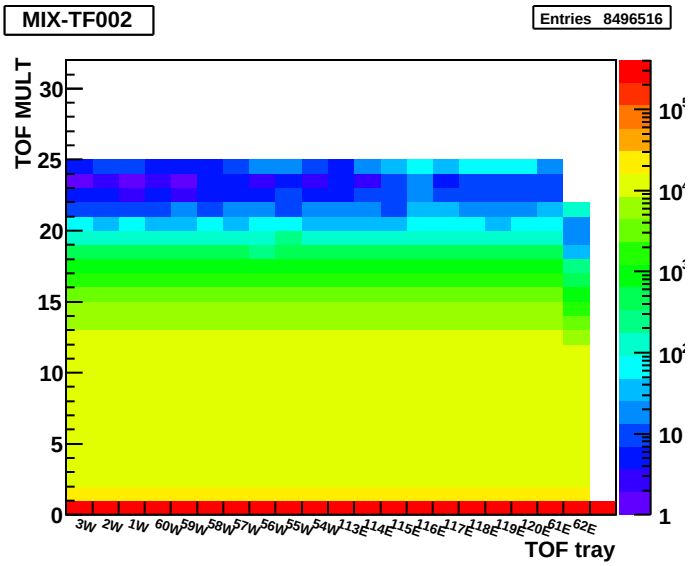


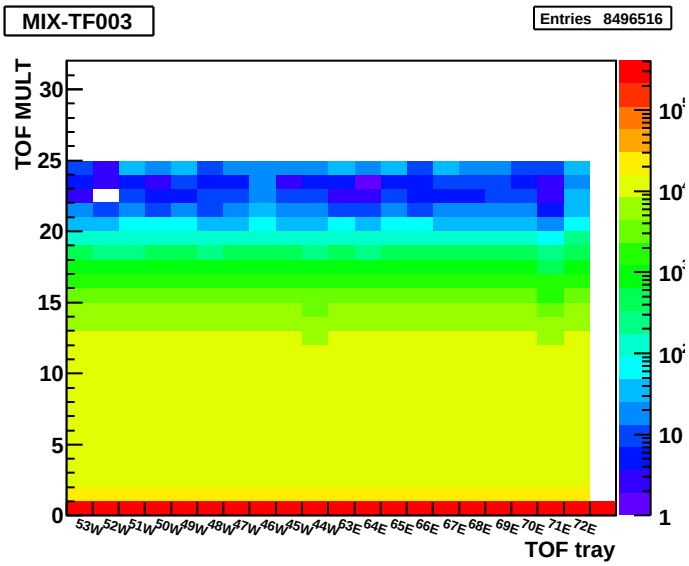
Unused (ch7)

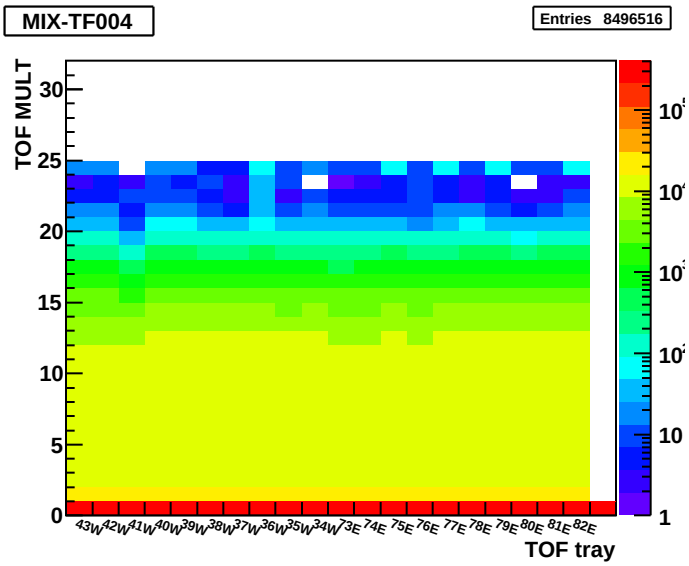
Entries 3236768

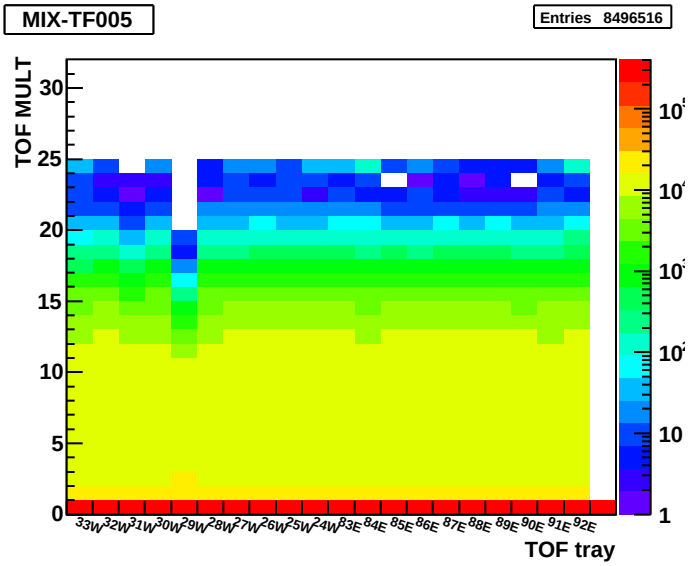


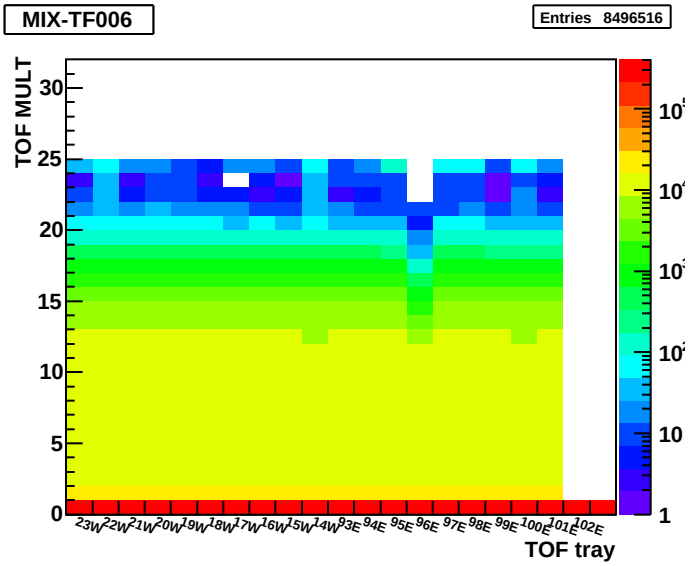

MIX-TF001
Entries 8496516
TOF MULT
TOF tray

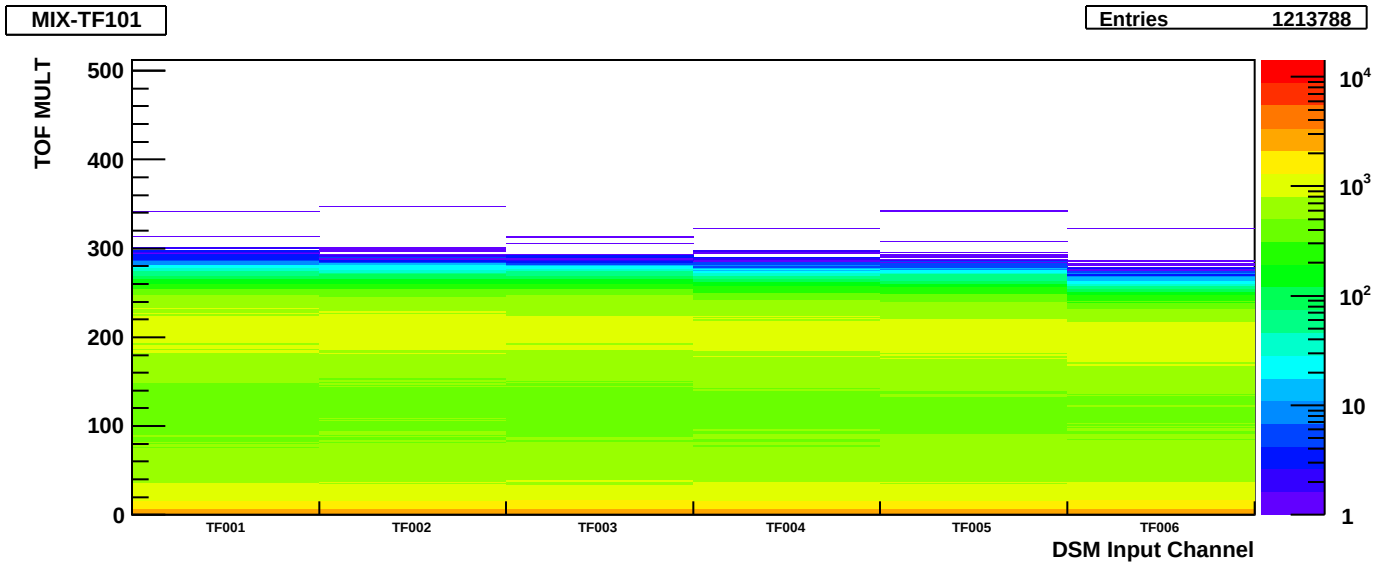

MIX-TF002
Entries 8496516
TOF MULT
TOF tray

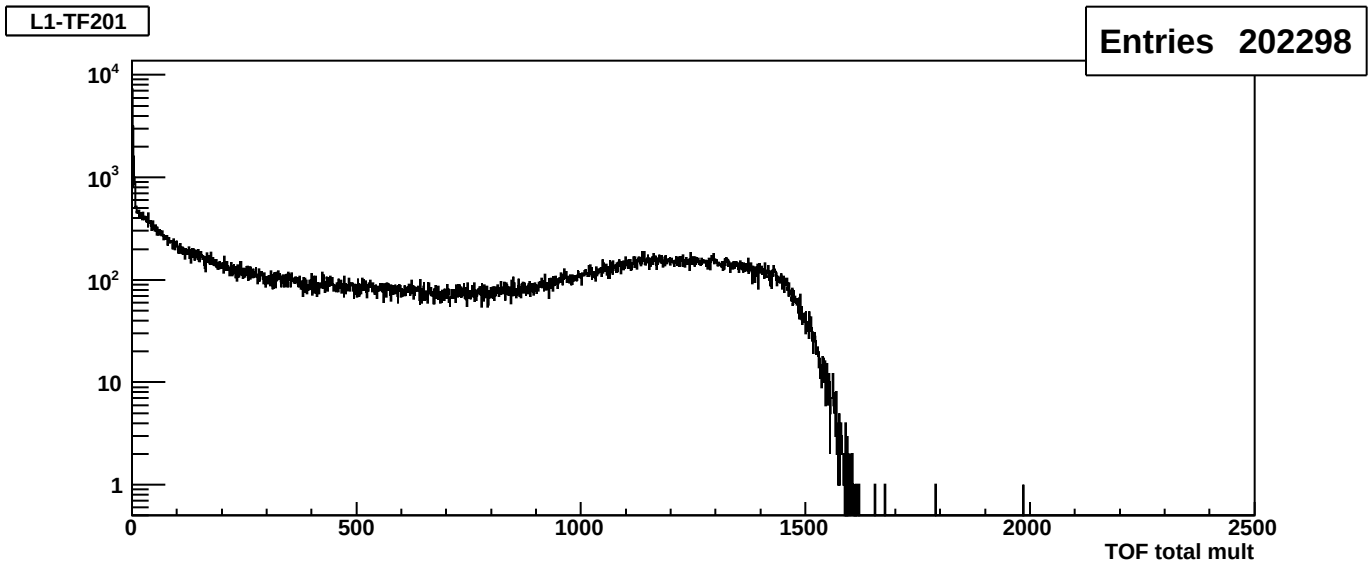

MIX-TF003
Entries 8496516
TOF MULT
TOF tray

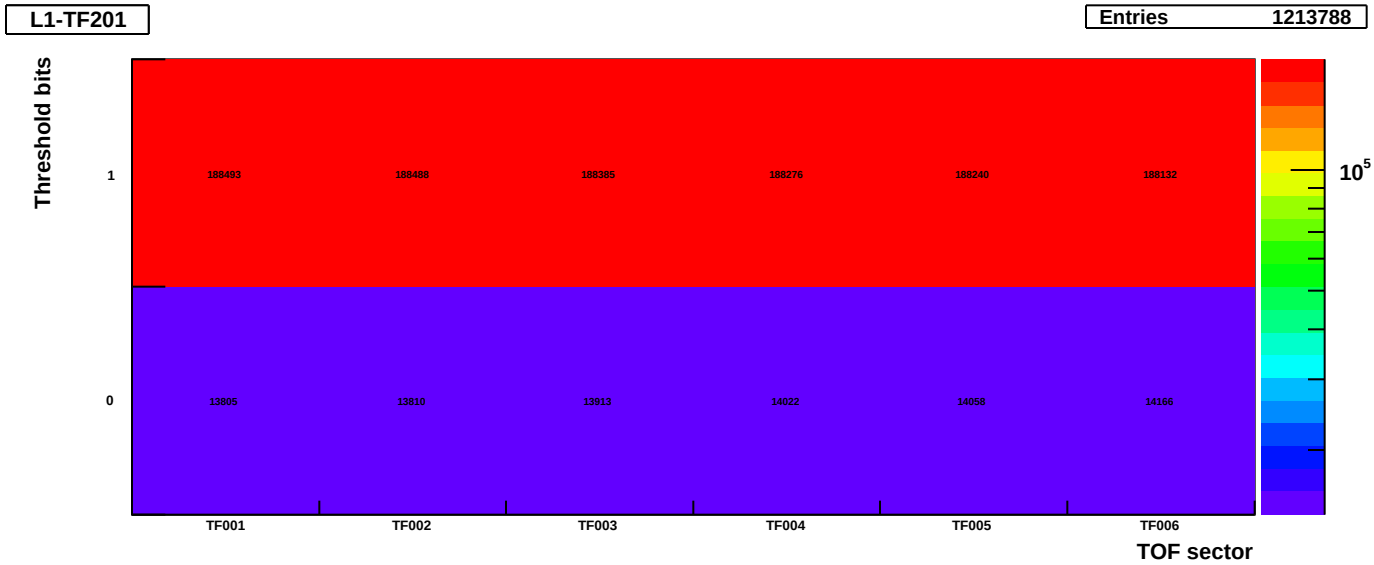

MIX-TF004
Entries 8496516
TOF MULT
TOF tray

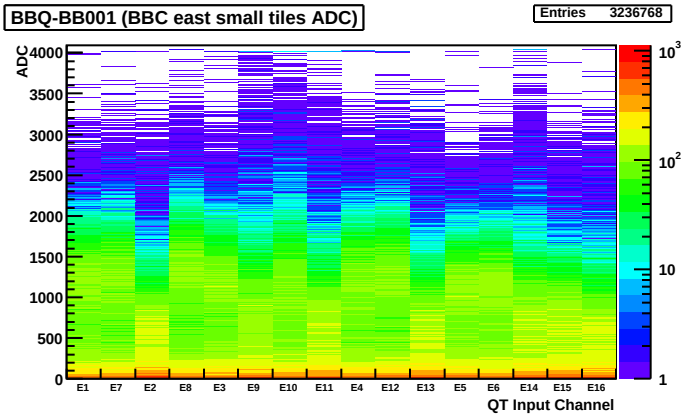

MIX-TF005
Entries 8496516
TOF MULT
TOF tray

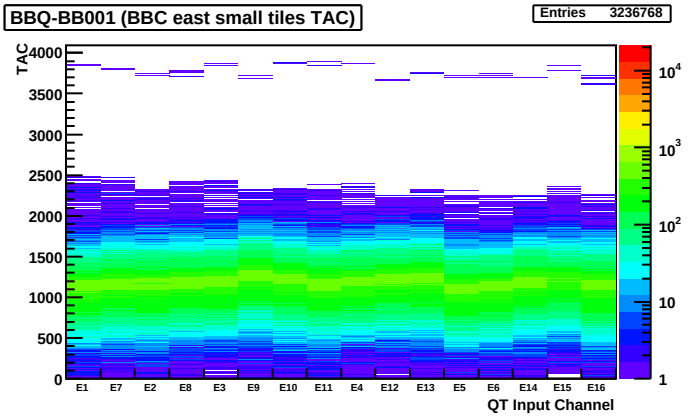

MIX-TF006
Entries 8496516
TOF MULT
TOF tray

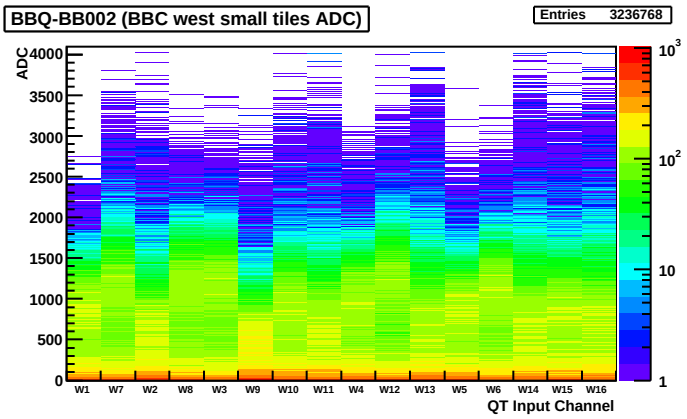

MIX-TF101
Entries 1213788
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

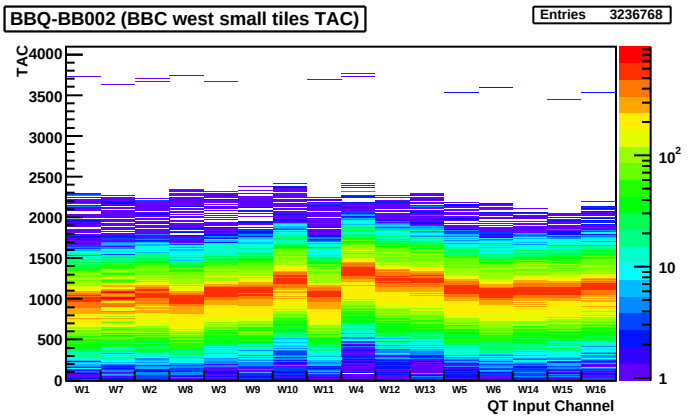

L1-TF201
Entries 202298
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

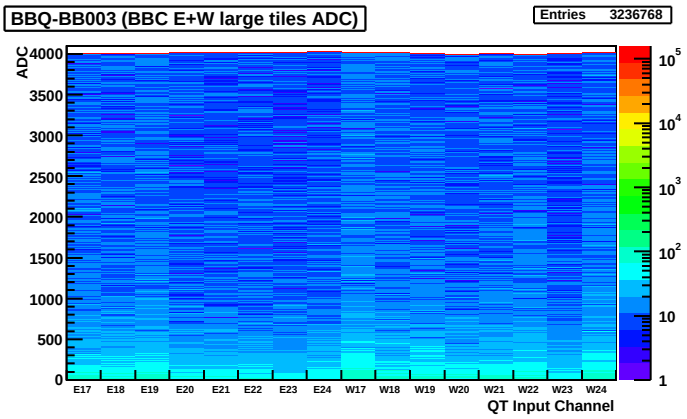

L1-TF201
Entries 1213788
Threshold bits
1
0
188493
188488
188385
188276
188240
188132
13805
13810
13913
14022
14058
14166
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

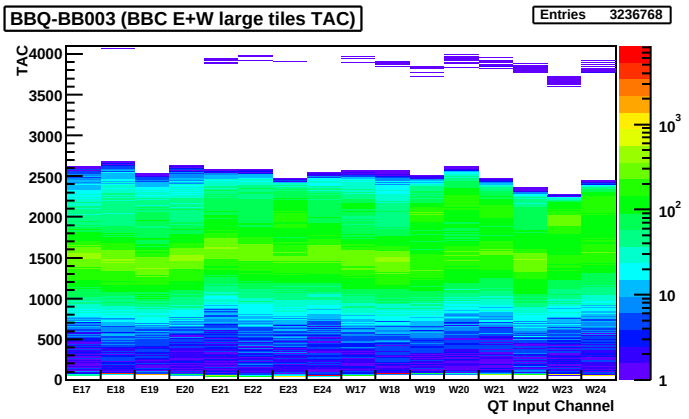

BBQ-BB001 (BBC east small tiles ADC)
Entries 3236768
ADC
QT Input Channel

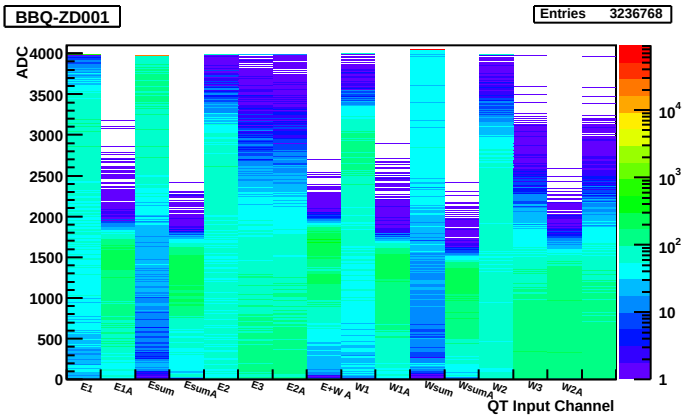

BBQ-BB001 (BBC east small tiles TAC)
Entries 3236768
TAC
QT Input Channel

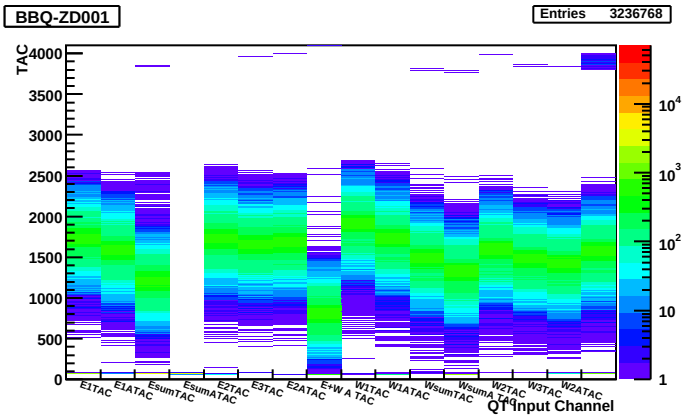

BBQ-BB002 (BBC west small tiles ADC)
Entries 3236768
ADC
QT Input Channel

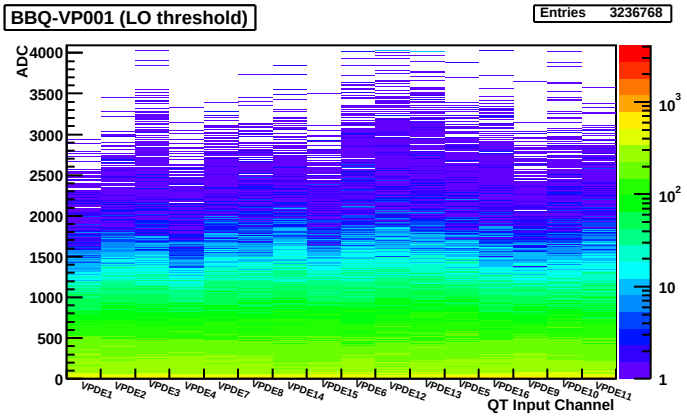

BBQ-BB002 (BBC west small tiles TAC)
Entries 3236768
TAC
QT Input Channel

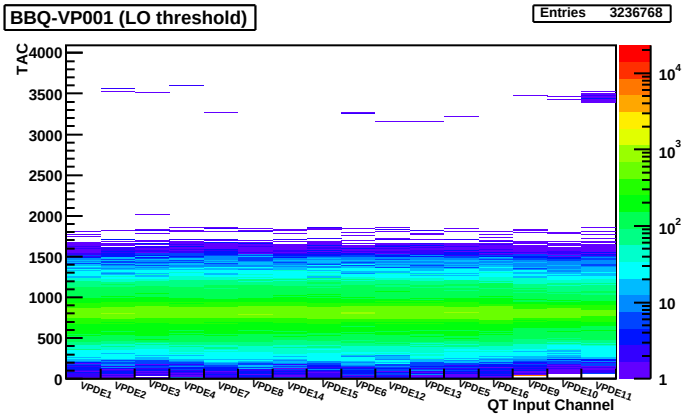

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3236768
ADC
QT Input Channel

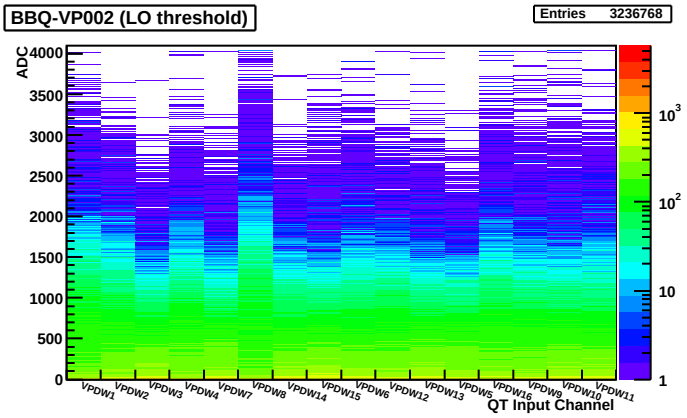

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3236768
TAC
QT Input Channel

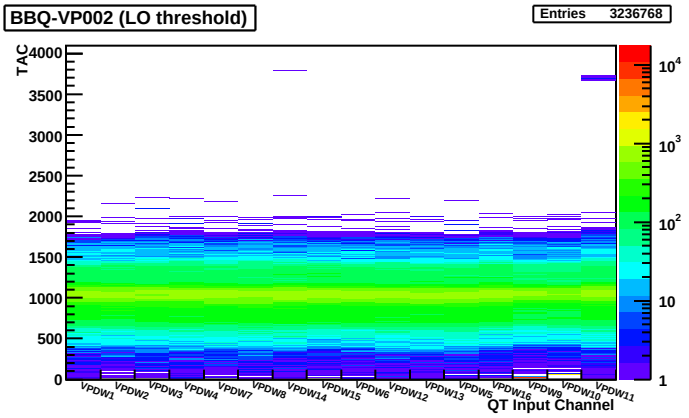

BBQ-ZD001
Entries 3236768
ADC
QT Input Channel

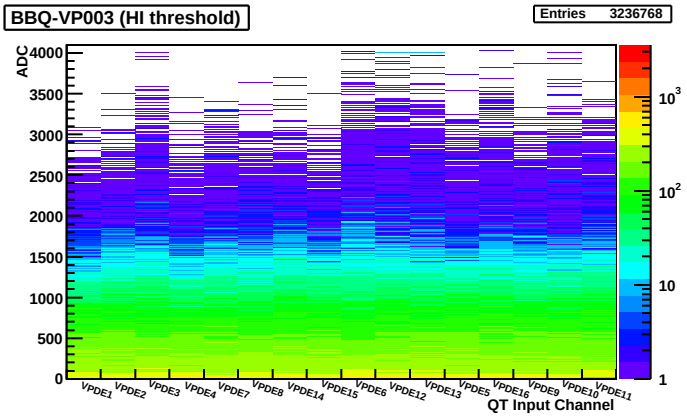

BBQ-ZD001
Entries 3236768
TAC
QT Input Channel

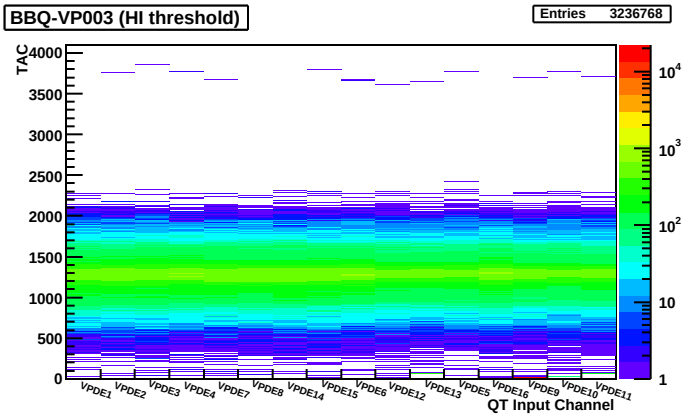

BBQ-VP001 (LO threshold)
Entries 3236768
ADC
QT Input Channel

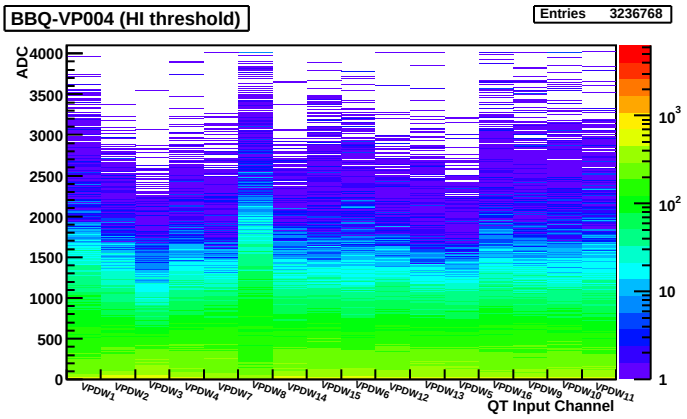

BBQ-VP001 (LO threshold)
Entries 3236768
TAC
QT Input Channel

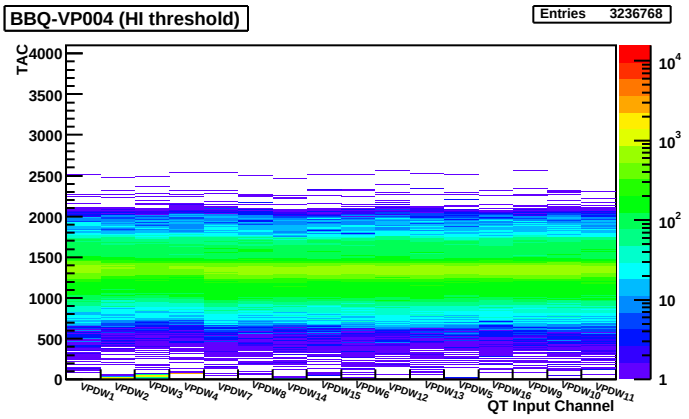

BBQ-VP002 (LO threshold)
Entries 3236768
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 3236768
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 3236768
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 3236768
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 3236768
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 3236768
TAC
QT Input Channel