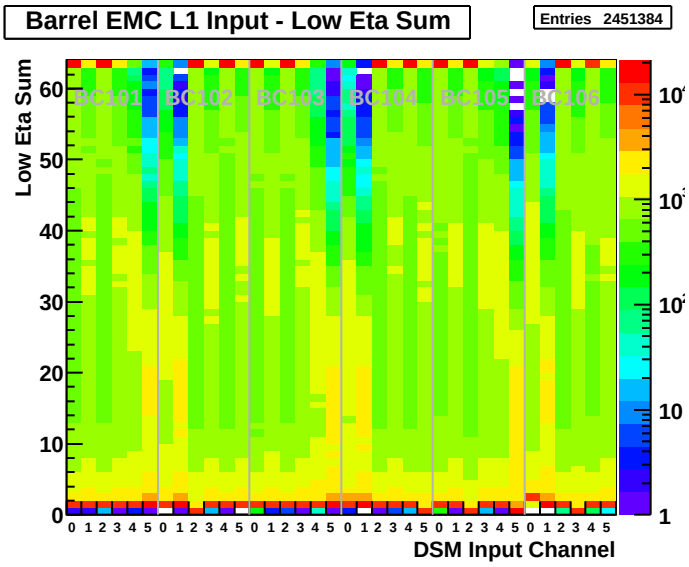
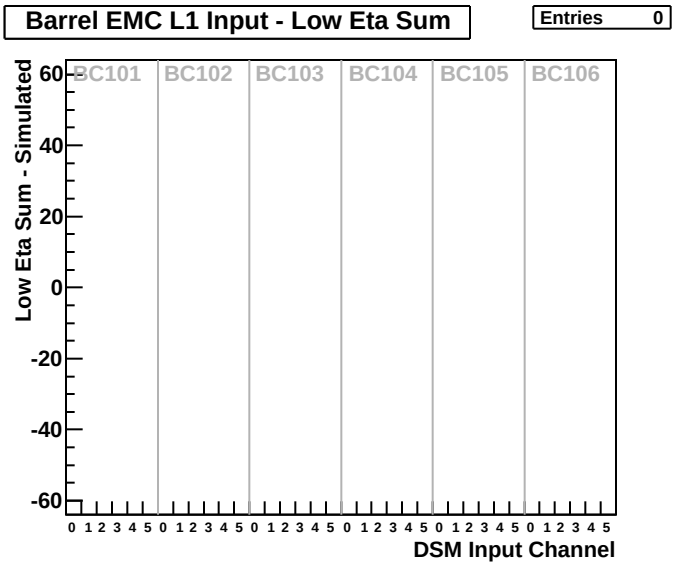
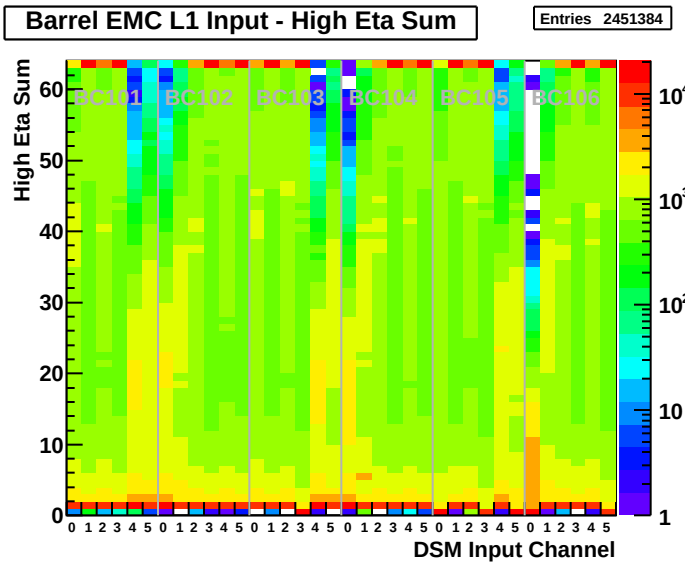
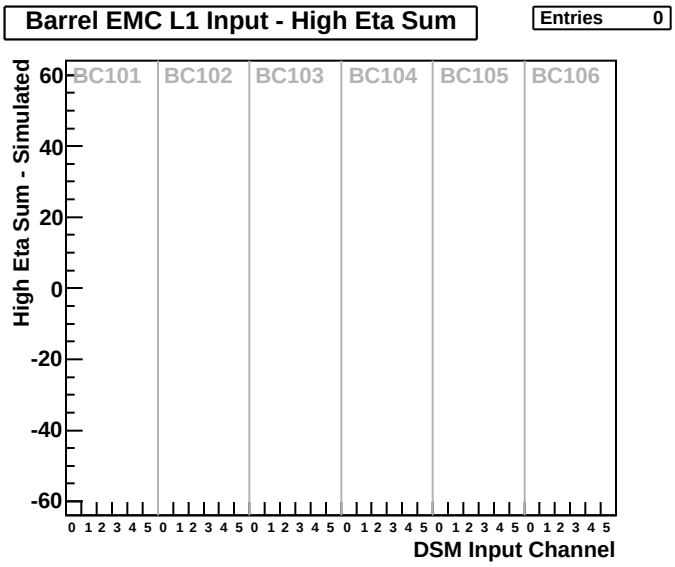
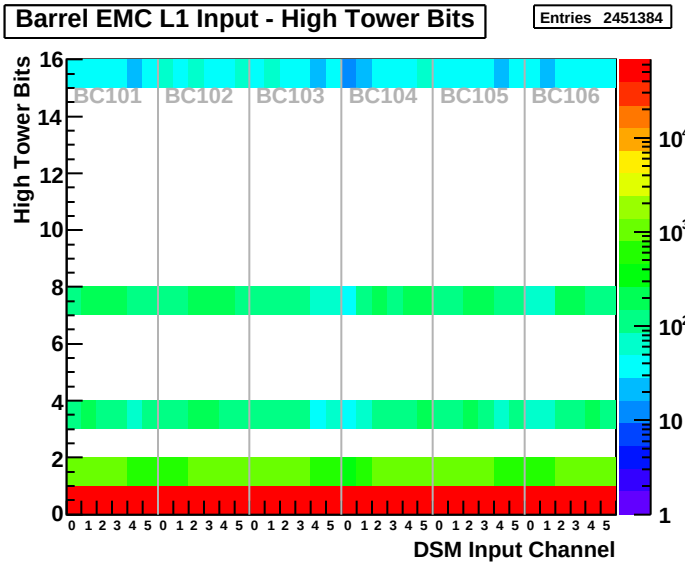
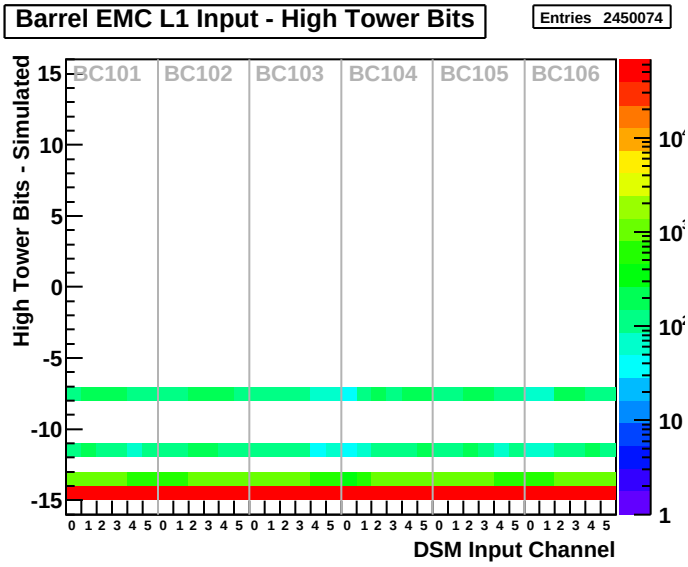

Barrel EMC L1 Input - Low Eta Sum
Entries 2451384
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

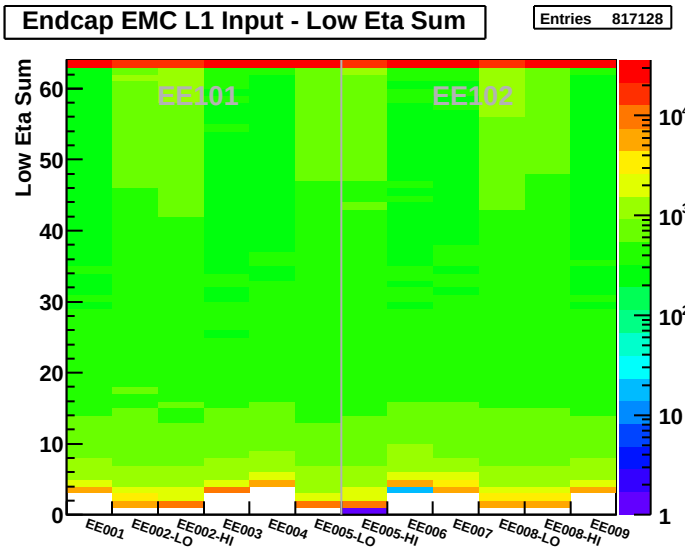

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

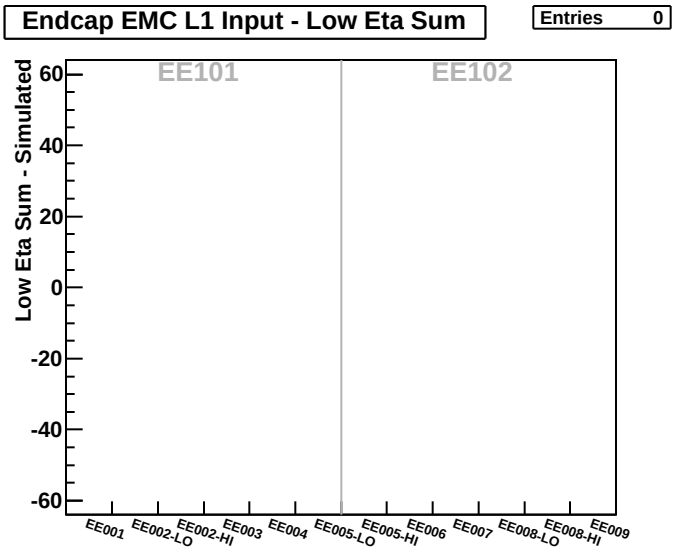

Barrel EMC L1 Input - High Eta Sum
Entries 2451384
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

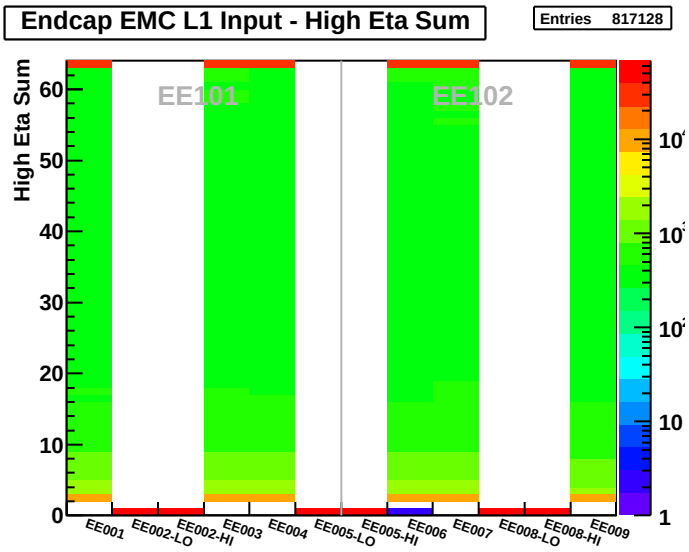

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

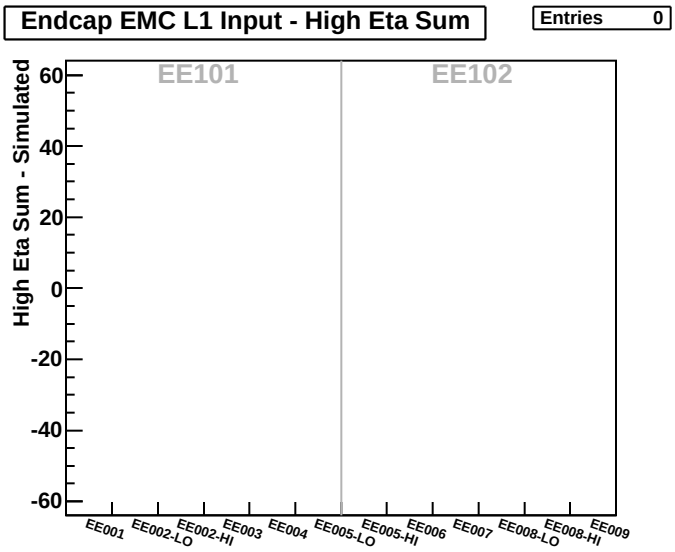

Barrel EMC L1 Input - High Tower Bits
Entries 2451384
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

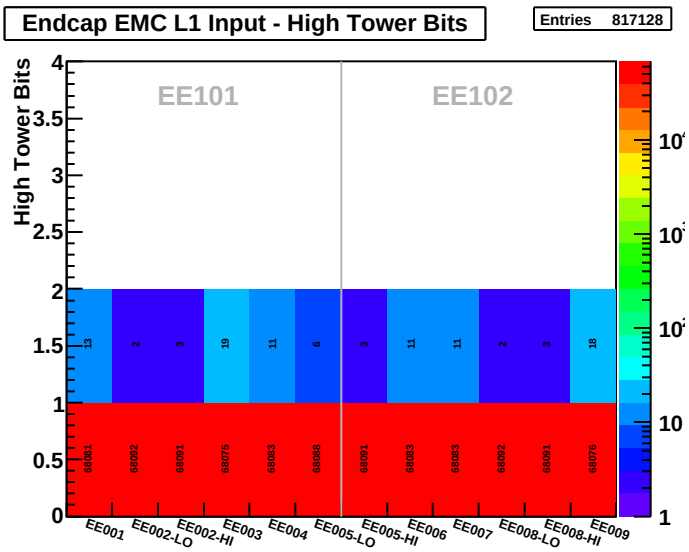

Barrel EMC L1 Input - High Tower Bits
Entries 2450074
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

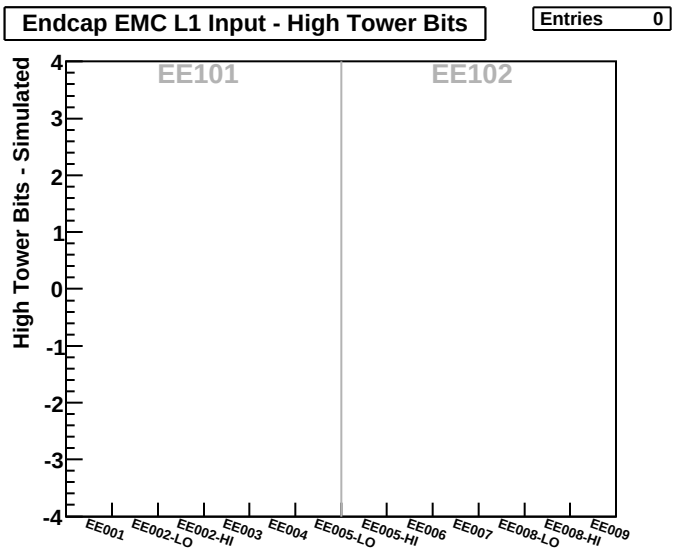

Endcap EMC L1 Input - Low Eta Sum
Entries 817128
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

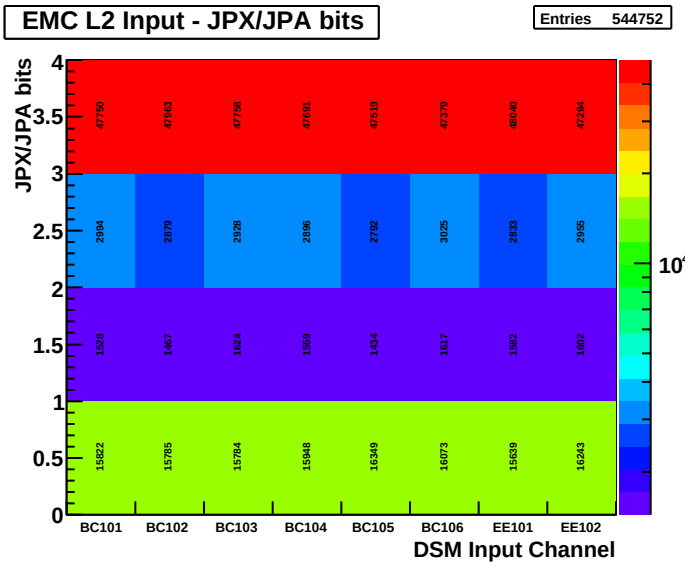

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

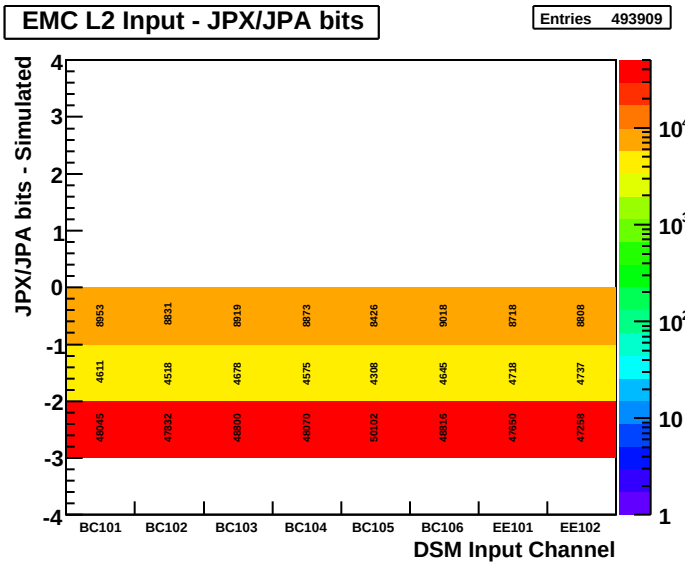

Endcap EMC L1 Input - High Eta Sum
Entries 817128
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

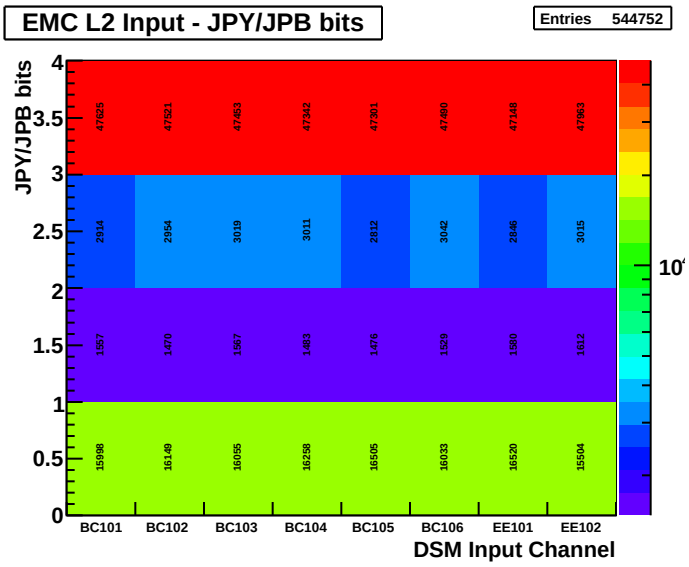

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

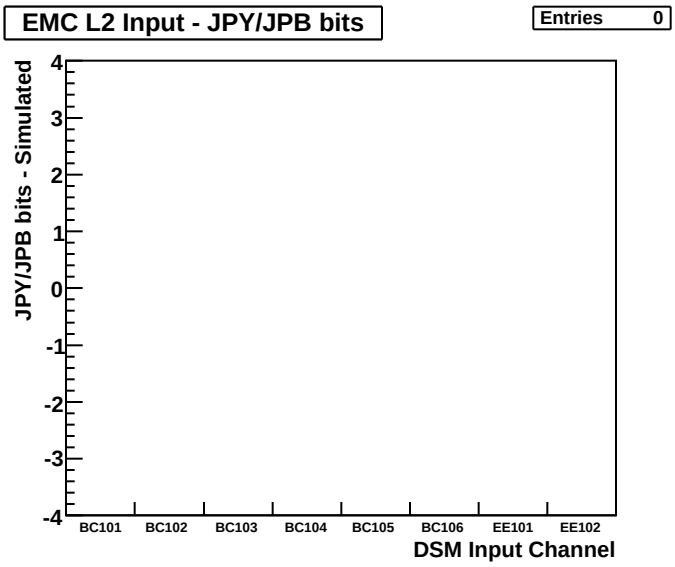

Endcap EMC L1 Input - High Tower Bits
Entries 817128
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

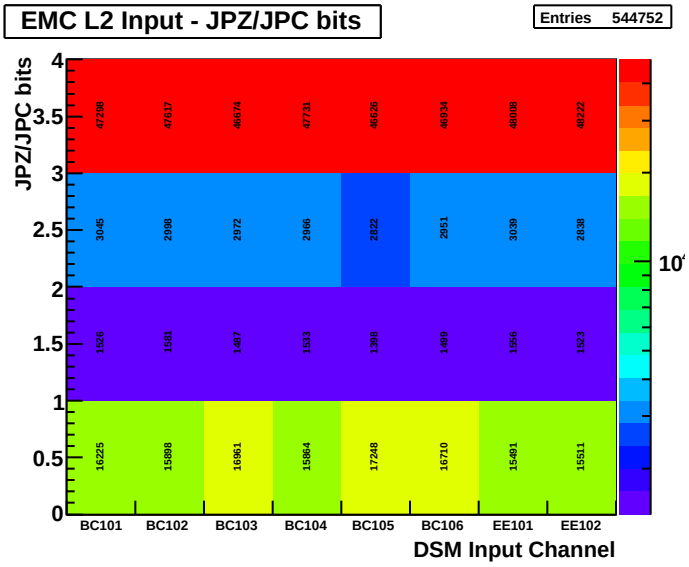

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

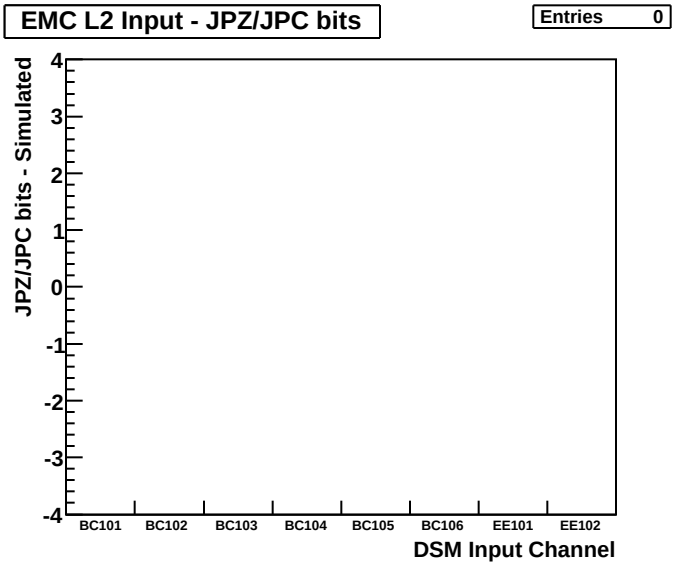

EMC L2 Input - JPX/JPA bits
Entries 544752
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
47750 47963 47758 47691 47519 47379 48040 47294
2994 2879 2928 2896 2792 3025 2833 2955
1526 1467 1624 1559 1434 1617 1582 1602
15822 15785 15784 15948 16349 16073 15639 16243

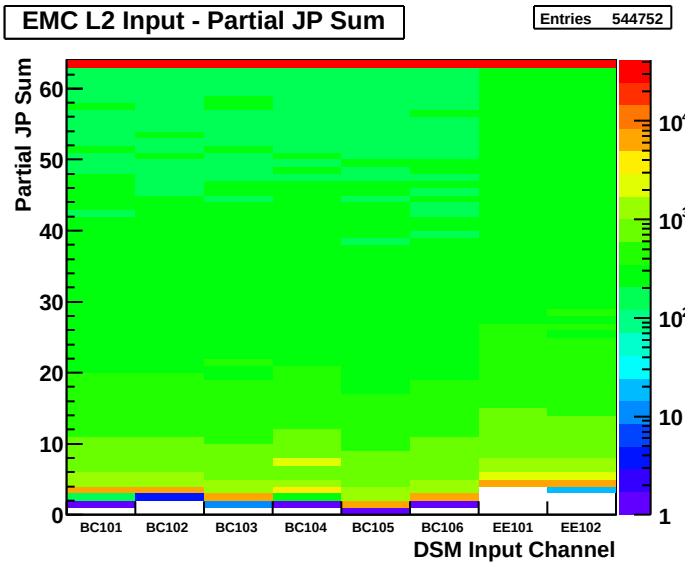

EMC L2 Input - JPX/JPA bits
Entries 493909
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8953 8831 8919 8873 8426 9018 8718 8808
4611 4518 4678 4575 4308 4645 4718 4737
49045 47832 48800 48070 50102 48816 47650 47258

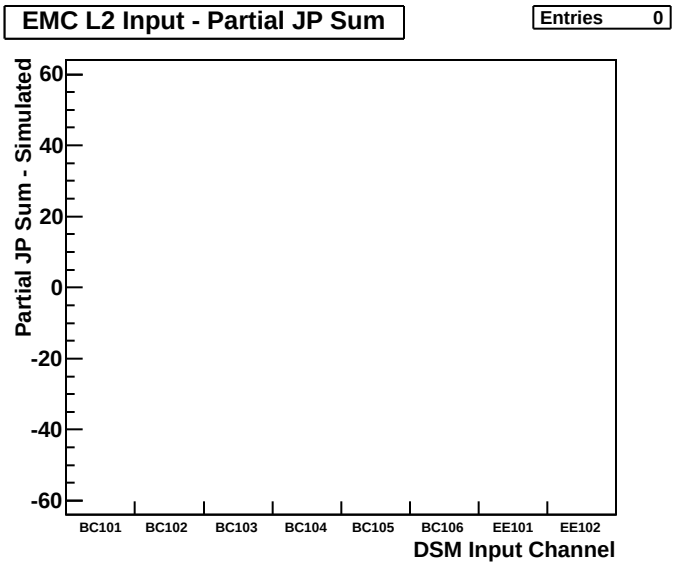

EMC L2 Input - JPY/JPB bits
Entries 544752
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
47625 47521 47453 47342 47301 47490 47148 47963
2914 2954 3019 3011 2812 3042 2846 3015
1557 1470 1567 1483 1476 1529 1580 1612
15998 16149 16055 16258 16505 16033 16520 15504

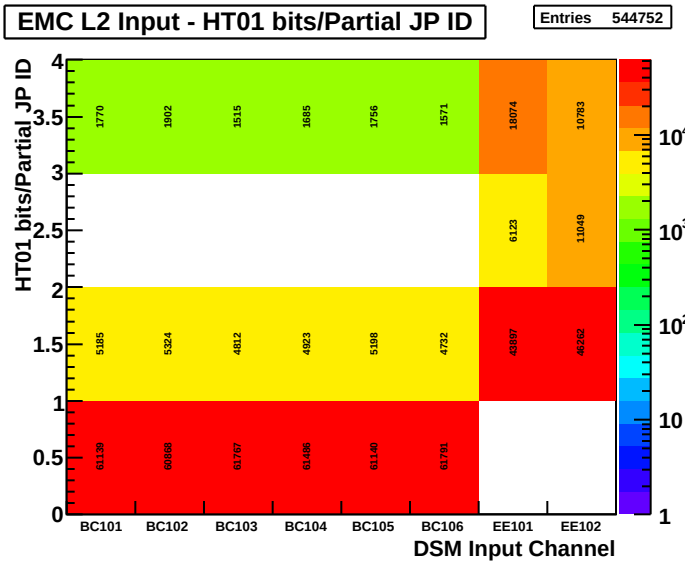

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

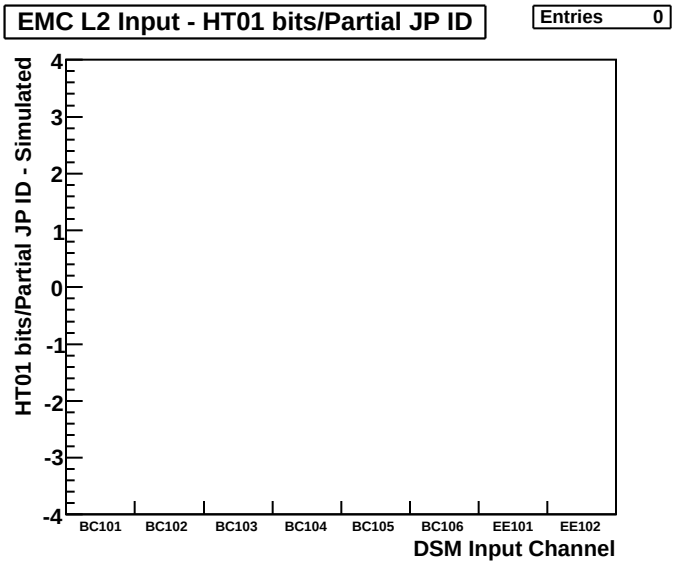

EMC L2 Input - JPZ/JPC bits
Entries 544752
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
47298 47617 46674 47731 46526 46934 48008 48222
3045 2998 2972 2966 2822 2951 3039 2838
1526 1561 1487 1533 1398 1499 1556 1523
16225 15898 16961 15864 17248 16710 15491 15511

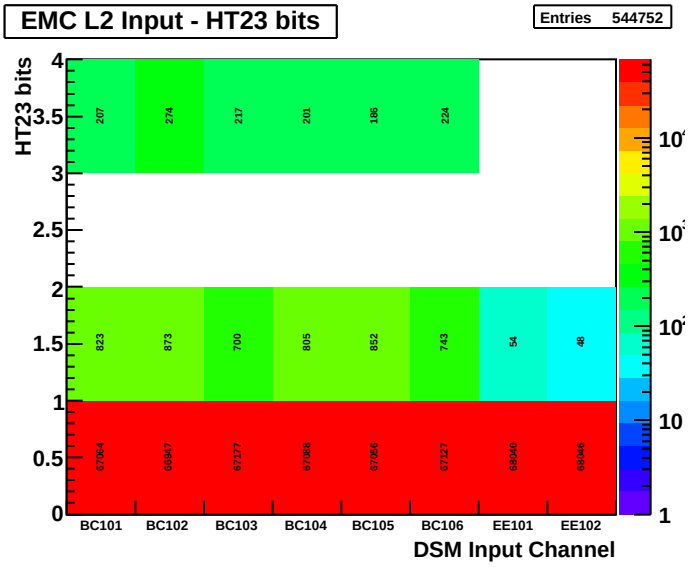

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

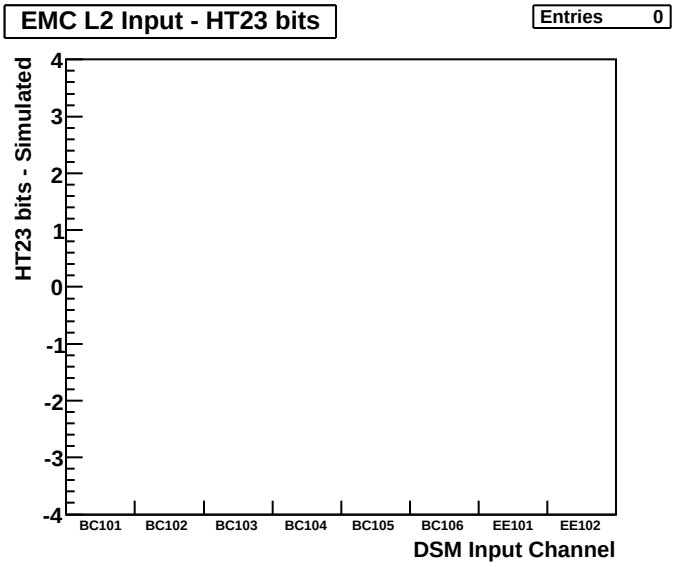

EMC L2 Input - Partial JP Sum
Entries 544752
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

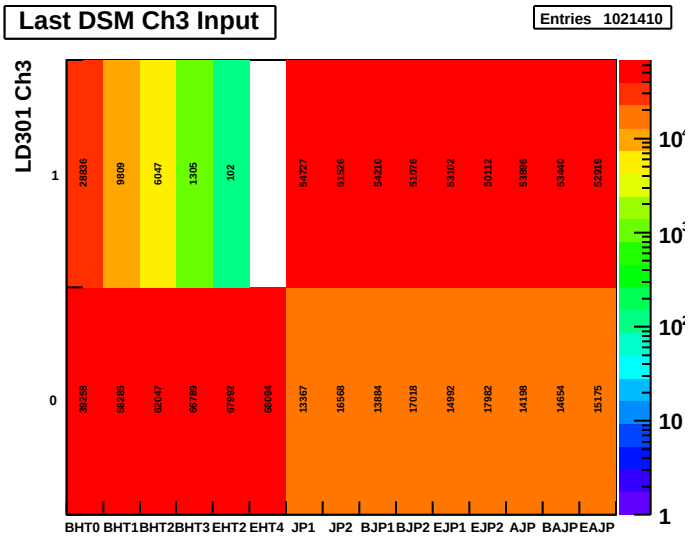

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

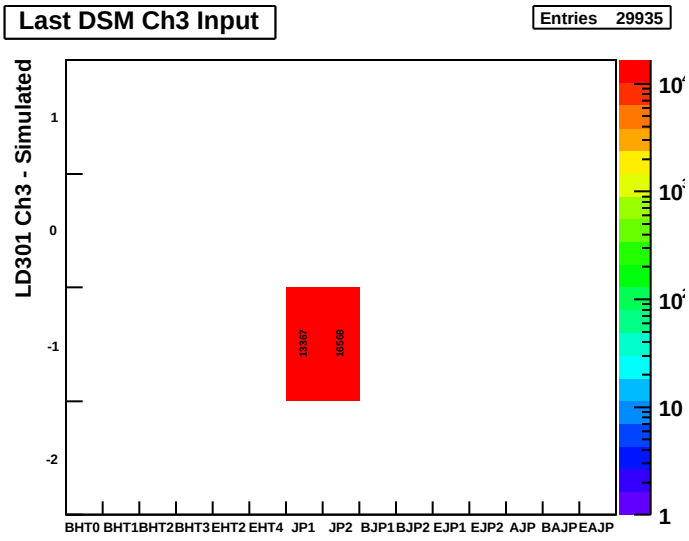

EMC L2 Input - HT01 bits/Partial JP ID
Entries 544752
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

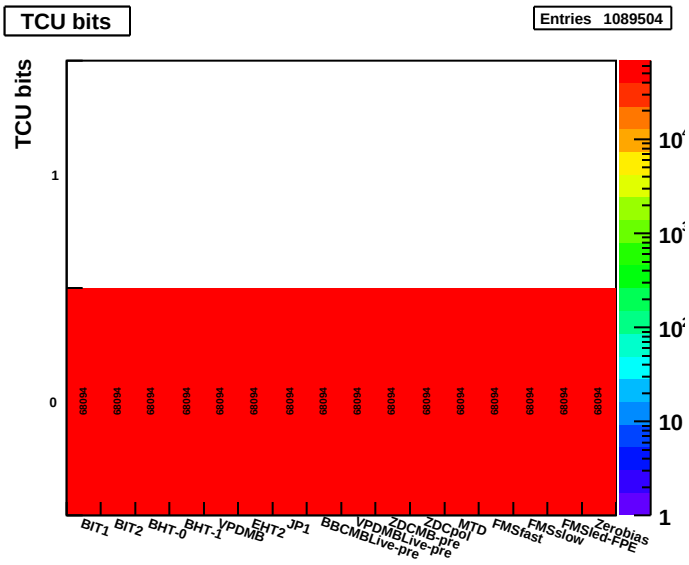

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

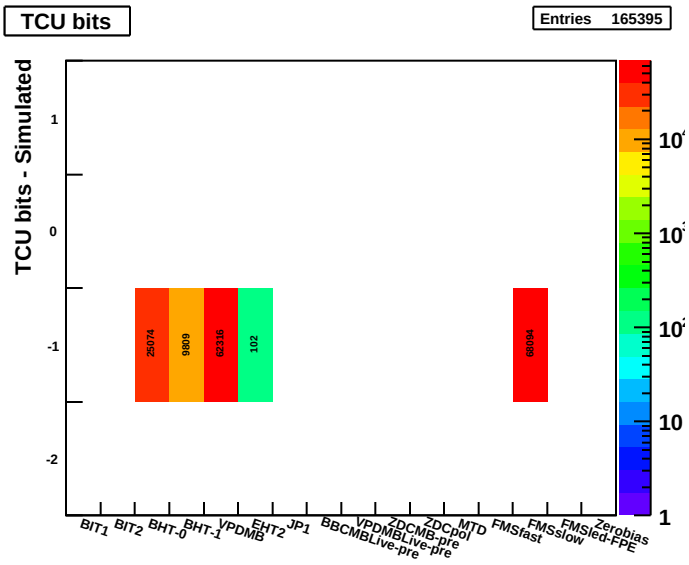

EMC L2 Input - HT23 bits
Entries 544752
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

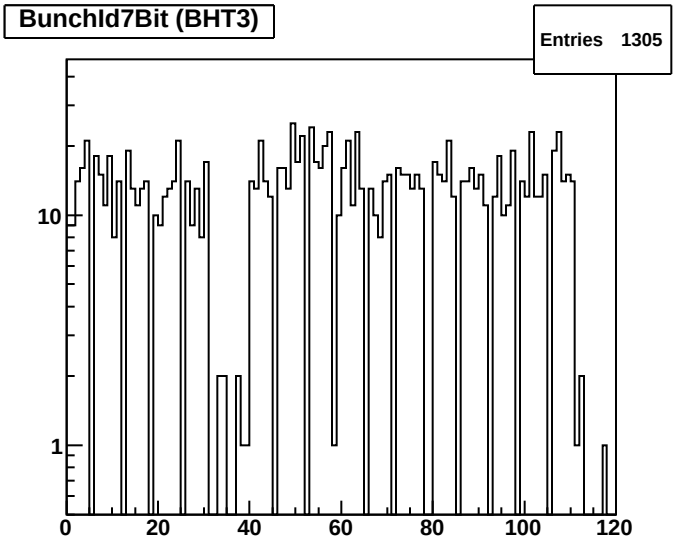

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

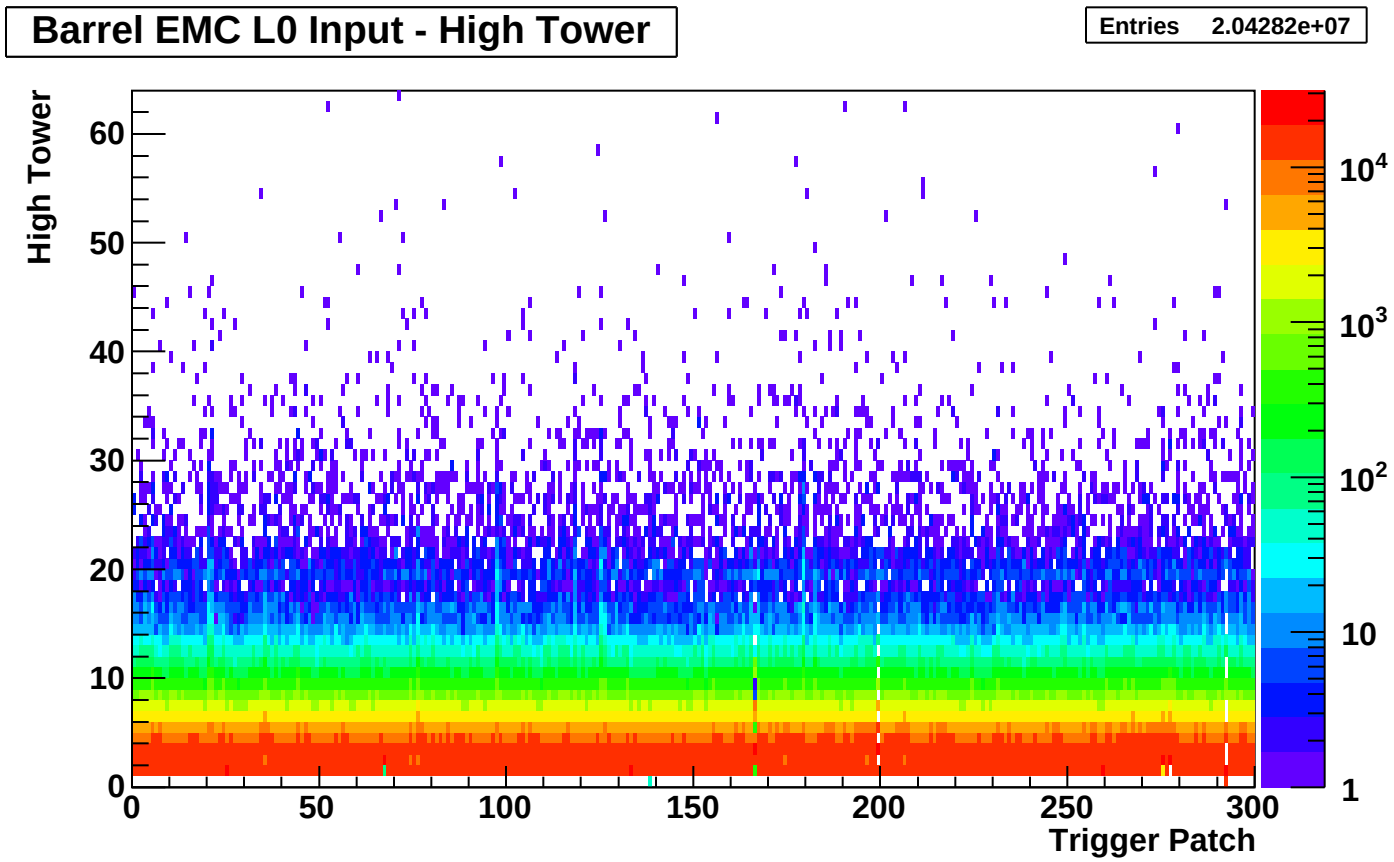

Last DSM Ch3 Input
Entries 1021410
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

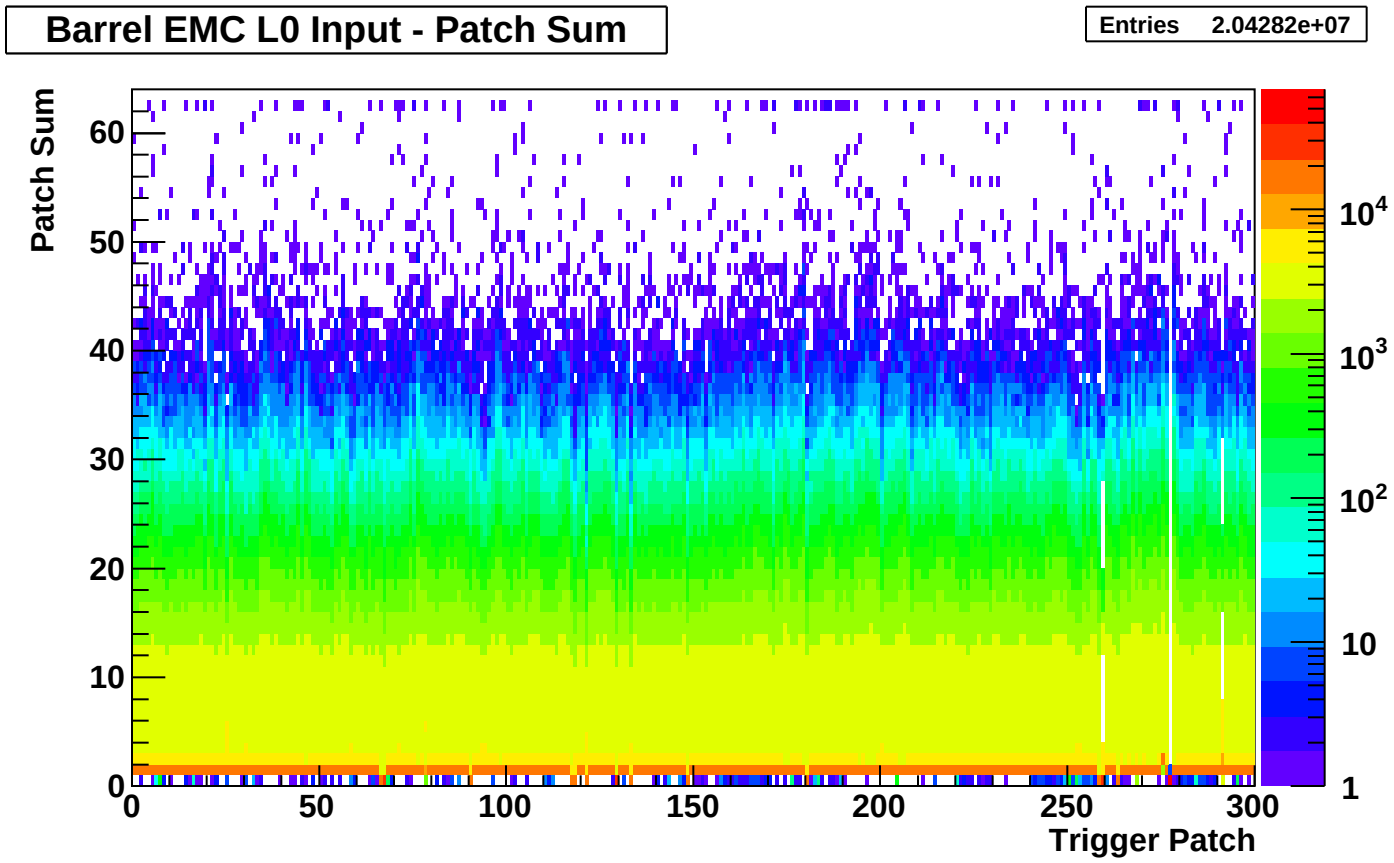

Last DSM Ch3 Input
Entries 29935
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

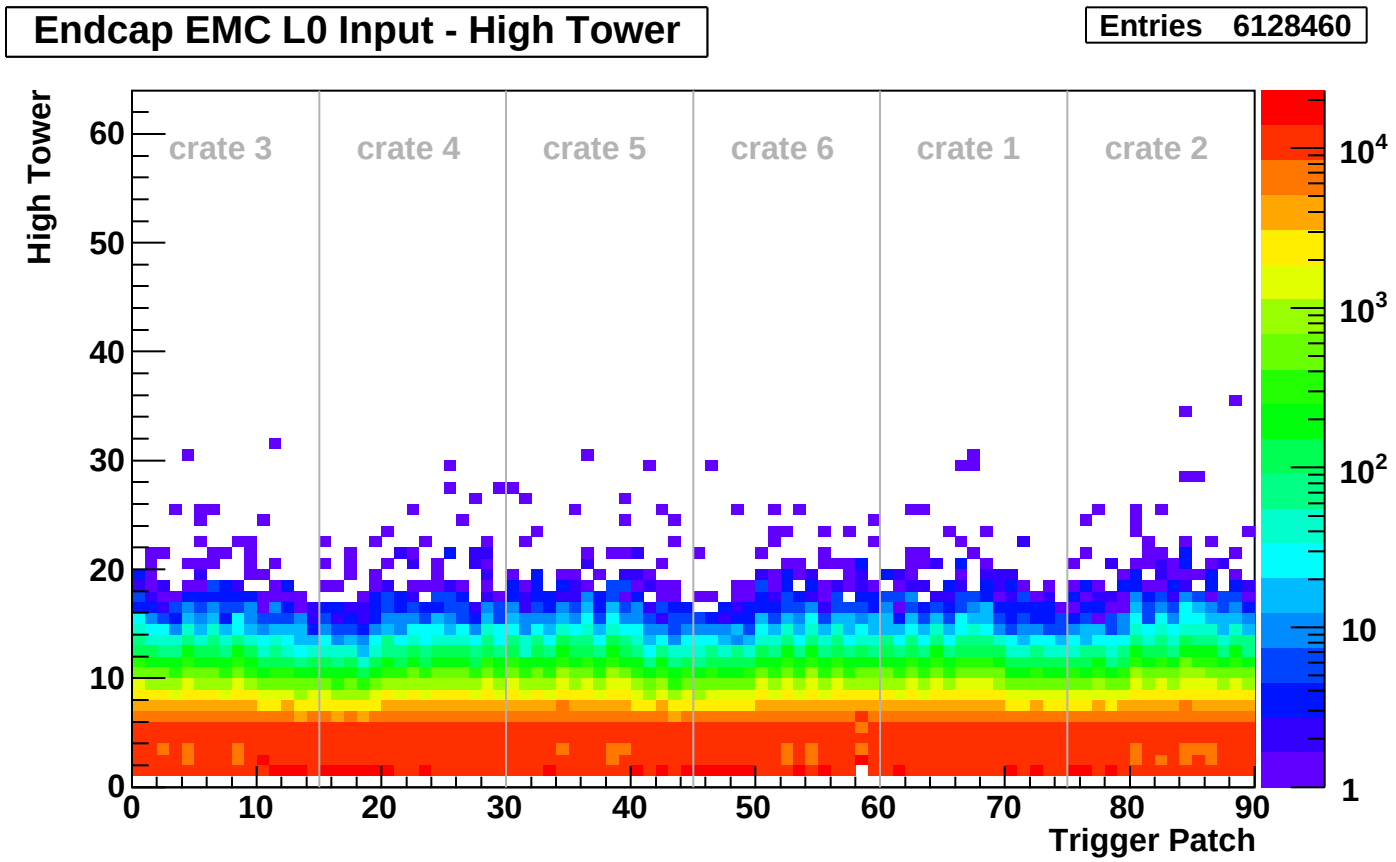

TCU bits
Entries 1089504
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

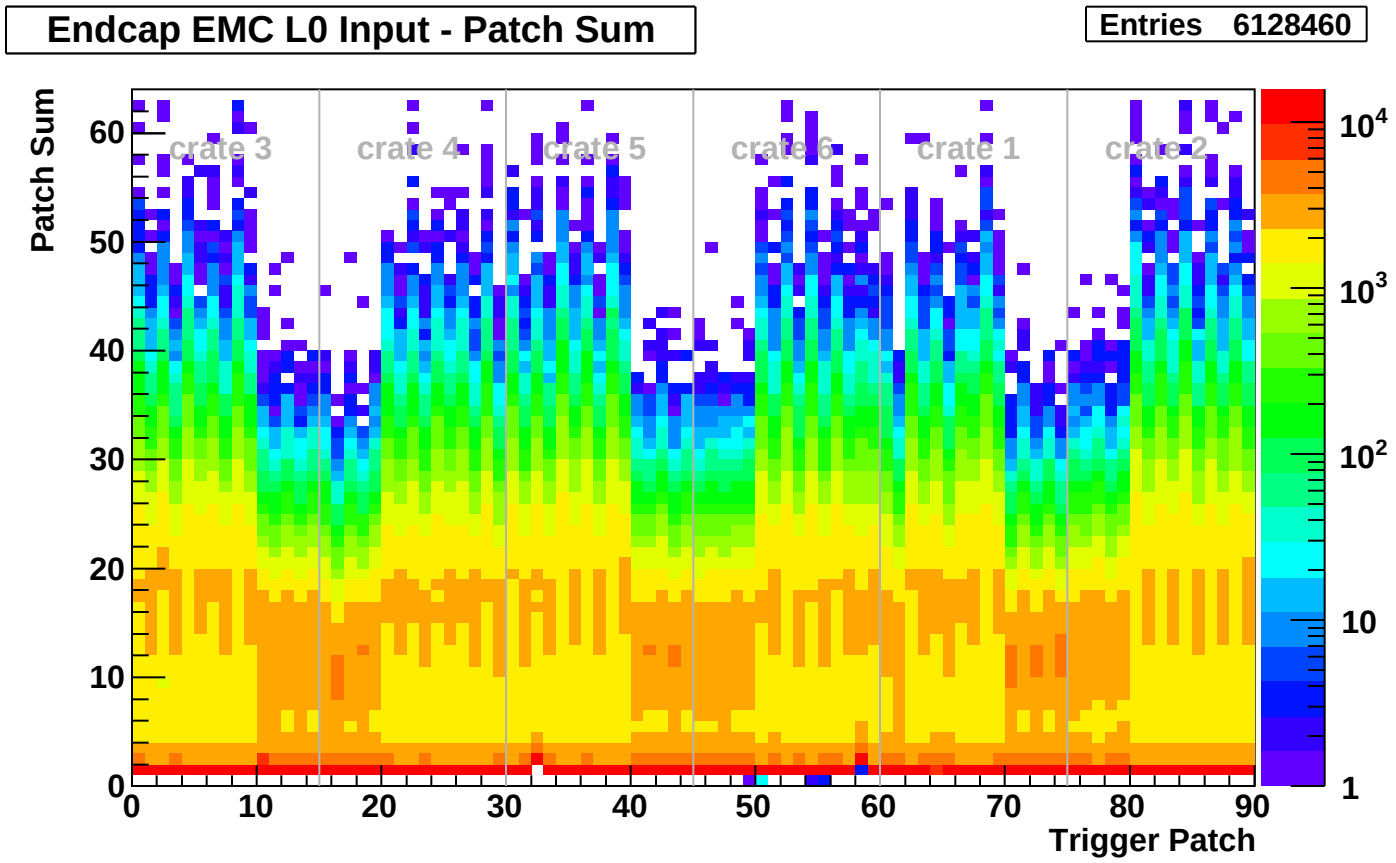

TCU bits
Entries 165395
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

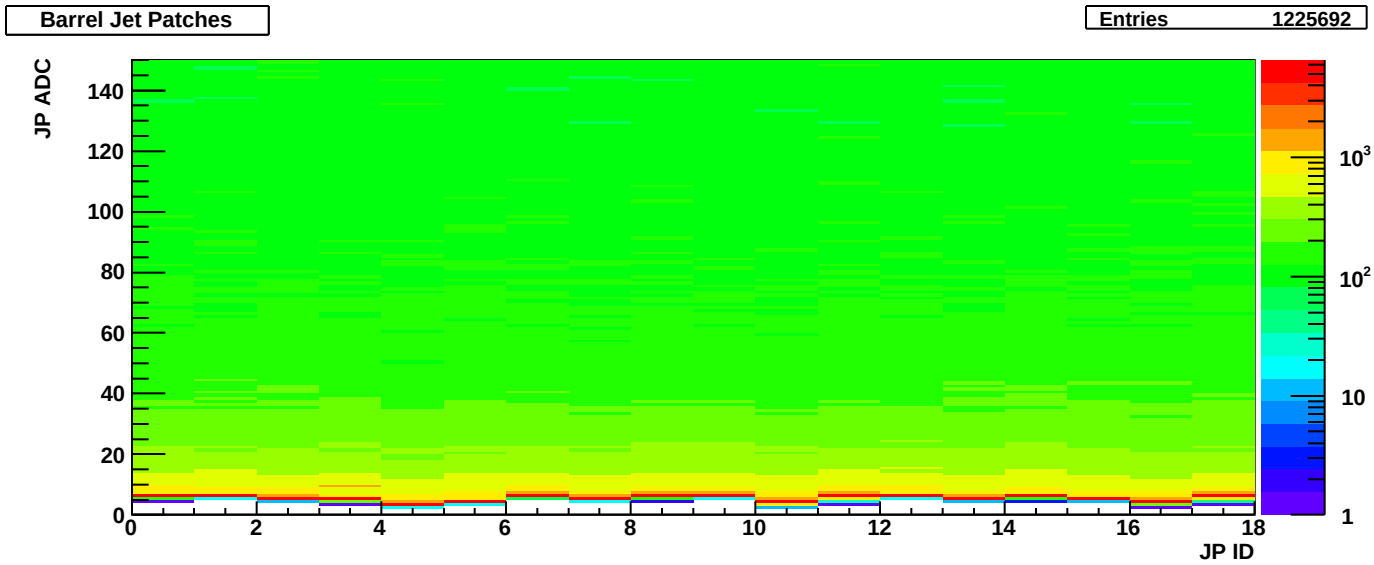

BunchId7Bit (BHT3)
Entries 1305

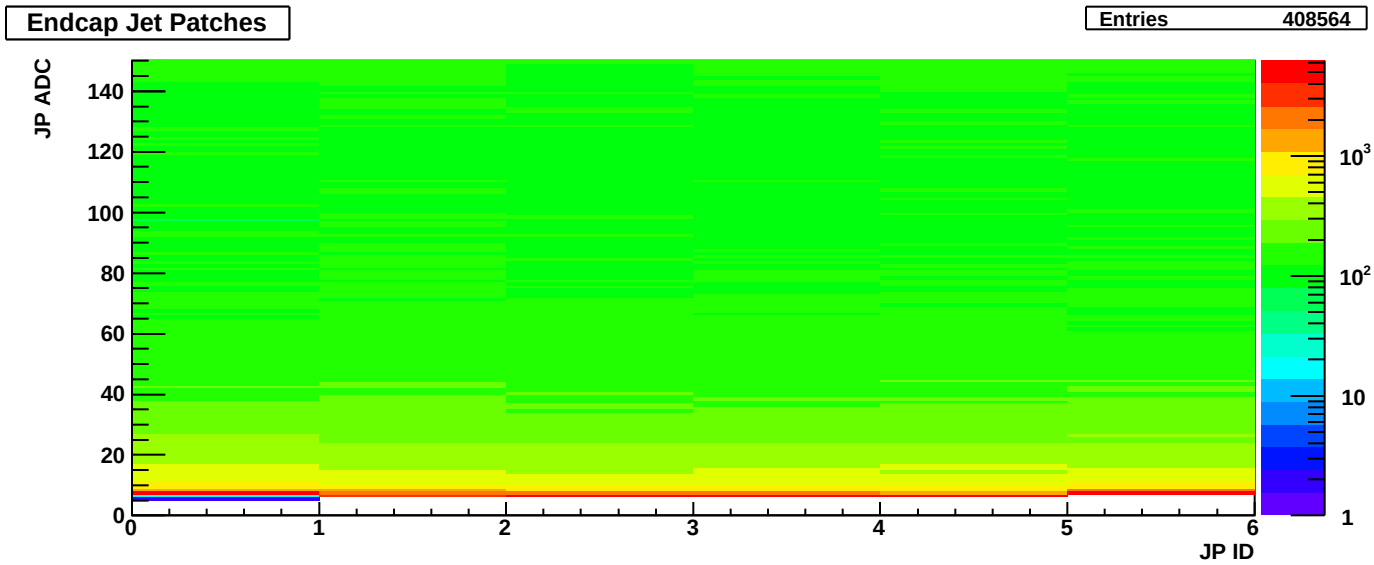

Barrel EMC L0 Input - High Tower
Entries 2.04282e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

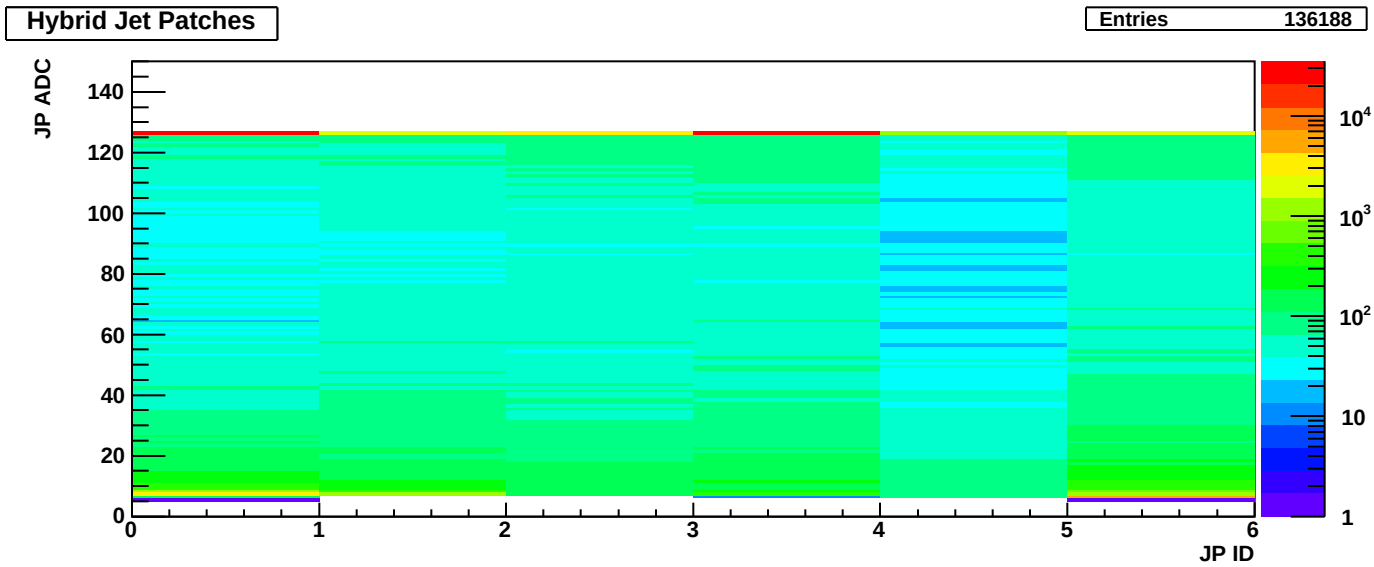

Barrel EMC L0 Input - Patch Sum
Entries 2.04282e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

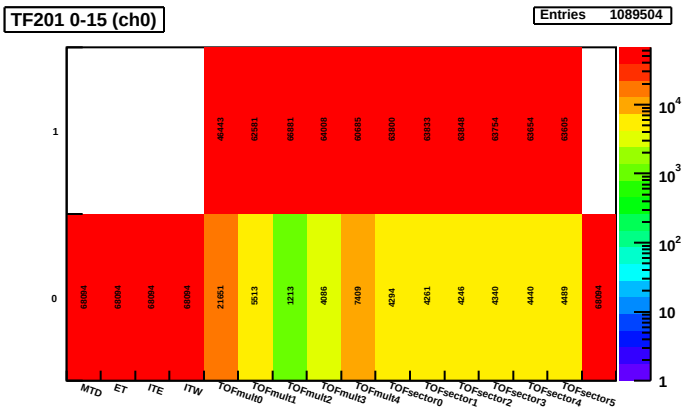

Endcap EMC L0 Input - High Tower
Entries 6128460
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

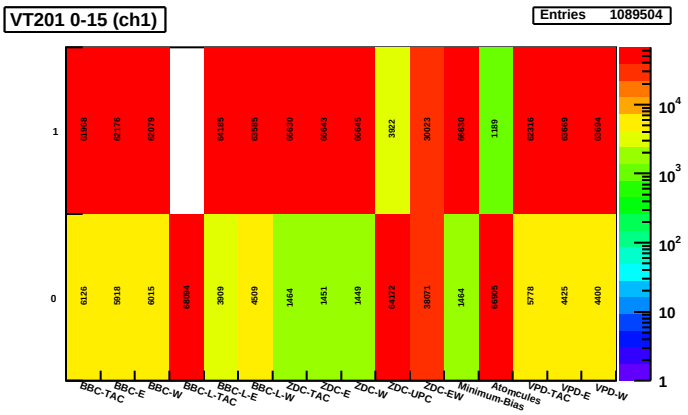

Endcap EMC L0 Input - Patch Sum
Entries 6128460
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

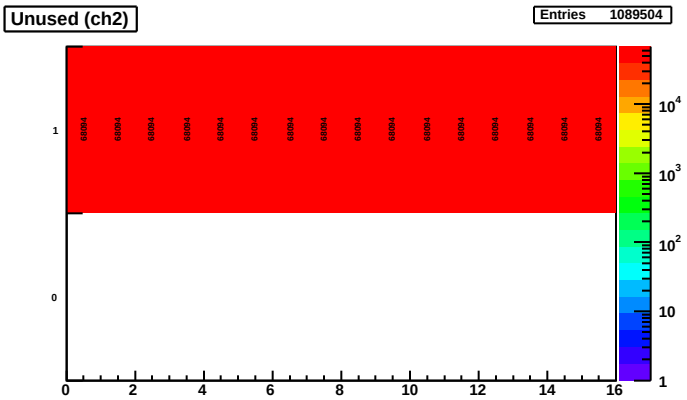

Barrel Jet Patches
Entries 1225692
JP ADC
JP ID

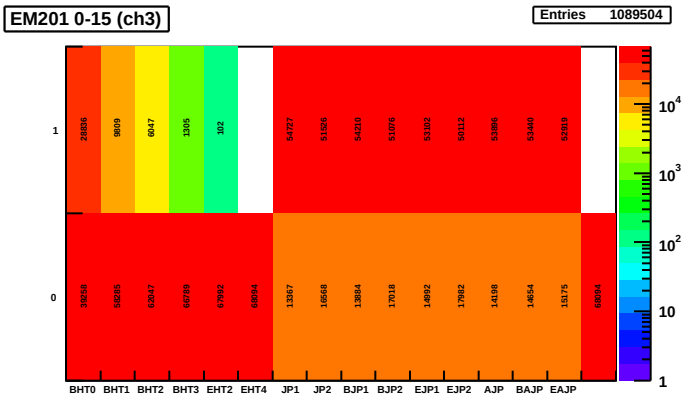

Endcap Jet Patches
Entries 408564
JP ADC
JP ID

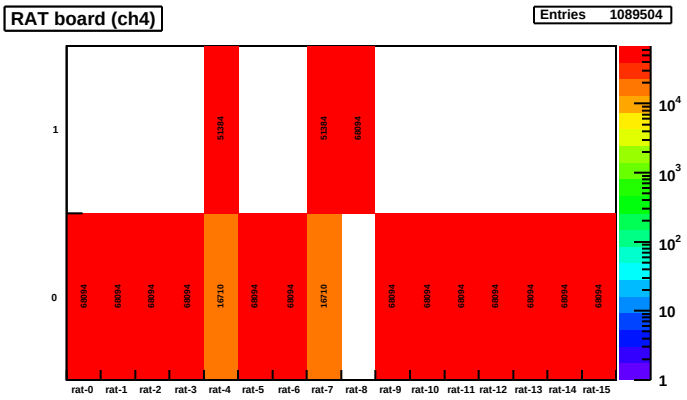

Hybrid Jet Patches
Entries 136188
JP ADC
JP ID

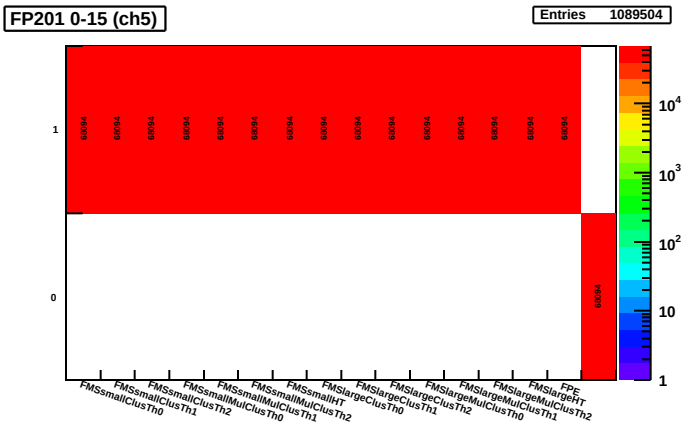

TF201 0-15 (ch0)
Entries 1089504
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

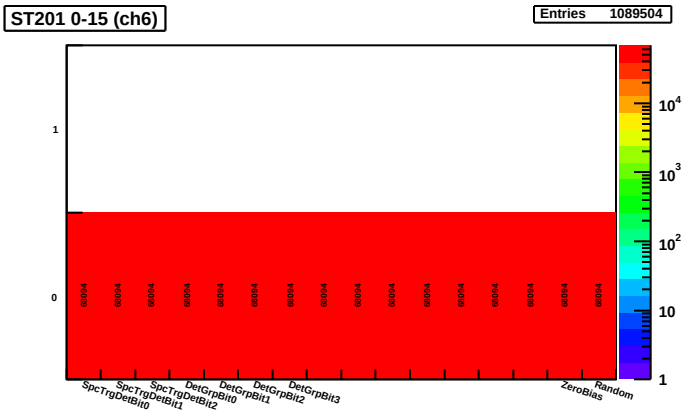

VT201 0-15 (ch1)
Entries 1089504
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

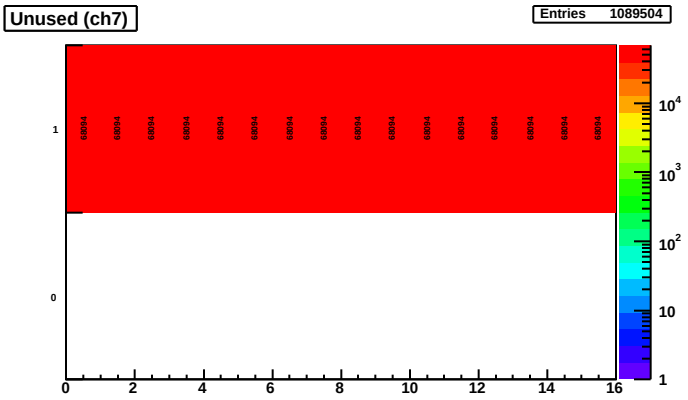

Unused (ch2)
Entries 1089504

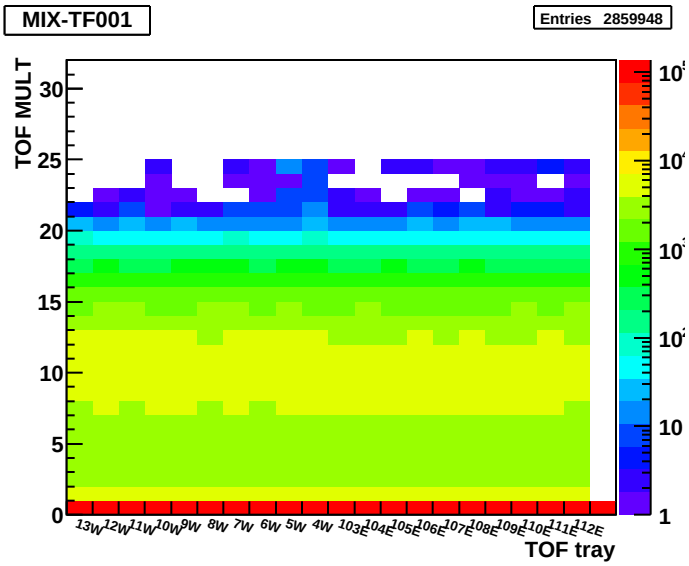

EM201 0-15 (ch3)
Entries 1089504
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

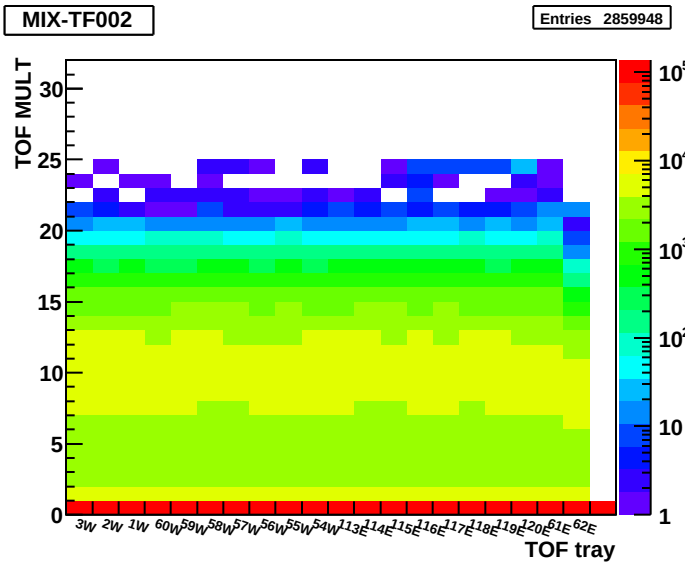

RAT board (ch4)
Entries 1089504
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

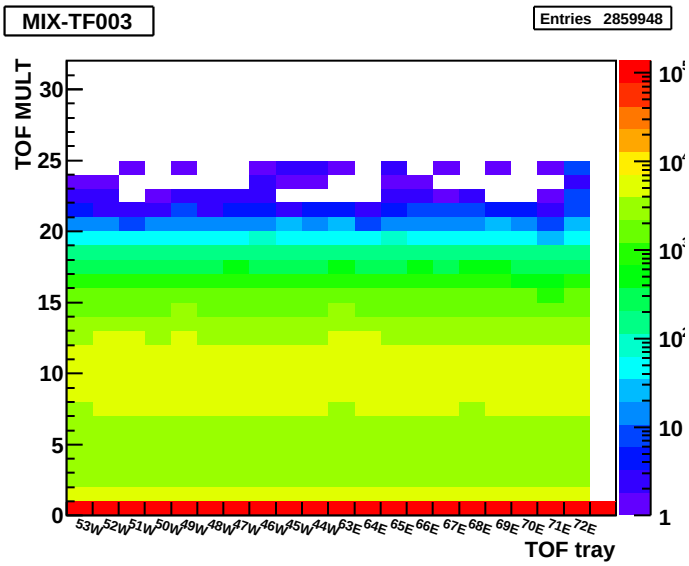

FP201 0-15 (ch5)
Entries 1089504
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

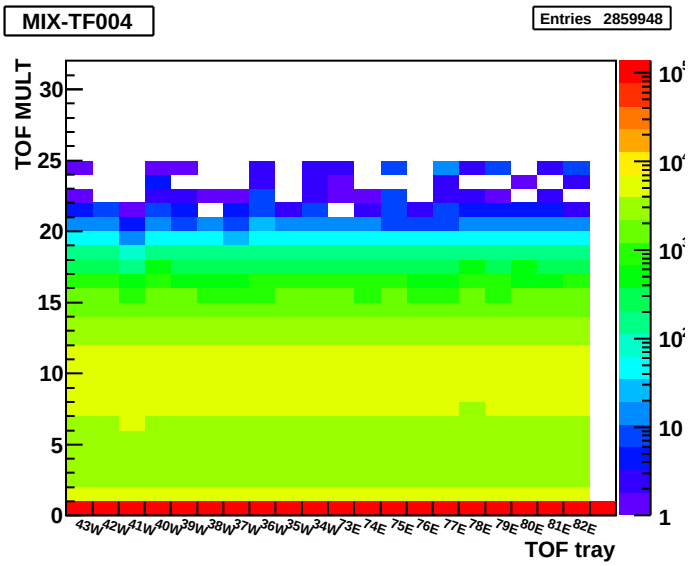

ST201 0-15 (ch6)
Entries 1089504
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

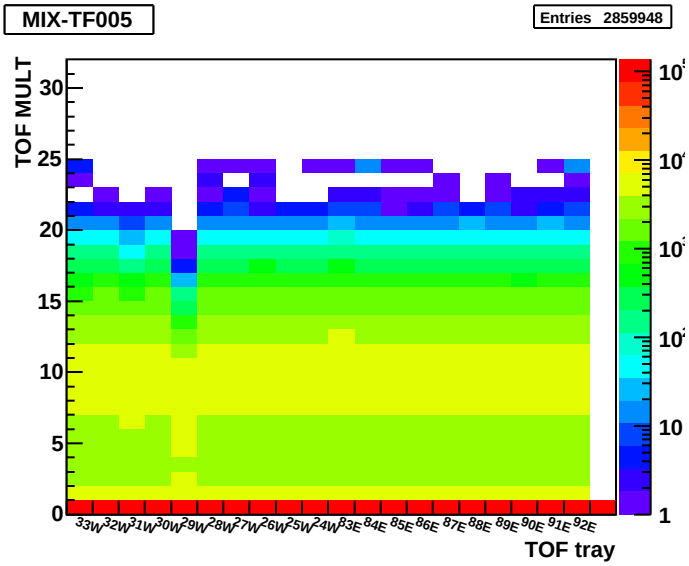

Unused (ch7)
Entries 1089504

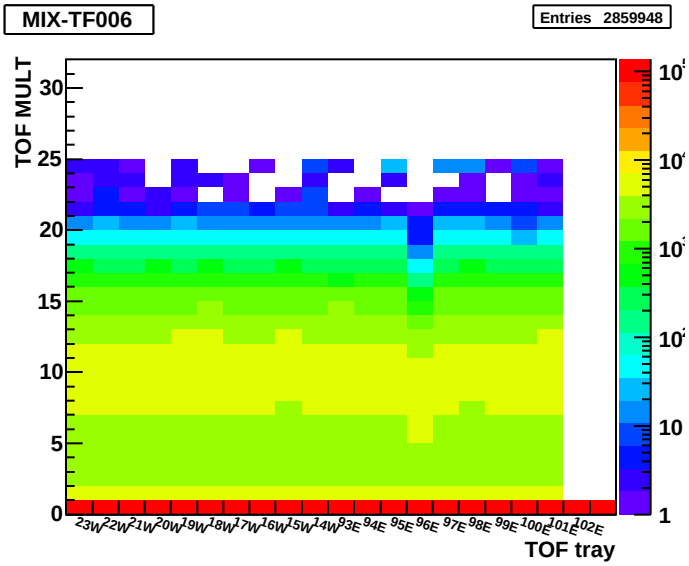

MIX-TF001
Entries 2859948
TOF MULT
TOF tray

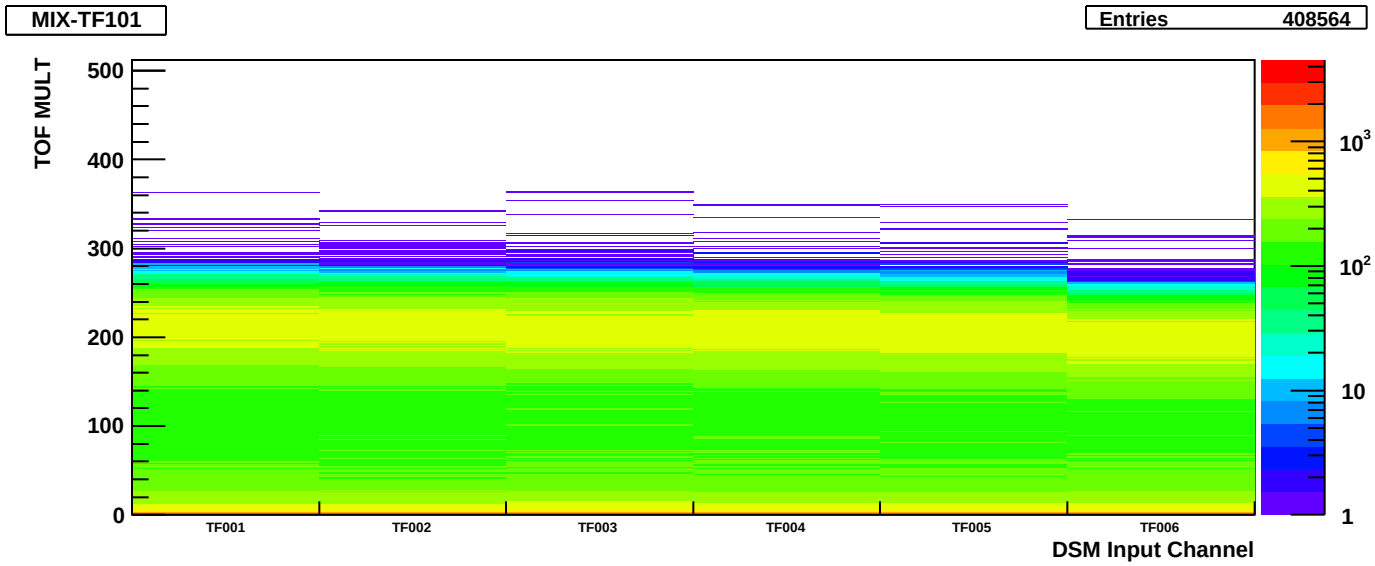

MIX-TF002
Entries 2859948
TOF MULT
TOF tray

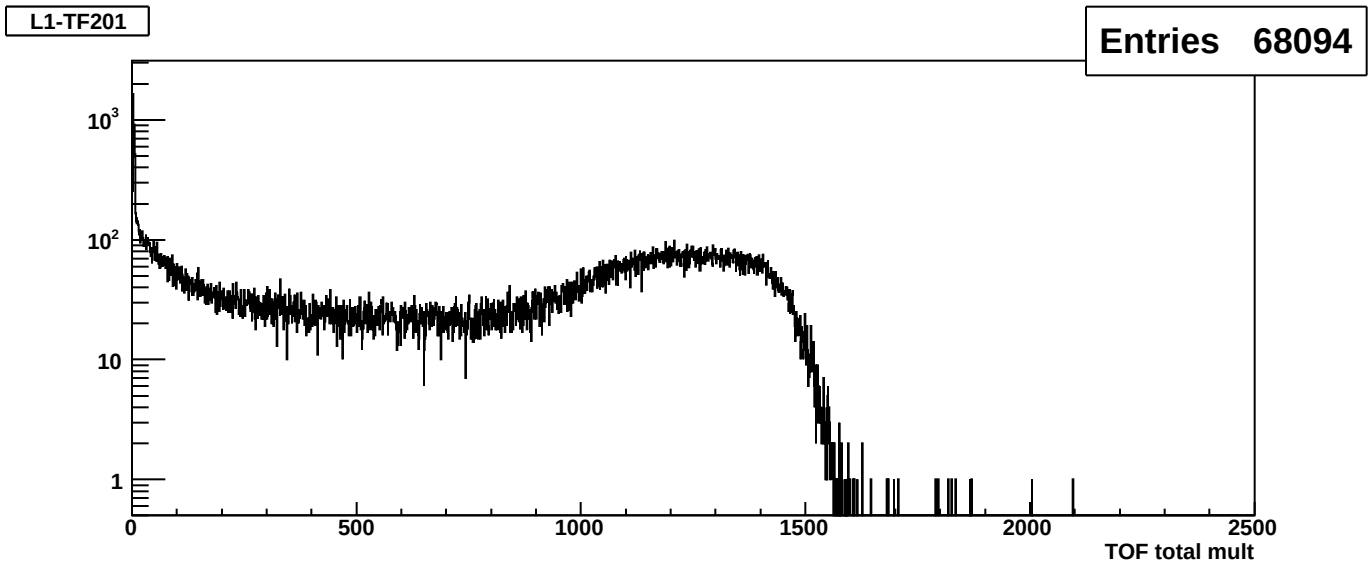

MIX-TF003
Entries 2859948
TOF MULT
TOF tray

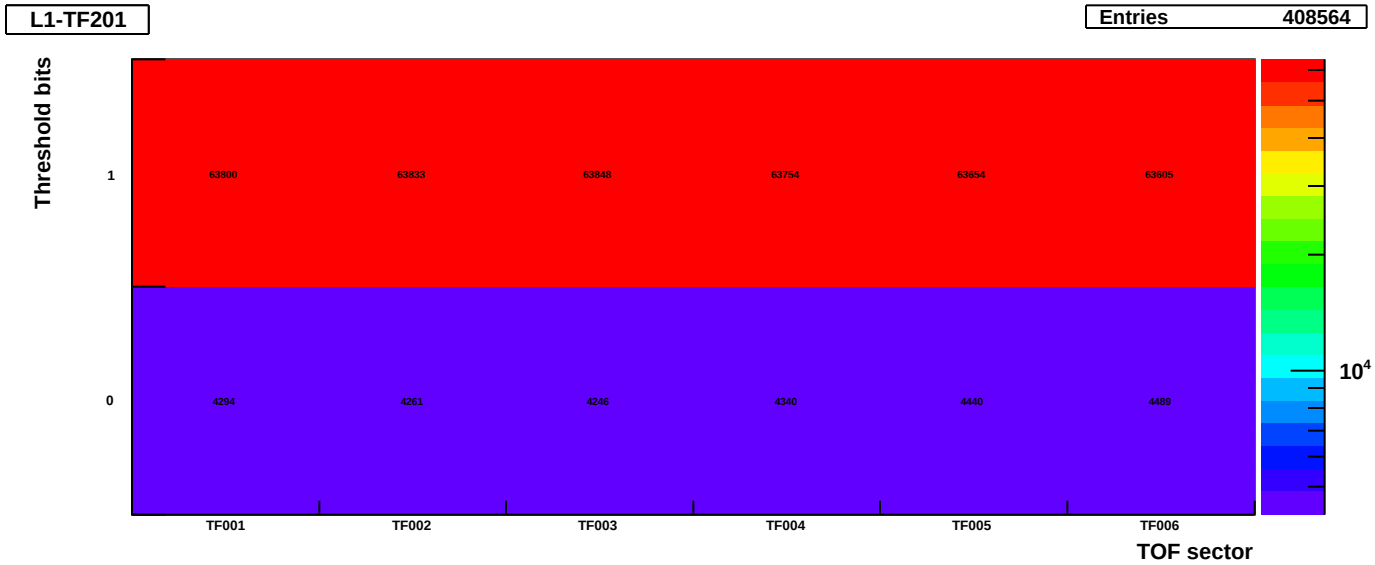

MIX-TF004
Entries 2859948
TOF MULT
TOF tray

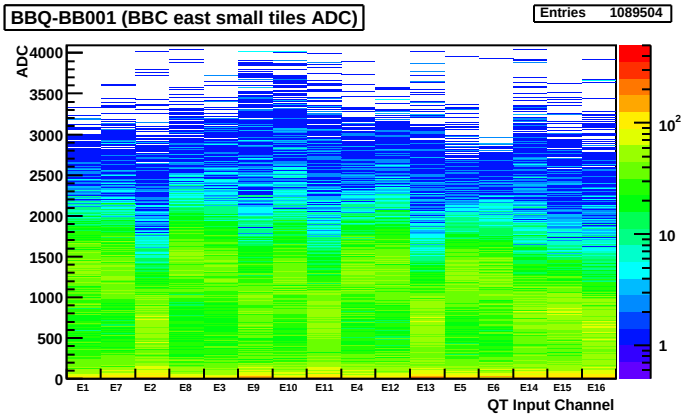

MIX-TF005
Entries 2859948
TOF MULT
TOF tray

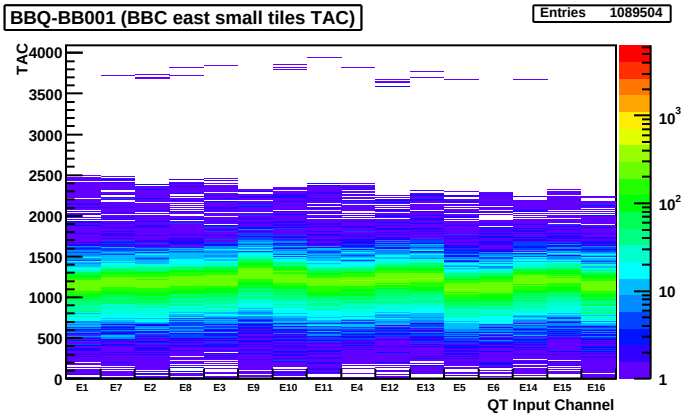

MIX-TF006
Entries 2859948
TOF MULT
TOF tray

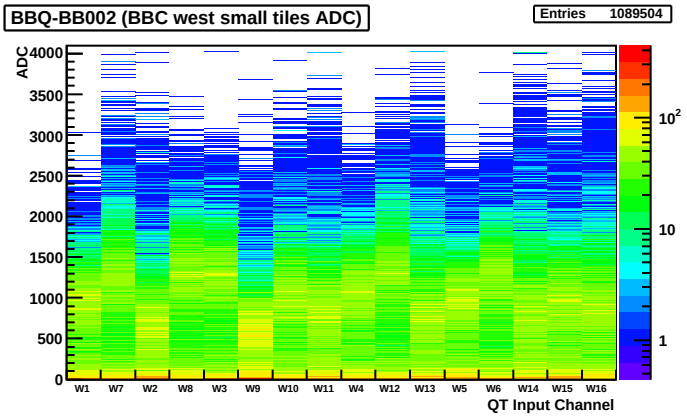

MIX-TF101
Entries 408564
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

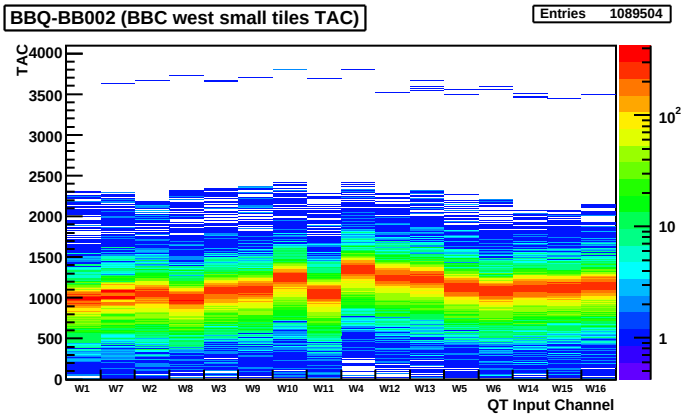

L1-TF201
Entries 68094
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

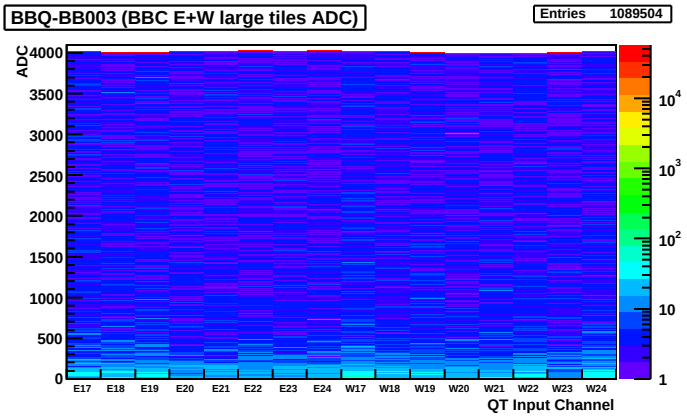

L1-TF201
Entries 408564
Threshold bits
1
0
63800
63833
63848
63754
63654
63605
4294
4261
4246
4340
4440
4489
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

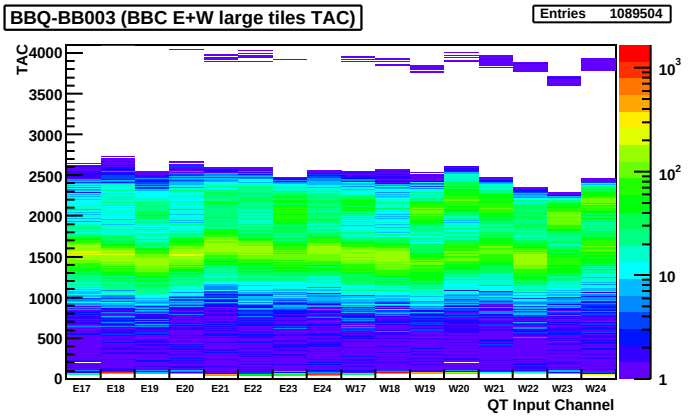

BBQ-BB001 (BBC east small tiles ADC)
Entries 1089504
ADC
QT Input Channel

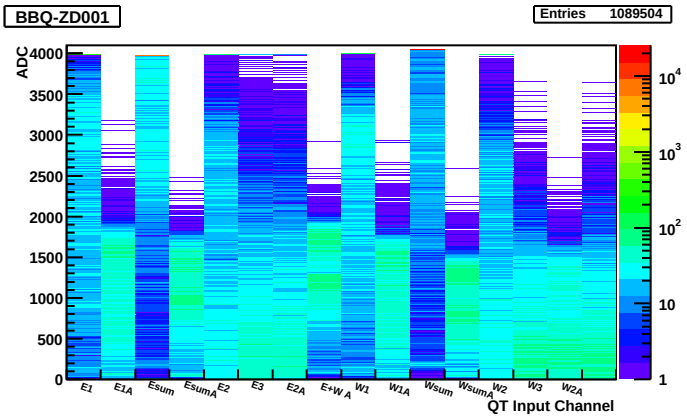

BBQ-BB001 (BBC east small tiles TAC)
Entries 1089504
TAC
QT Input Channel

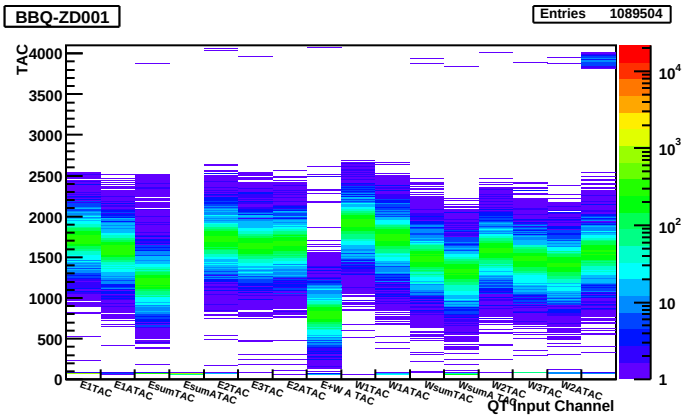

BBQ-BB002 (BBC west small tiles ADC)
Entries 1089504
ADC
QT Input Channel

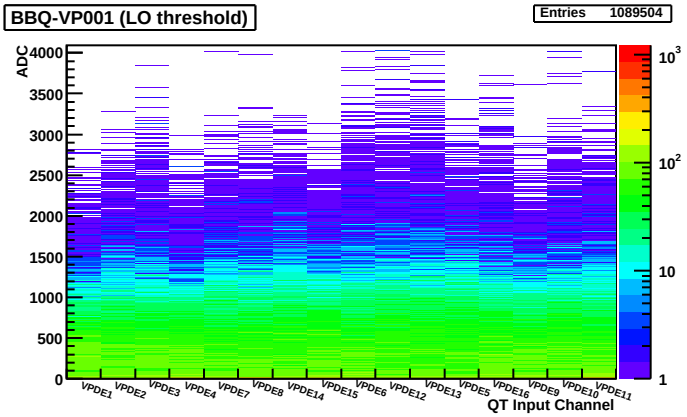

BBQ-BB002 (BBC west small tiles TAC)
Entries 1089504
TAC
QT Input Channel

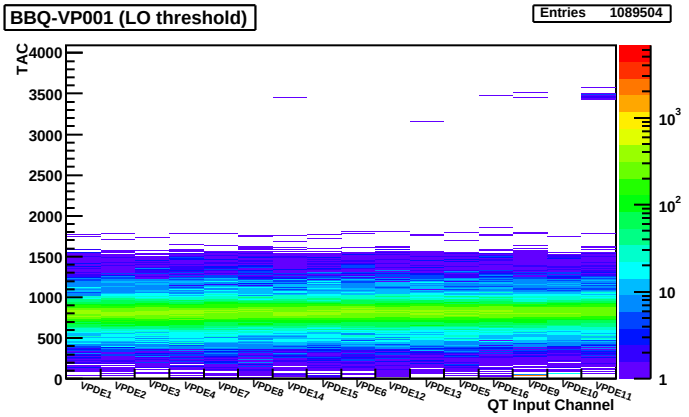

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1089504
ADC
QT Input Channel

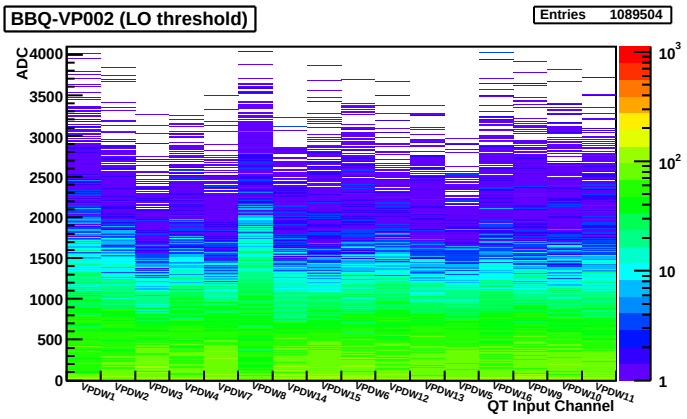

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1089504
TAC
QT Input Channel

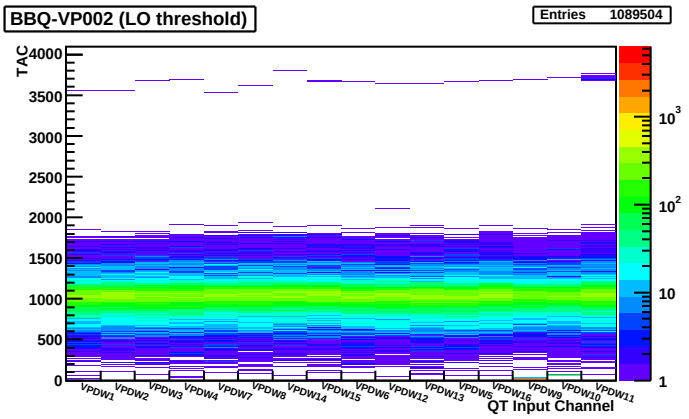

BBQ-ZD001
Entries 1089504
ADC
QT Input Channel

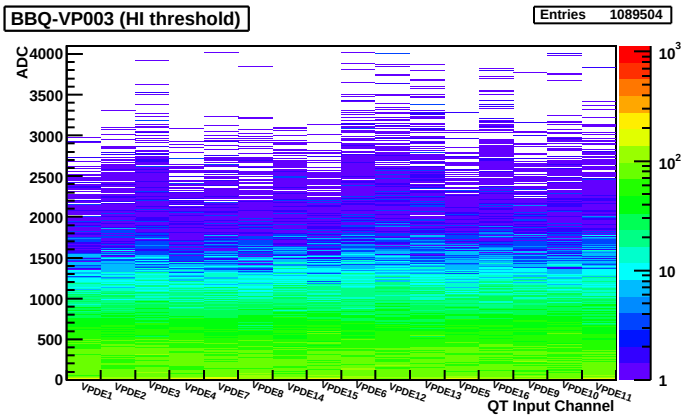

BBQ-ZD001
Entries 1089504
TAC
QT Input Channel

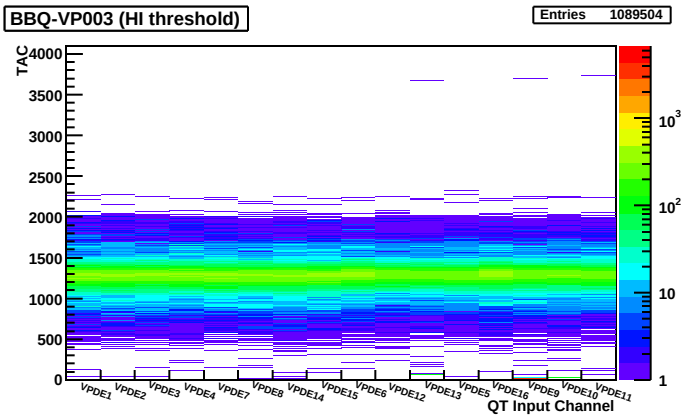

BBQ-VP001 (LO threshold)
Entries 1089504
ADC
QT Input Channel

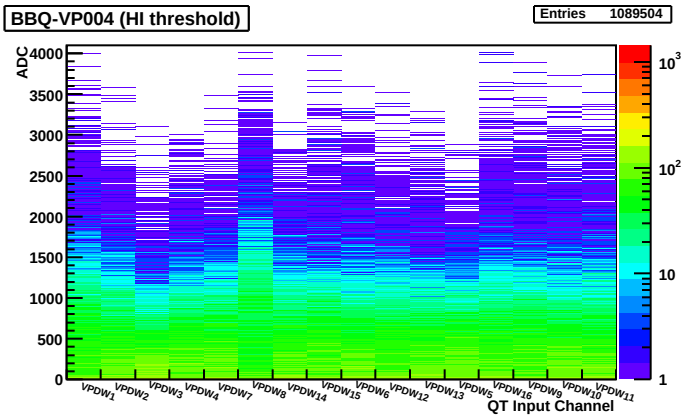

BBQ-VP001 (LO threshold)
Entries 1089504
TAC
QT Input Channel

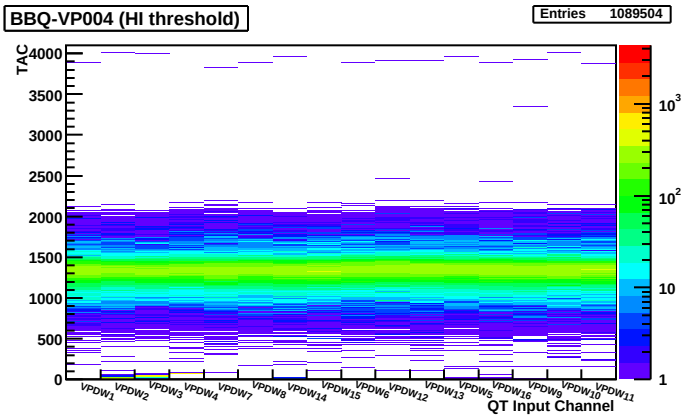

BBQ-VP002 (LO threshold)
Entries 1089504
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 1089504
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 1089504
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 1089504
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 1089504
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 1089504
TAC
QT Input Channel