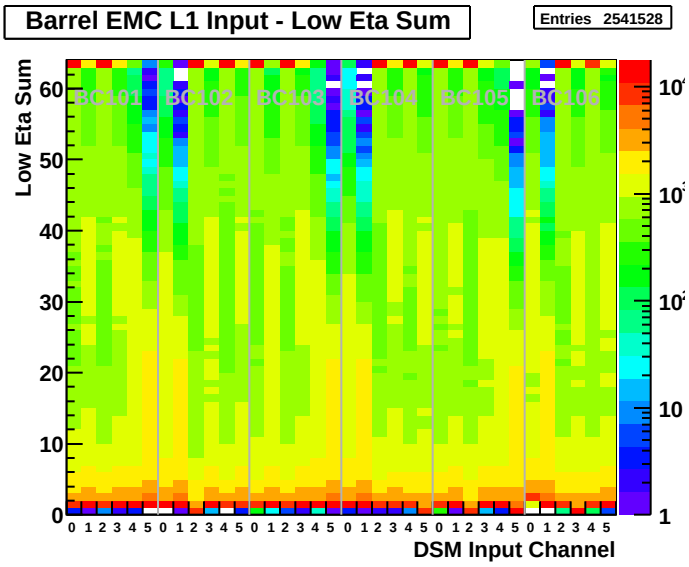
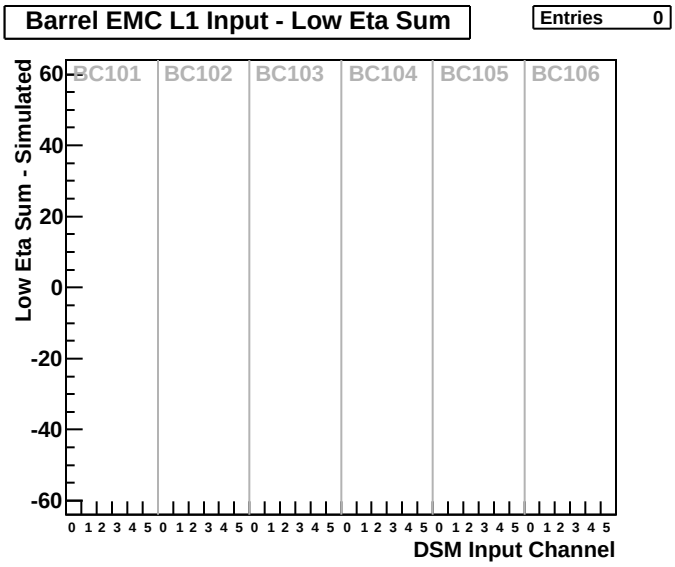
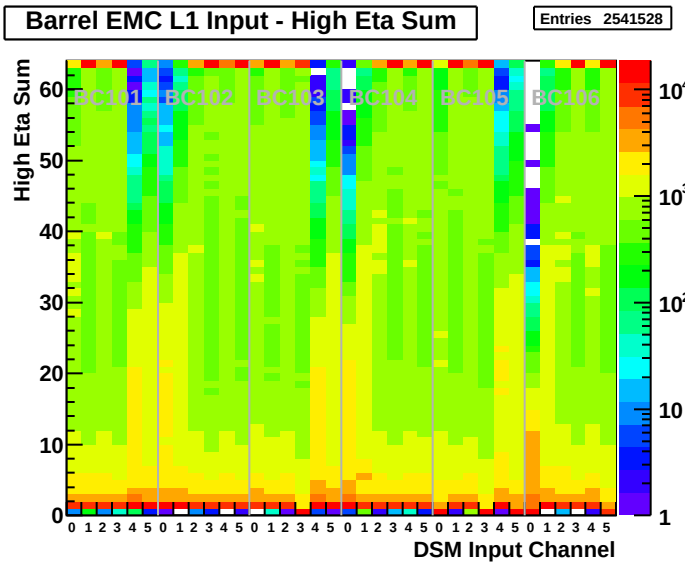
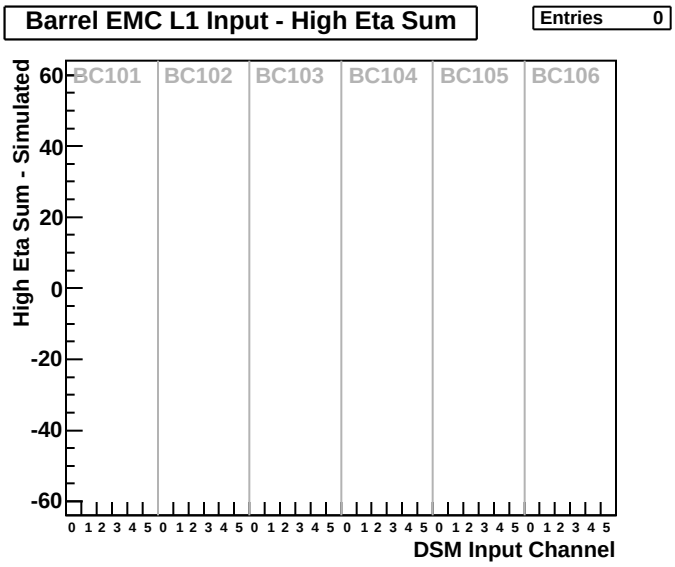
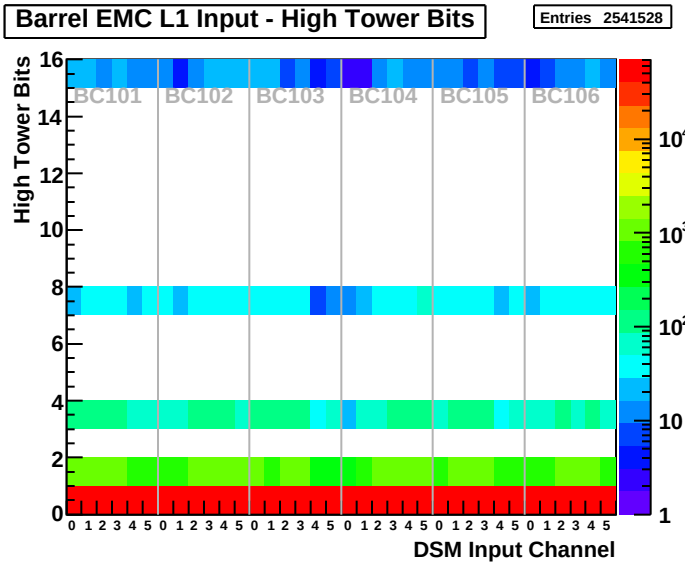
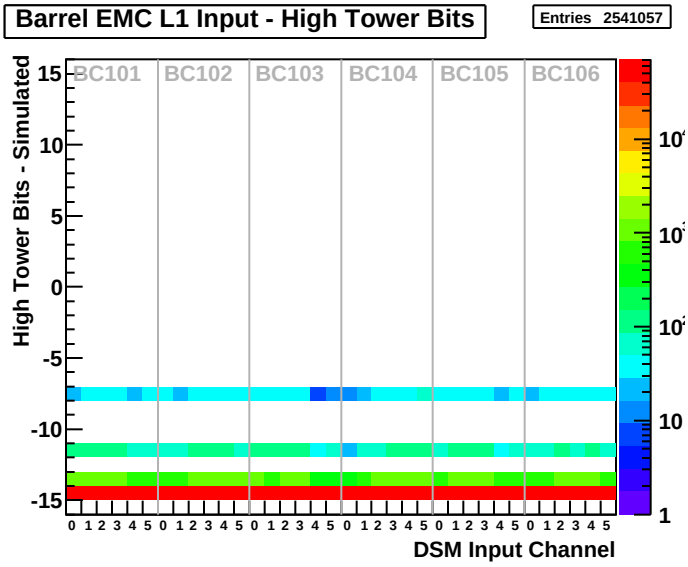

Barrel EMC L1 Input - Low Eta Sum
Entries 2541528
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

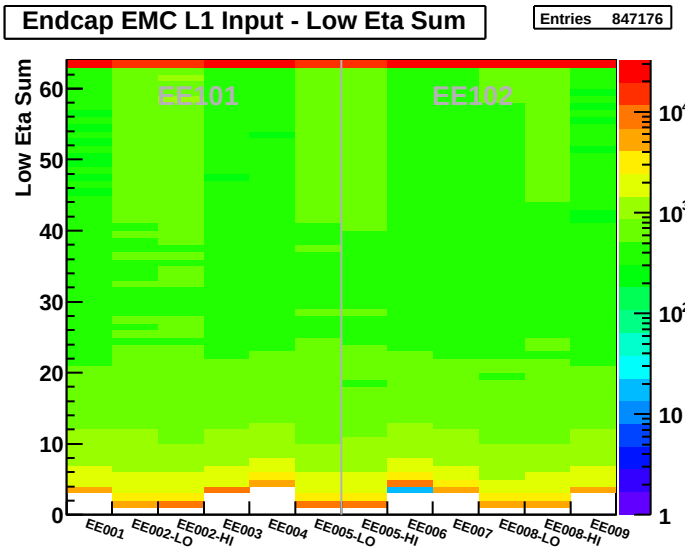

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

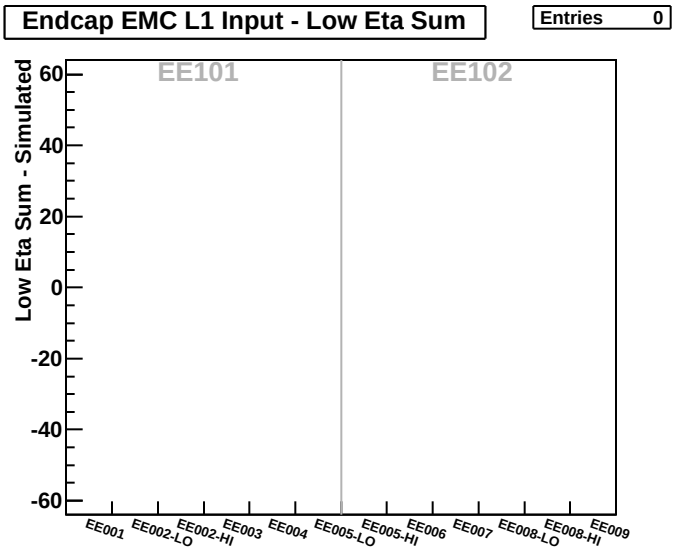

Barrel EMC L1 Input - High Eta Sum
Entries 2541528
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

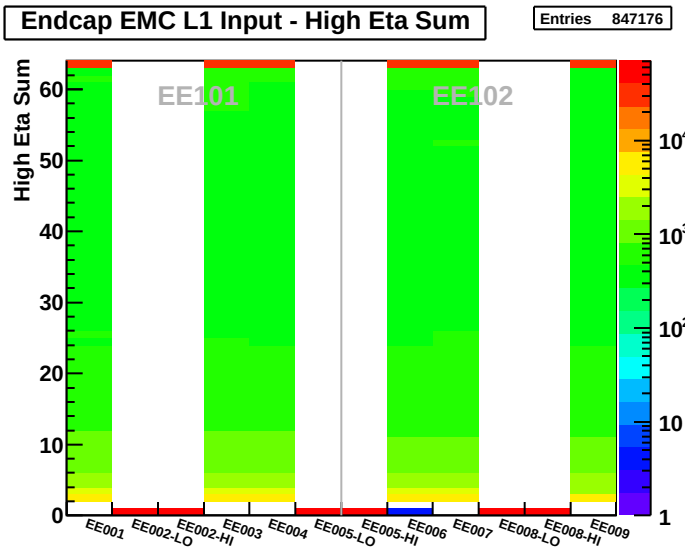

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

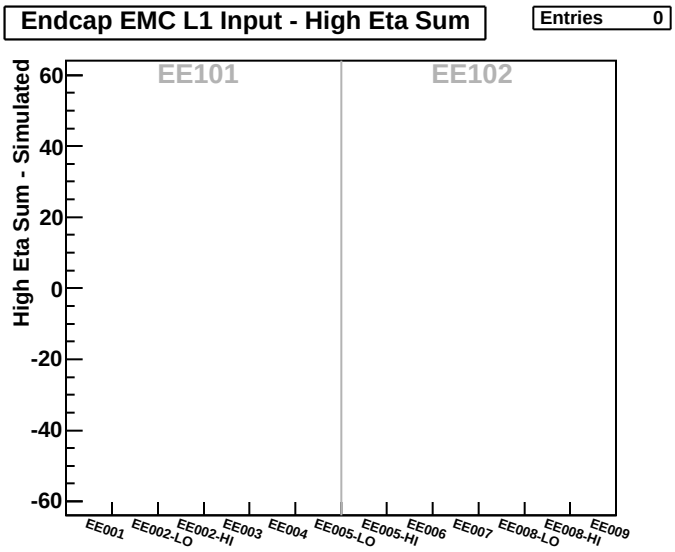

Barrel EMC L1 Input - High Tower Bits
Entries 2541528
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

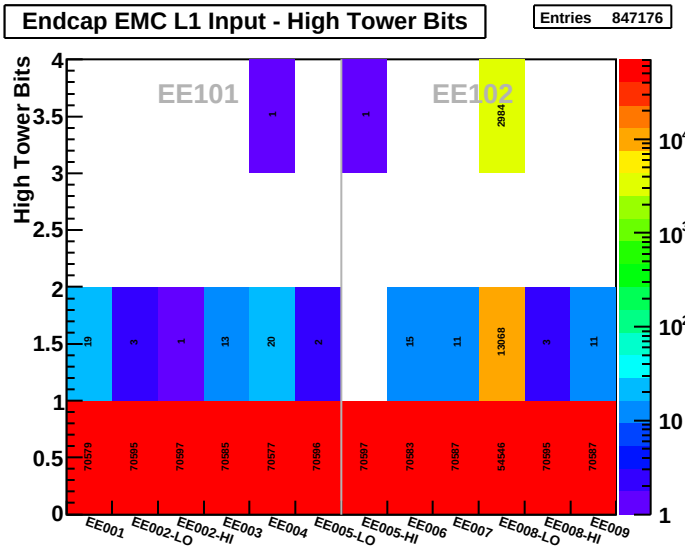

Barrel EMC L1 Input - High Tower Bits
Entries 2541057
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

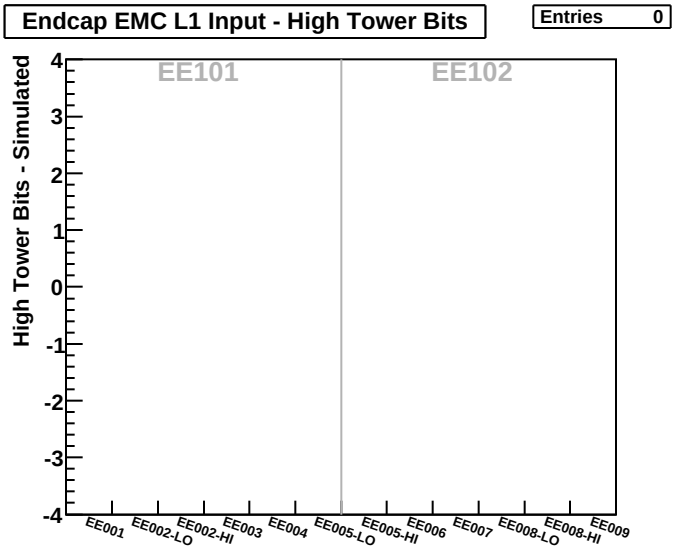

Endcap EMC L1 Input - Low Eta Sum
Entries 847176
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

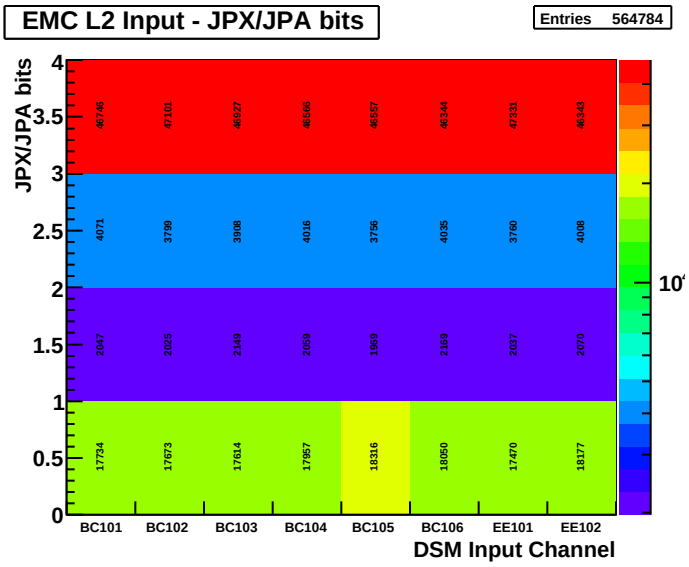

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

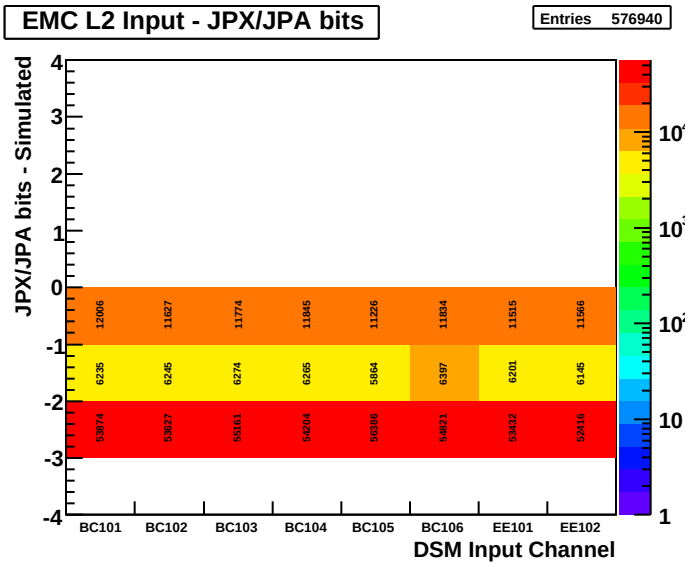

Endcap EMC L1 Input - High Eta Sum
Entries 847176
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

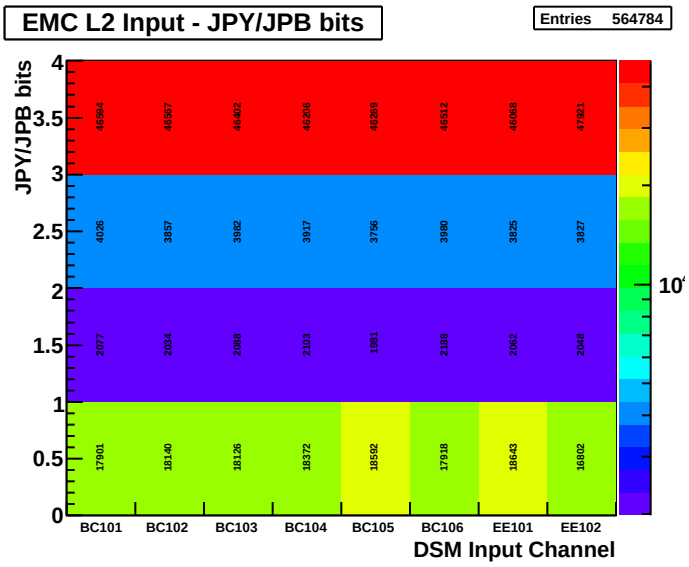

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

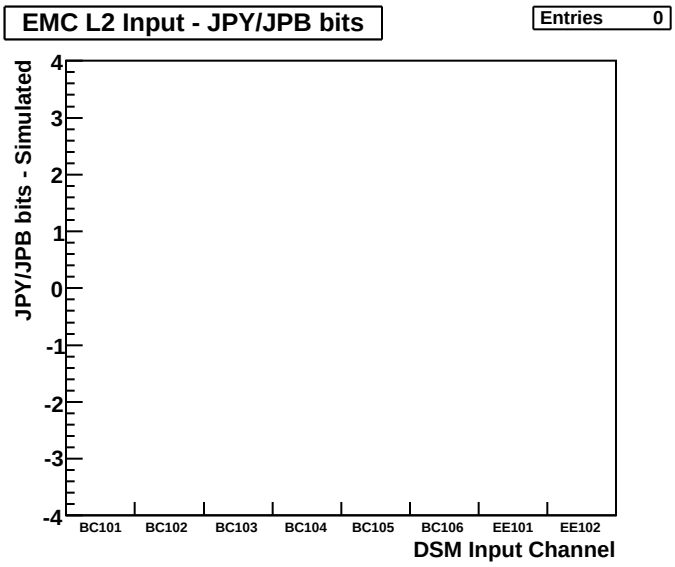

Endcap EMC L1 Input - High Tower Bits
Entries 847176
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

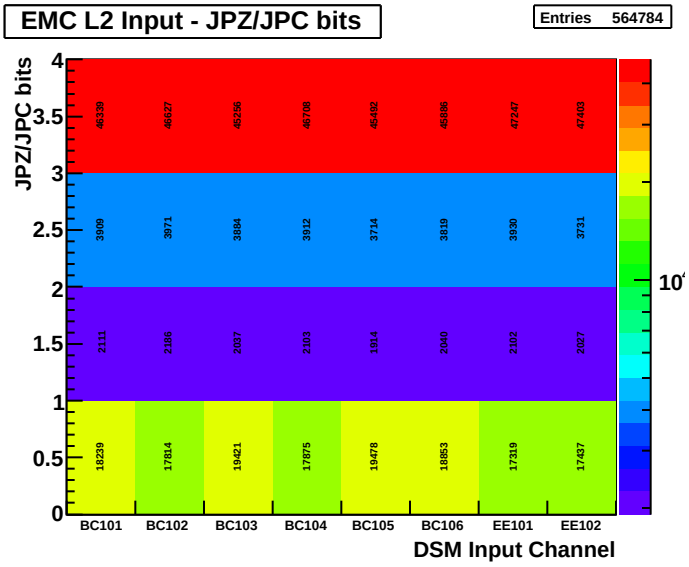

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

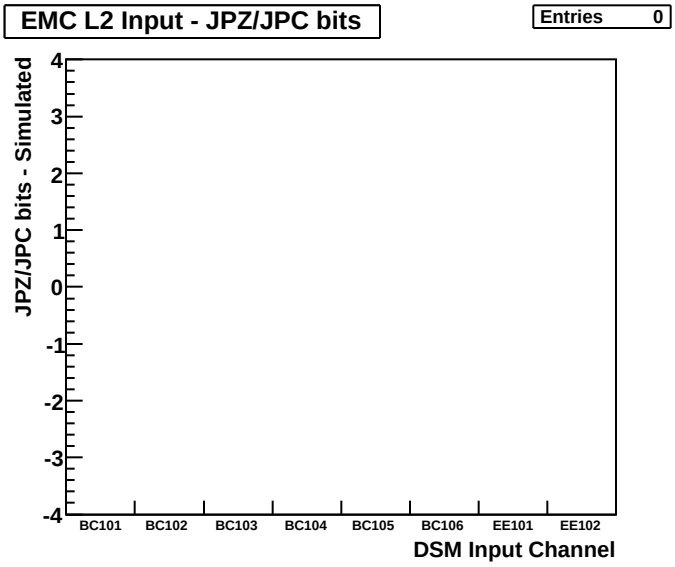

EMC L2 Input - JPX/JPA bits
Entries 564784
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
46746 47101 46927 46566 46557 46344 47331 46343
4071 3799 3908 4016 3756 4035 3760 4008
2047 2025 2149 2059 1969 2169 2037 2070
17734 17673 17614 17957 18316 18050 17470 18177

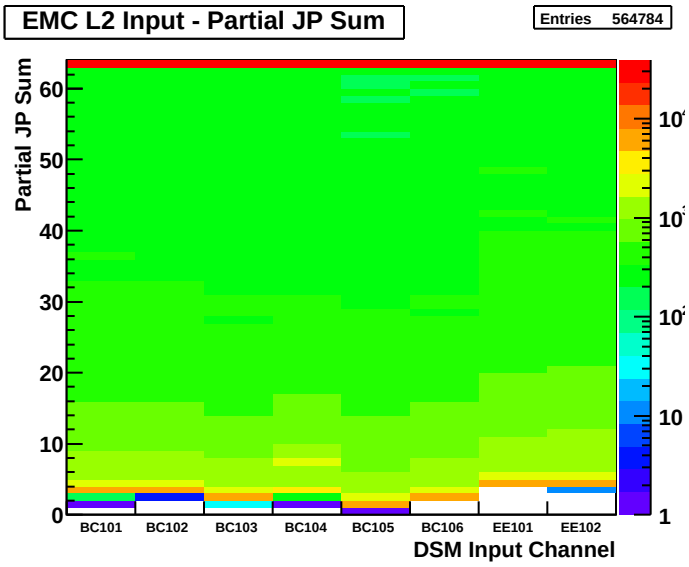

EMC L2 Input - JPX/JPA bits
Entries 576940
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
12006 11627 11774 11845 11226 11834 11515 11566
6235 6245 6274 6265 5864 6397 6201 6145
53874 53627 55161 54204 56386 54821 53432 52416

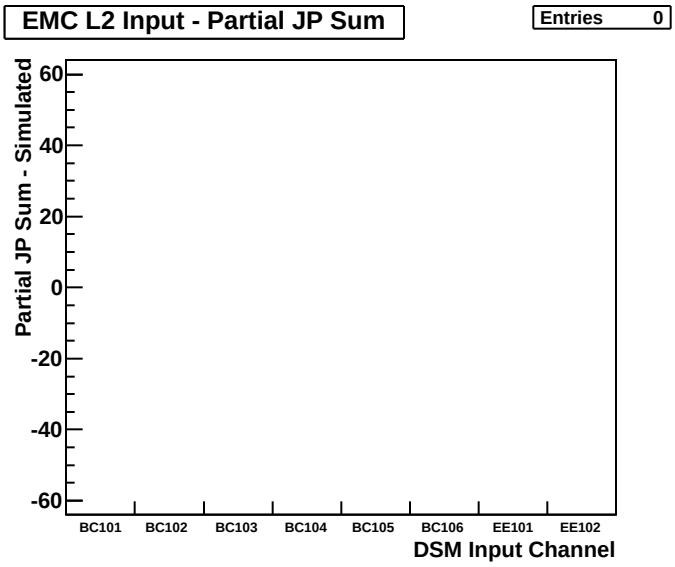

EMC L2 Input - JPY/JPB bits
Entries 564784
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
45594 45567 45402 45206 45289 45512 45068 47921
4026 3857 3982 3917 3756 3980 3825 3827
2077 2034 2086 2103 1981 2198 2052 2048
17901 18140 18126 18372 18592 17918 18643 16802

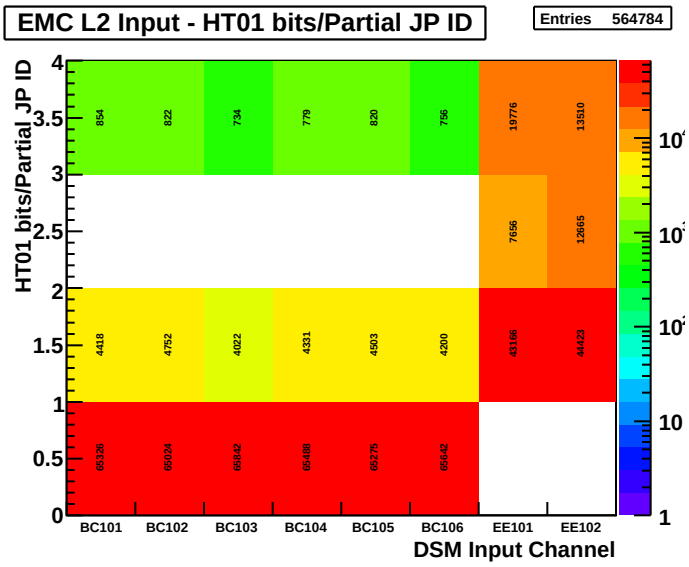

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

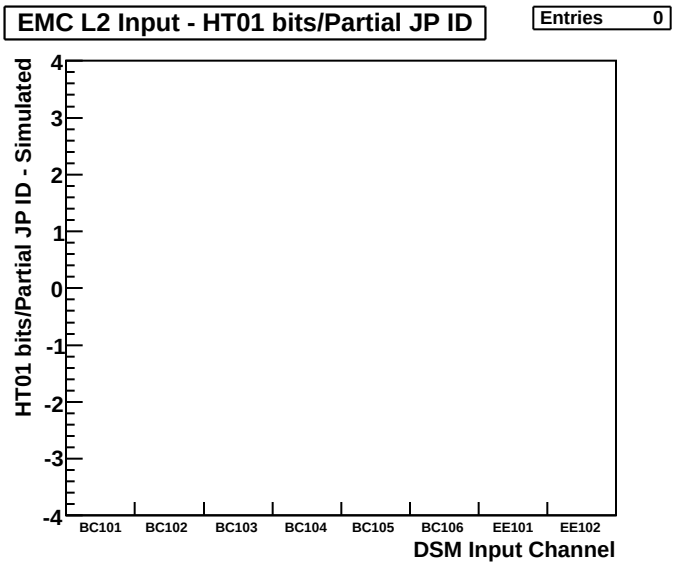

EMC L2 Input - JPZ/JPC bits
Entries 564784
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
46339 46627 45256 46708 45492 45886 47247 47403
3909 3971 3884 3912 3714 3819 3930 3731
2111 2186 2037 2103 1914 2040 2102 2027
18239 17814 19421 17675 19478 18853 17319 17437

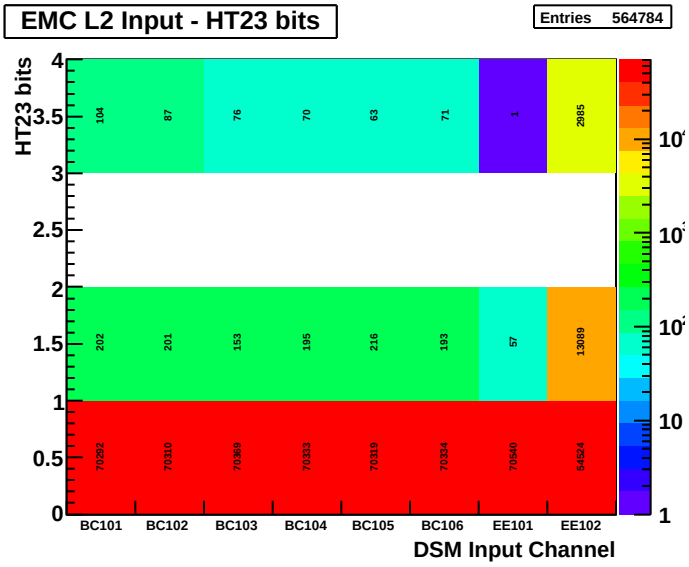

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

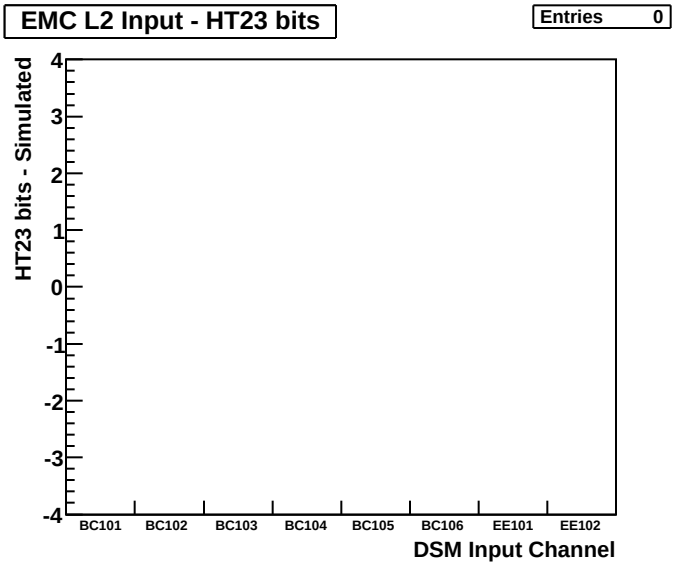

EMC L2 Input - Partial JP Sum
Entries 564784
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

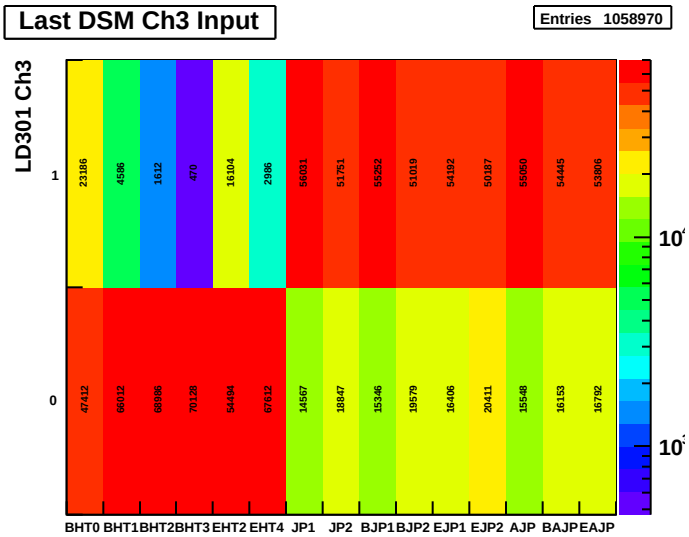

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

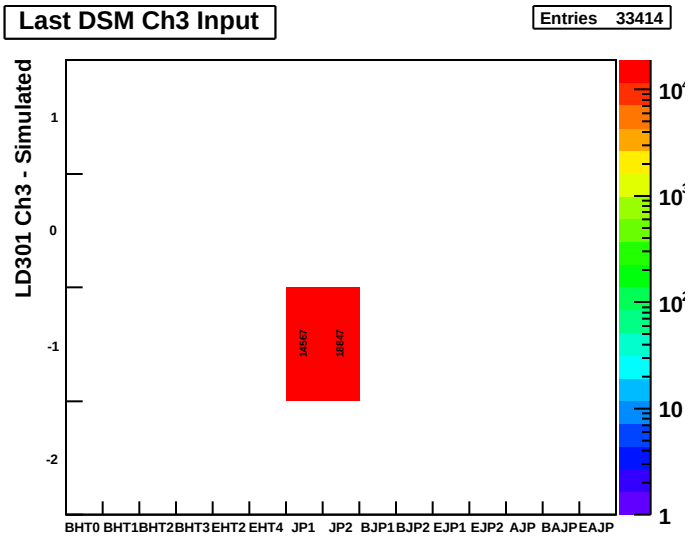

EMC L2 Input - HT01 bits/Partial JP ID
Entries 564784
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

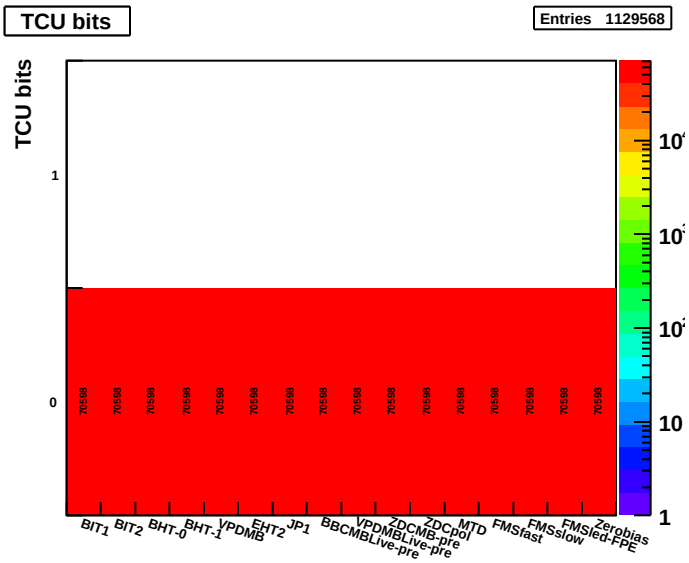

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

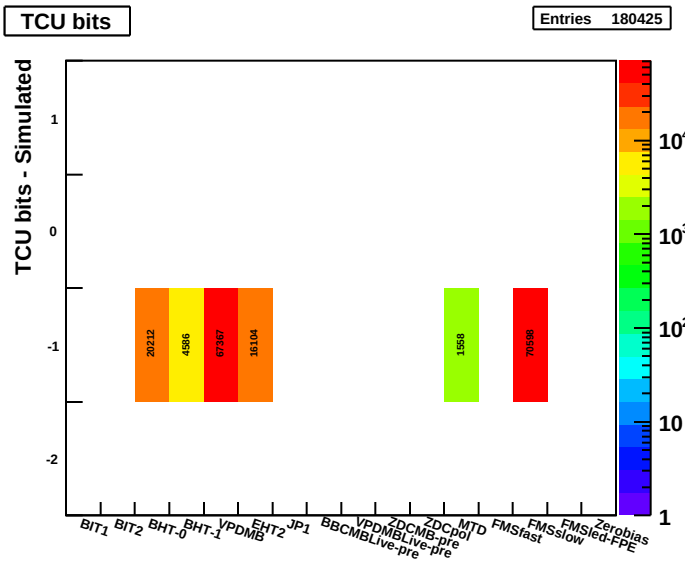

EMC L2 Input - HT23 bits
Entries 564784
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

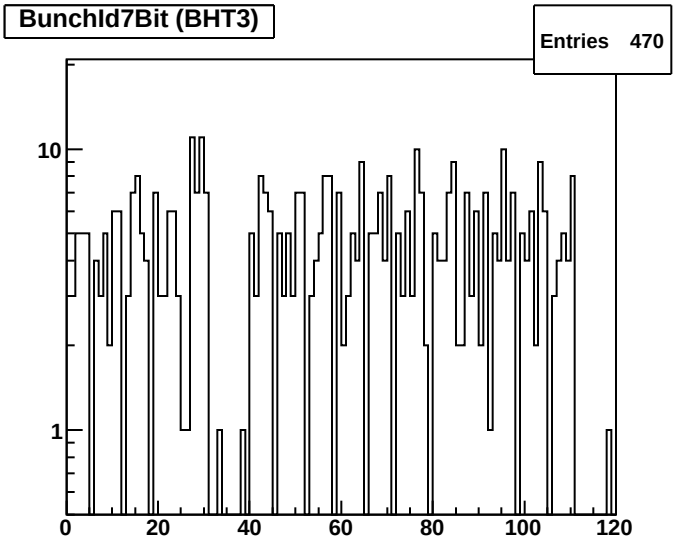

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

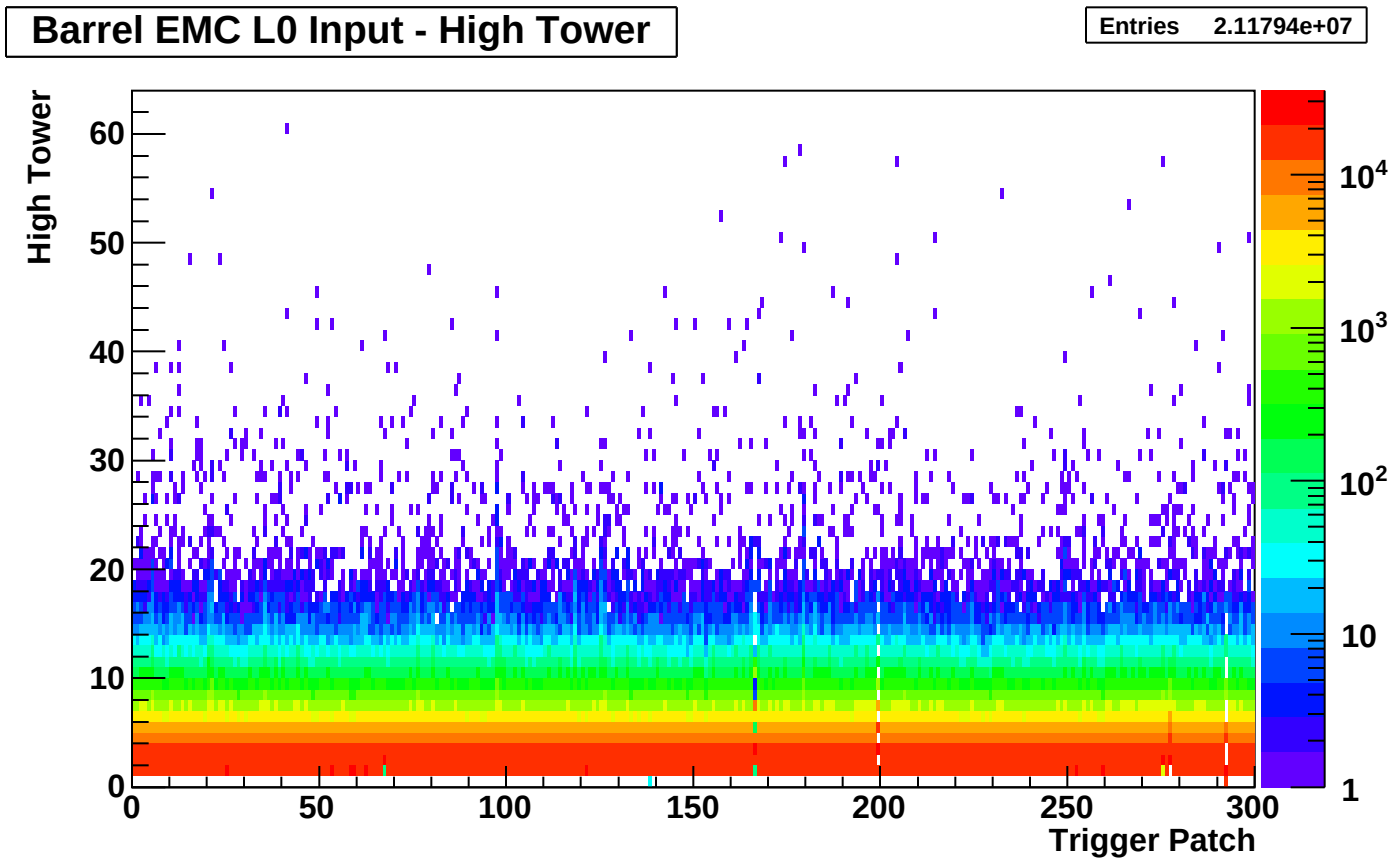

Last DSM Ch3 Input
Entries 1058970
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

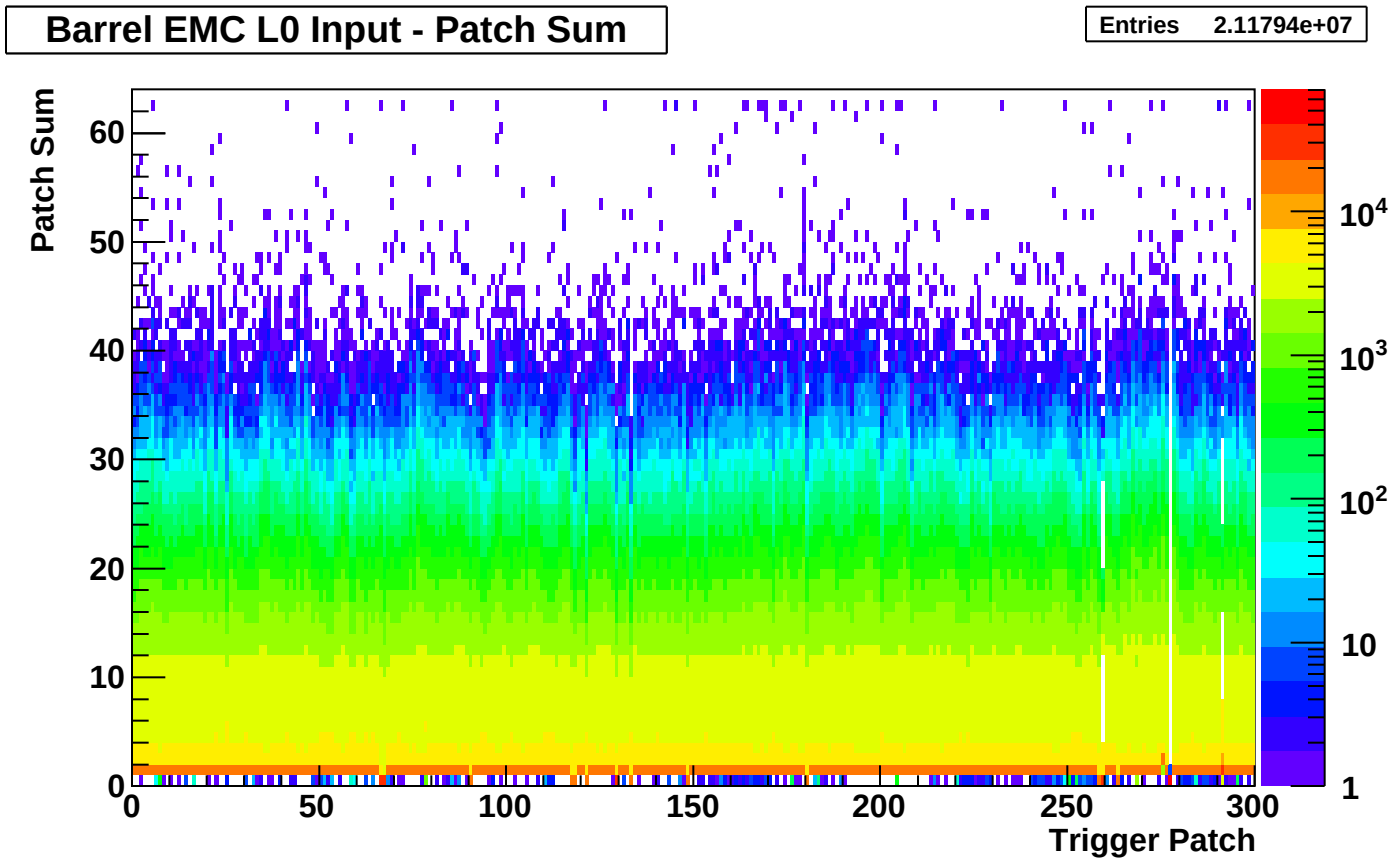

Last DSM Ch3 Input
Entries 33414
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

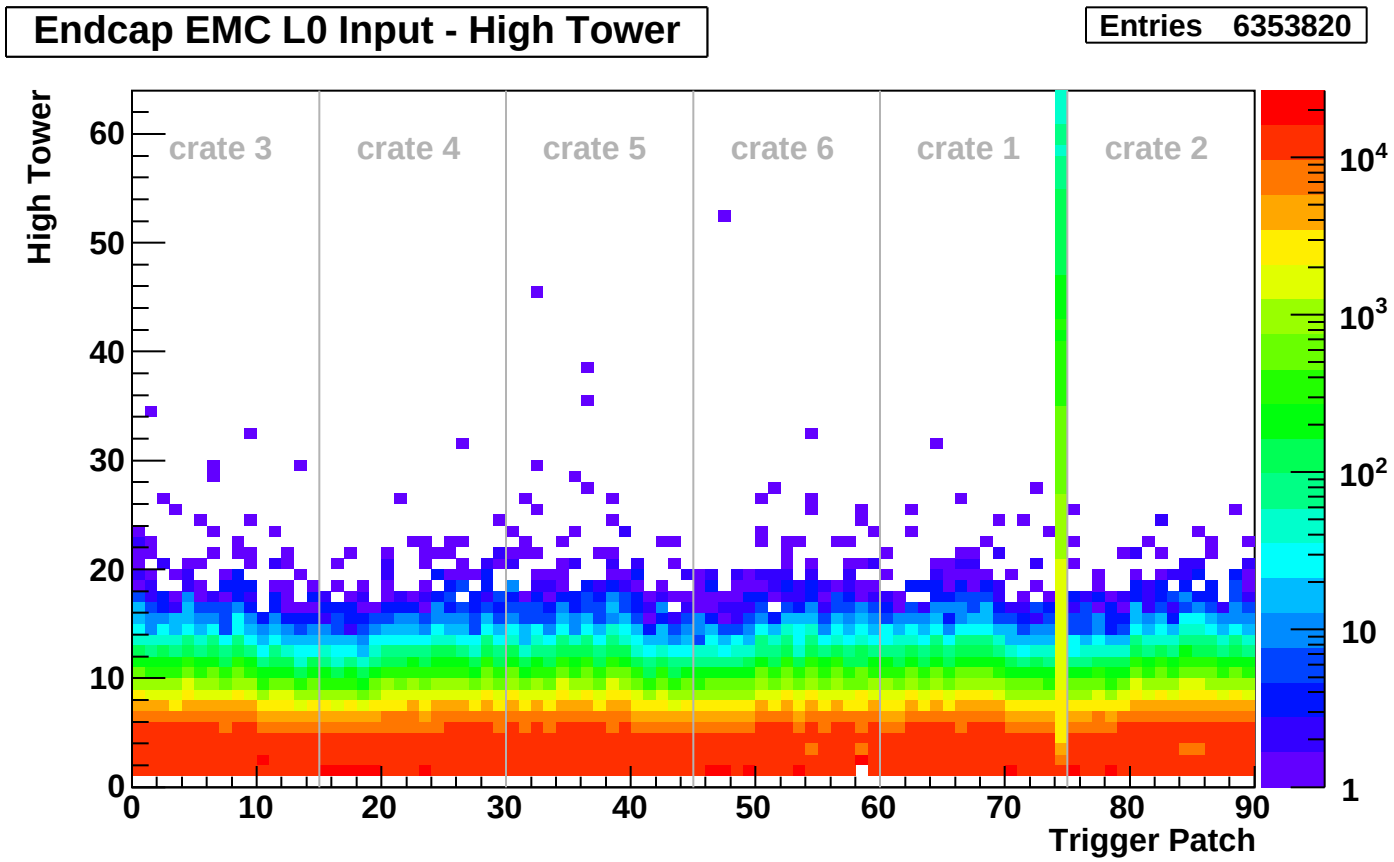

TCU bits
Entries 1129568
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

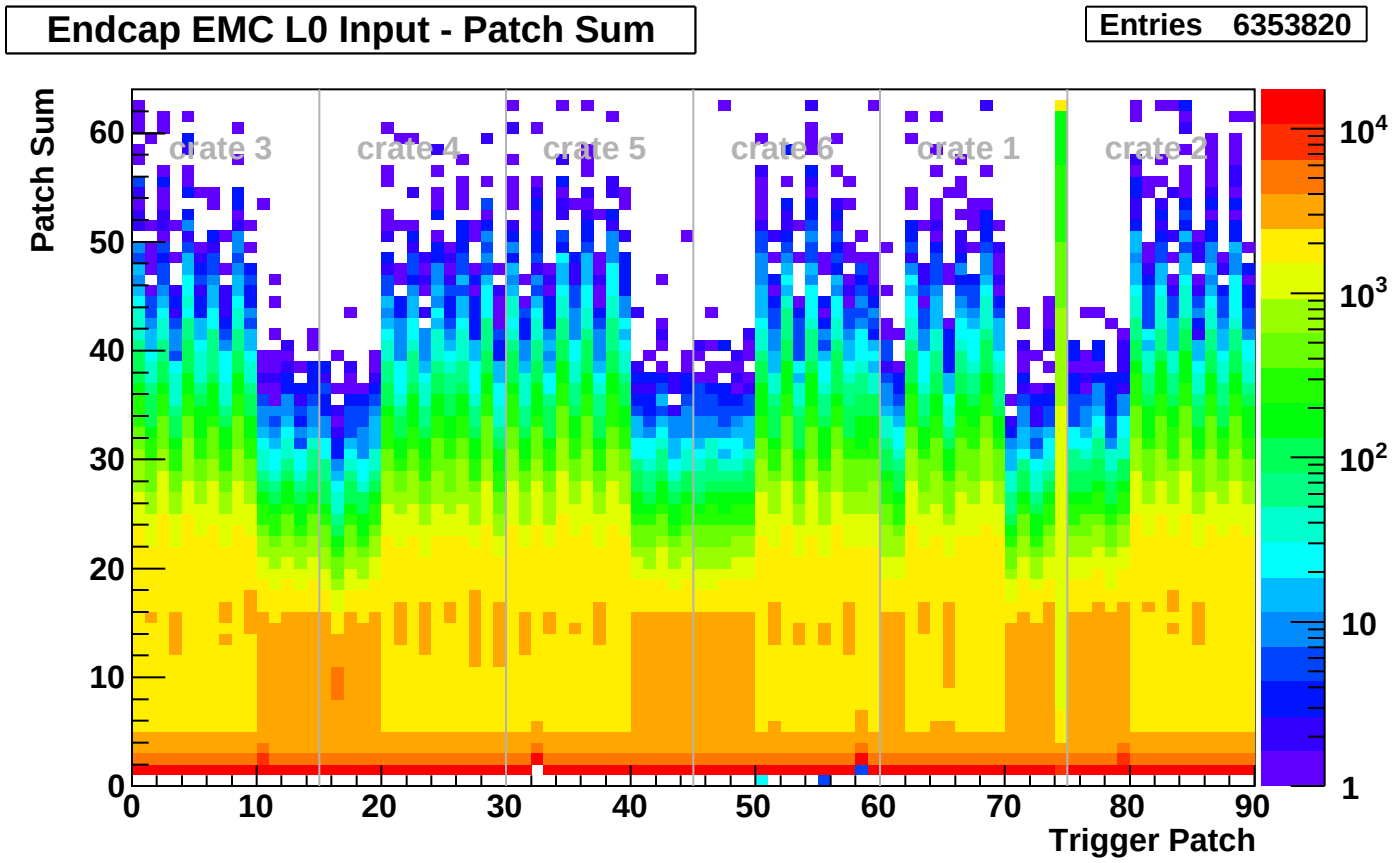

TCU bits
Entries 180425
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

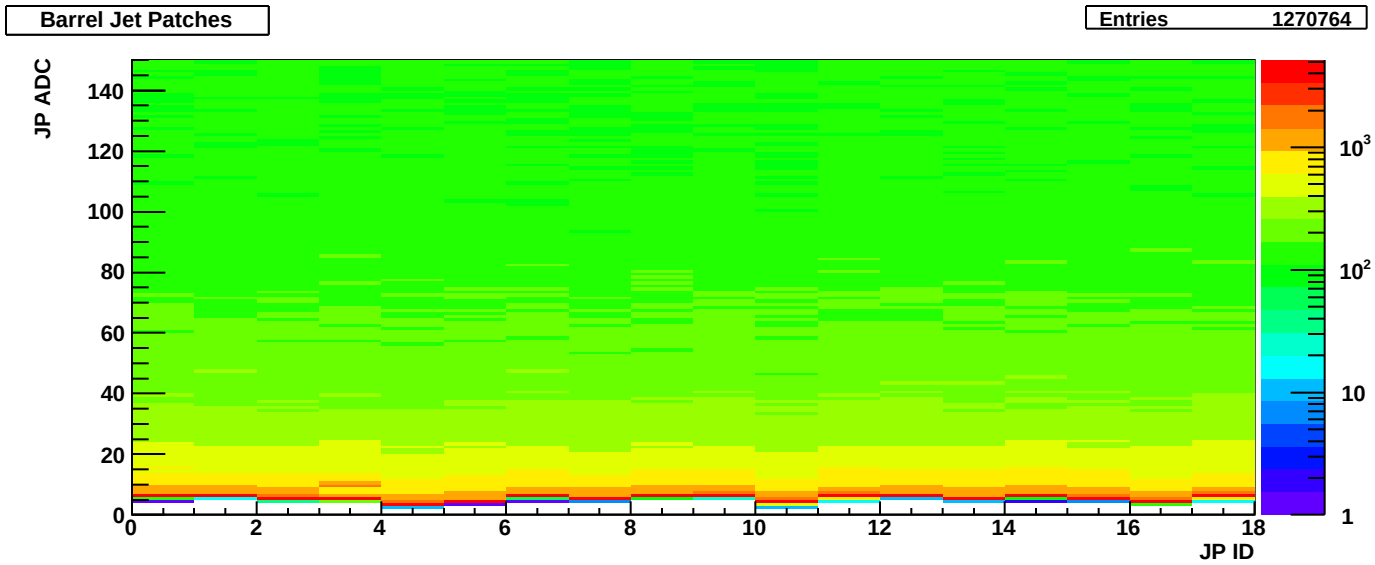

BunchId7Bit (BHT3)
Entries 470

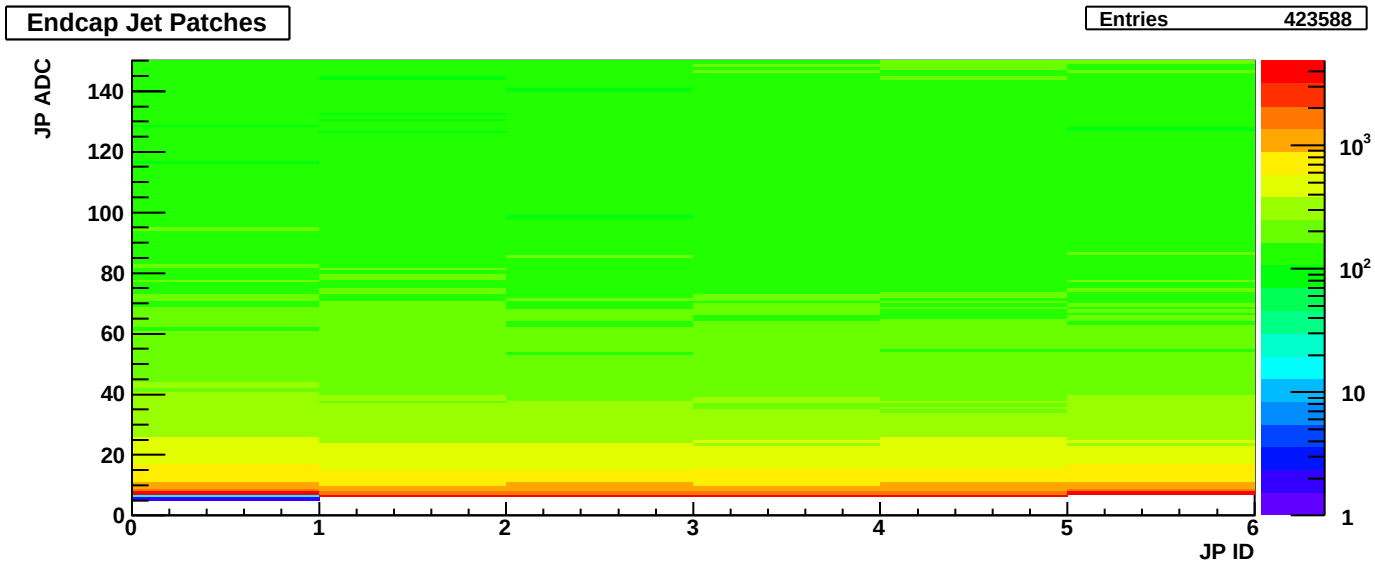

Barrel EMC L0 Input - High Tower
Entries 2.11794e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

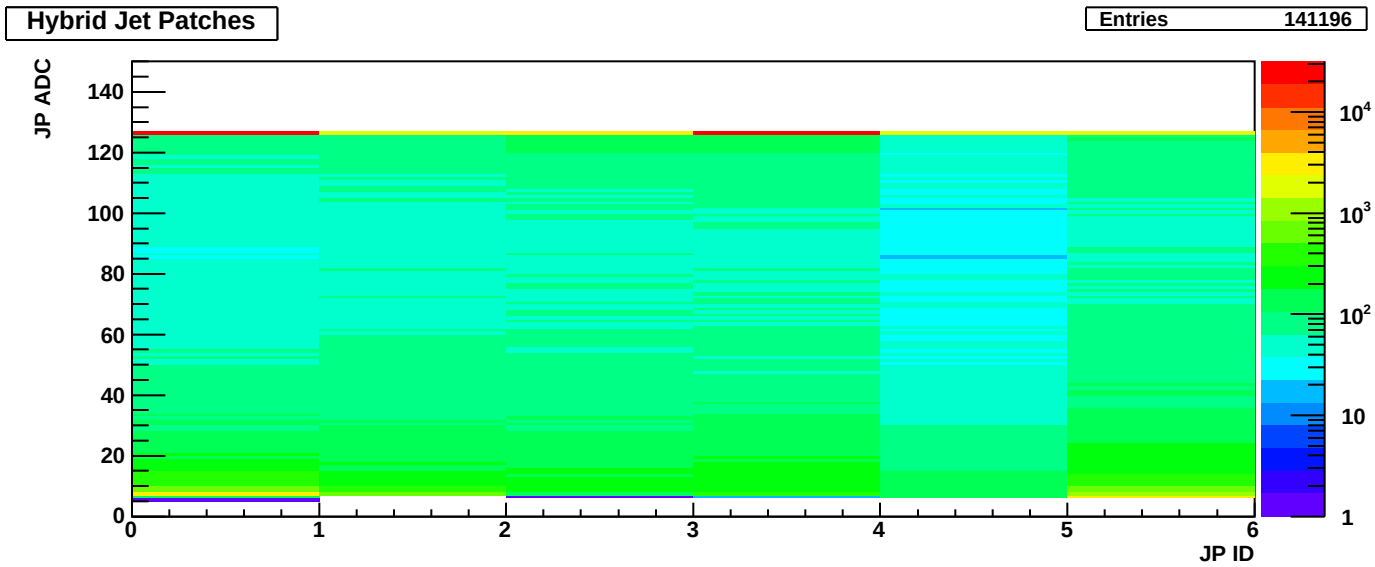

Barrel EMC L0 Input - Patch Sum
Entries 2.11794e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

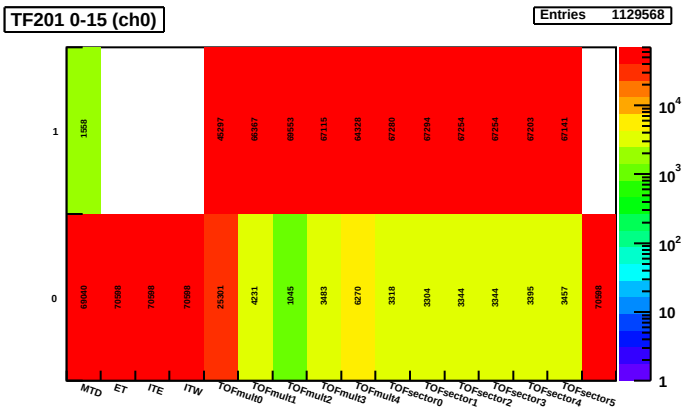

Endcap EMC L0 Input - High Tower
Entries 6353820
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

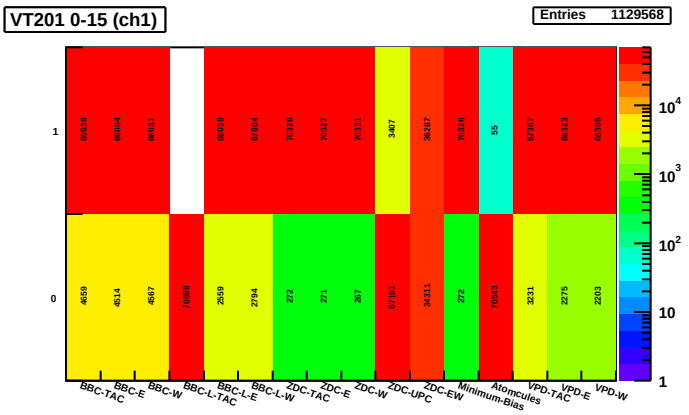

Endcap EMC L0 Input - Patch Sum
Entries 6353820
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

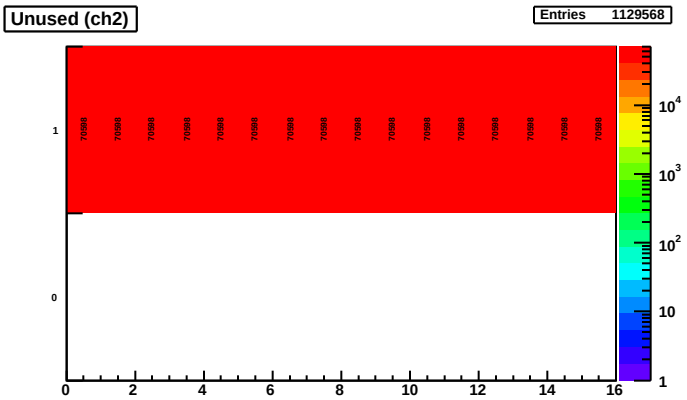

Barrel Jet Patches
Entries 1270764
JP ADC
JP ID

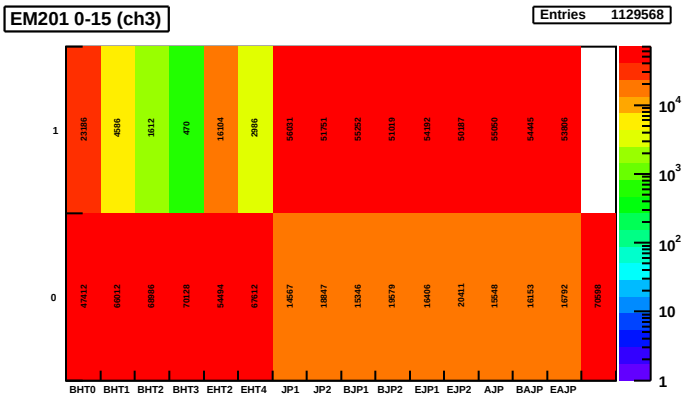

Endcap Jet Patches
Entries 423588
JP ADC
JP ID

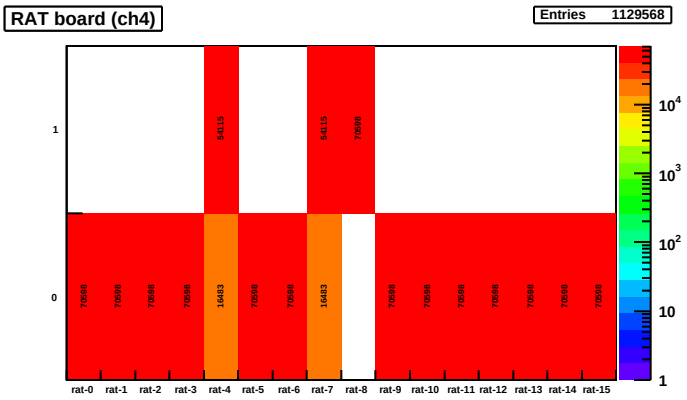

Hybrid Jet Patches
Entries 141196
JP ADC
JP ID

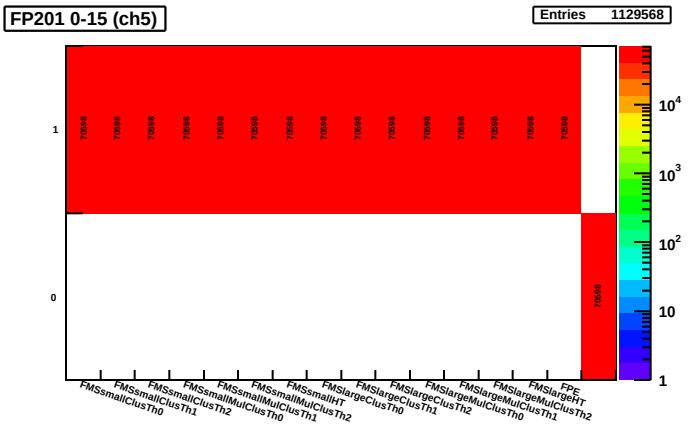

TF201 0-15 (ch0)
Entries 1129568
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

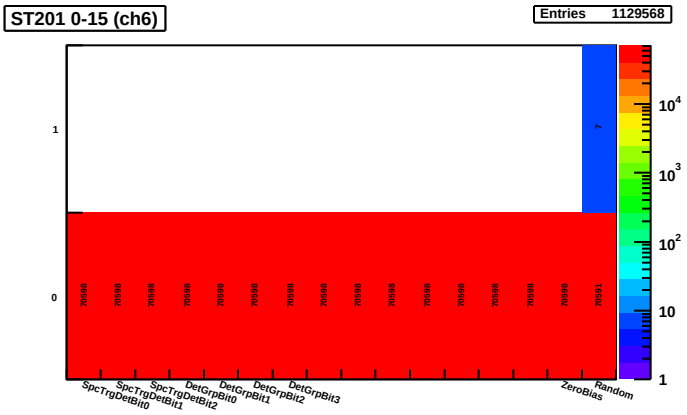

VT201 0-15 (ch1)
Entries 1129568
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

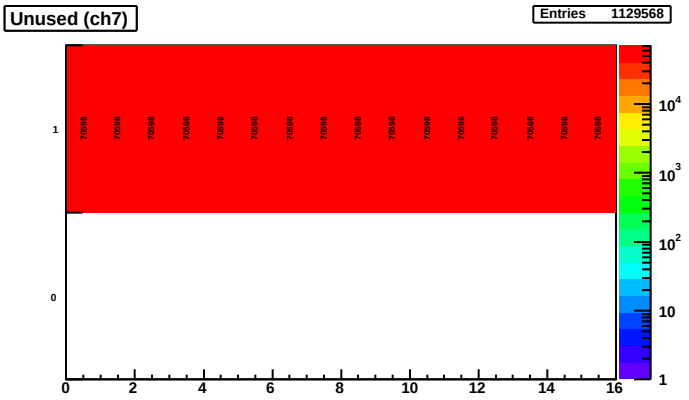

Unused (ch2)
Entries 1129568

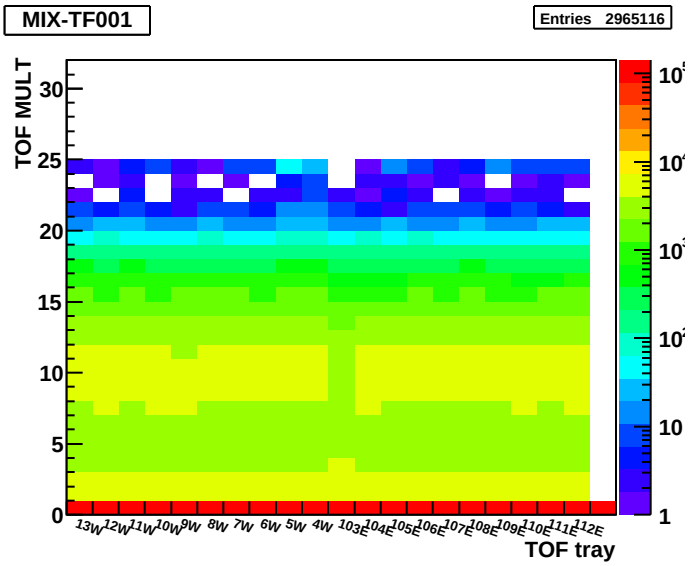

EM201 0-15 (ch3)
Entries 1129568
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

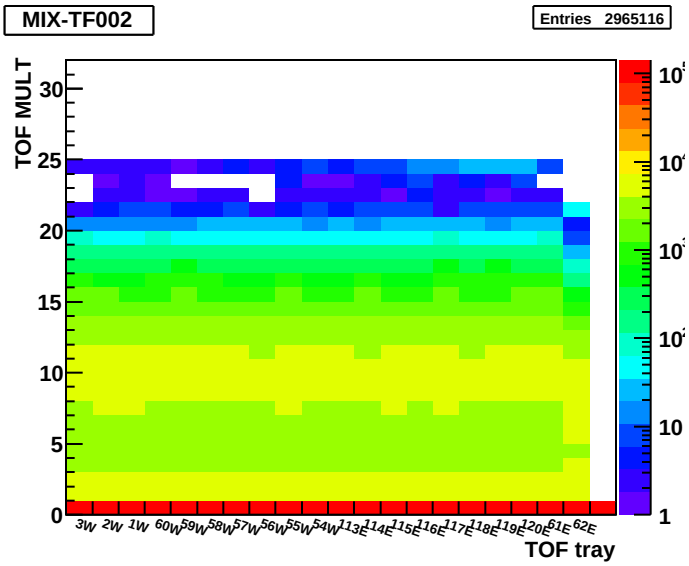

RAT board (ch4)
Entries 1129568
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

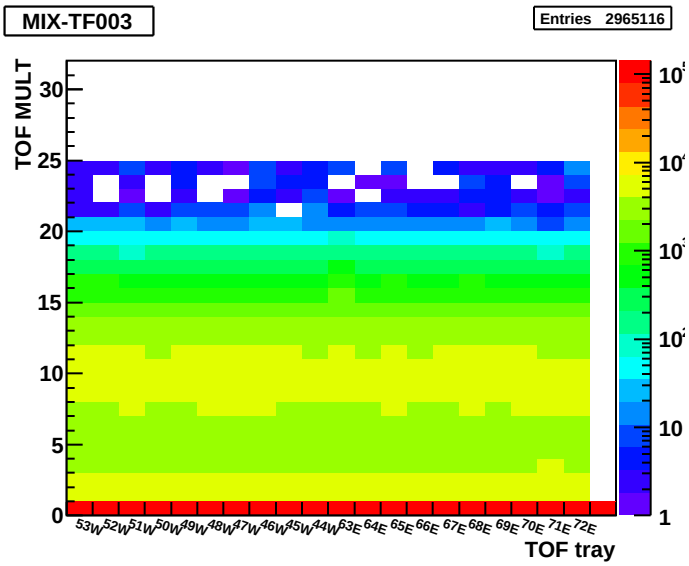

FP201 0-15 (ch5)
Entries 1129568
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

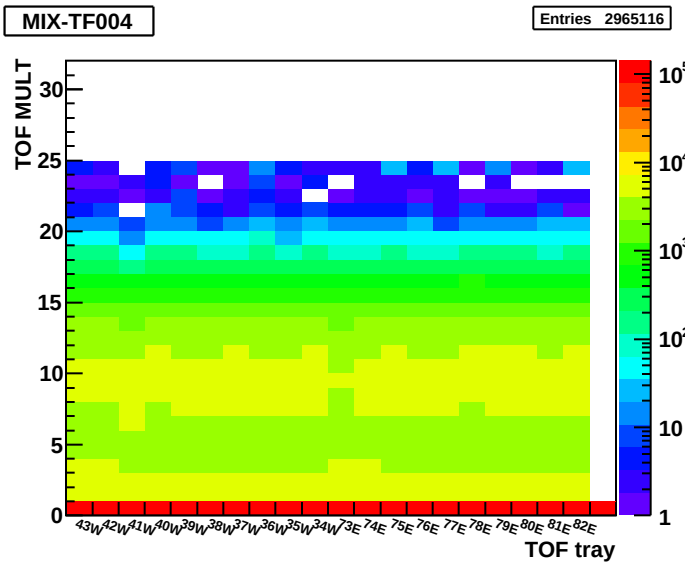

ST201 0-15 (ch6)
Entries 1129568
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

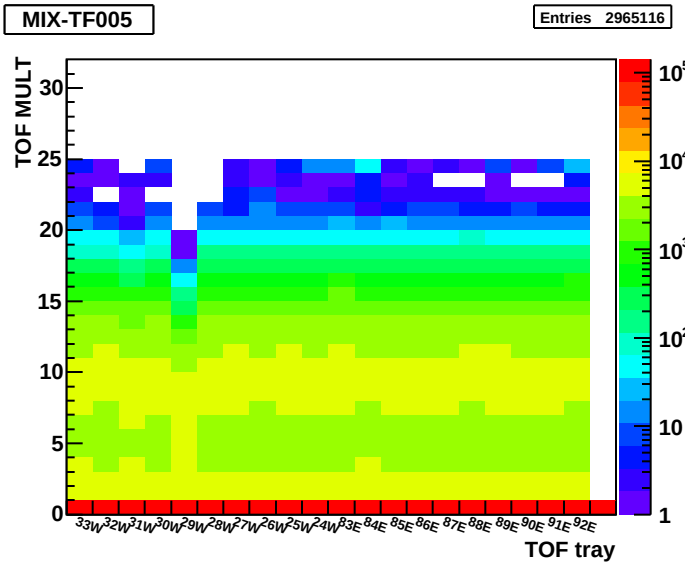

Unused (ch7)
Entries 1129568

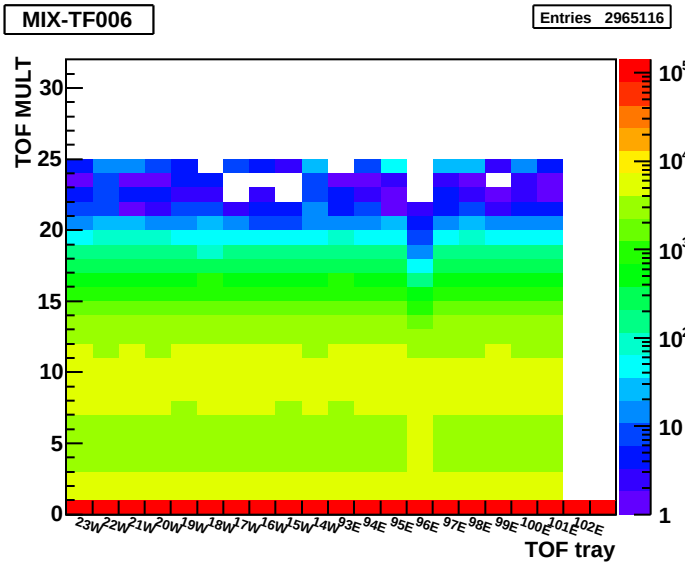

MIX-TF001
Entries 2965116
TOF MULT
TOF tray

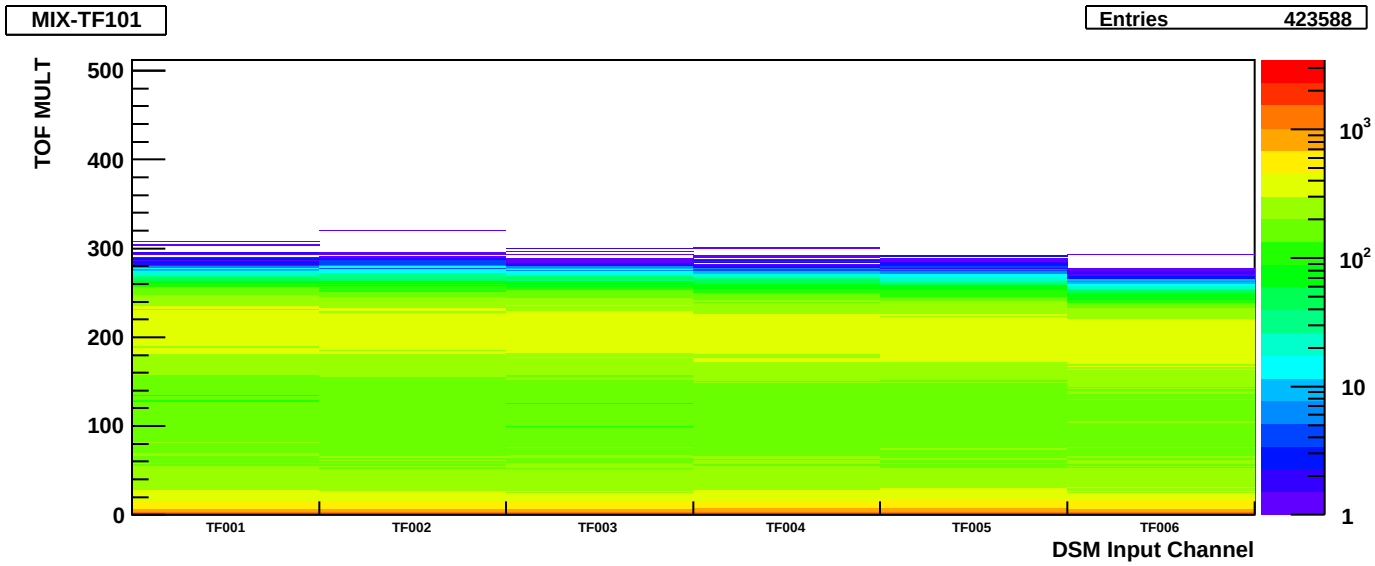

MIX-TF002
Entries 2965116
TOF MULT
TOF tray

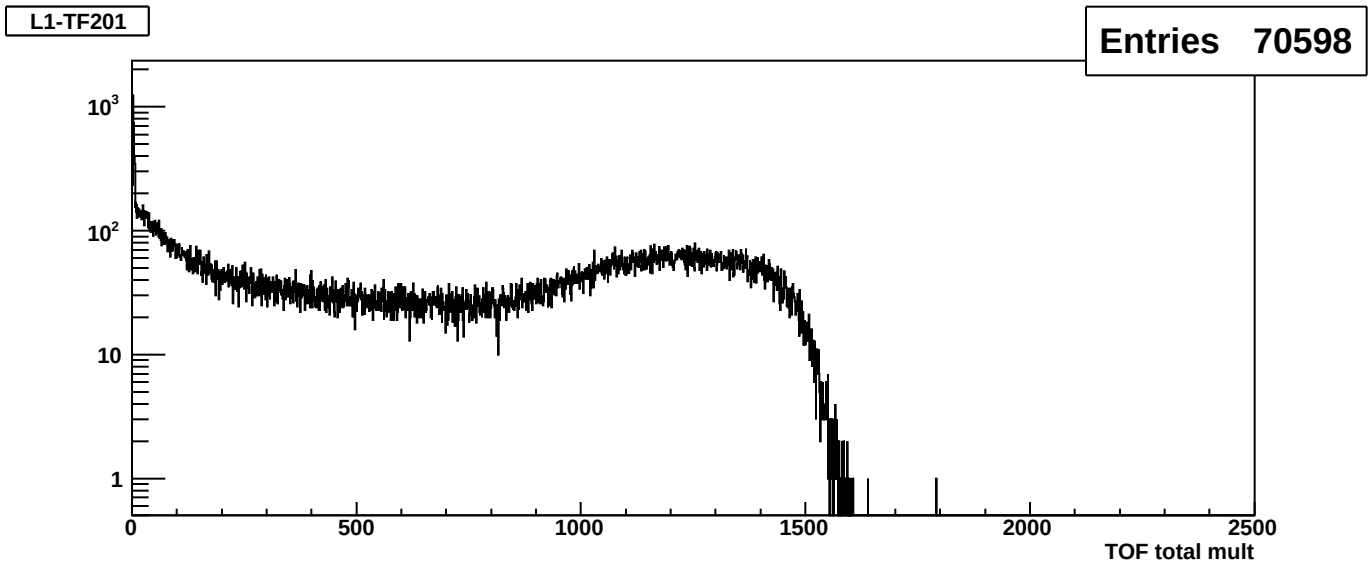

MIX-TF003
Entries 2965116
TOF MULT
TOF tray

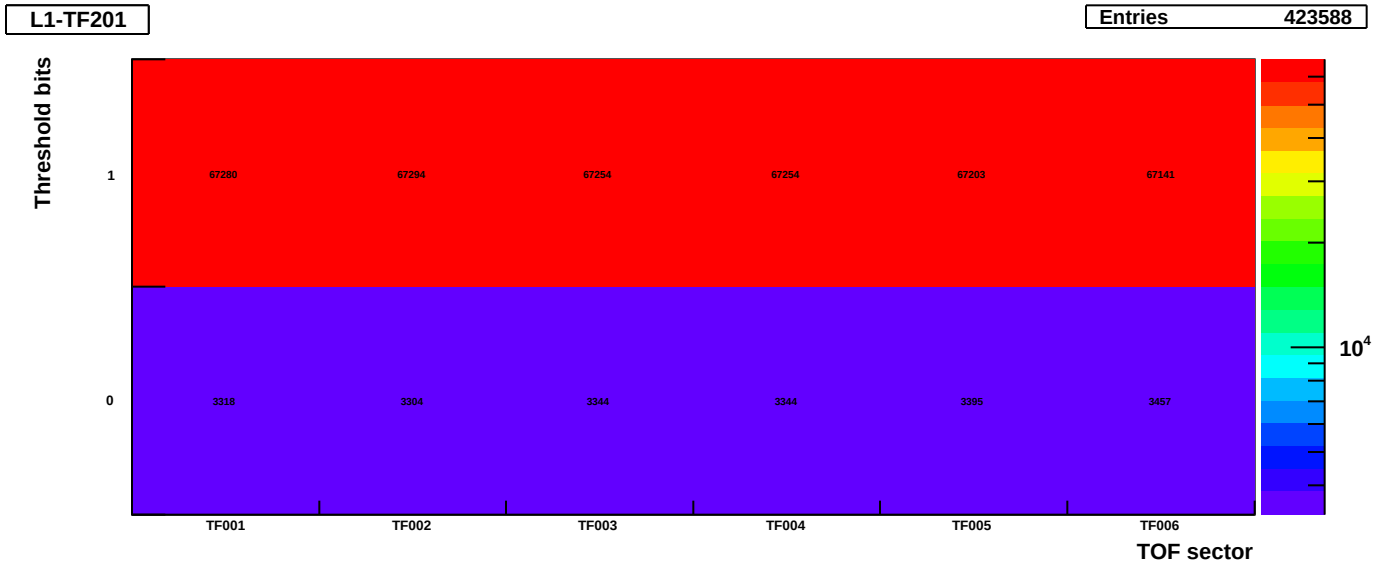

MIX-TF004
Entries 2965116
TOF MULT
TOF tray

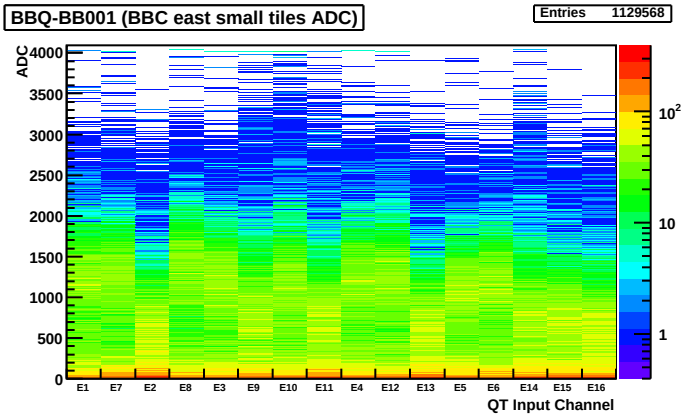

MIX-TF005
Entries 2965116
TOF MULT
TOF tray

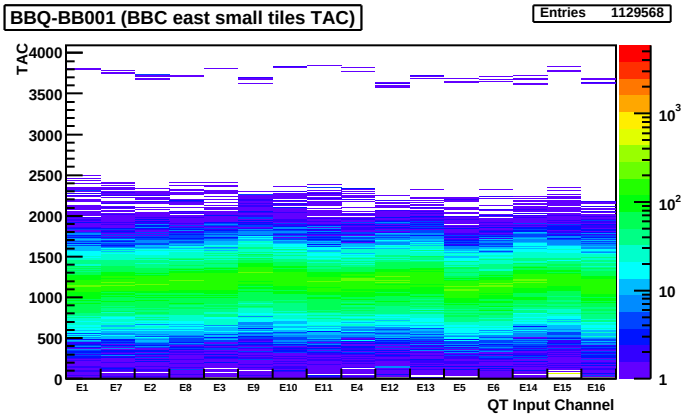

MIX-TF006
Entries 2965116
TOF MULT
TOF tray

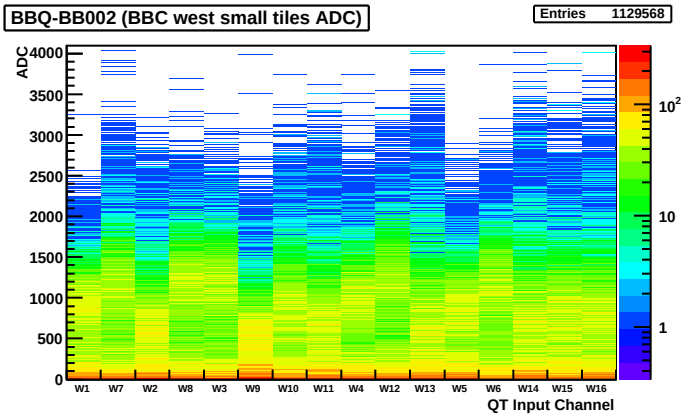

MIX-TF101
Entries 423588
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

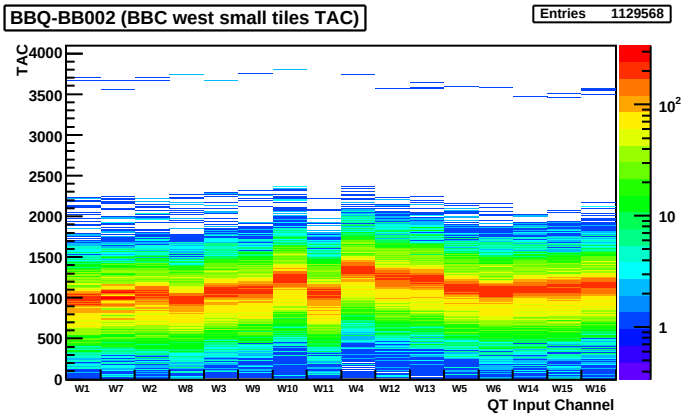

L1-TF201
Entries 70598
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

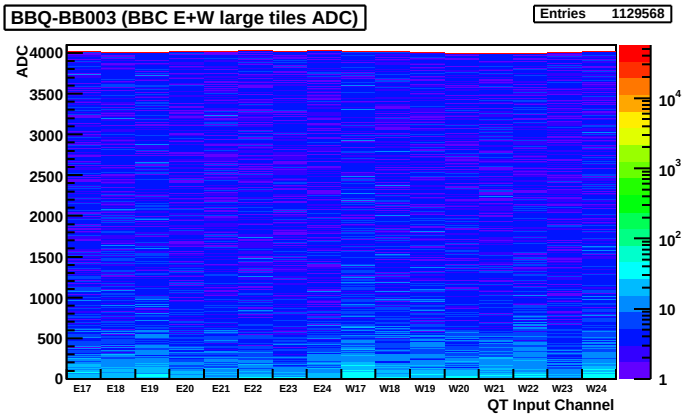

L1-TF201
Entries 423588
Threshold bits
1
0
67280
67294
67254
67254
67203
67141
3318
3304
3344
3344
3395
3457
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

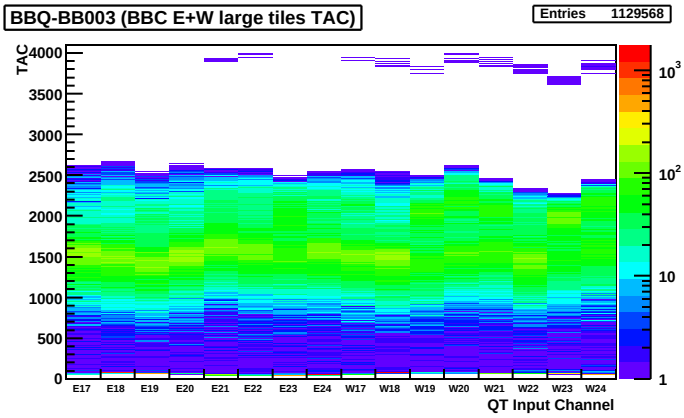

BBQ-BB001 (BBC east small tiles ADC)
Entries 1129568
ADC
QT Input Channel

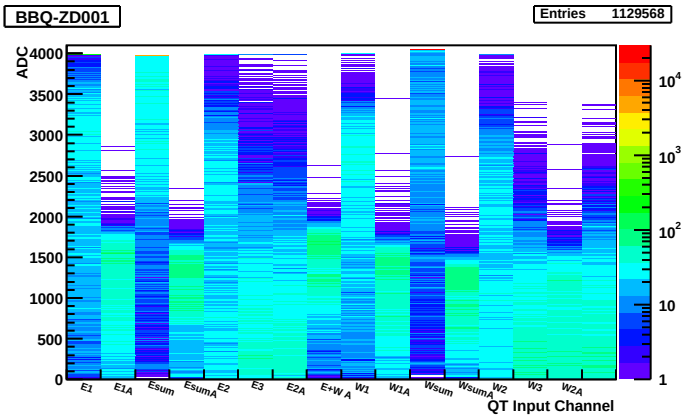

BBQ-BB001 (BBC east small tiles TAC)
Entries 1129568
TAC
QT Input Channel

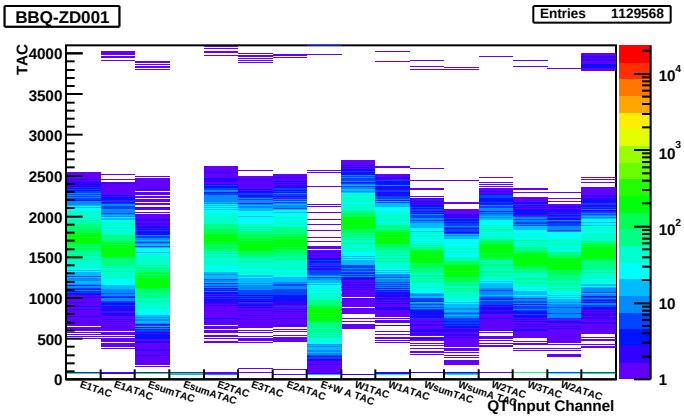

BBQ-BB002 (BBC west small tiles ADC)
Entries 1129568
ADC
QT Input Channel

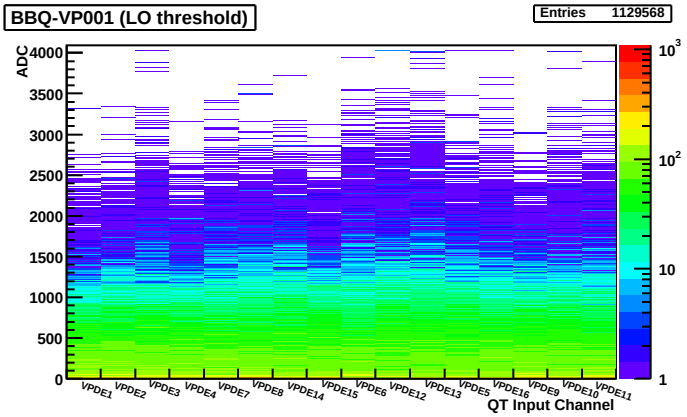

BBQ-BB002 (BBC west small tiles TAC)
Entries 1129568
TAC
QT Input Channel

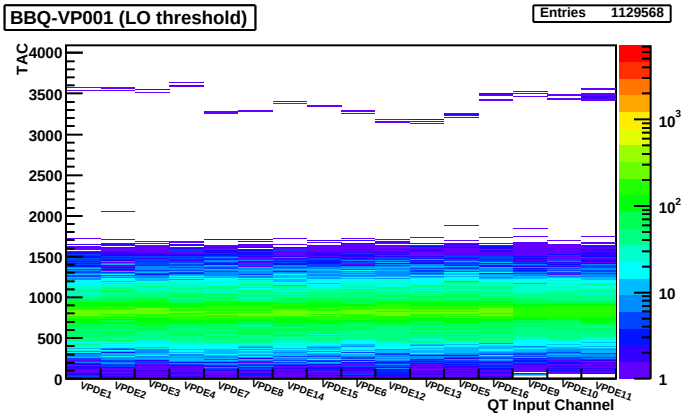

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1129568
ADC
QT Input Channel

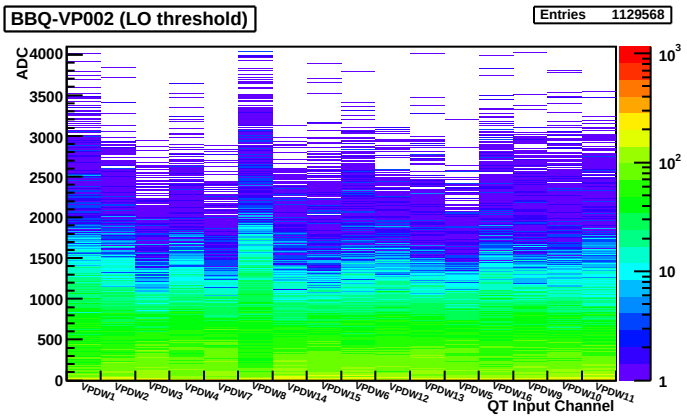

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1129568
TAC
QT Input Channel

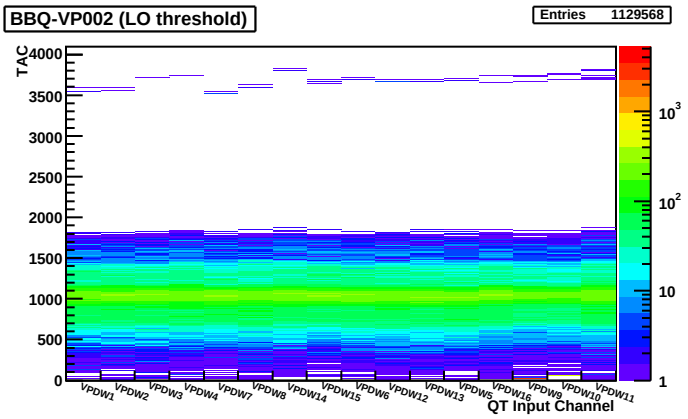

BBQ-ZD001
Entries 1129568
ADC
QT Input Channel

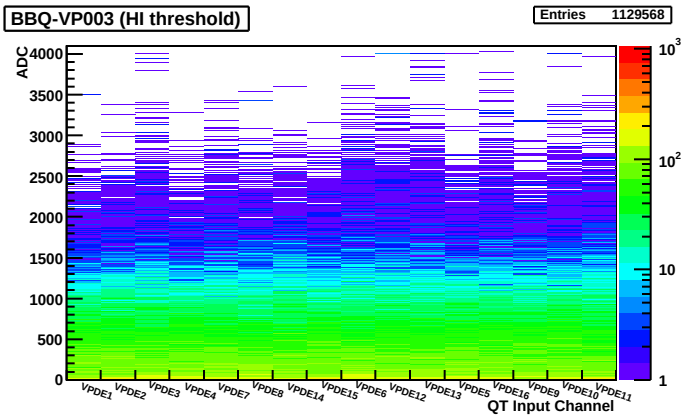

BBQ-ZD001
Entries 1129568
TAC
QT Input Channel

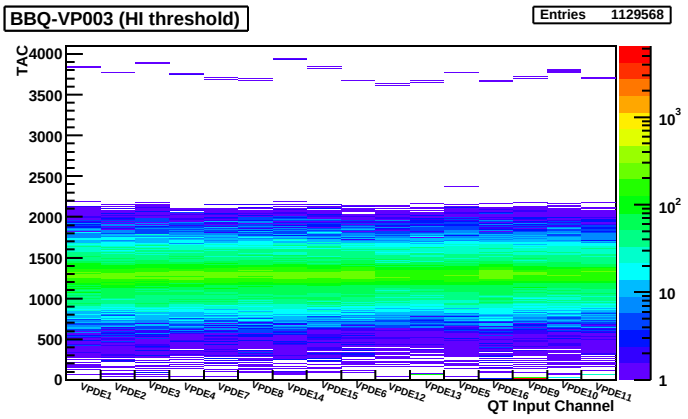

BBQ-VP001 (LO threshold)
Entries 1129568
ADC
QT Input Channel

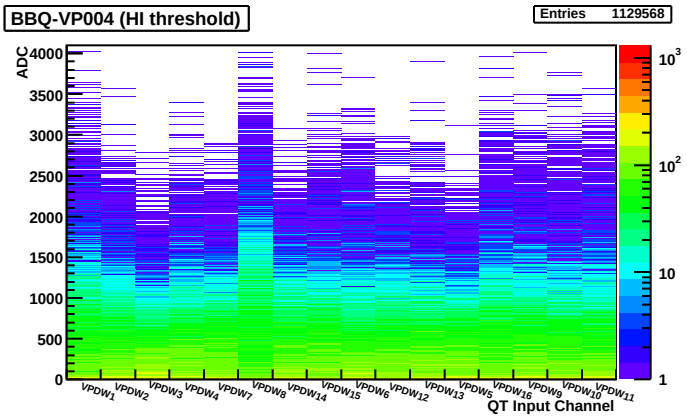

BBQ-VP001 (LO threshold)
Entries 1129568
TAC
QT Input Channel

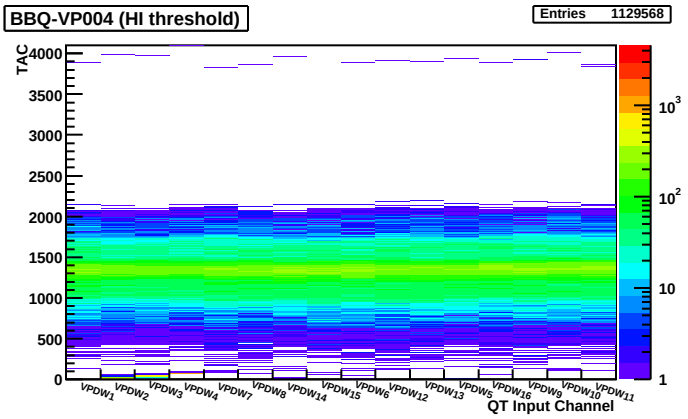

BBQ-VP002 (LO threshold)
Entries 1129568
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 1129568
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 1129568
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 1129568
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 1129568
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 1129568
TAC
QT Input Channel