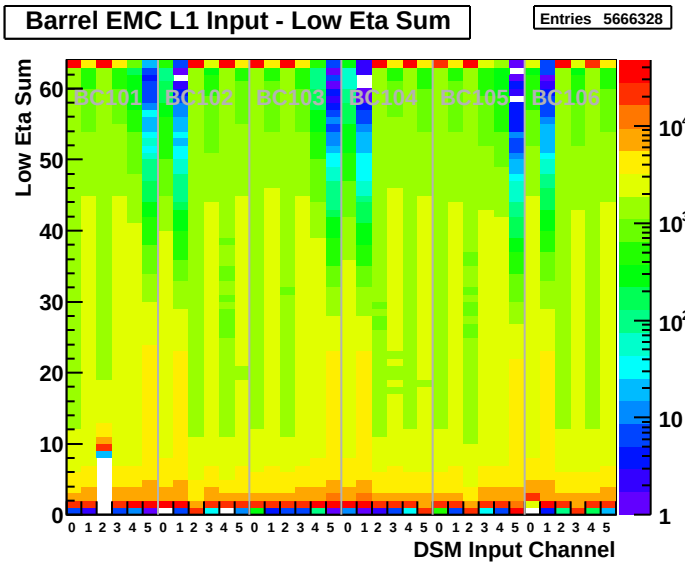
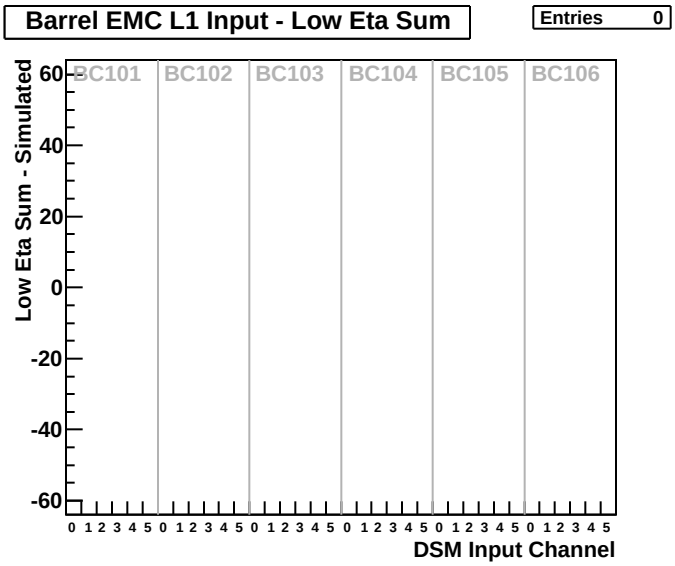
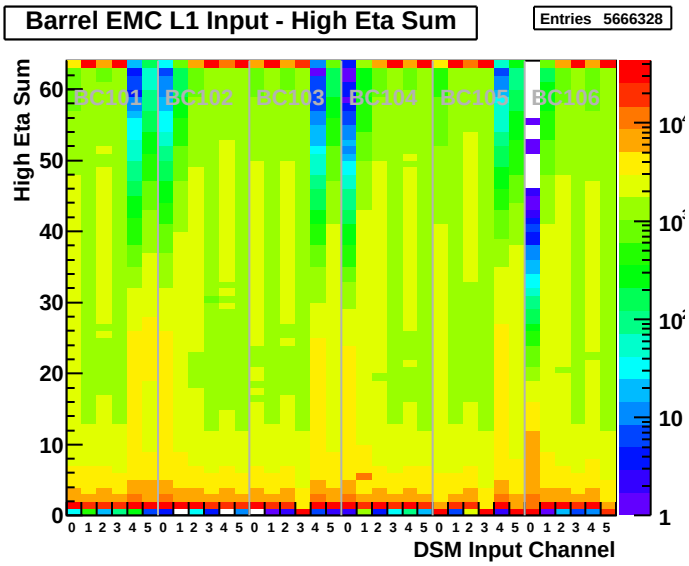
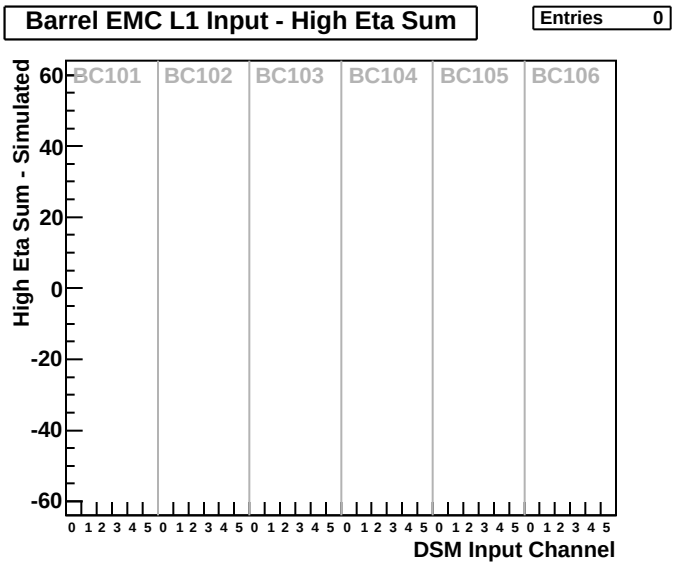
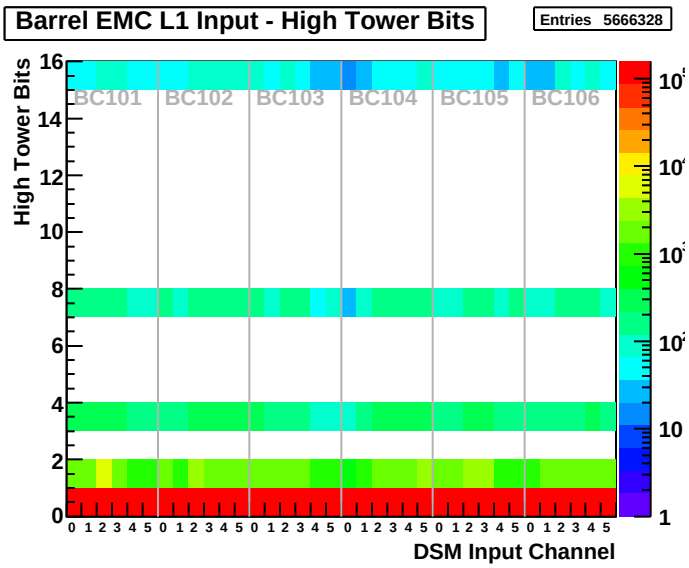
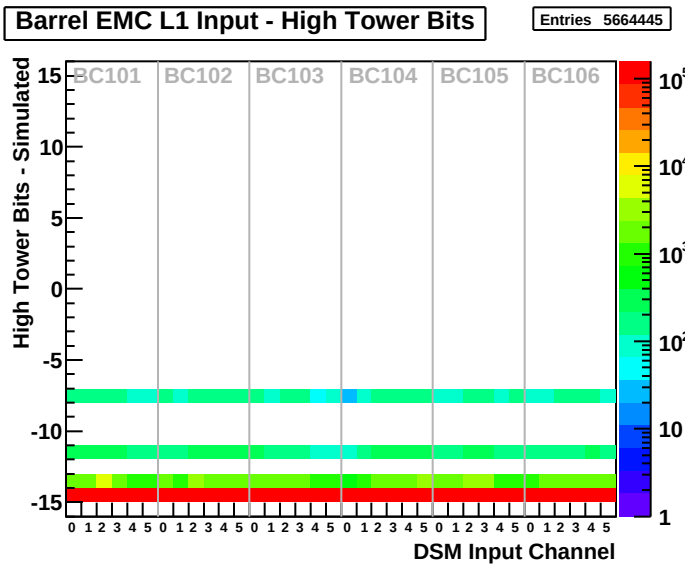

Barrel EMC L1 Input - Low Eta Sum
Entries 5666328
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

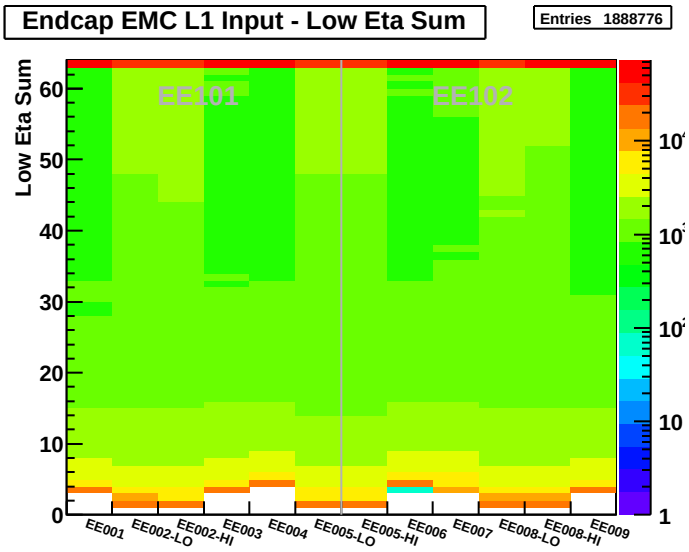

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

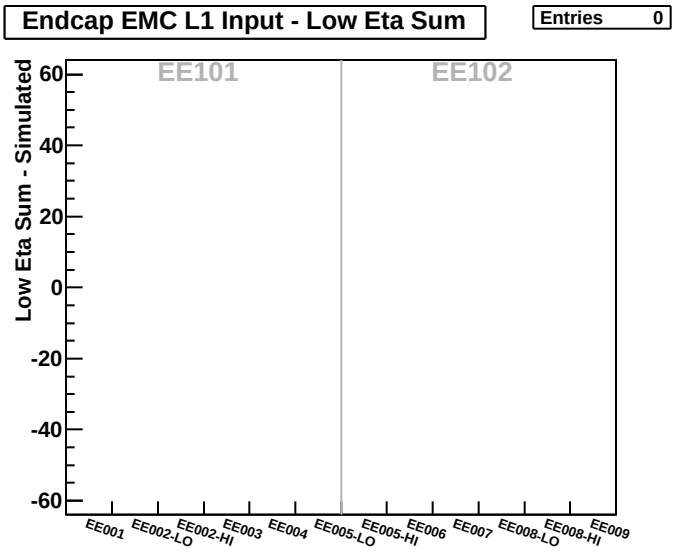

Barrel EMC L1 Input - High Eta Sum
Entries 5666328
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

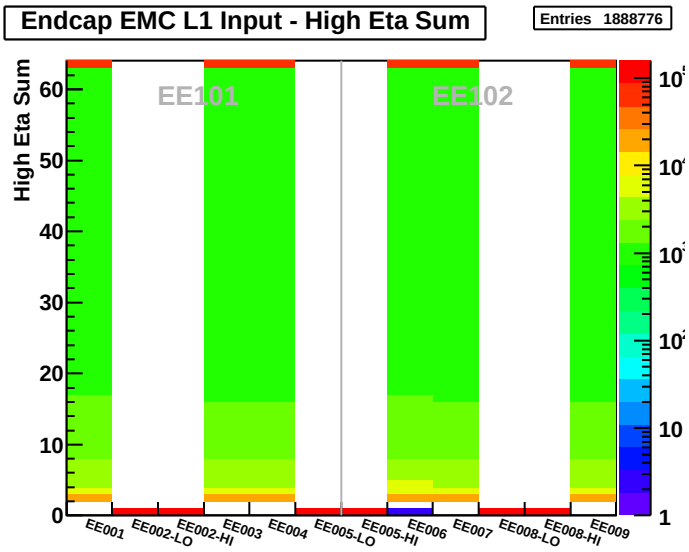

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

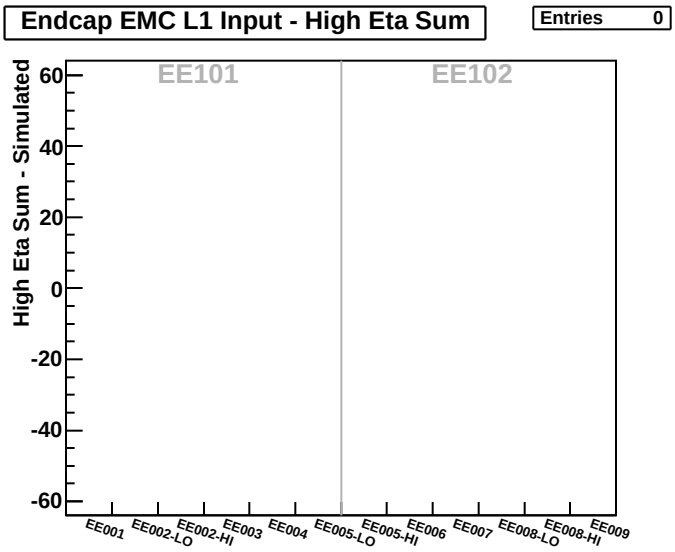

Barrel EMC L1 Input - High Tower Bits
Entries 5666328
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

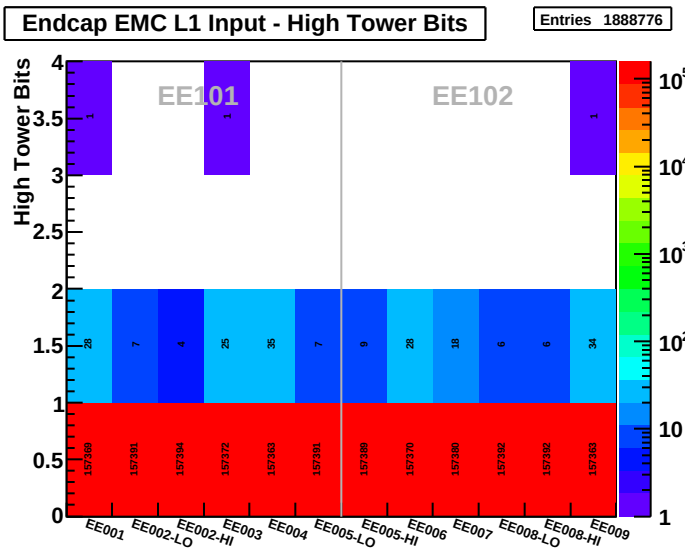

Barrel EMC L1 Input - High Tower Bits
Entries 5664445
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

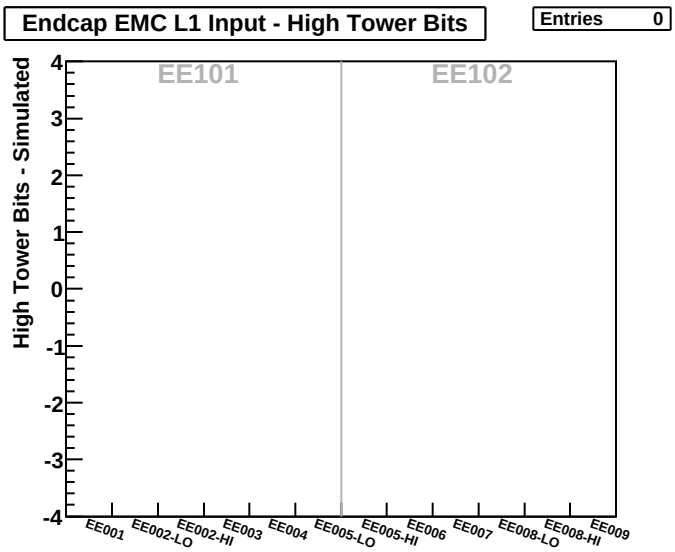

Endcap EMC L1 Input - Low Eta Sum
Entries 1888776
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

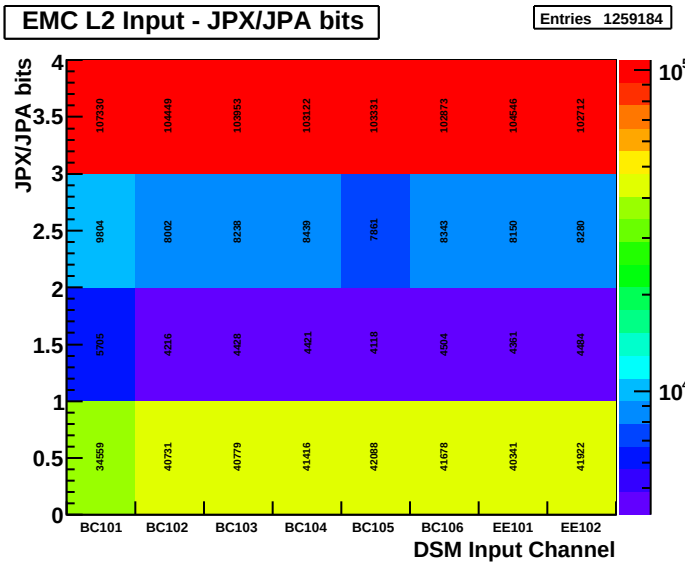

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

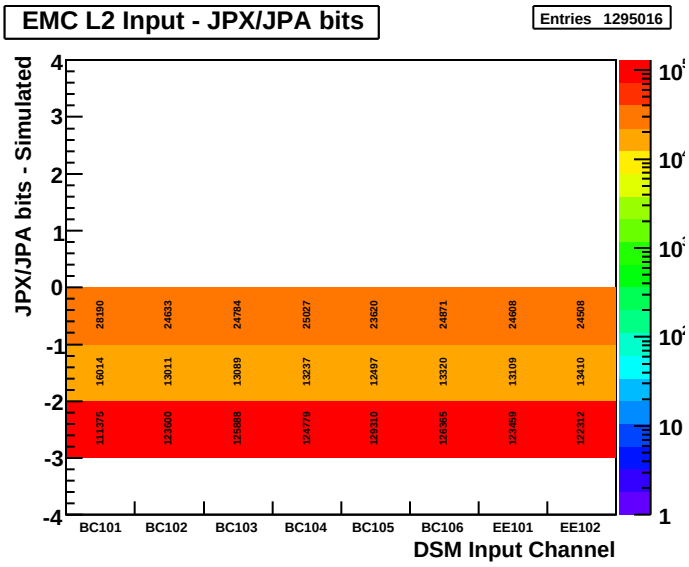

Endcap EMC L1 Input - High Eta Sum
Entries 1888776
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

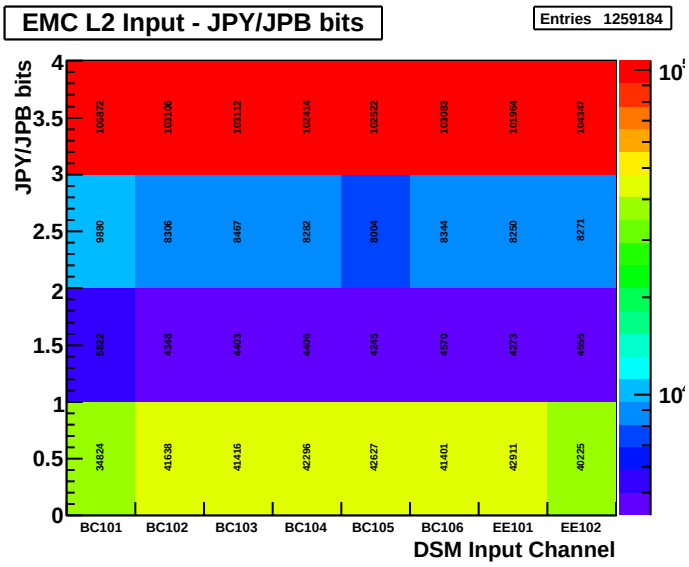

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

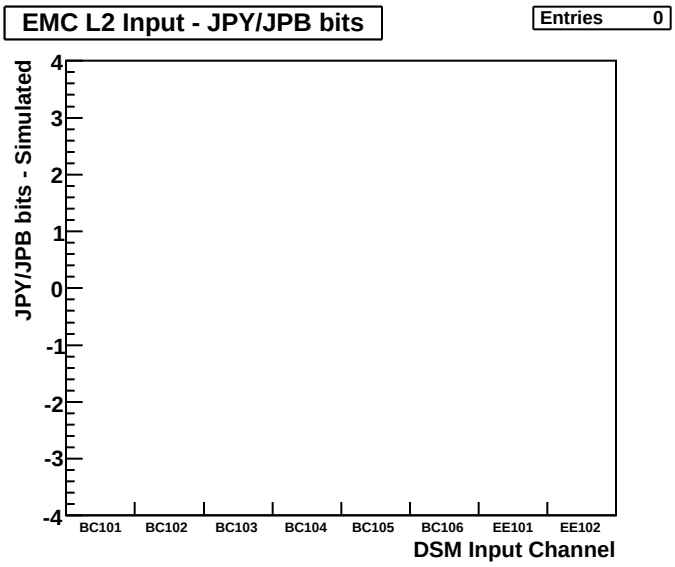

Endcap EMC L1 Input - High Tower Bits
Entries 1888776
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

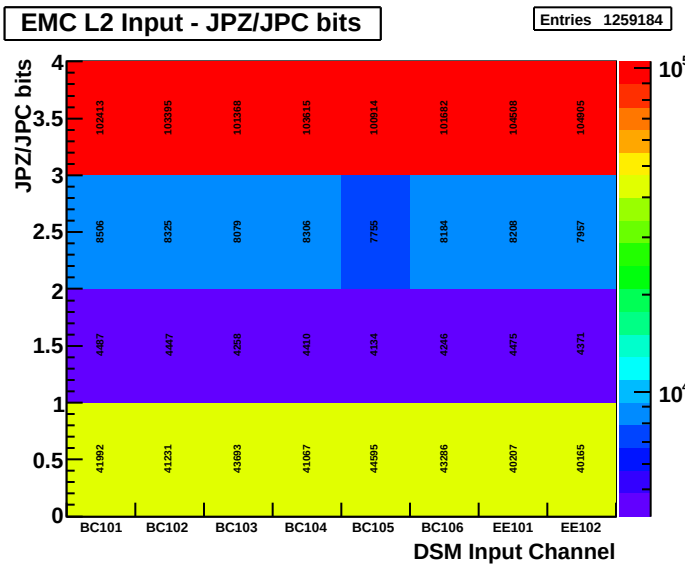

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

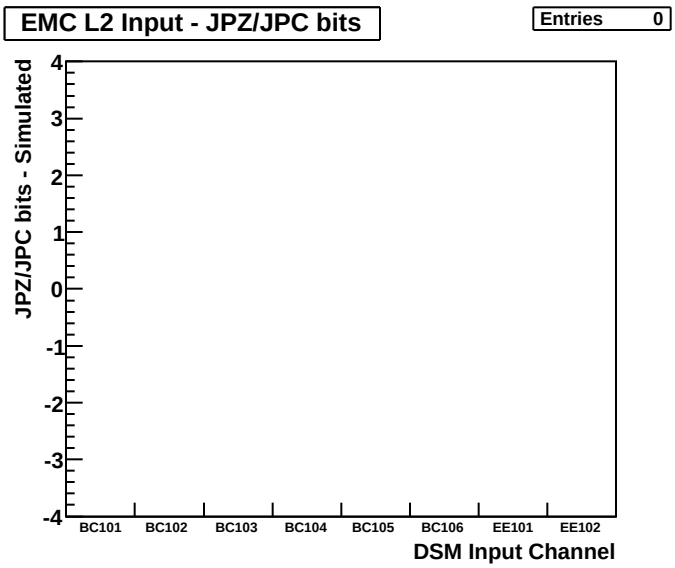

EMC L2 Input - JPX/JPA bits
Entries 1259184
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
107330 104449 103853 103122 103331 102873 104546 102712
9804 8002 8238 8439 7861 8343 8150 8280
5705 4216 4428 4421 4118 4504 4381 4484
34559 40731 40779 41416 42088 41678 40341 41922

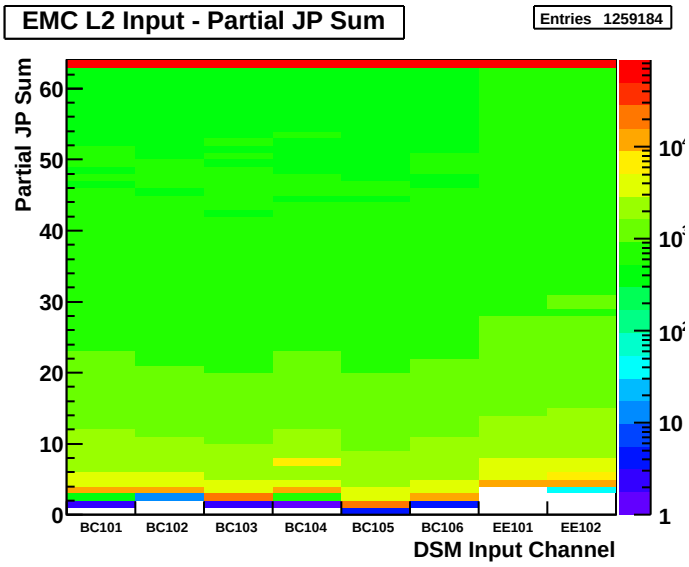

EMC L2 Input - JPX/JPA bits
Entries 1295016
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
28190 24633 24784 25027 23620 24871 24608 24508
16014 13011 13089 13237 12497 13320 13109 13410
111375 123600 125868 124779 129310 126365 123459 122312

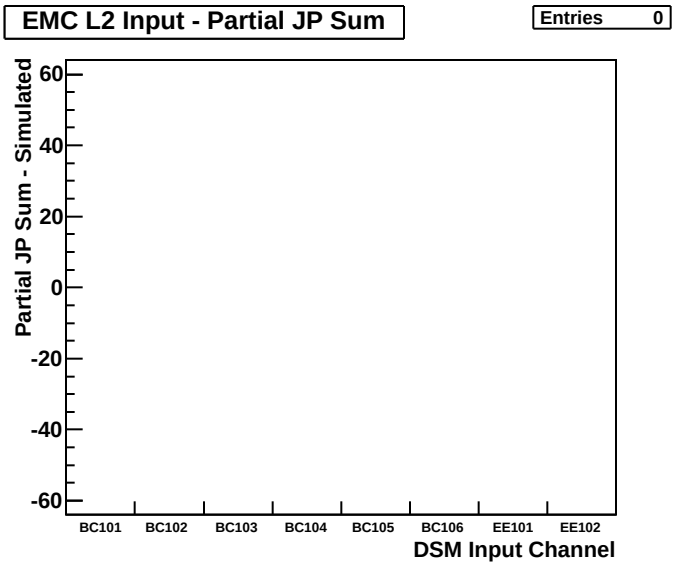

EMC L2 Input - JPY/JPB bits
Entries 1259184
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
106872 103106 103112 102414 102522 103093 101964 104347
9880 8306 8467 8282 8004 8344 8250 8271
5822 4340 4403 4406 4245 4570 4273 4355
34824 41638 41416 42296 42627 41401 42911 40225

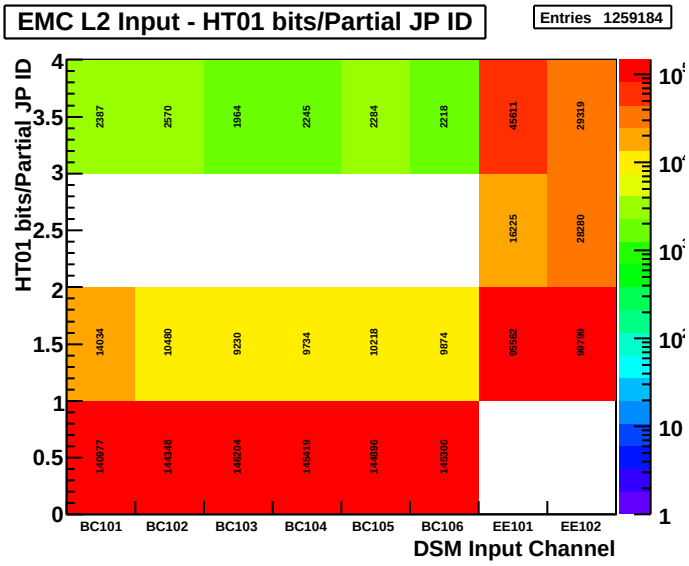

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

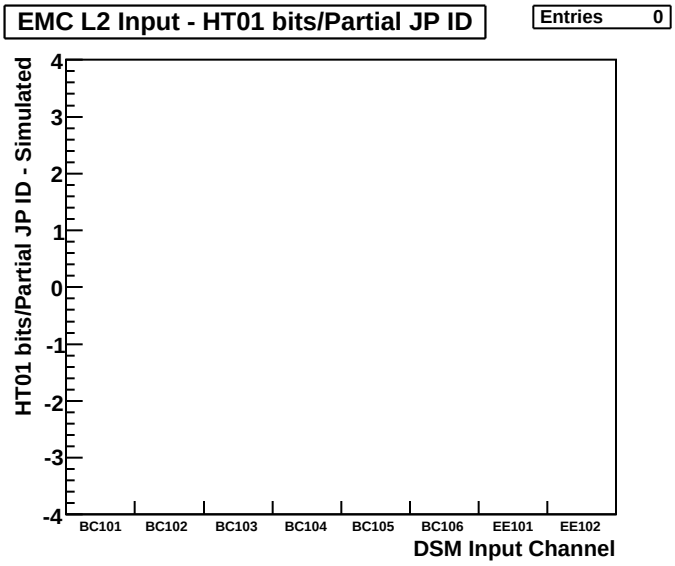

EMC L2 Input - JPZ/JPC bits
Entries 1259184
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
102413 103395 101368 103815 100914 101682 104508 104905
8506 8325 8079 8306 7755 8184 8208 7957
4487 4447 4258 4410 4134 4246 4475 4371
41992 41231 43693 41067 44595 43286 40207 40165

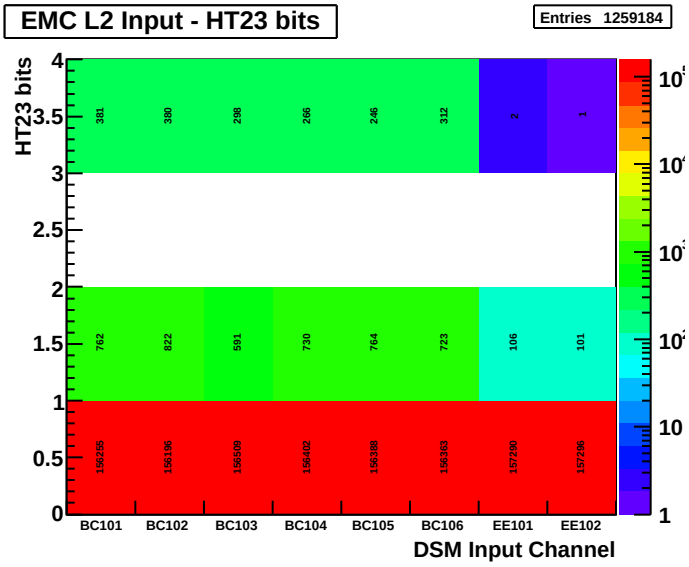

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

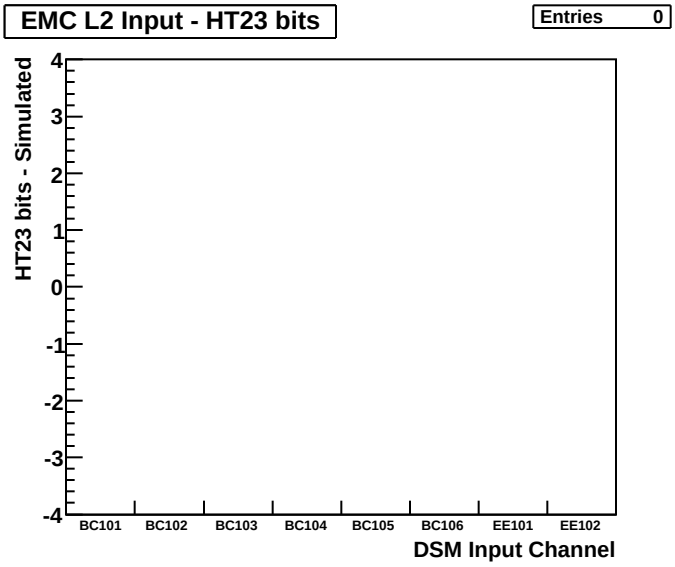

EMC L2 Input - Partial JP Sum
Entries 1259184
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

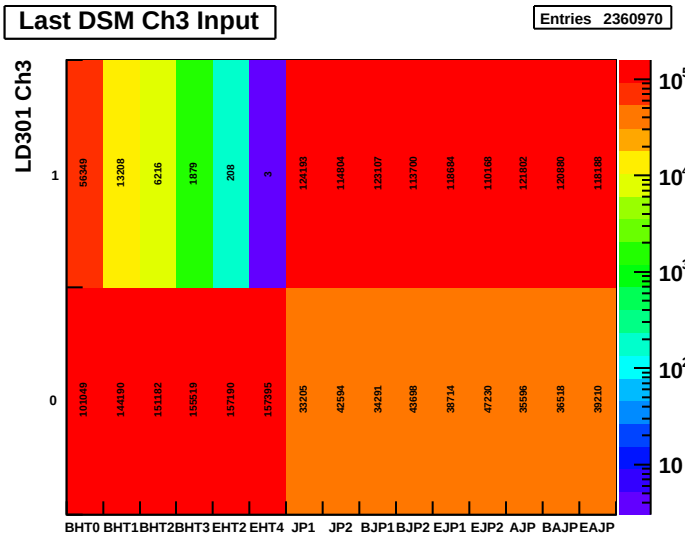

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

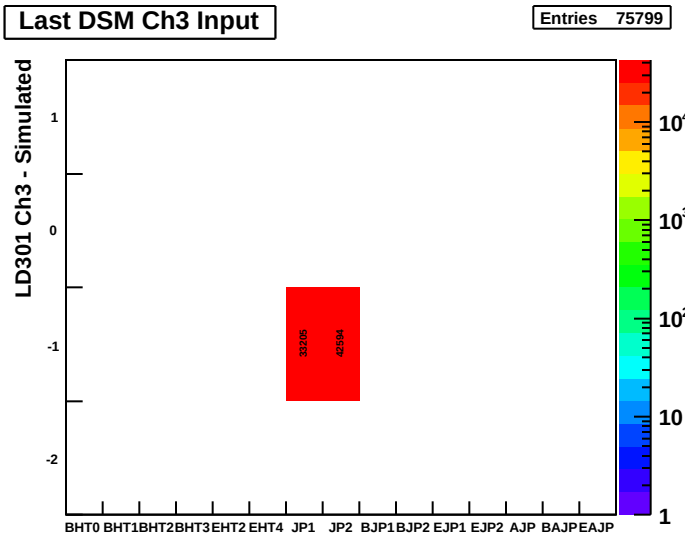

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1259184
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

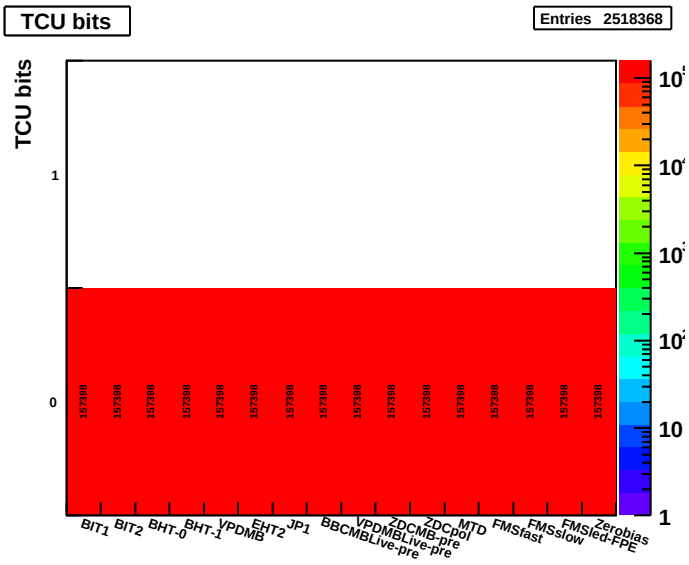

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

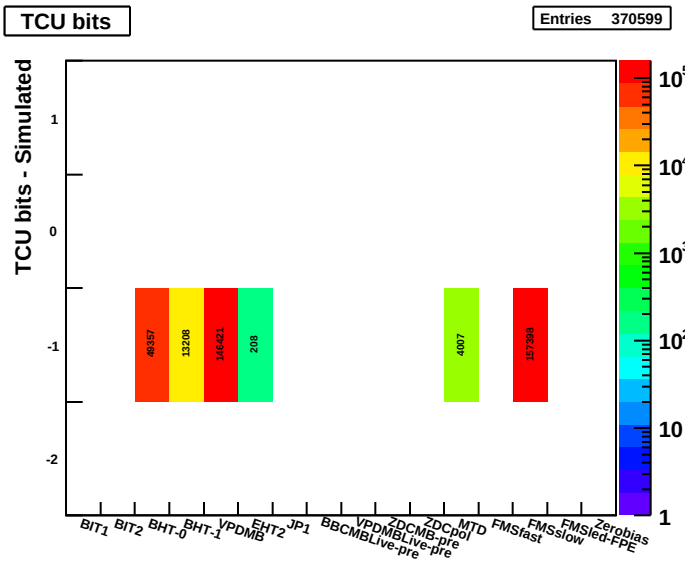

EMC L2 Input - HT23 bits
Entries 1259184
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

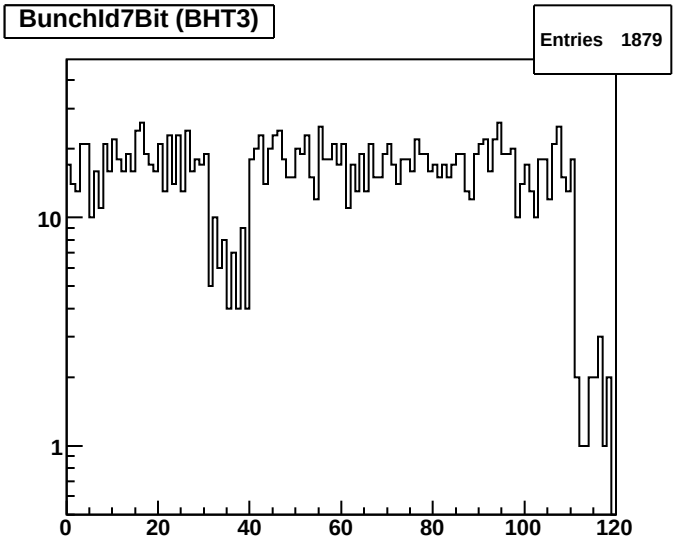

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

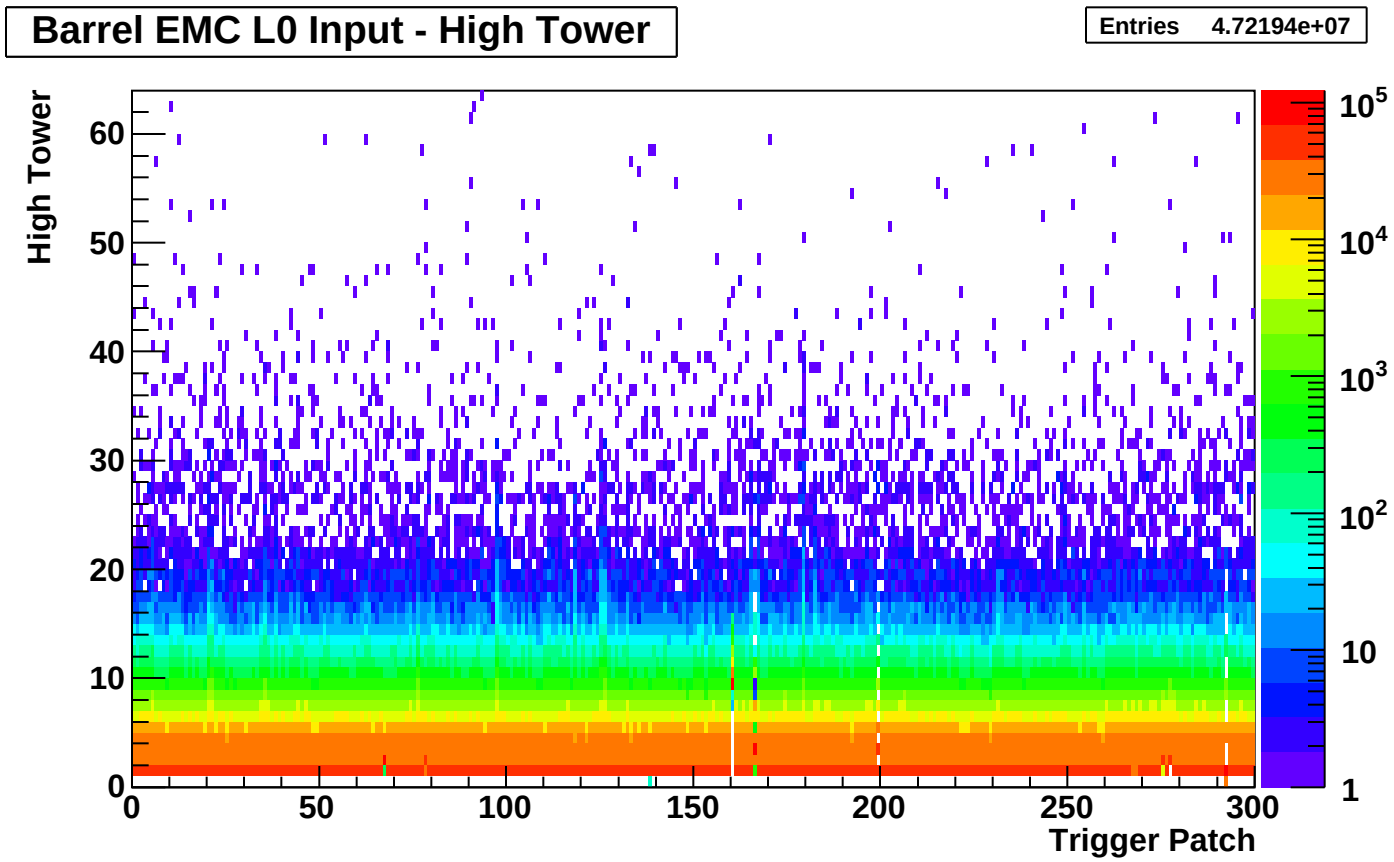

Last DSM Ch3 Input
Entries 2360970
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

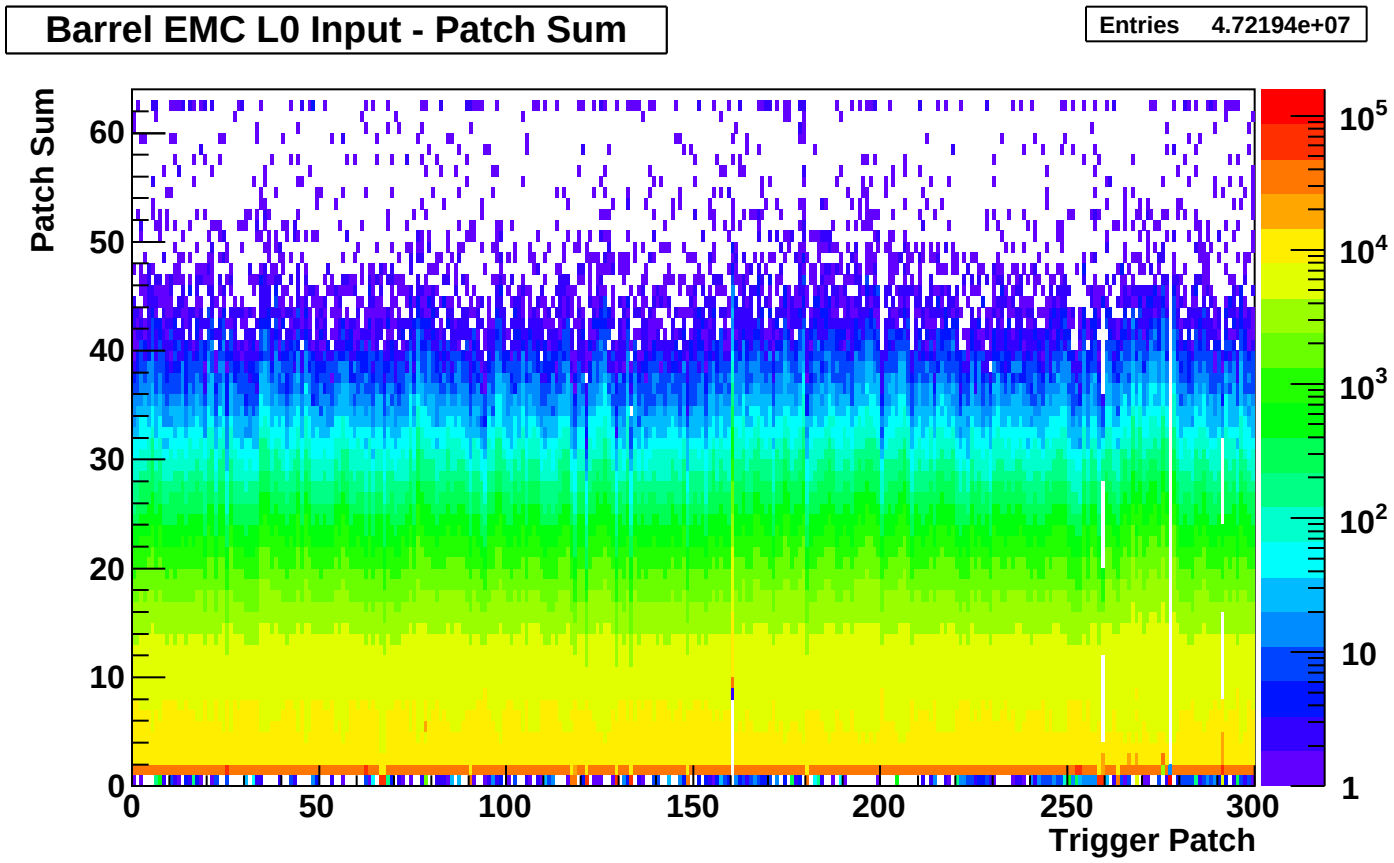

Last DSM Ch3 Input
Entries 75799
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

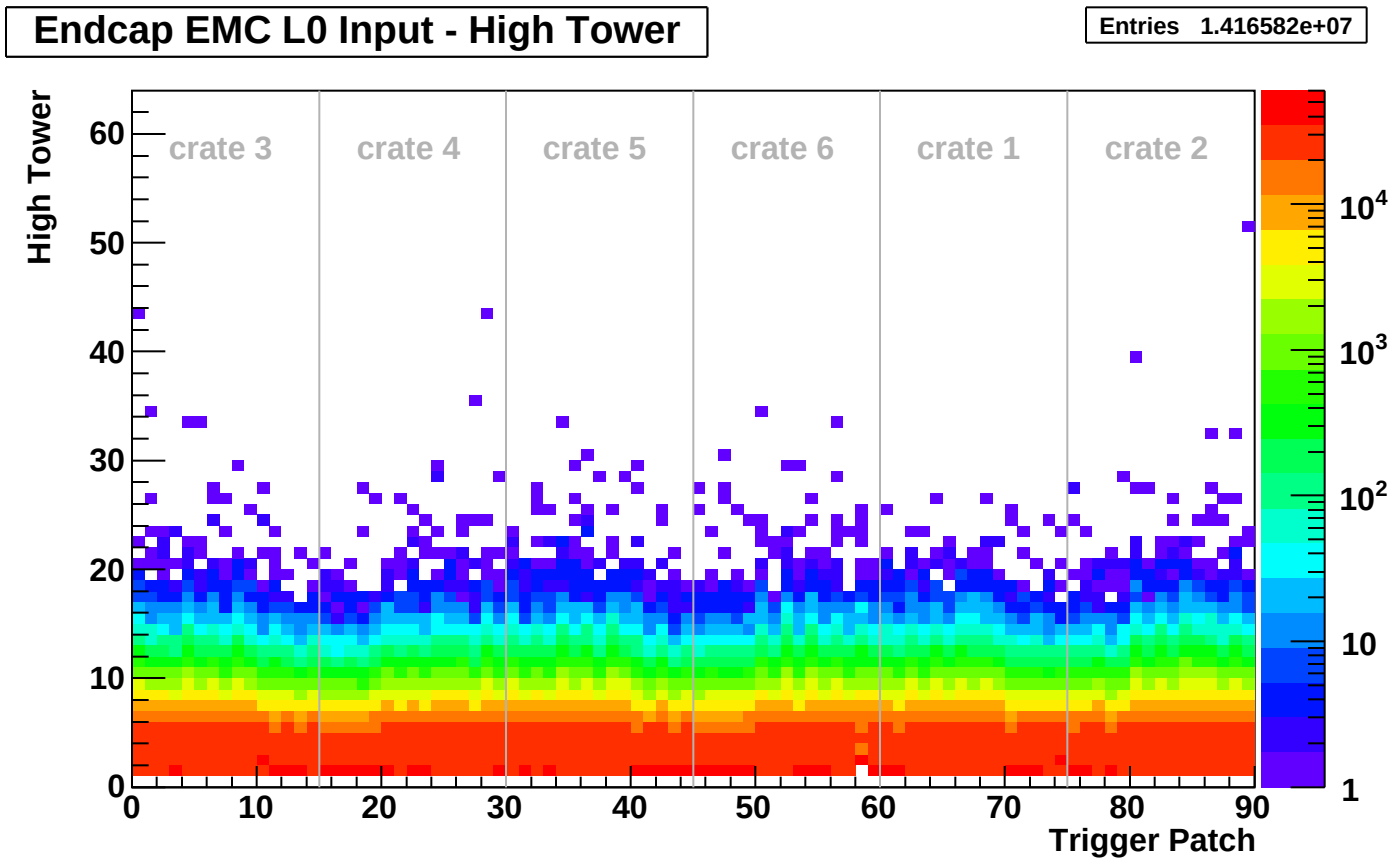

TCU bits
Entries 2518368
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

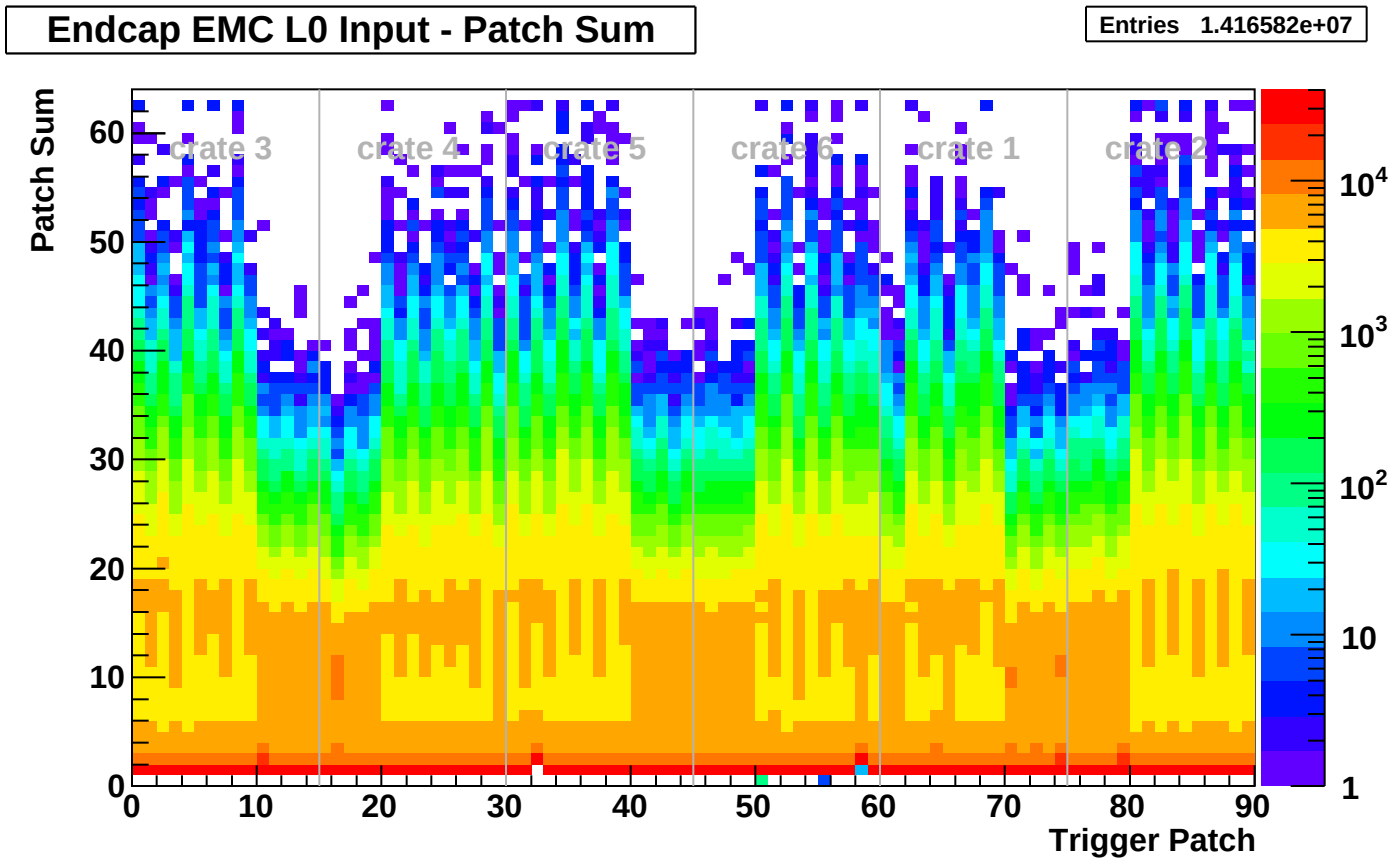

TCU bits
Entries 370599
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

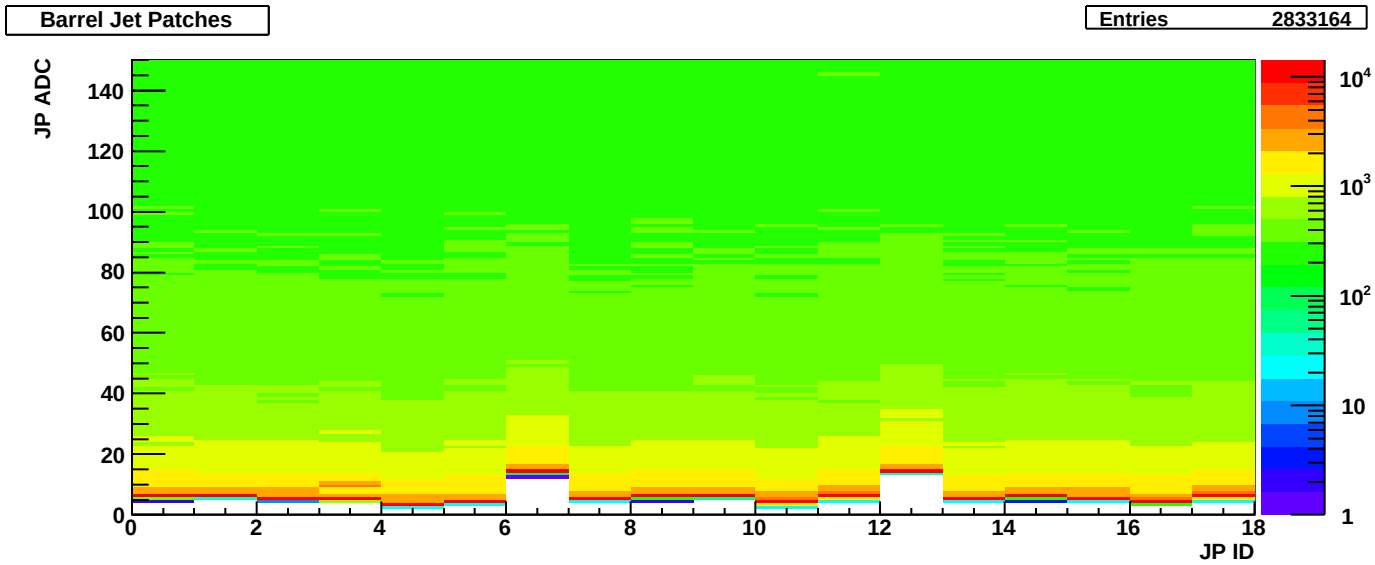

BunchId7Bit (BHT3)
Entries 1879

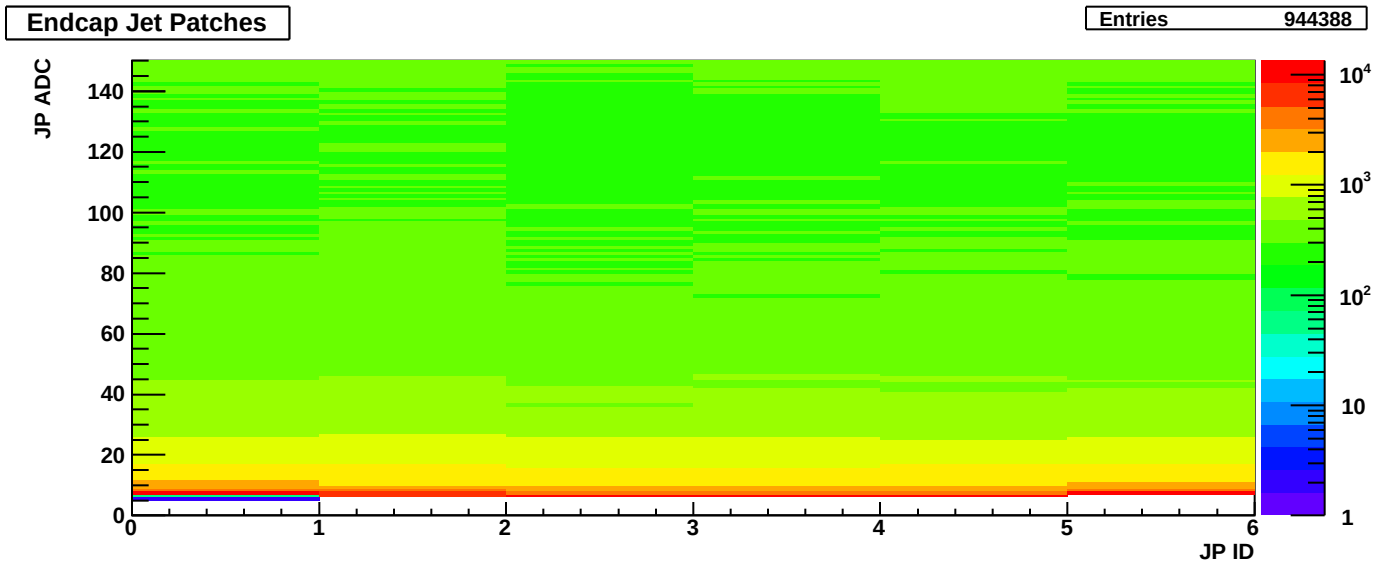

Barrel EMC L0 Input - High Tower
Entries 4.72194e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

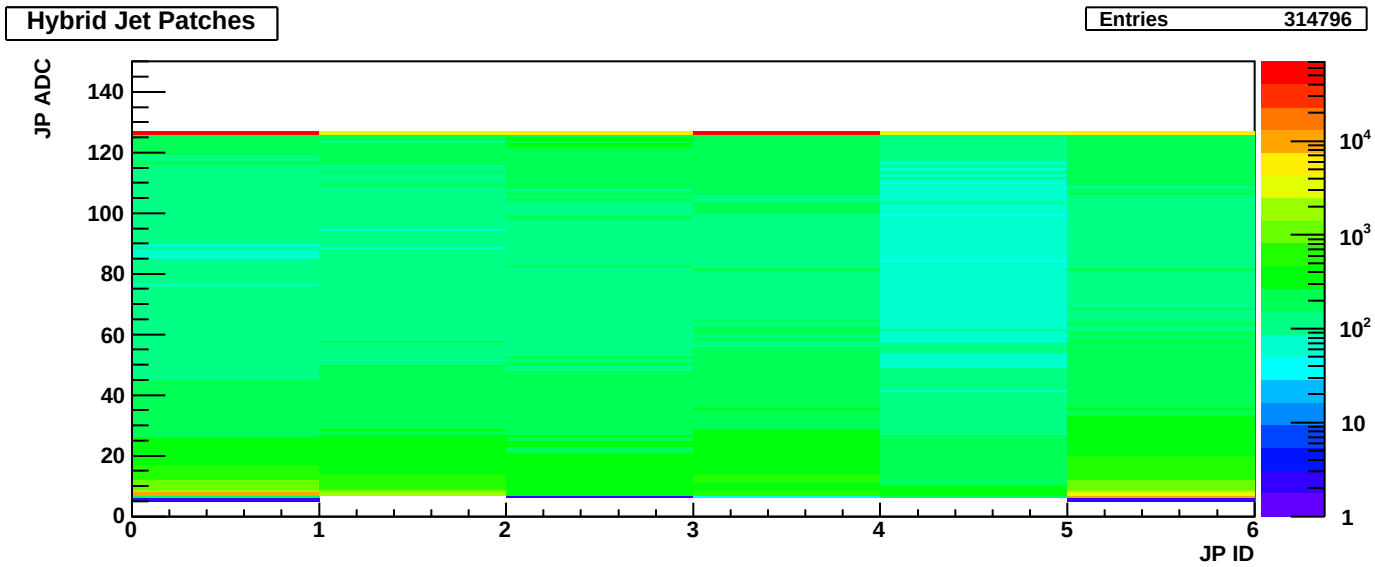

Barrel EMC L0 Input - Patch Sum
Entries 4.72194e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 1.416582e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

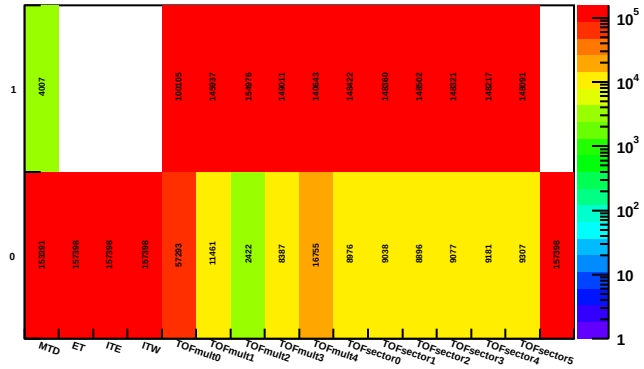

Endcap EMC L0 Input - Patch Sum
Entries 1.416582e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch


Barrel Jet Patches
Entries 2833164
JP ADC
JP ID

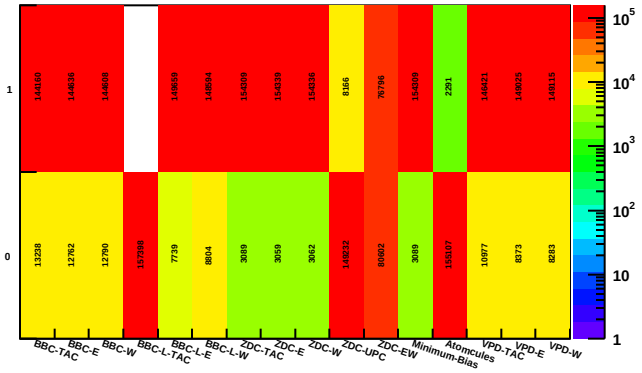

Endcap Jet Patches
Entries 944388
JP ADC
JP ID


Hybrid Jet Patches
Entries 314796
JP ADC
JP ID

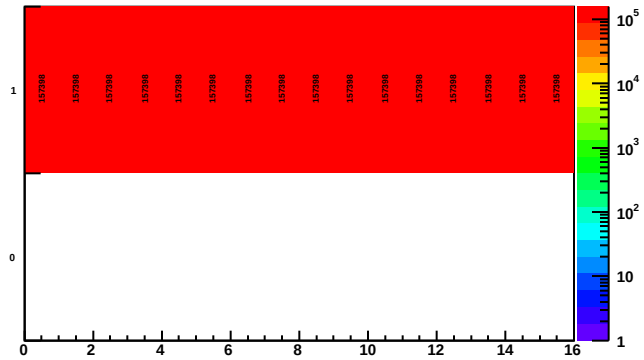
TF201 0-15 (ch0) Entries 2518368



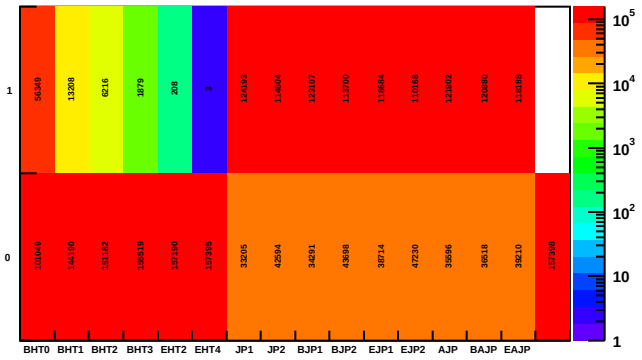
VT201 0-15 (ch1) Entries 2518368



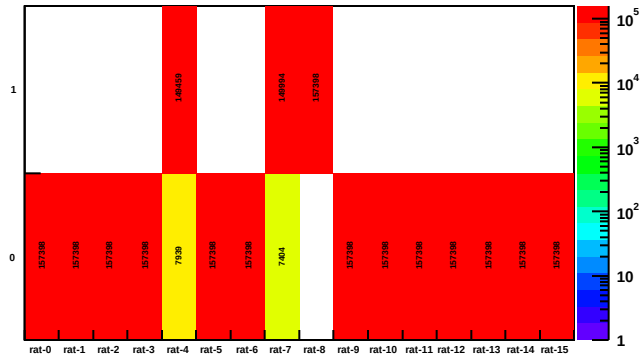
Unused (ch2) Entries 2518368



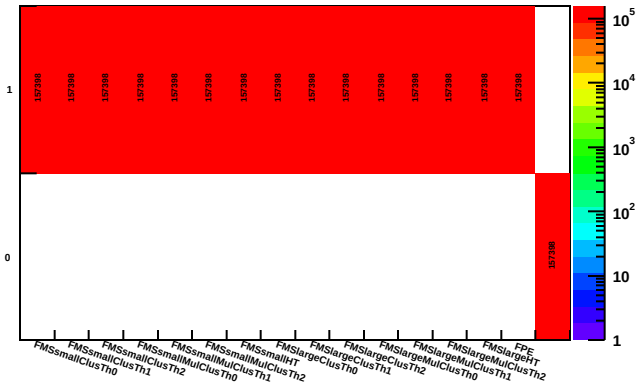
EM201 0-15 (ch3) Entries 2518368



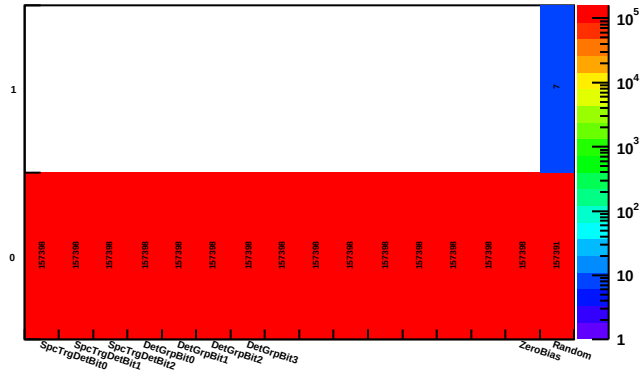
RAT board (ch4) Entries 2518368



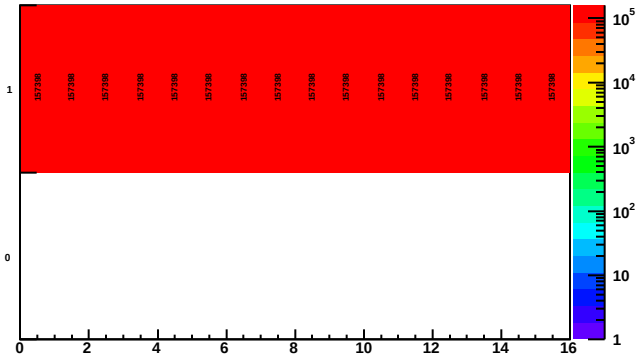
FP201 0-15 (ch5) Entries 2518368

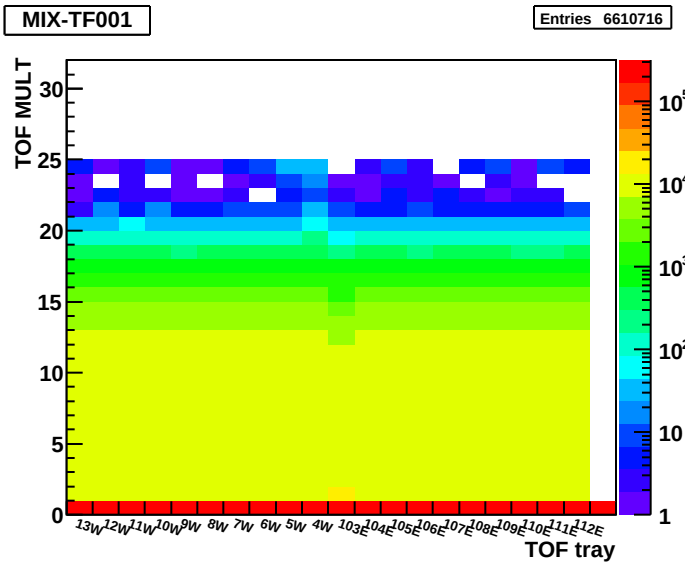


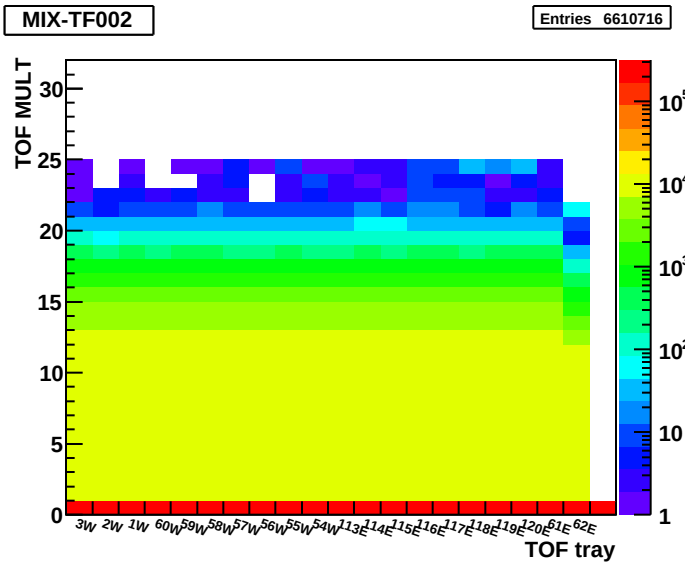
ST201 0-15 (ch6) Entries 2518368

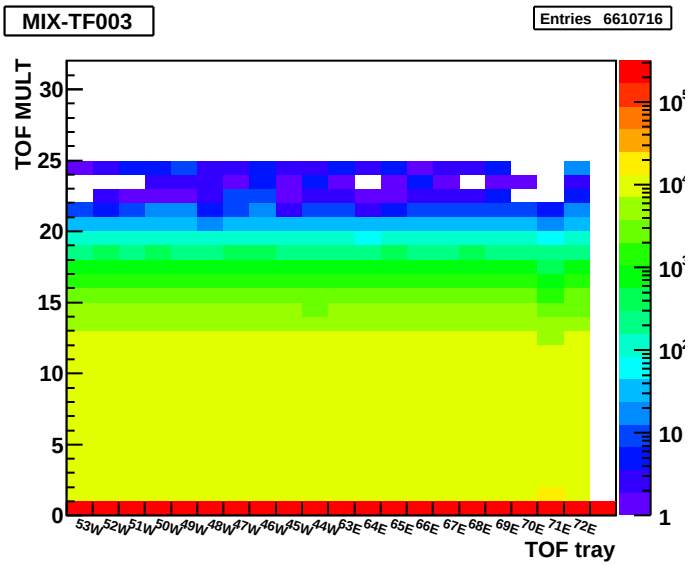


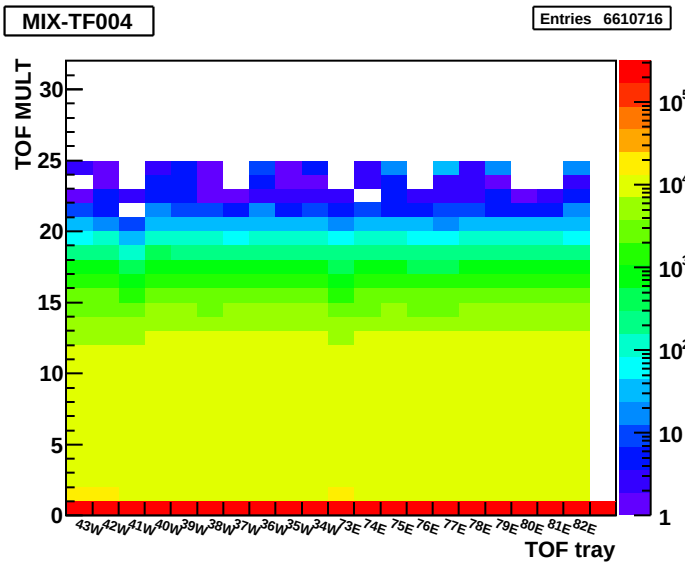
Unused (ch7) Entries 2518368

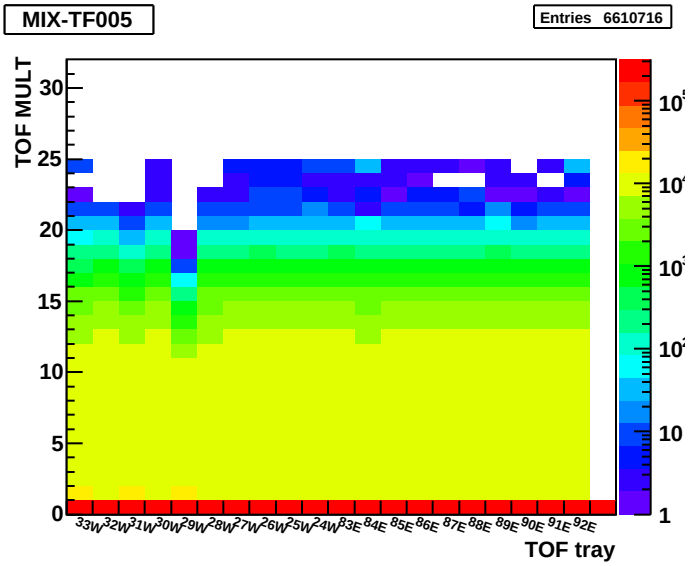


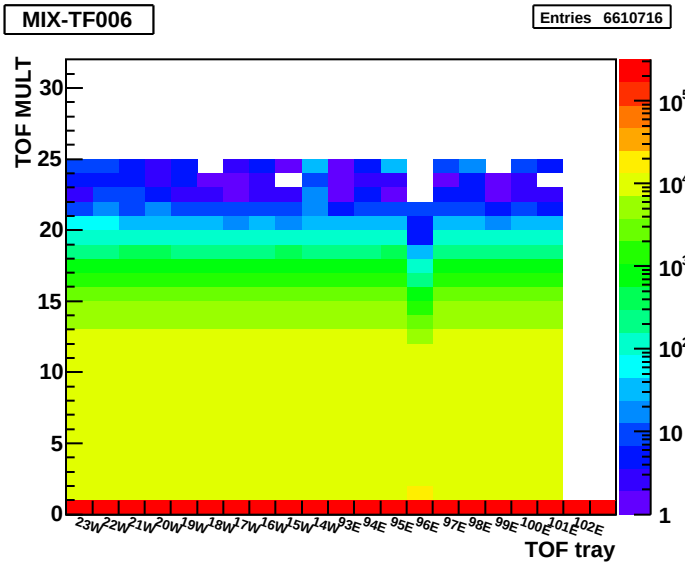

MIX-TF001
Entries 6610716
TOF MULT
TOF tray

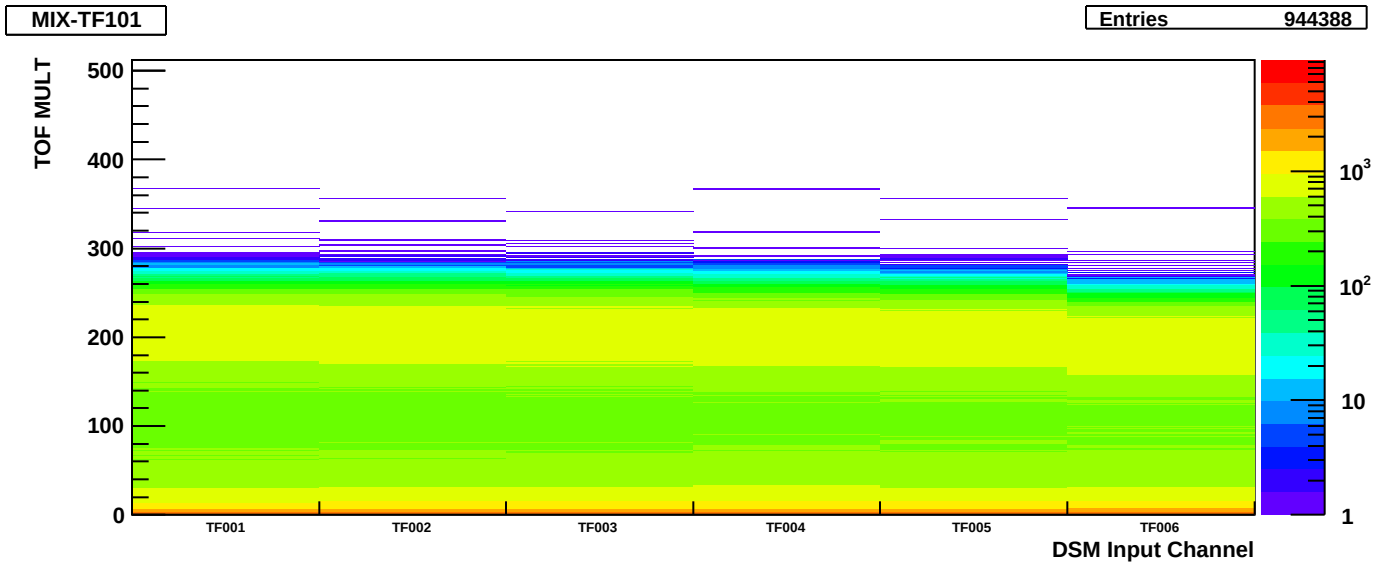

MIX-TF002
Entries 6610716
TOF MULT
TOF tray

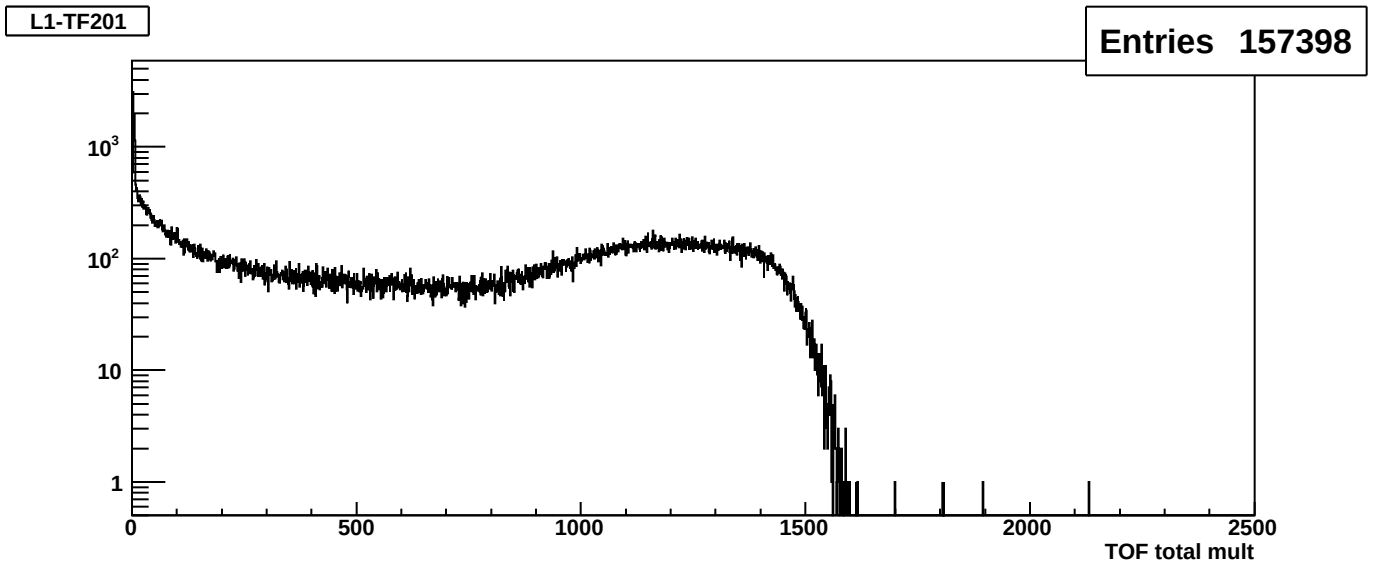

MIX-TF003
Entries 6610716
TOF MULT
TOF tray

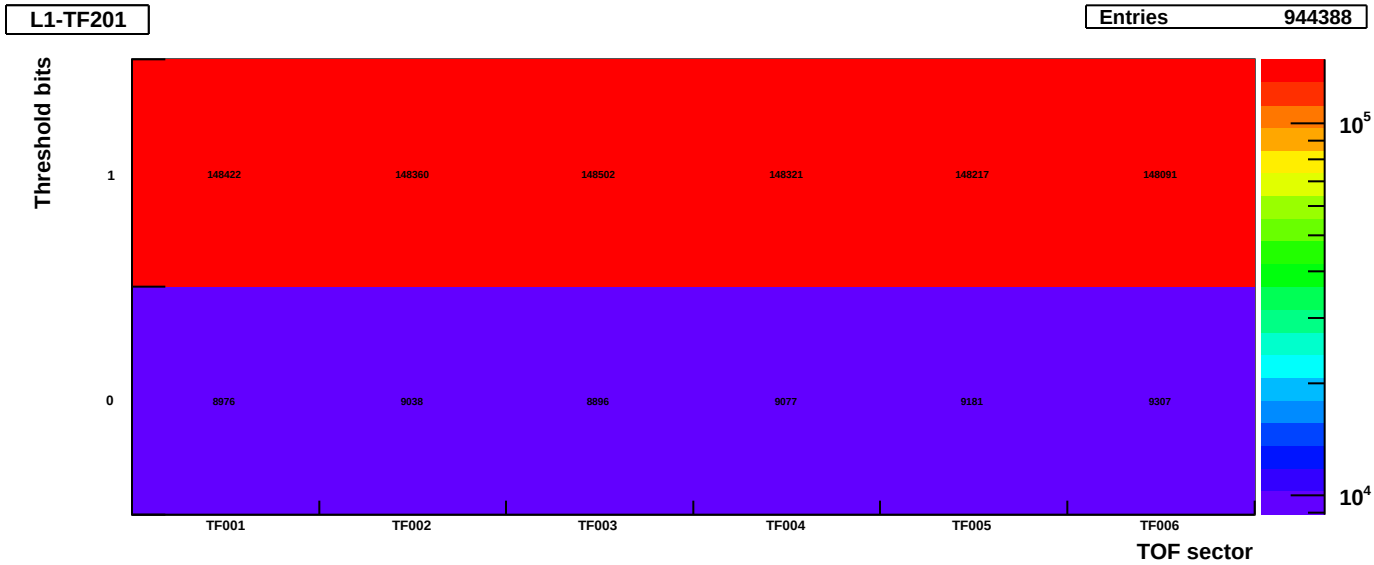

MIX-TF004
Entries 6610716
TOF MULT
TOF tray

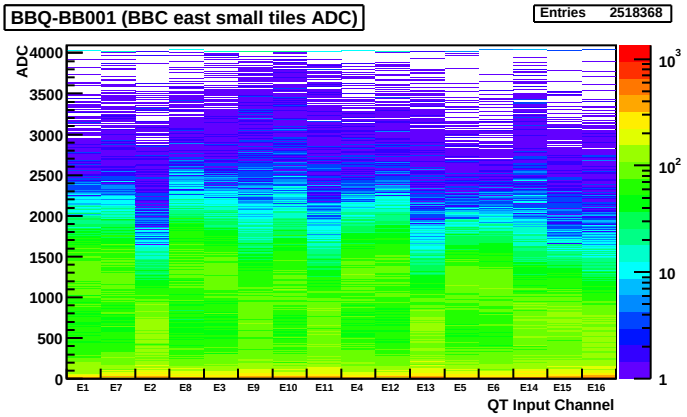

MIX-TF005
Entries 6610716
TOF MULT
TOF tray

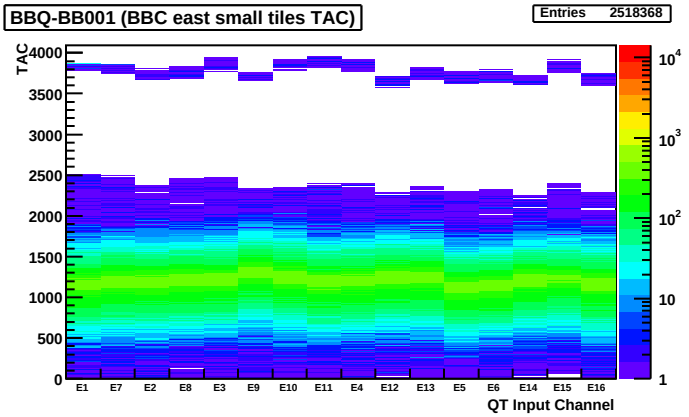

MIX-TF006
Entries 6610716
TOF MULT
TOF tray

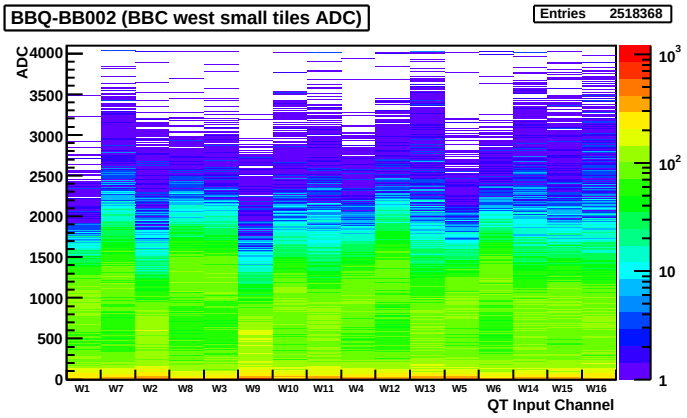

MIX-TF101
Entries 944388
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

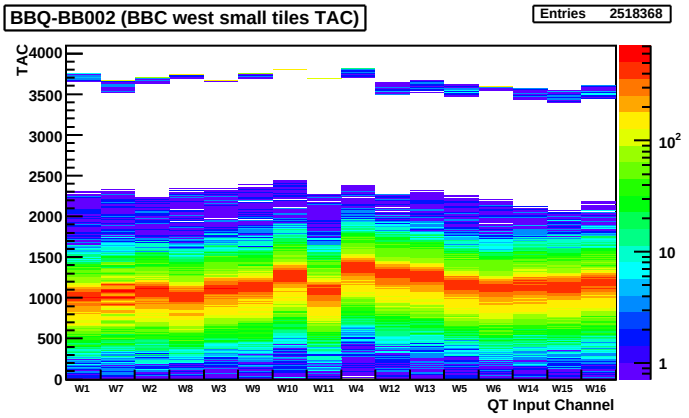

L1-TF201
Entries 157398
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

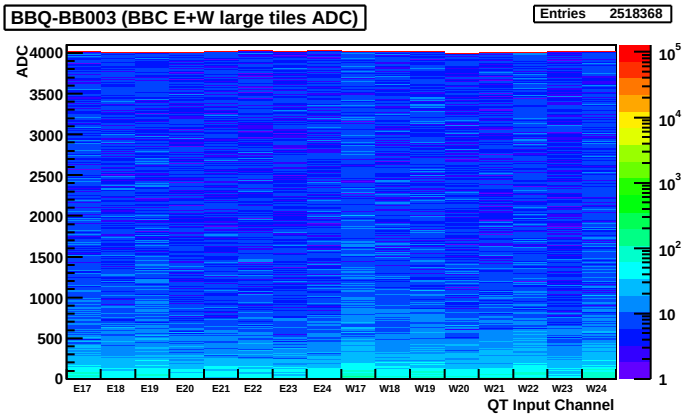

L1-TF201
Entries 944388
Threshold bits
1
0
148422
148360
148502
148321
148217
148091
8976
9038
8896
9077
9181
9307
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4

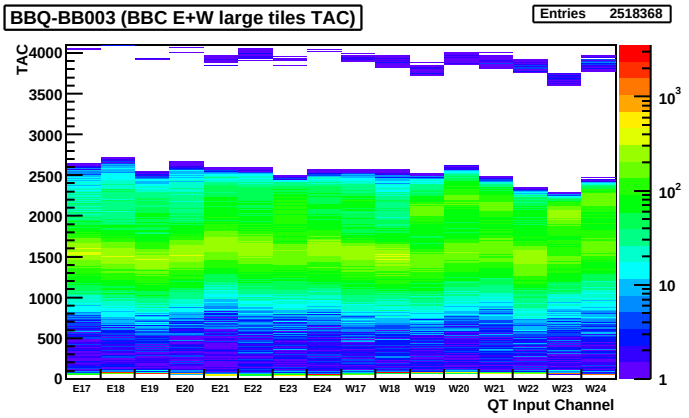

BBQ-BB001 (BBC east small tiles ADC)
Entries 2518368
ADC
QT Input Channel

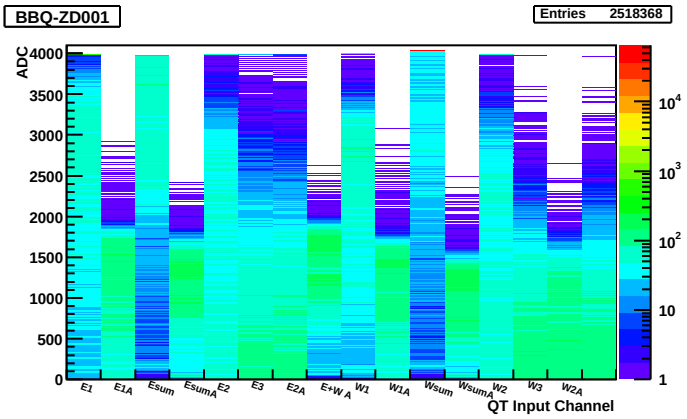

BBQ-BB001 (BBC east small tiles TAC)
Entries 2518368
TAC
QT Input Channel

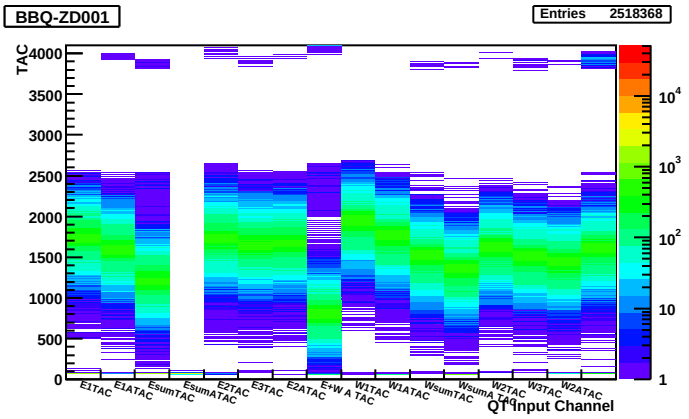

BBQ-BB002 (BBC west small tiles ADC)
Entries 2518368
ADC
QT Input Channel

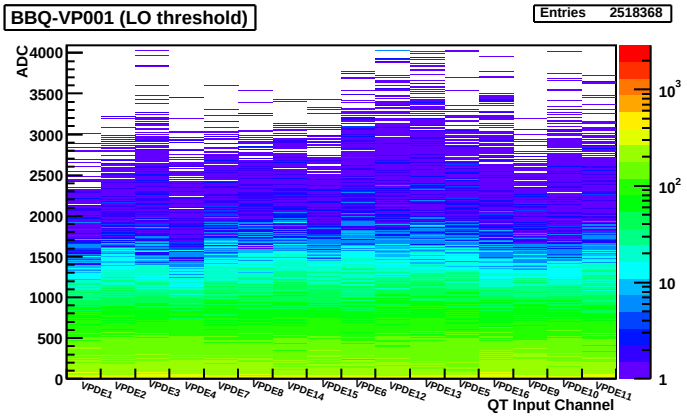

BBQ-BB002 (BBC west small tiles TAC)
Entries 2518368
TAC
QT Input Channel

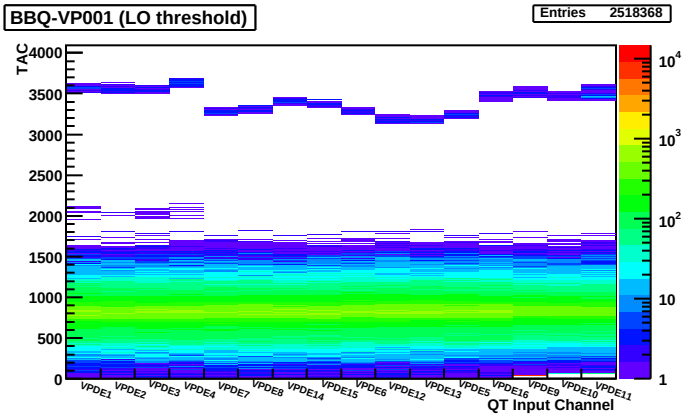

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2518368
ADC
QT Input Channel

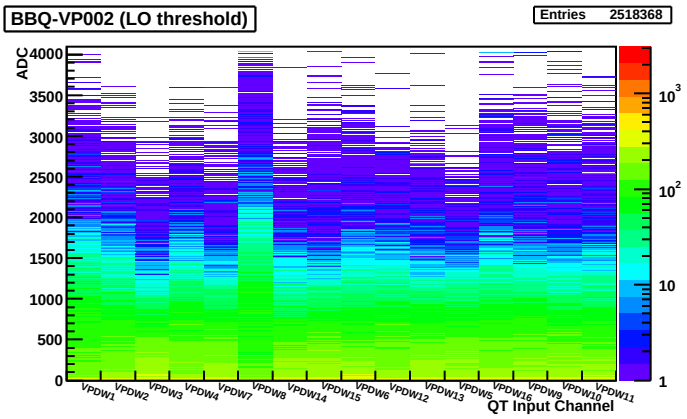

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2518368
TAC
QT Input Channel

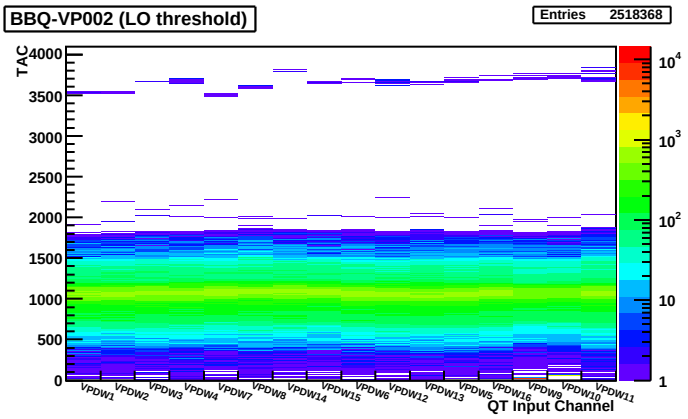

BBQ-ZD001
Entries 2518368
ADC
QT Input Channel

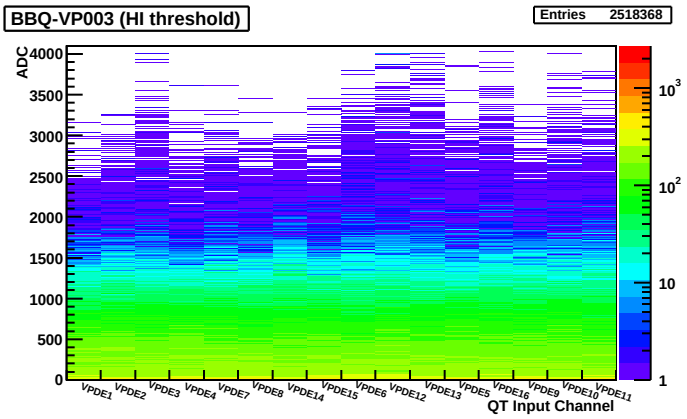

BBQ-ZD001
Entries 2518368
TAC
QT Input Channel

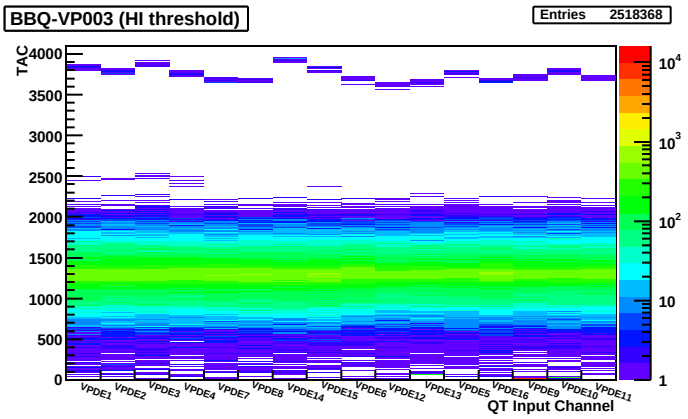

BBQ-VP001 (LO threshold)
Entries 2518368
ADC
QT Input Channel

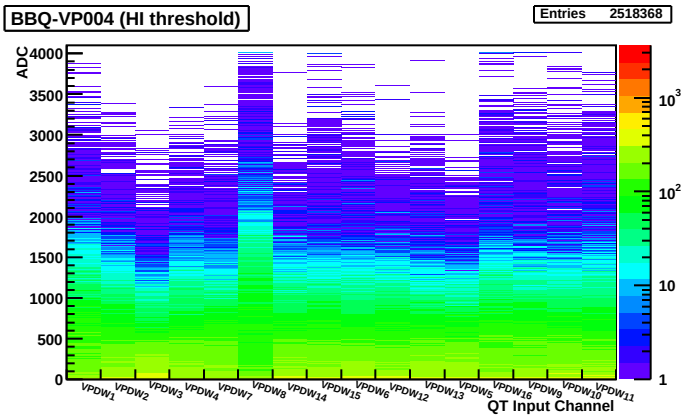

BBQ-VP001 (LO threshold)
Entries 2518368
TAC
QT Input Channel

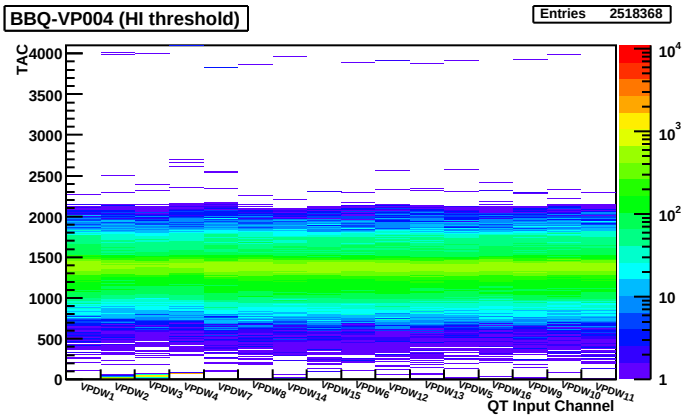

BBQ-VP002 (LO threshold)
Entries 2518368
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 2518368
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 2518368
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 2518368
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 2518368
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 2518368
TAC
QT Input Channel