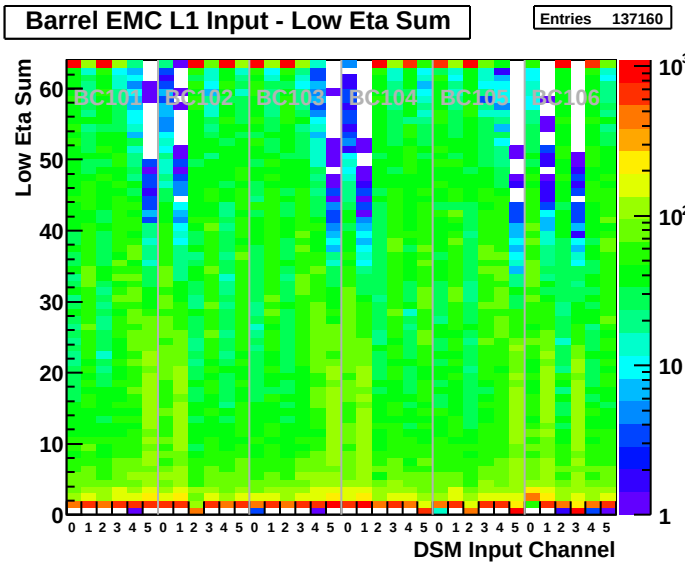
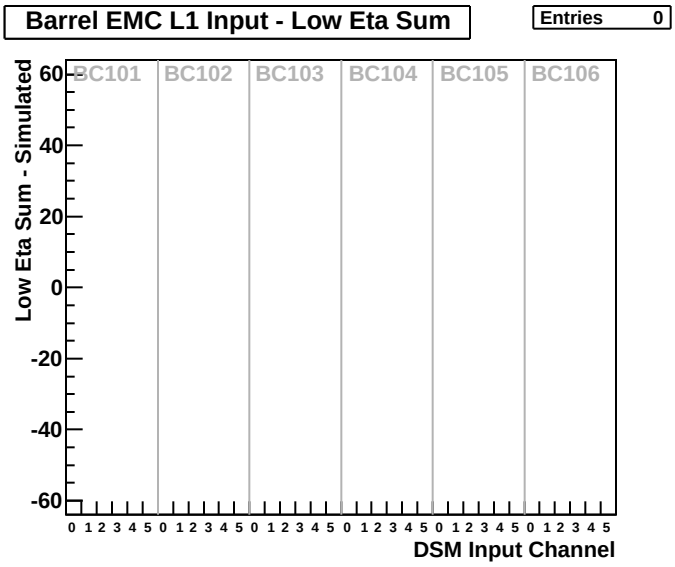
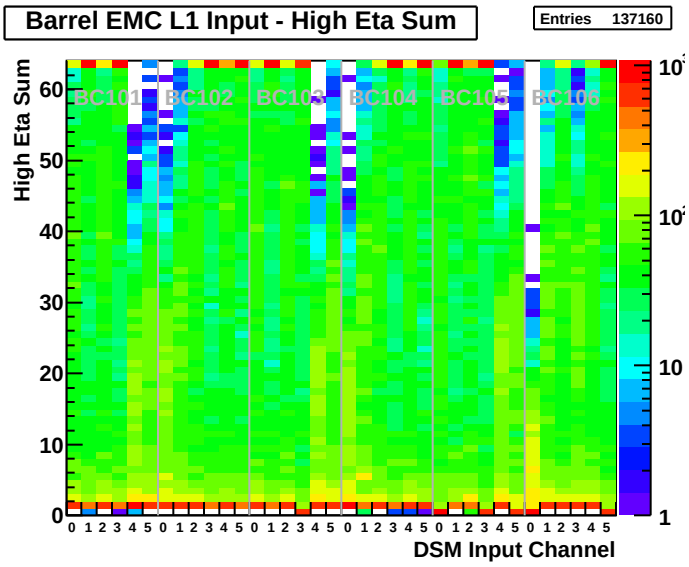
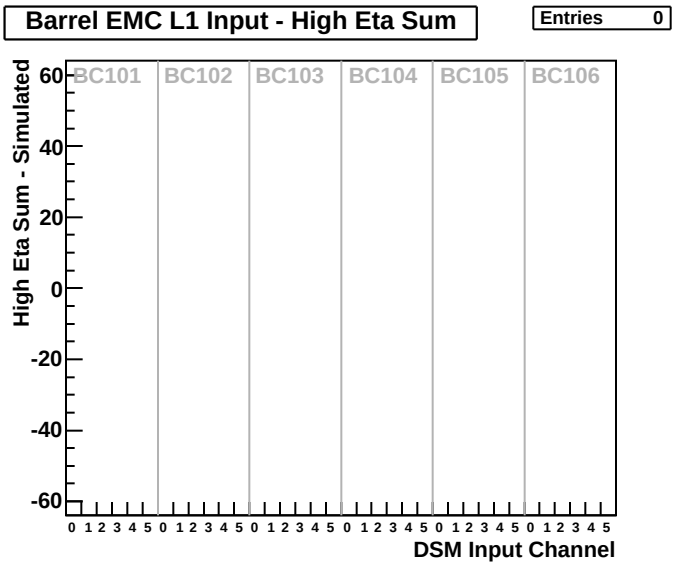
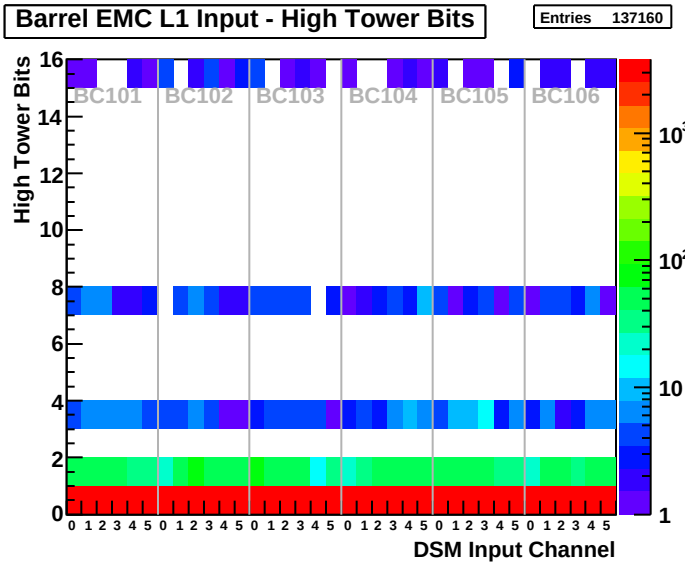
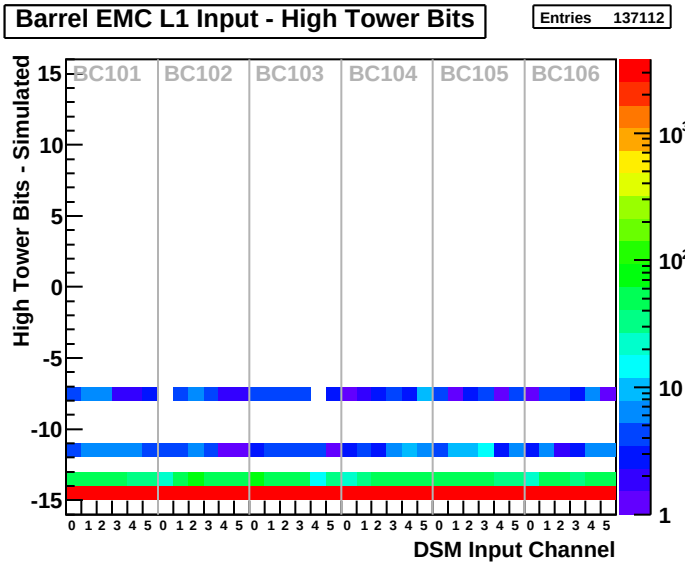

Barrel EMC L1 Input - Low Eta Sum
Entries 137160
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

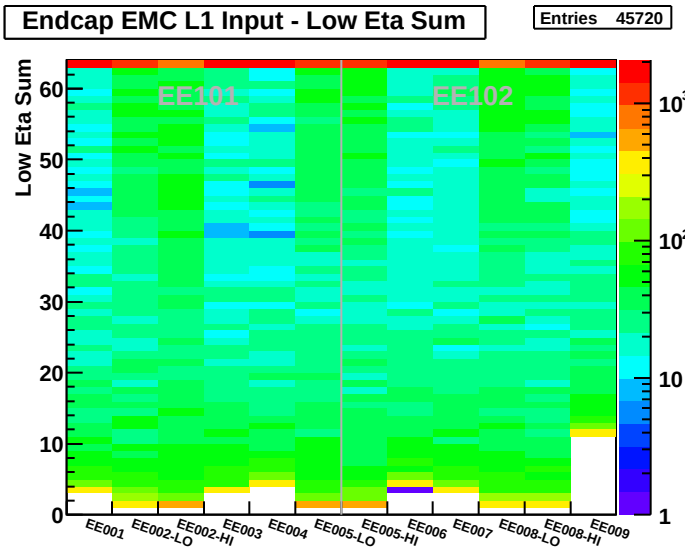

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

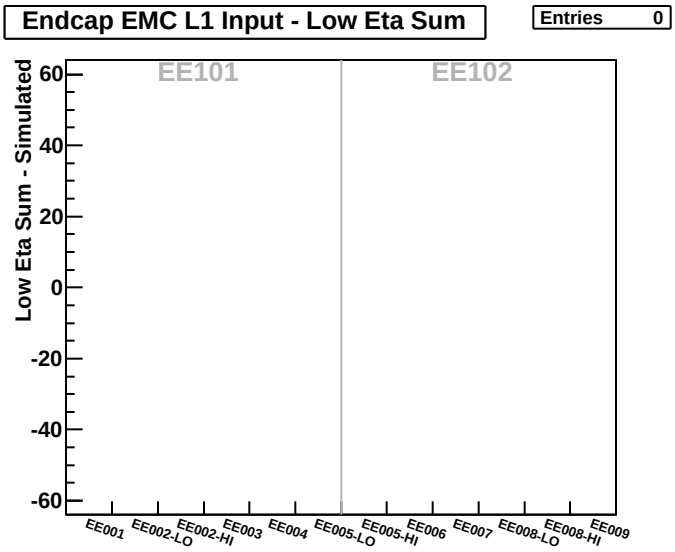

Barrel EMC L1 Input - High Eta Sum
Entries 137160
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

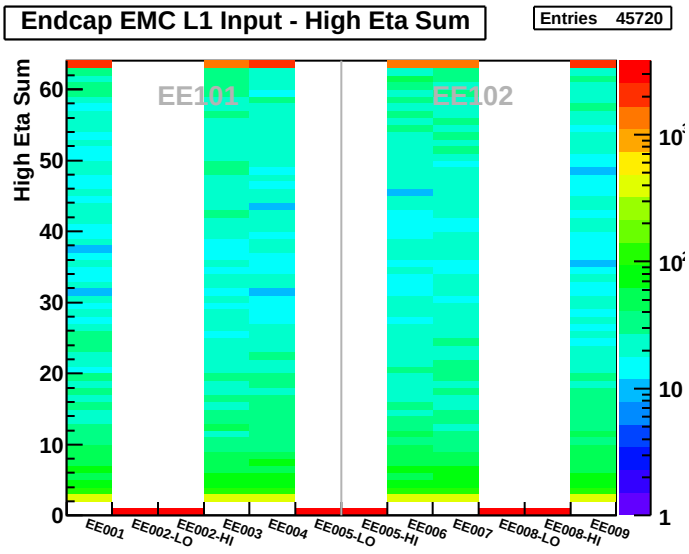

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

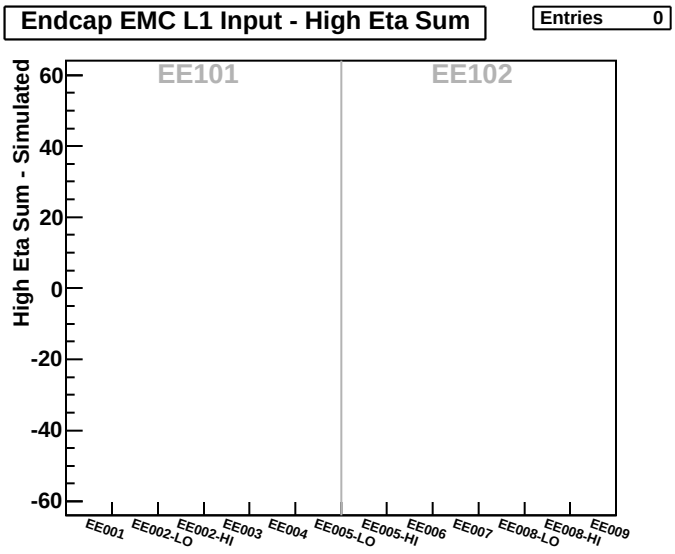

Barrel EMC L1 Input - High Tower Bits
Entries 137160
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

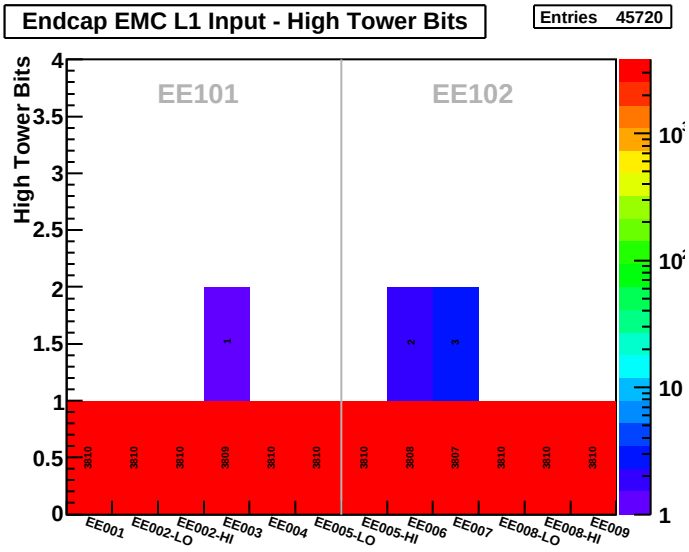

Barrel EMC L1 Input - High Tower Bits
Entries 137112
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

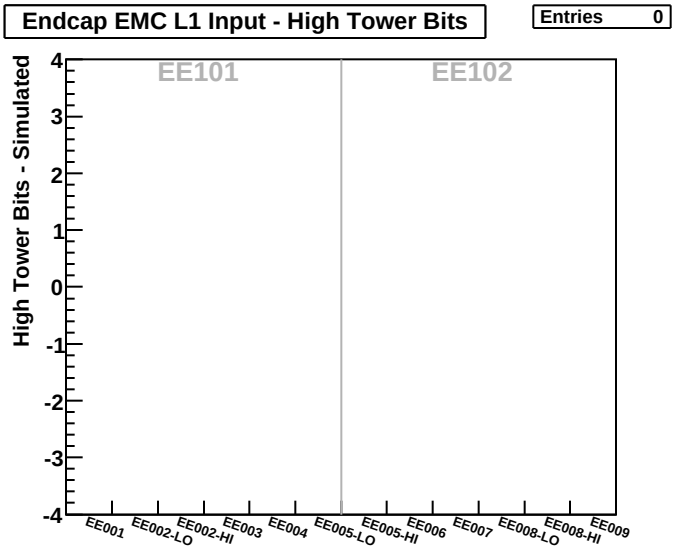

Endcap EMC L1 Input - Low Eta Sum
Entries 45720
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

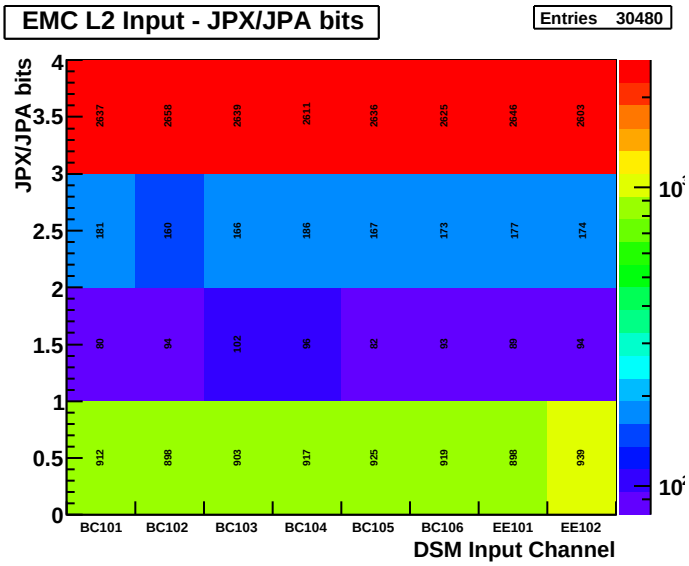

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

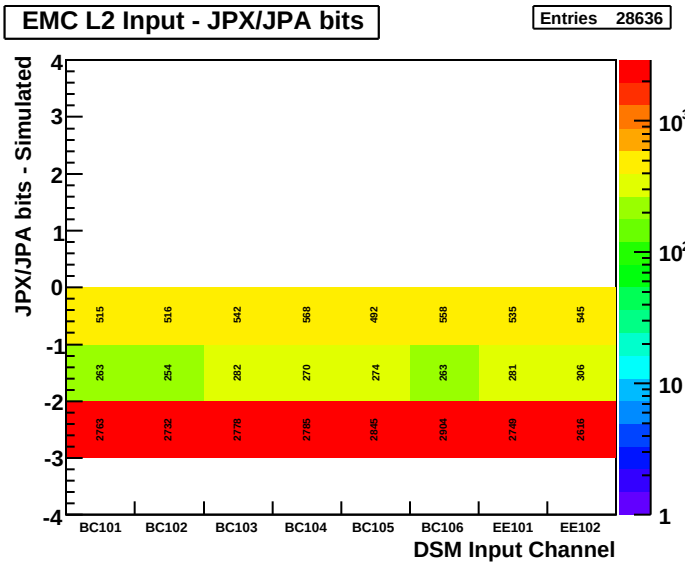

Endcap EMC L1 Input - High Eta Sum
Entries 45720
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

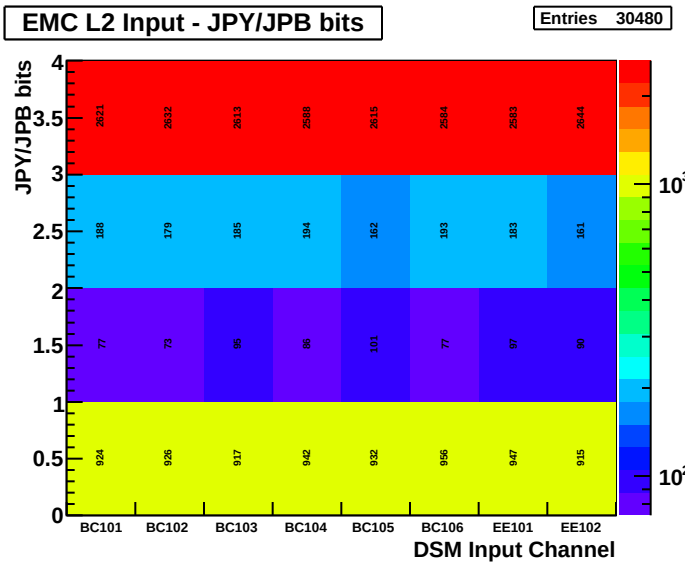

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

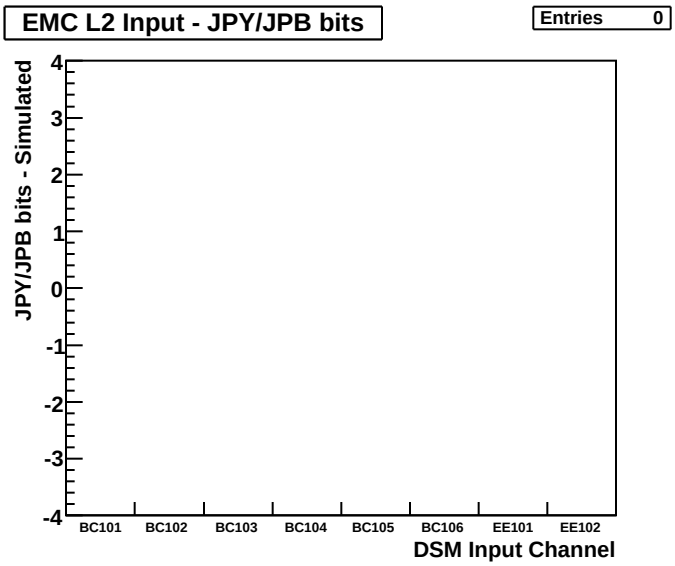

Endcap EMC L1 Input - High Tower Bits
Entries 45720
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

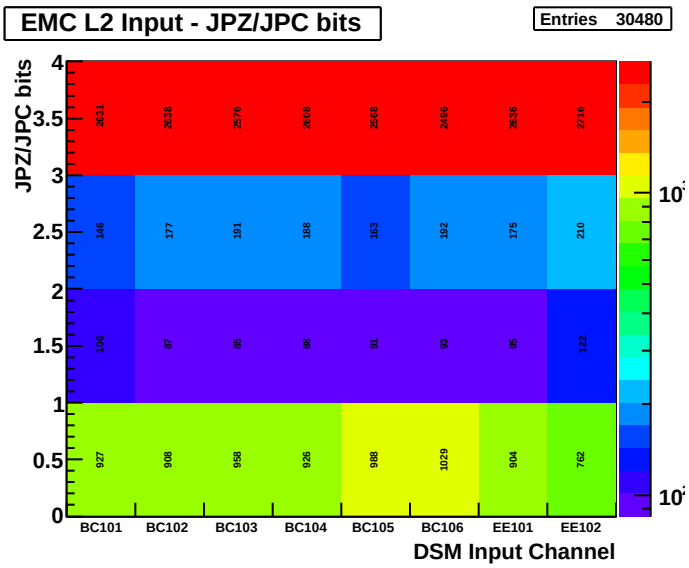

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

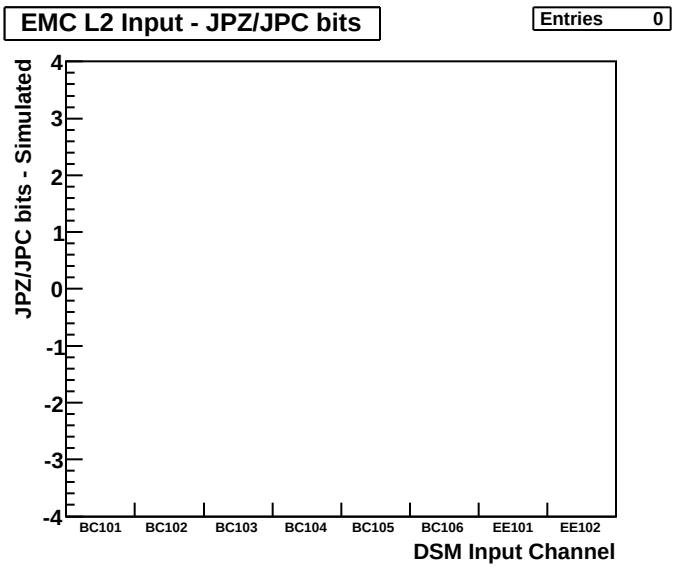

EMC L2 Input - JPX/JPA bits
Entries 30480
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2637 2658 2639 2611 2636 2625 2646 2603
181 160 166 186 167 173 177 174
80 94 102 95 82 93 89 94
912 898 903 917 925 919 898 939

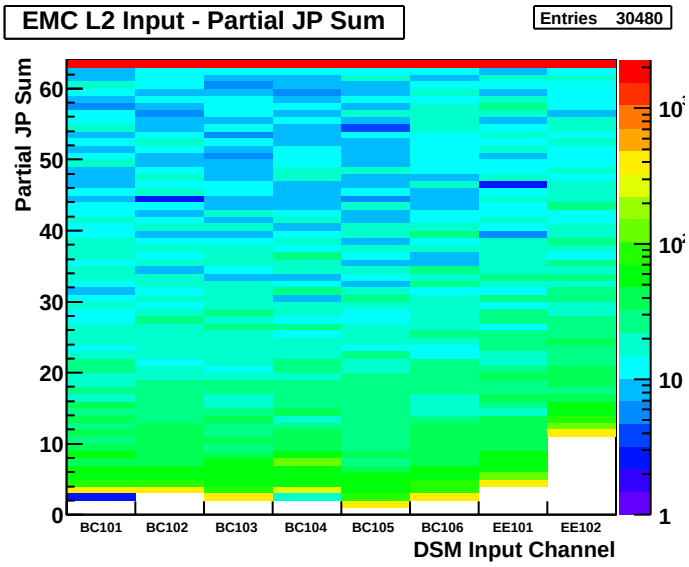

EMC L2 Input - JPX/JPA bits
Entries 28636
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
515 516 542 568 492 558 535 545
263 254 282 270 274 263 281 306
2763 2732 2778 2785 2845 2904 2749 2616

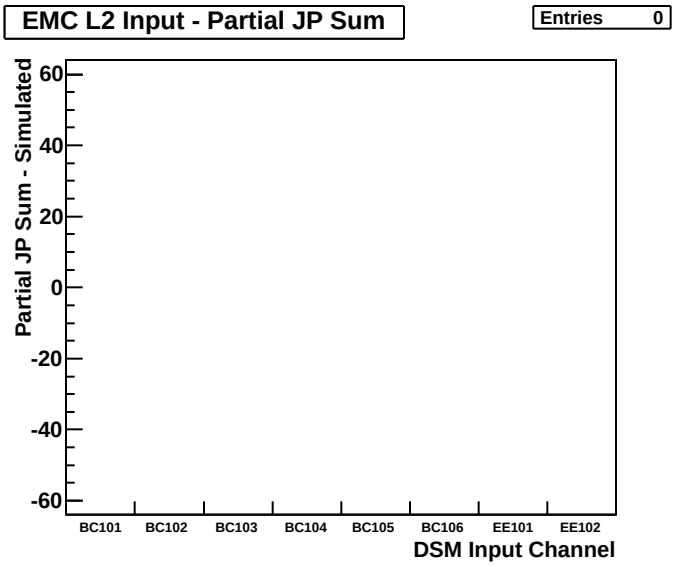

EMC L2 Input - JPY/JPB bits
Entries 30480
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2621 2632 2613 2588 2615 2584 2583 2644
188 179 185 194 162 193 183 161
77 73 95 86 101 77 97 90
924 926 917 942 932 956 947 915

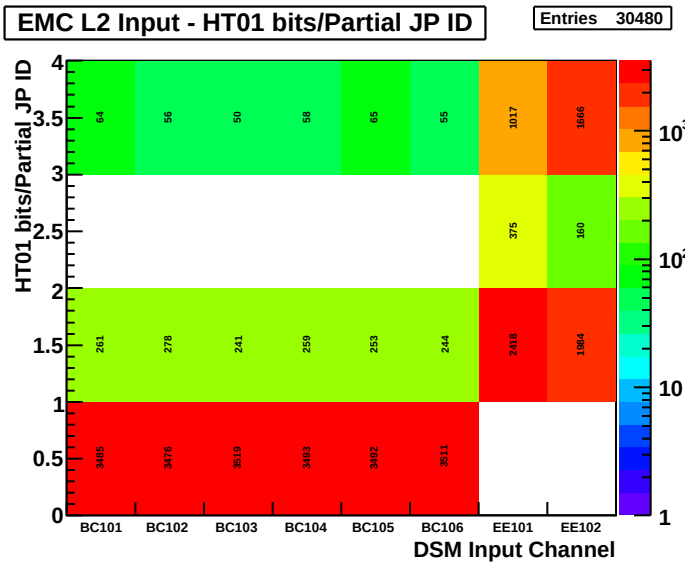

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

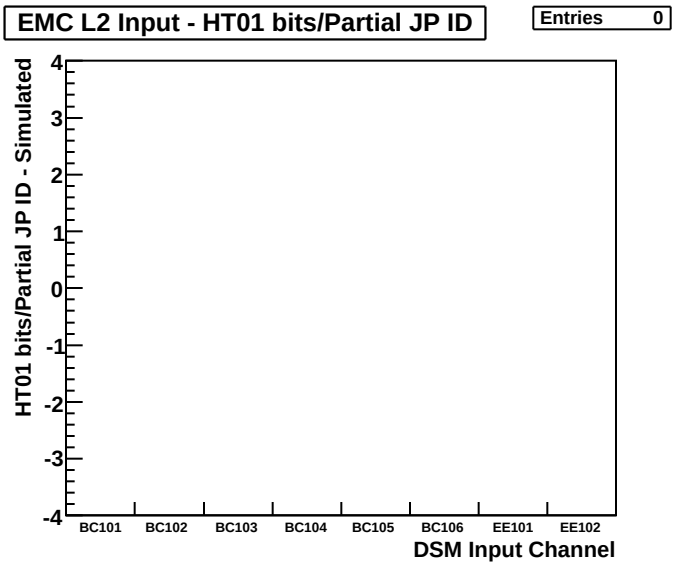

EMC L2 Input - JPZ/JPC bits
Entries 30480
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2631 2638 2576 2608 2568 2496 2636 2716
146 177 191 188 163 192 175 210
106 87 85 88 91 93 95 122
927 908 958 926 988 1029 904 762

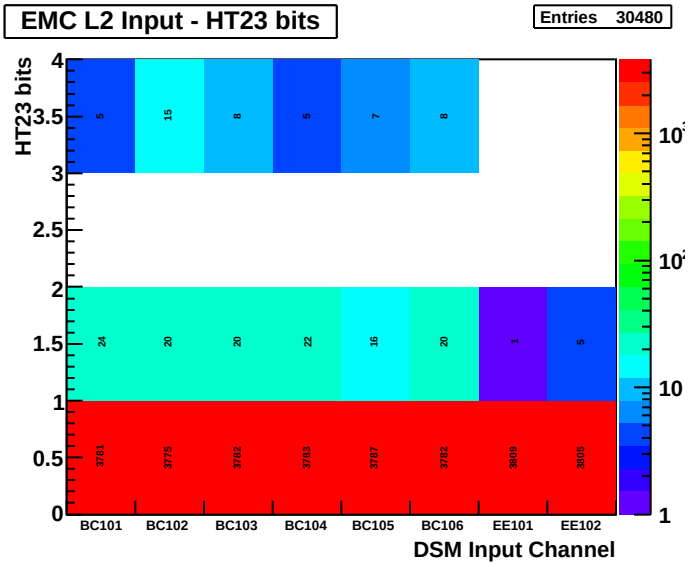

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

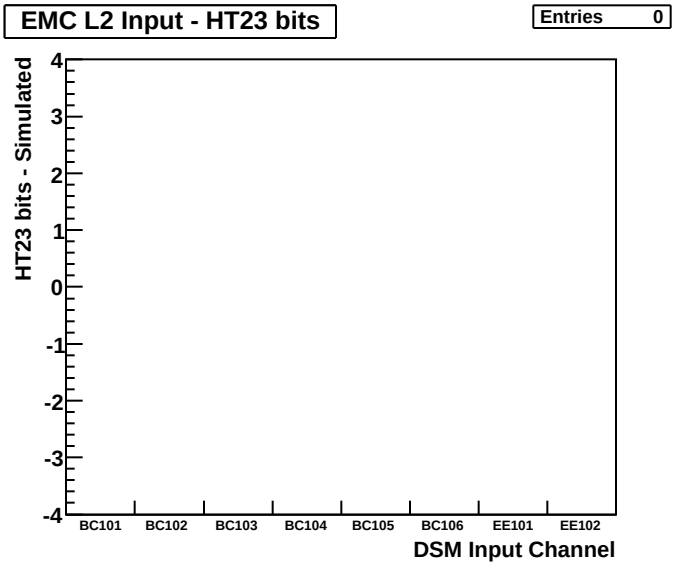

EMC L2 Input - Partial JP Sum
Entries 30480
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

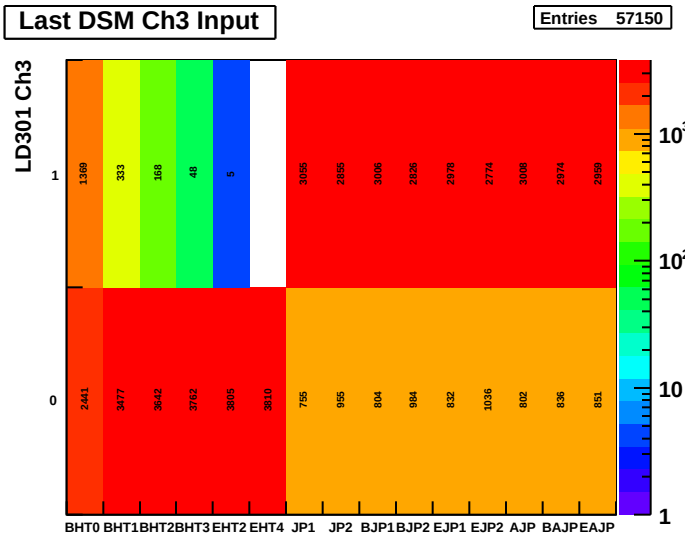

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

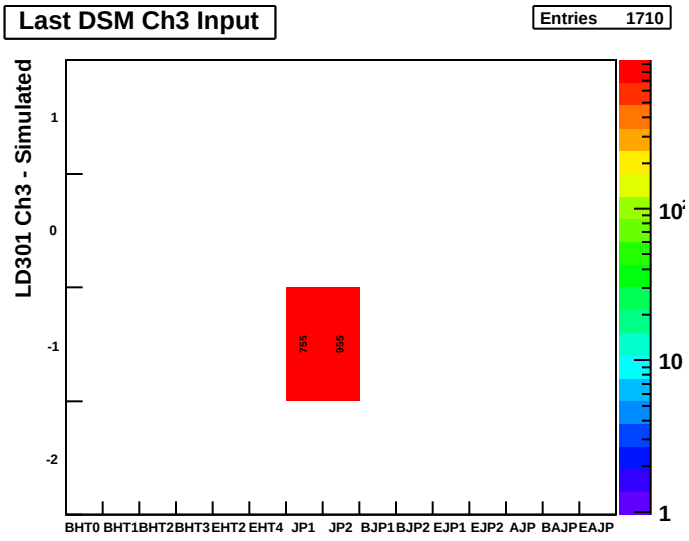

EMC L2 Input - HT01 bits/Partial JP ID
Entries 30480
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

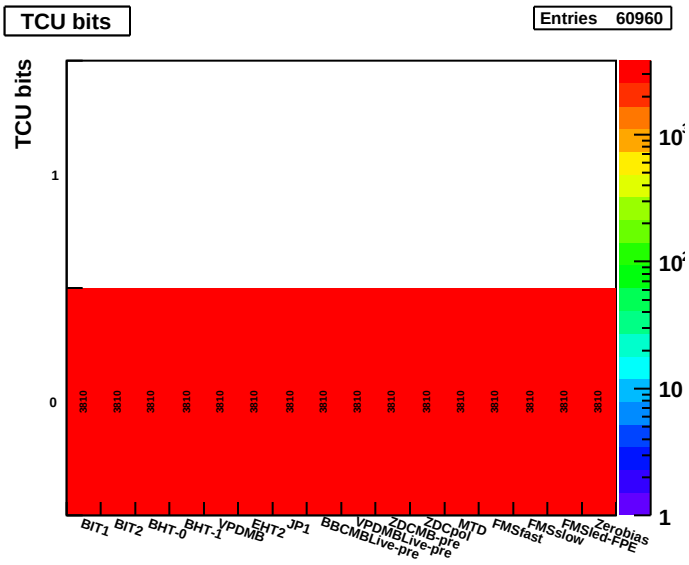

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

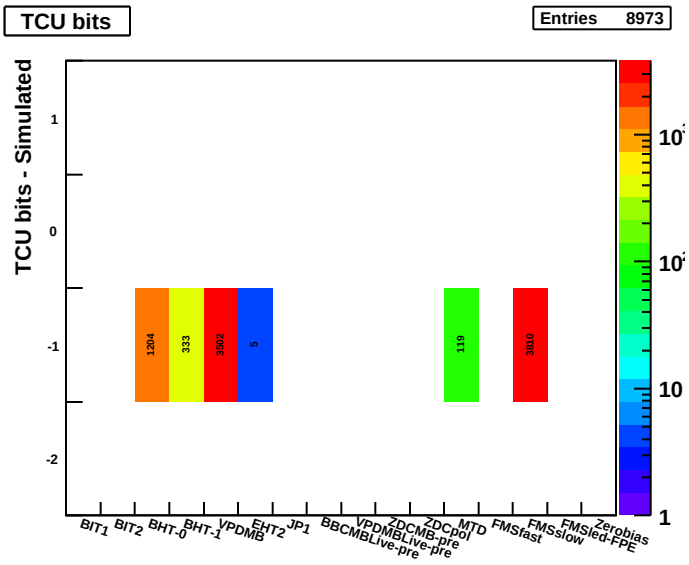

EMC L2 Input - HT23 bits
Entries 30480
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

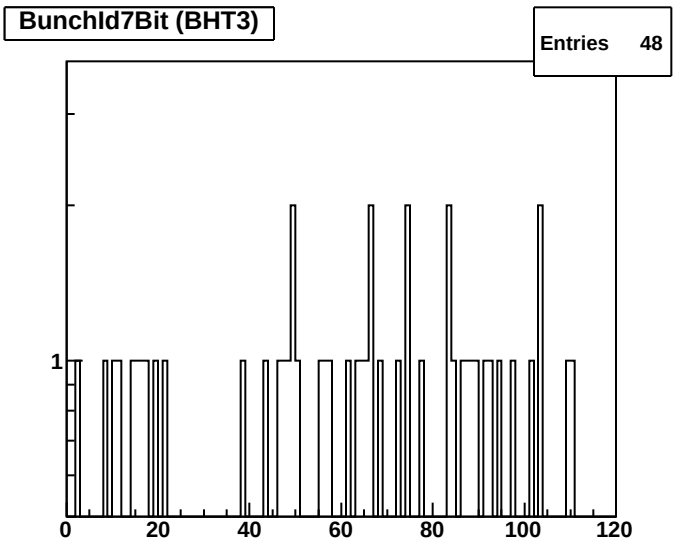

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

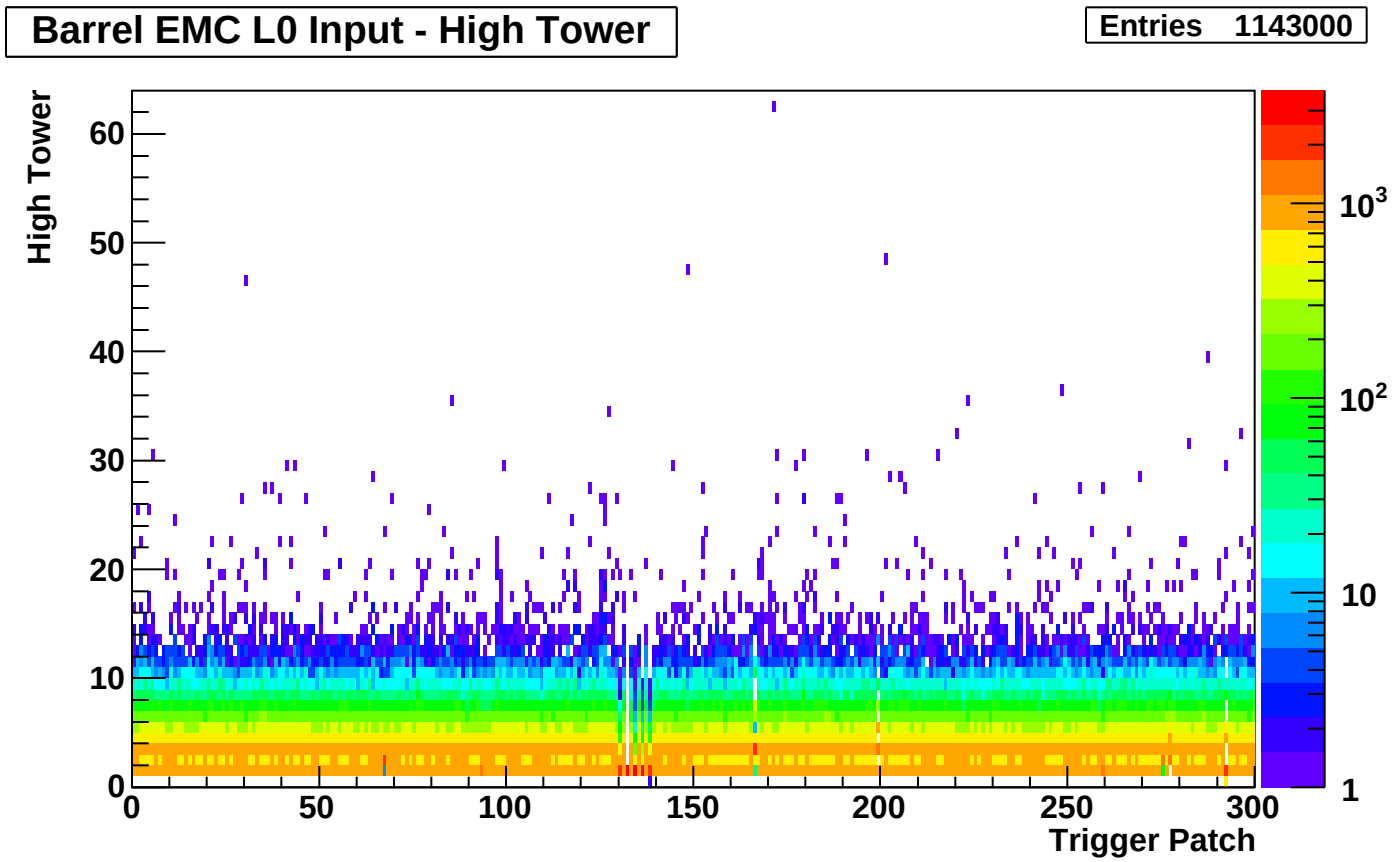

Last DSM Ch3 Input
Entries 57150
LD301 Ch3
1
0
1369
333
168
48
5
3055
2855
3006
2826
2978
2774
3008
2974
2959
2441
3477
3642
3762
3805
3810
755
955
804
984
832
1036
802
836
851
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP
1

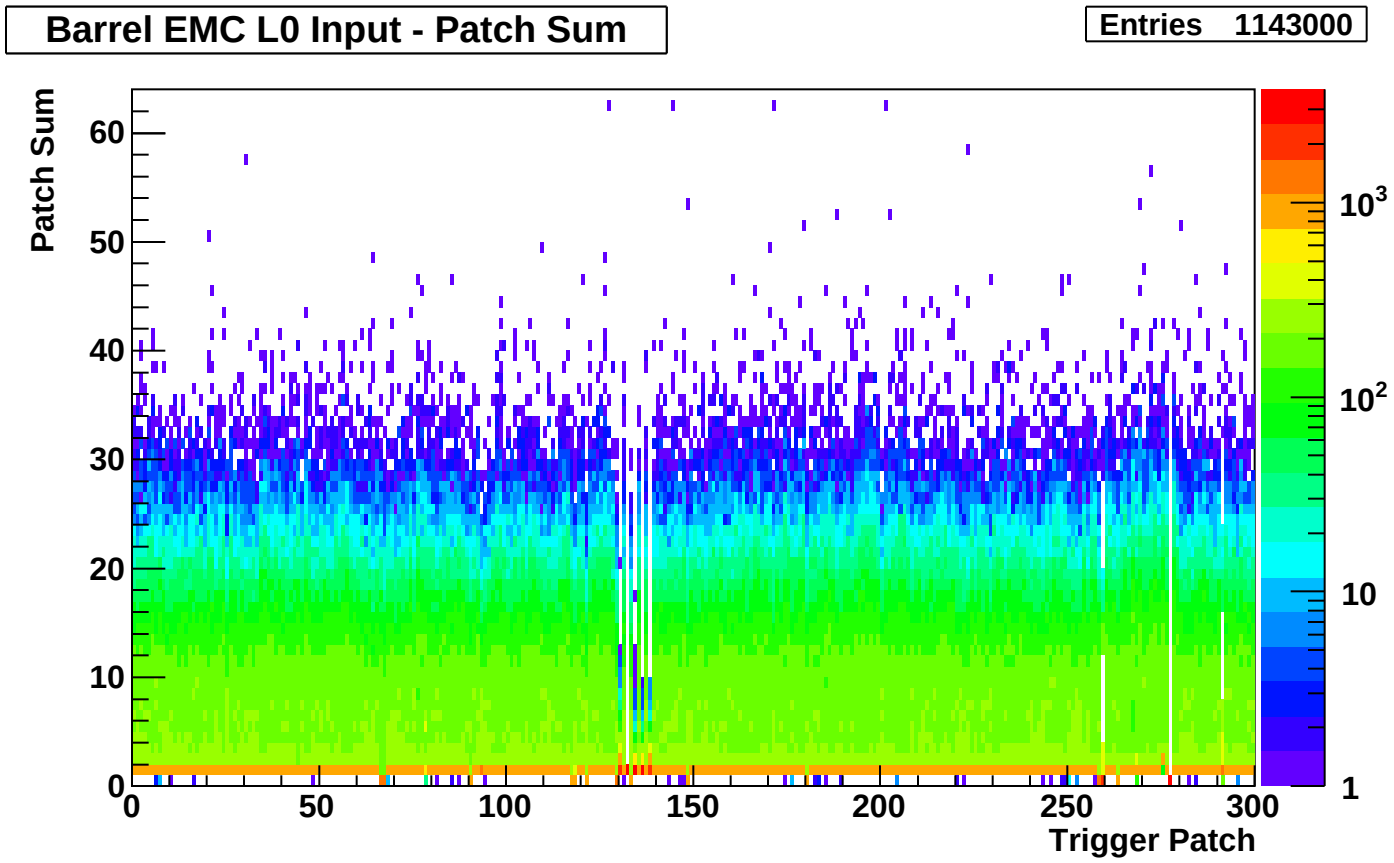

Last DSM Ch3 Input
Entries 1710
LD301 Ch3 - Simulated
1
0
-1
-2
755
955
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP
10
1

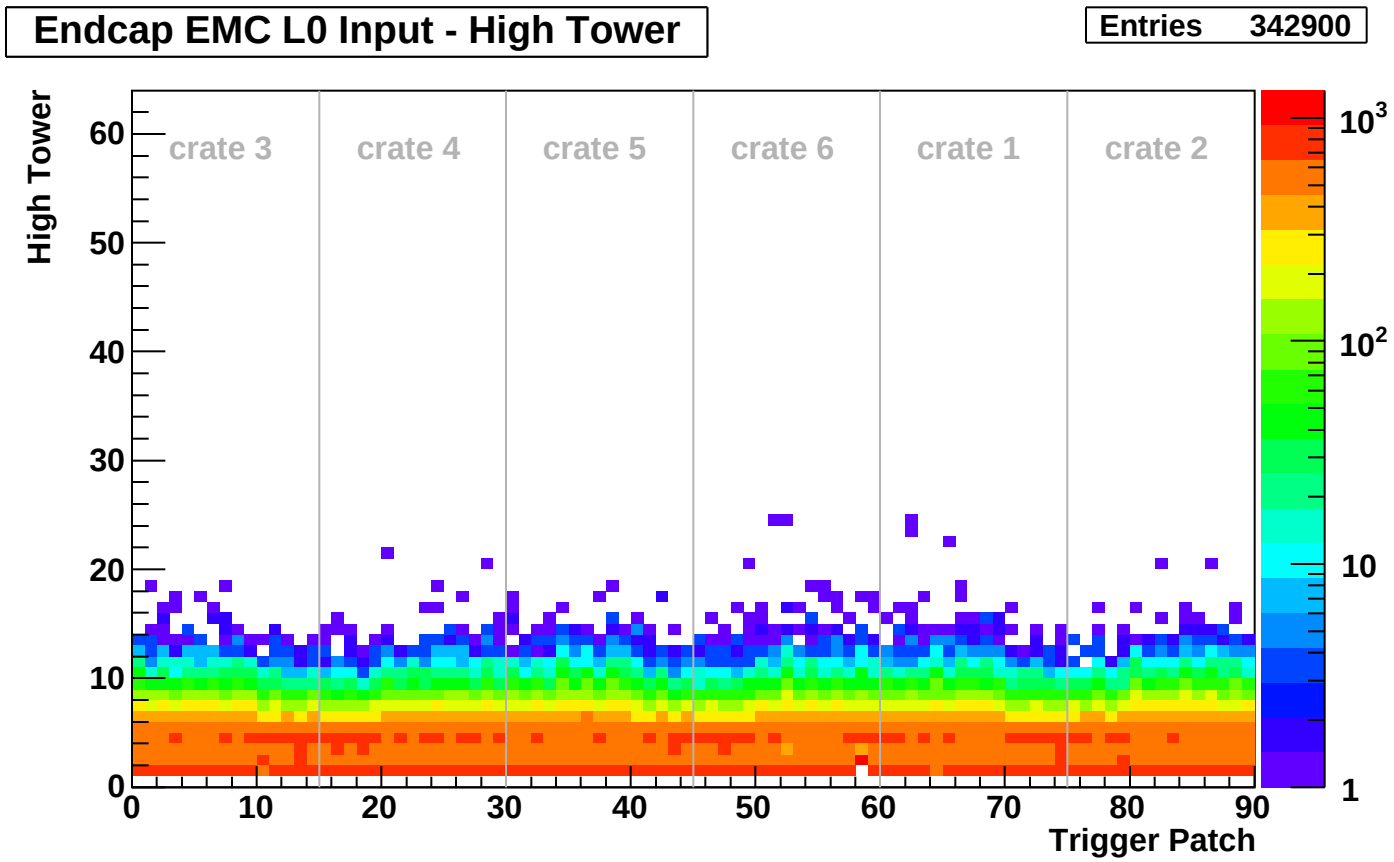

TCU bits
Entries 60960
TCU bits
1
0
3810
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
1

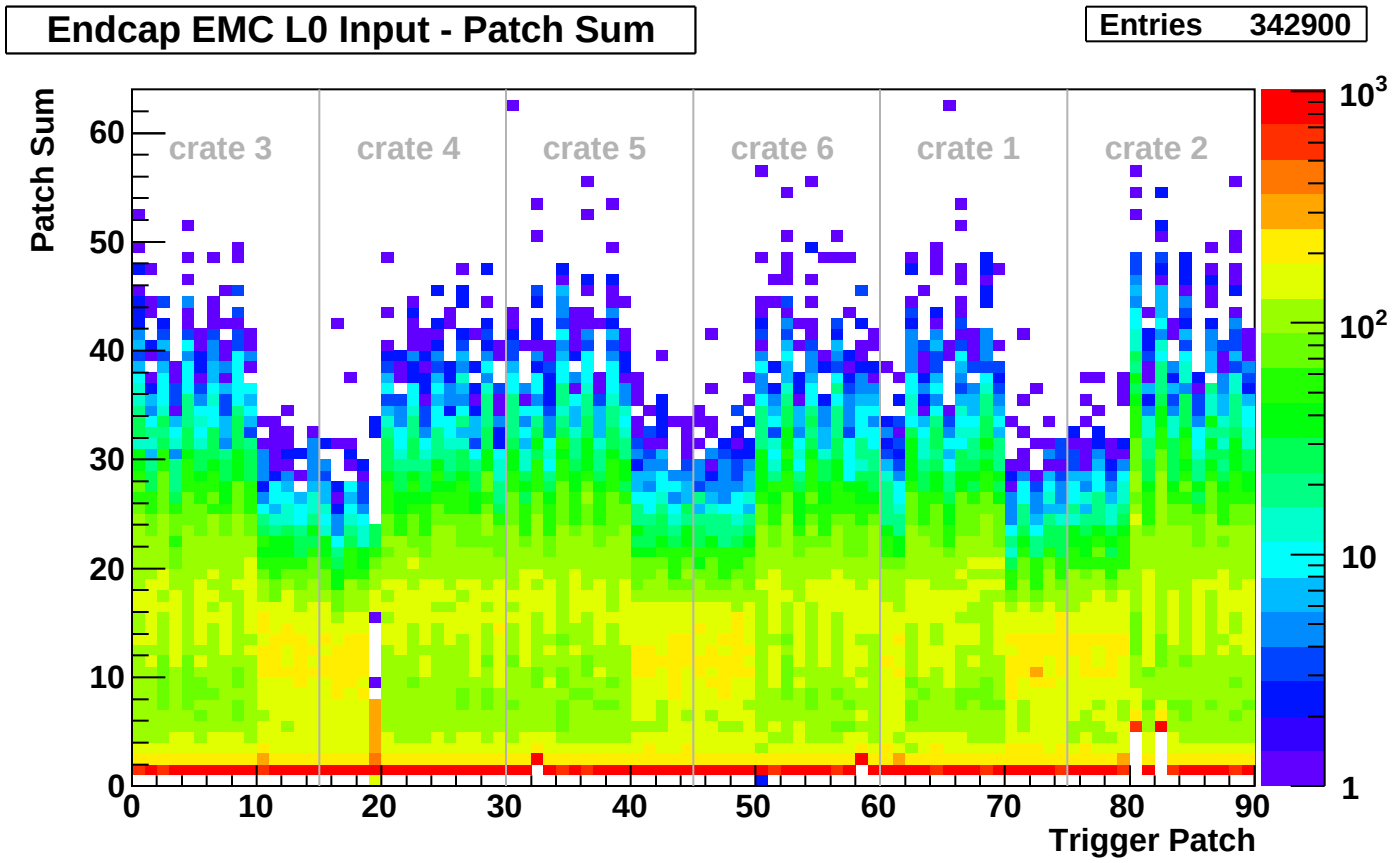

TCU bits
Entries 8973
TCU bits - Simulated
1
0
-1
-2
1204
333
3902
5
119
3810
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
10
1

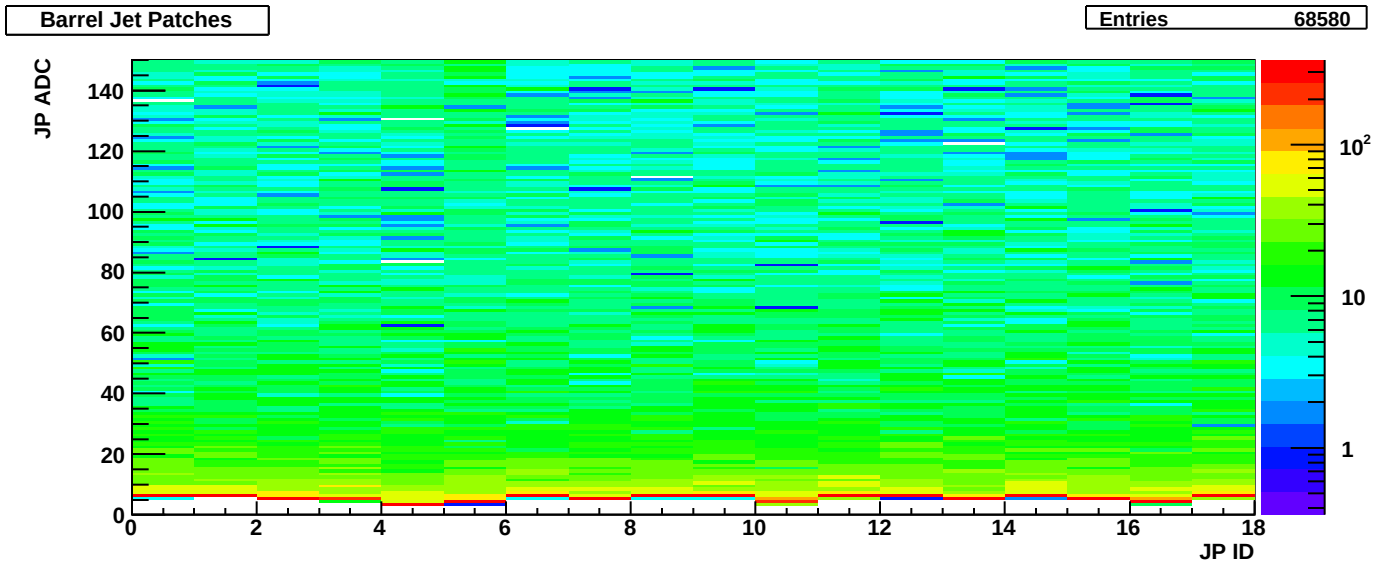

BunchId7Bit (BHT3)
Entries 48
1
0
20
40
60
80
100
120

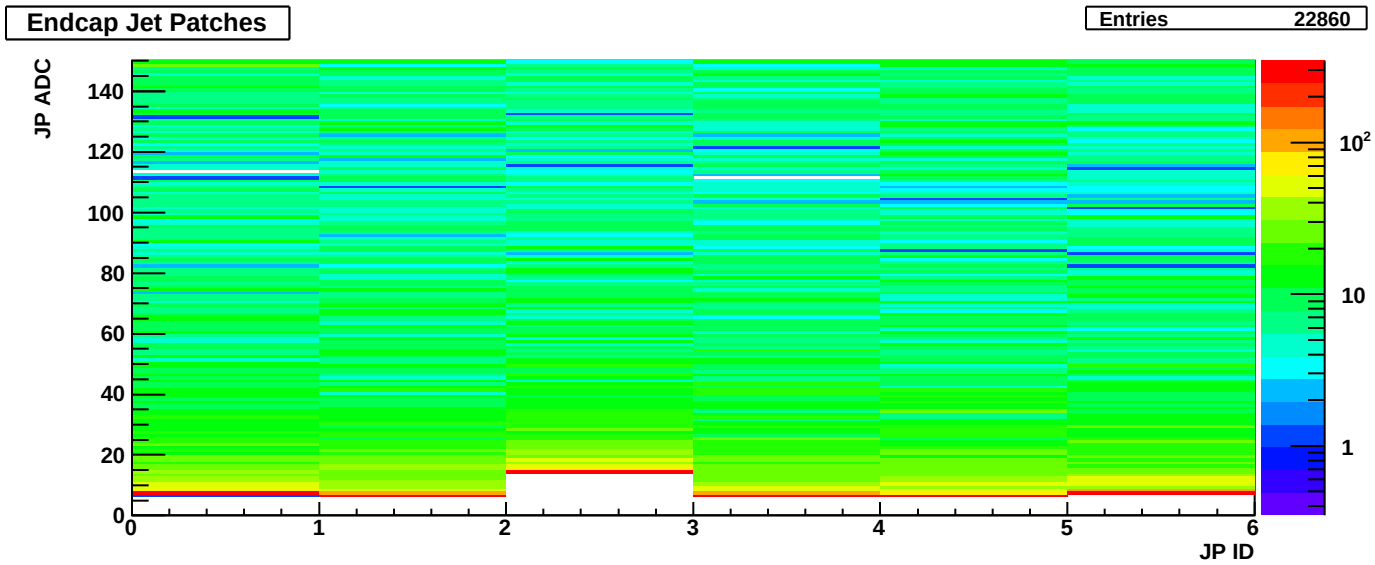

Barrel EMC L0 Input - High Tower
Entries 1143000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

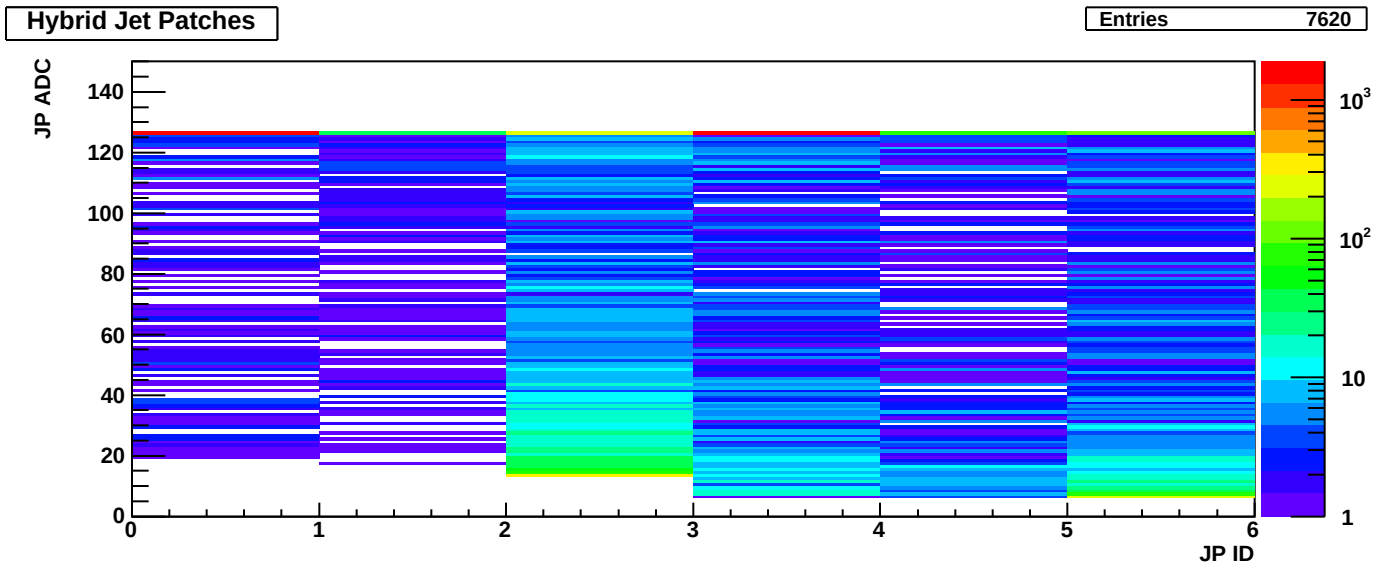

Barrel EMC L0 Input - Patch Sum
Entries 1143000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

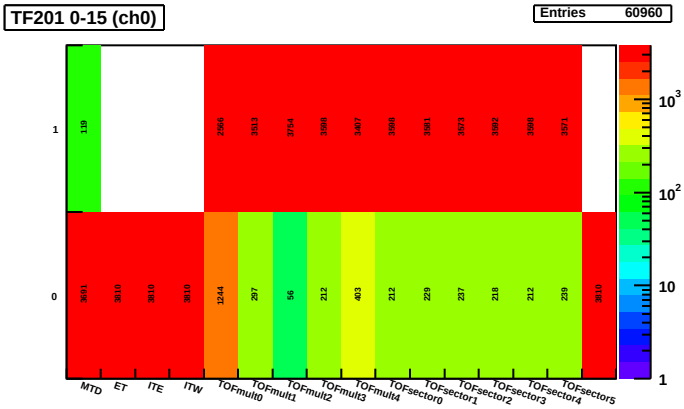

Endcap EMC L0 Input - High Tower
Entries 342900
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

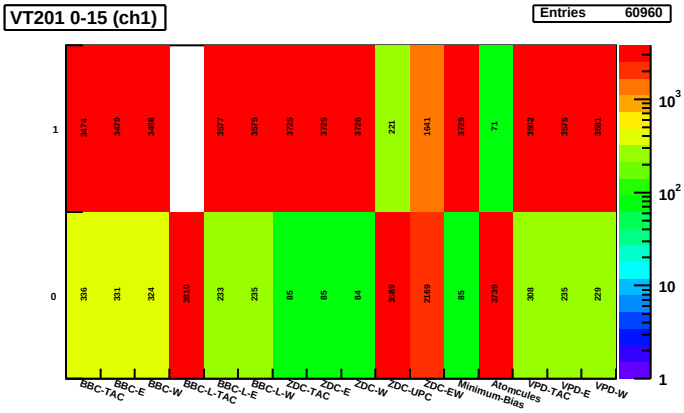

Endcap EMC L0 Input - Patch Sum
Entries 342900
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

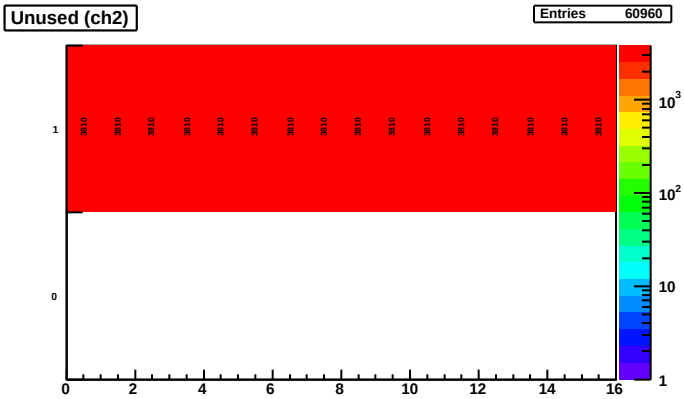

Barrel Jet Patches
Entries 68580
JP ADC
JP ID

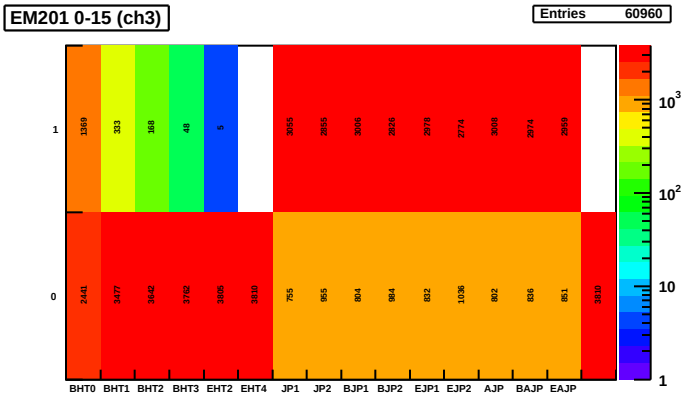

Endcap Jet Patches
Entries 22860
JP ADC
JP ID

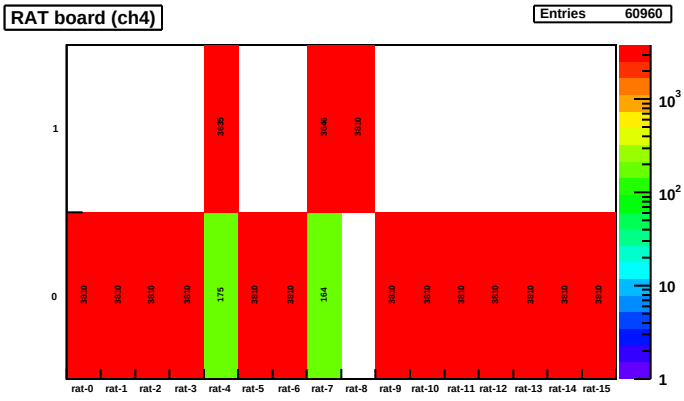

Hybrid Jet Patches
Entries 7620
JP ADC
JP ID

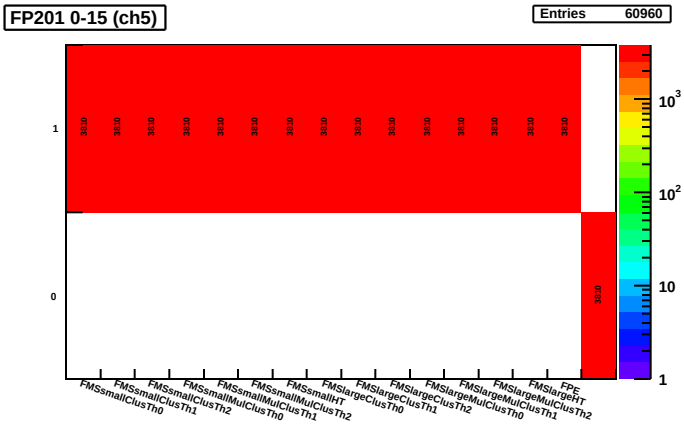

TF201 0-15 (ch0)
Entries 60960
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

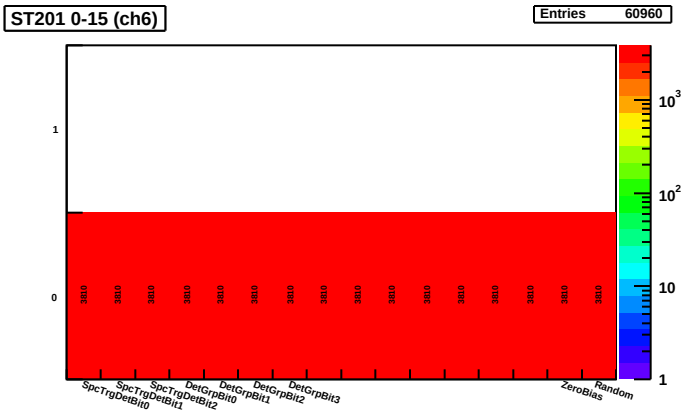

VT201 0-15 (ch1)
Entries 60960
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

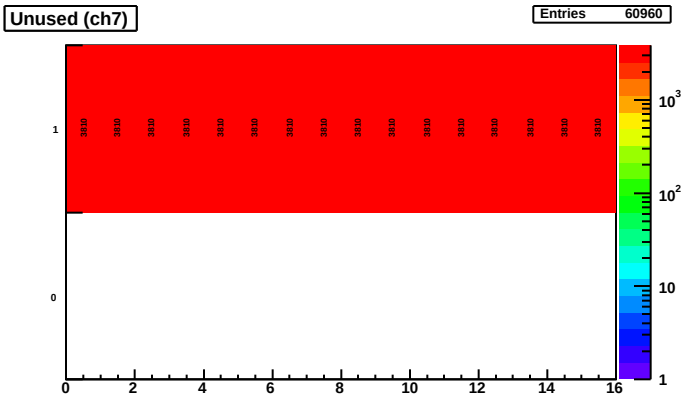

Unused (ch2)
Entries 60960

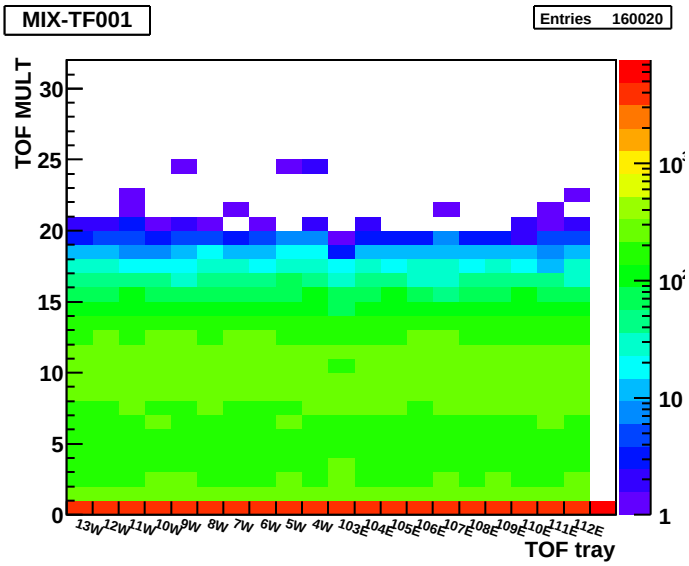

EM201 0-15 (ch3)
Entries 60960
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

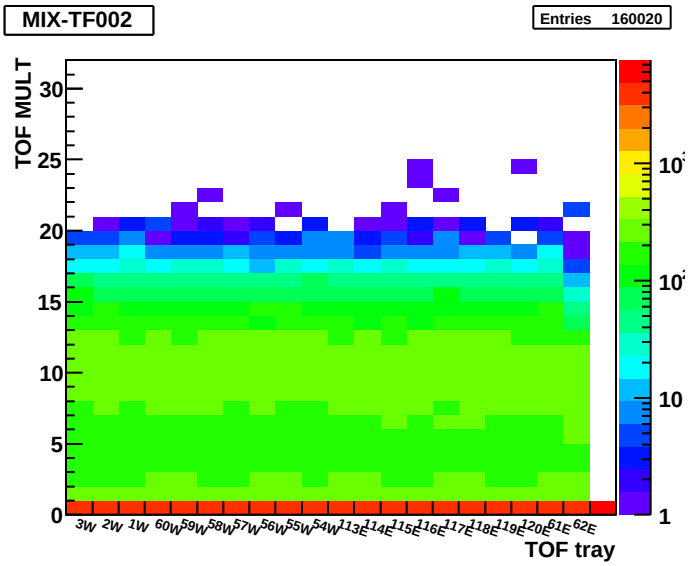

RAT board (ch4)
Entries 60960
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

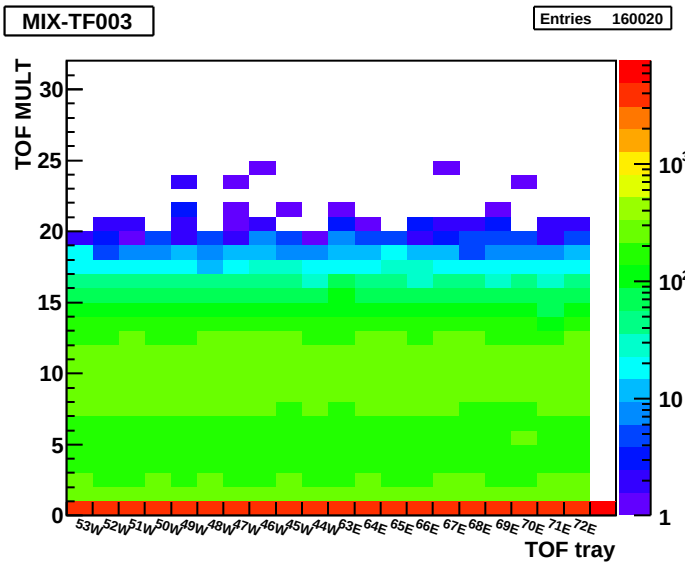

FP201 0-15 (ch5)
Entries 60960
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

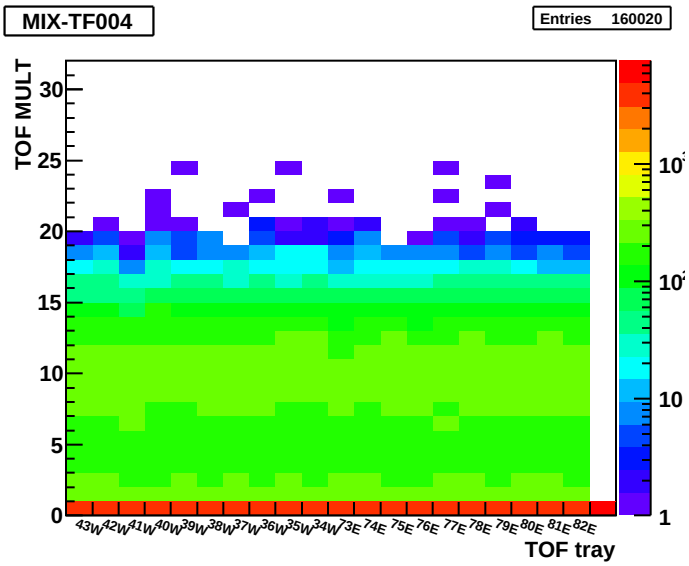

ST201 0-15 (ch6)
Entries 60960
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

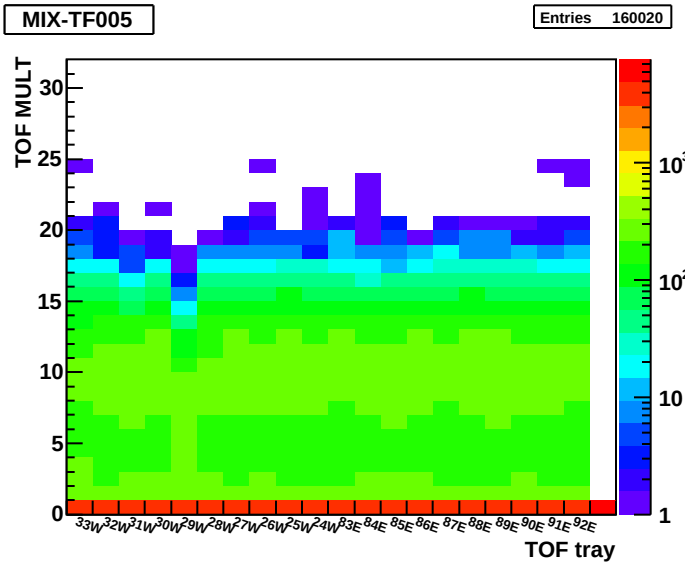

Unused (ch7)
Entries 60960

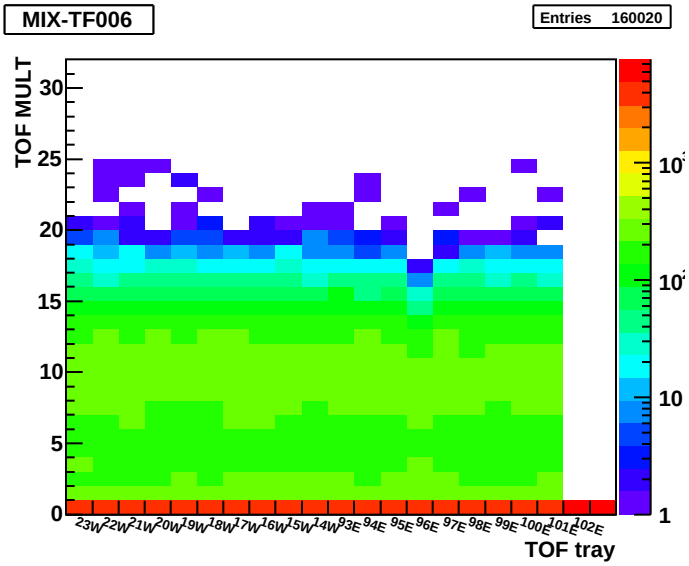

MIX-TF001
Entries 160020
TOF MULT
TOF tray

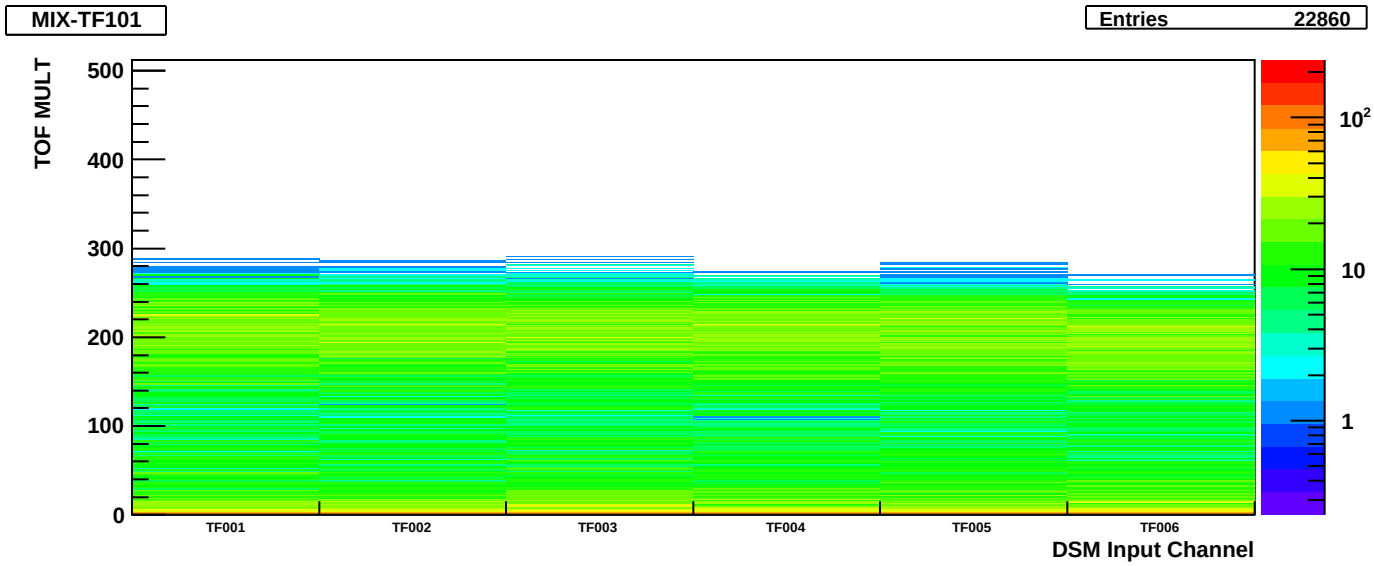

MIX-TF002
Entries 160020
TOF MULT
TOF tray

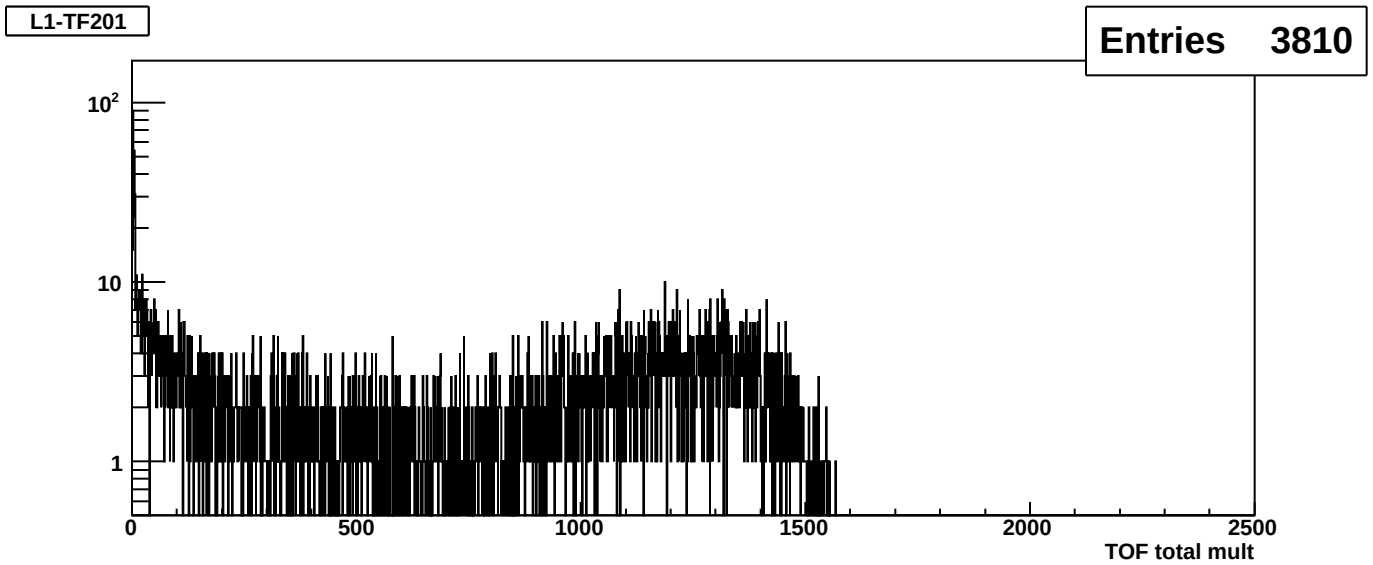

MIX-TF003
Entries 160020
TOF MULT
TOF tray

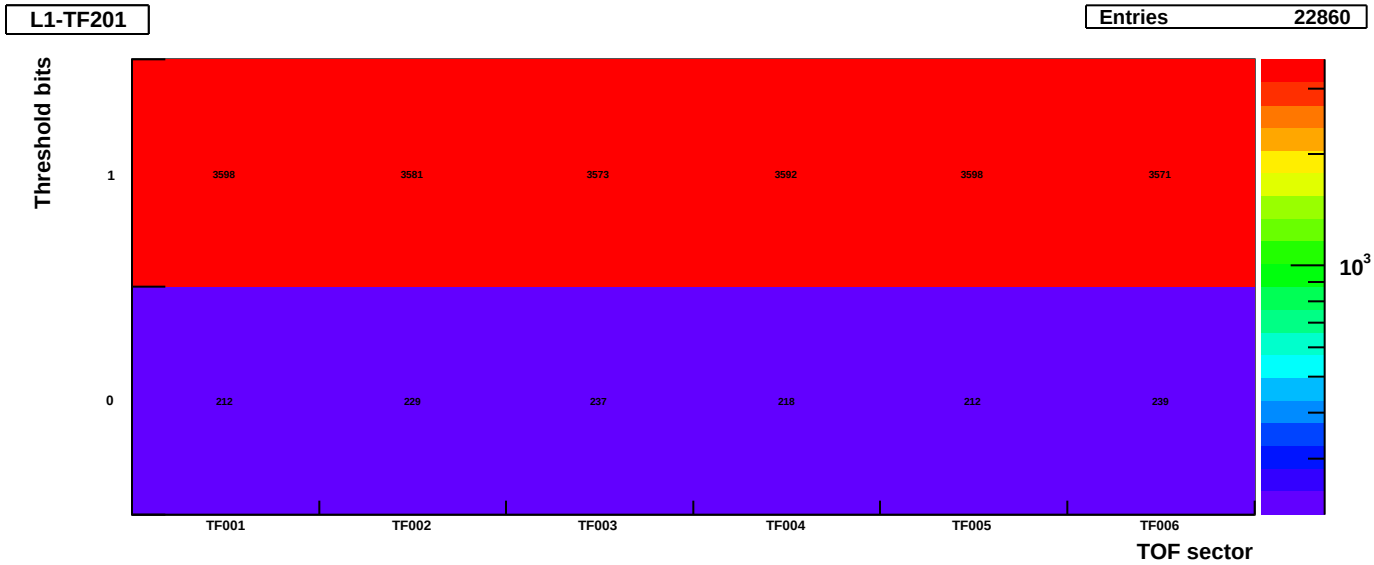

MIX-TF004
Entries 160020
TOF MULT
TOF tray

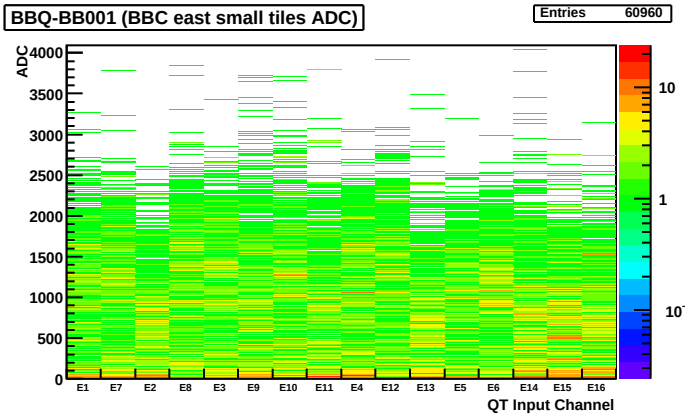

MIX-TF005
Entries 160020
TOF MULT
TOF tray

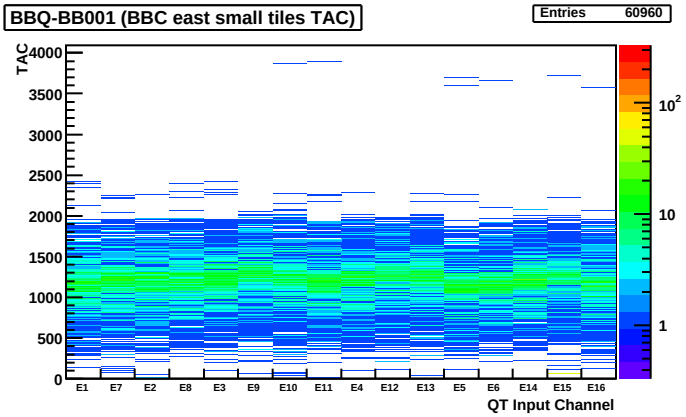

MIX-TF006
Entries 160020
TOF MULT
TOF tray

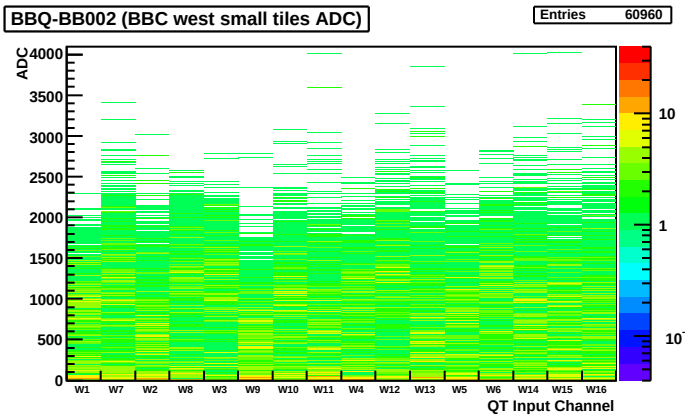

MIX-TF101
Entries 22860
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

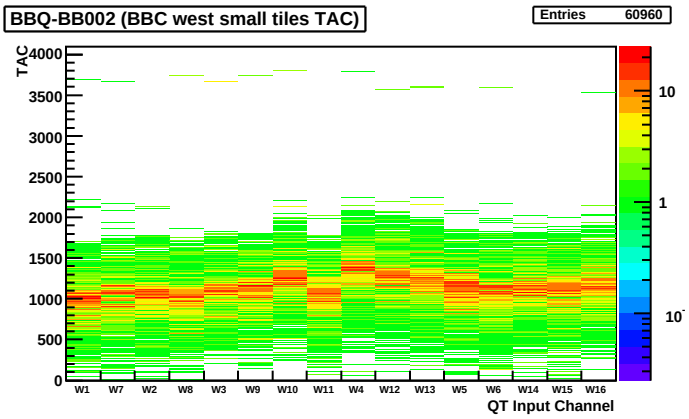

L1-TF201
Entries 3810
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

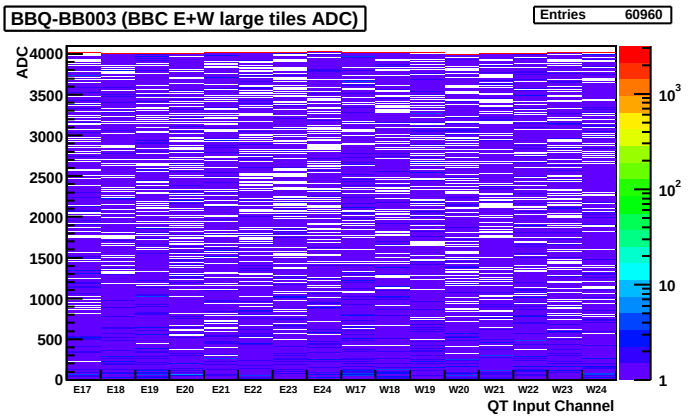

L1-TF201
Entries 22860
Threshold bits
1
0
3598
3581
3573
3592
3598
3571
212
229
237
218
212
239
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3

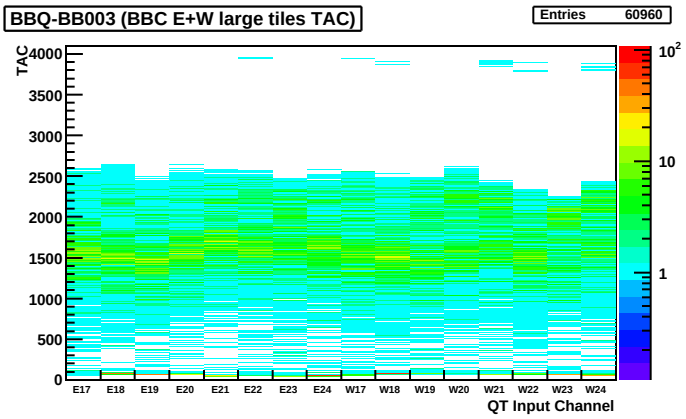

BBQ-BB001 (BBC east small tiles ADC)
Entries 60960
ADC
QT Input Channel

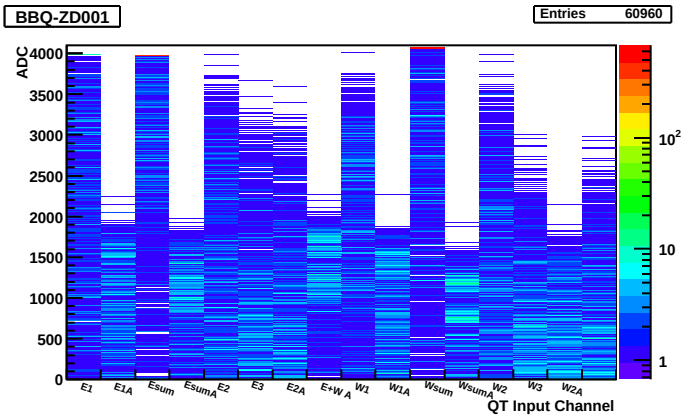

BBQ-BB001 (BBC east small tiles TAC)
Entries 60960
TAC
QT Input Channel

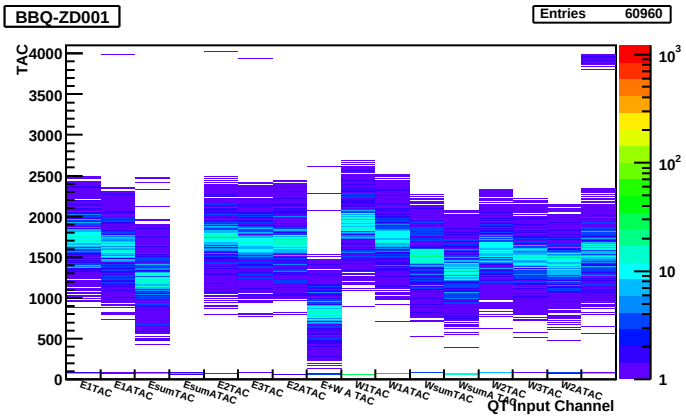

BBQ-BB002 (BBC west small tiles ADC)
Entries 60960
ADC
QT Input Channel

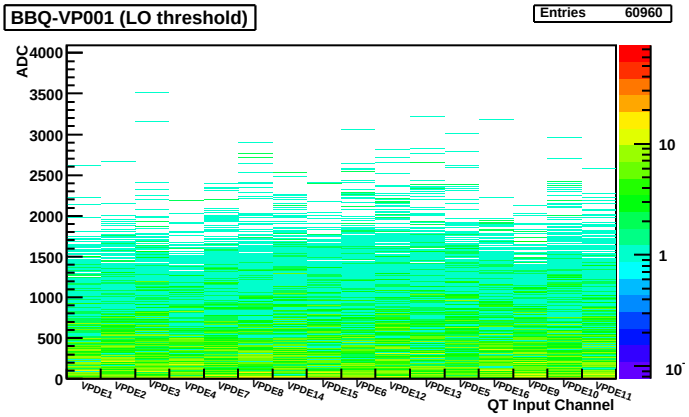

BBQ-BB002 (BBC west small tiles TAC)
Entries 60960
TAC
QT Input Channel

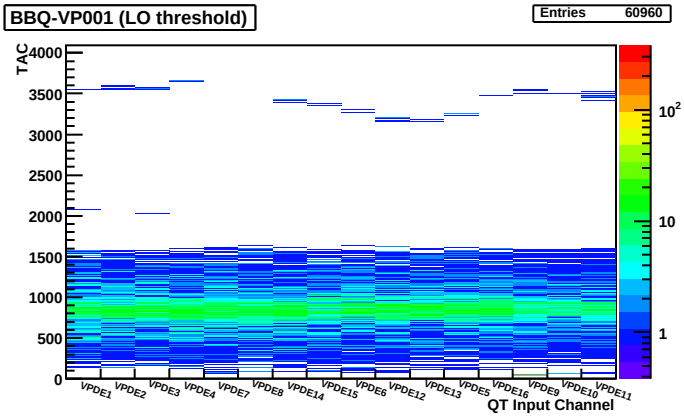

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 60960
ADC
QT Input Channel

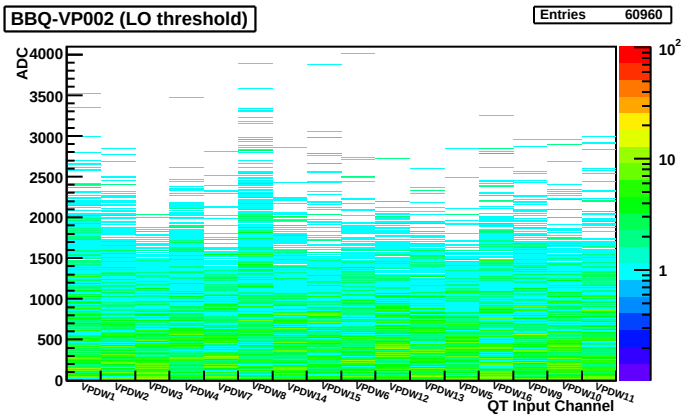

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 60960
TAC
QT Input Channel

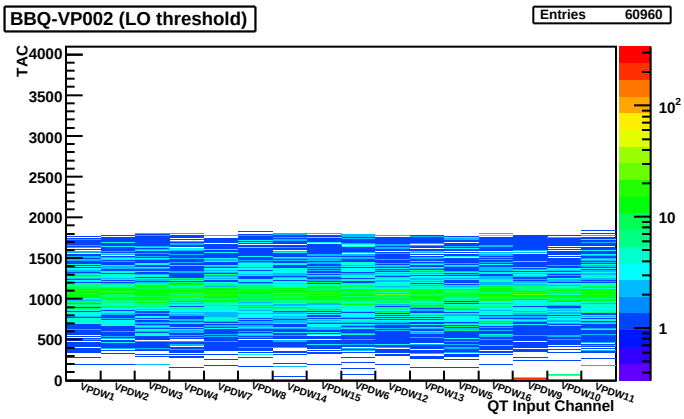

BBQ-ZD001
Entries 60960
ADC
QT Input Channel

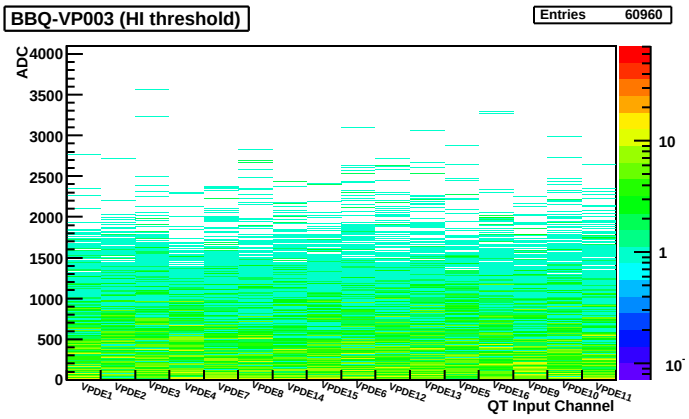

BBQ-ZD001
Entries 60960
TAC
QT Input Channel

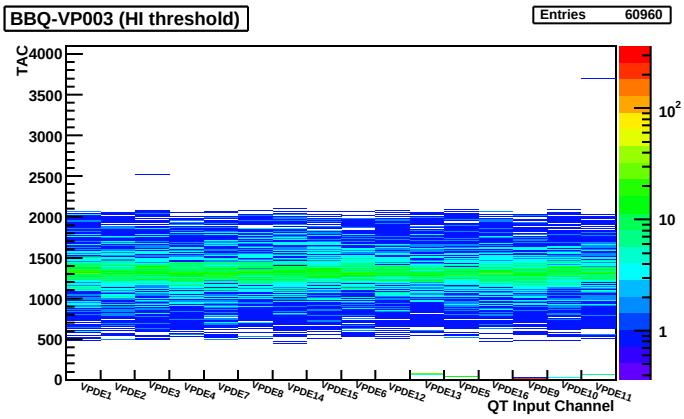

BBQ-VP001 (LO threshold)
Entries 60960
ADC
QT Input Channel

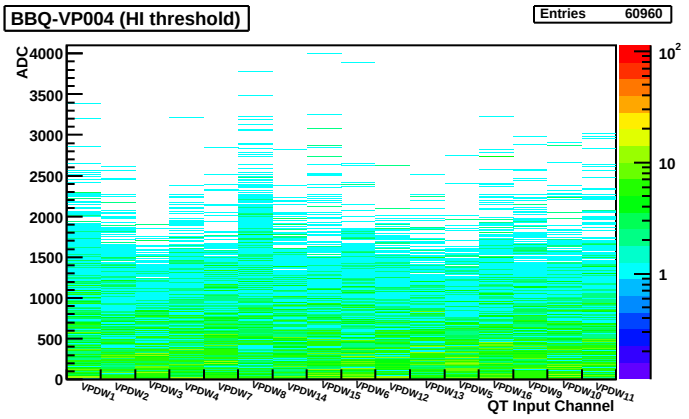

BBQ-VP001 (LO threshold)
Entries 60960
TAC
QT Input Channel

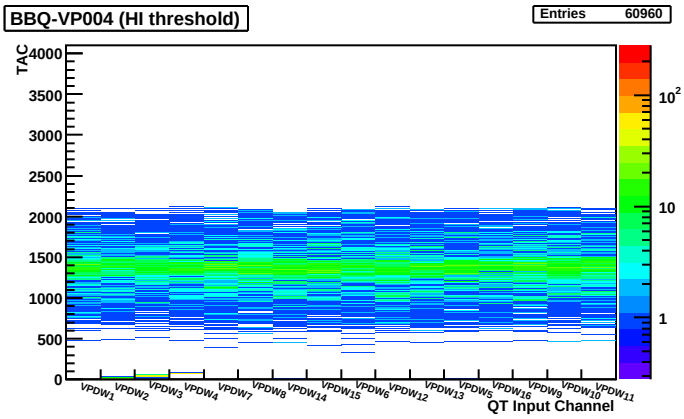

BBQ-VP002 (LO threshold)
Entries 60960
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 60960
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 60960
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 60960
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 60960
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 60960
TAC
QT Input Channel