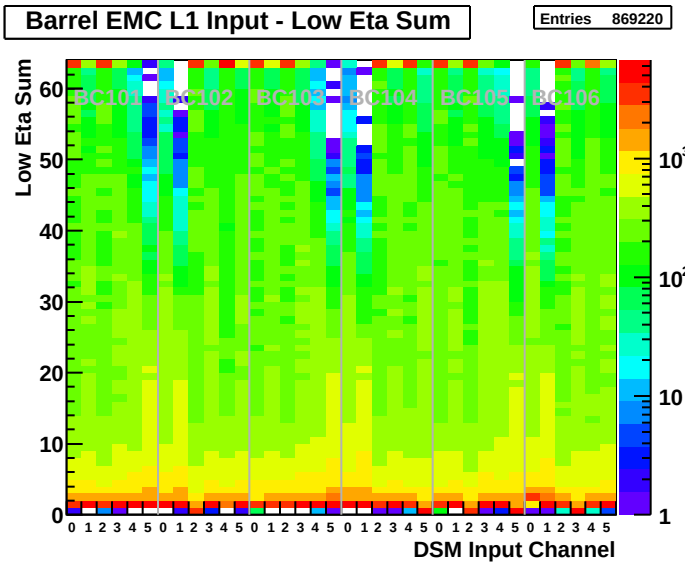
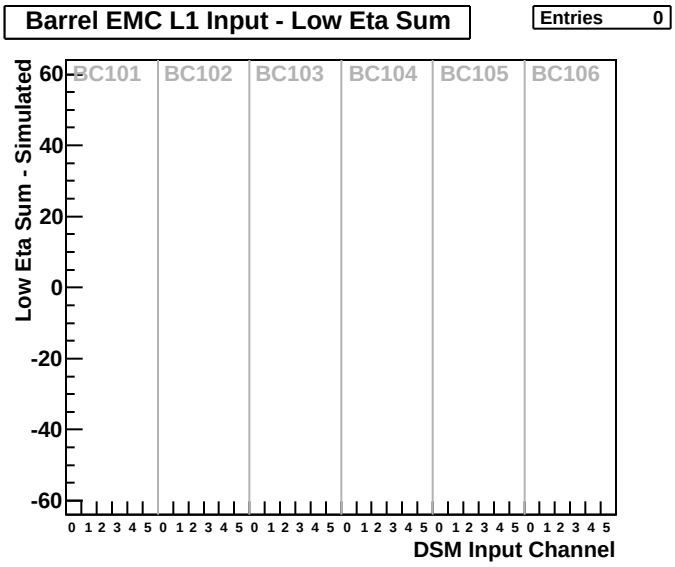
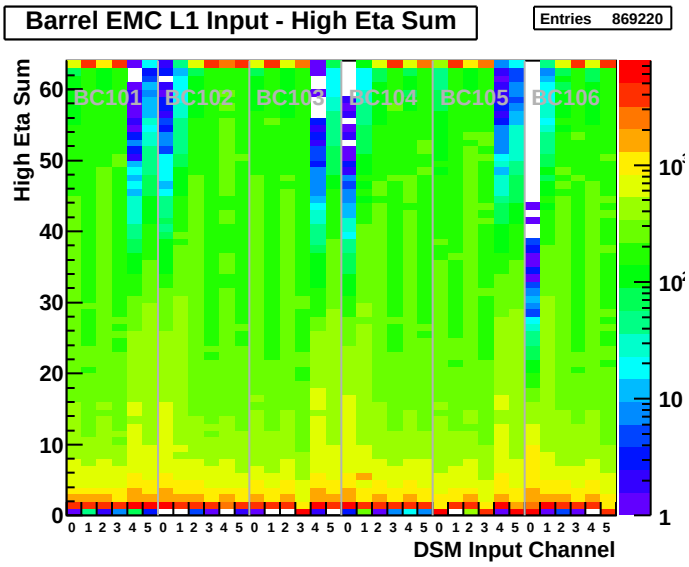
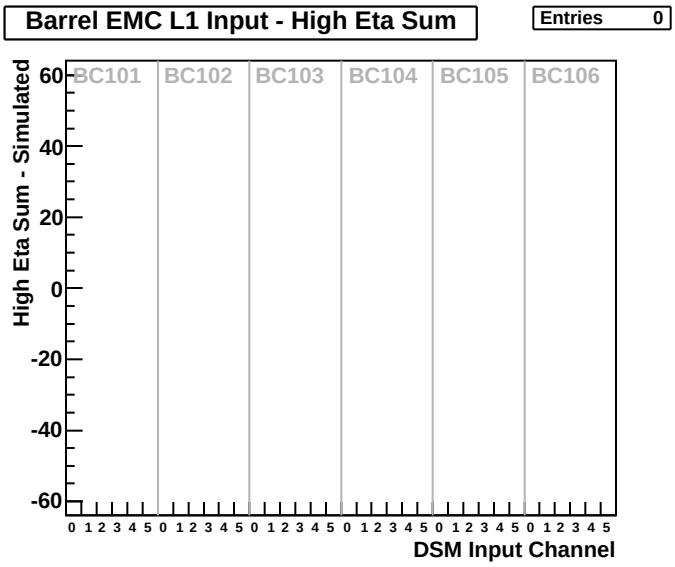
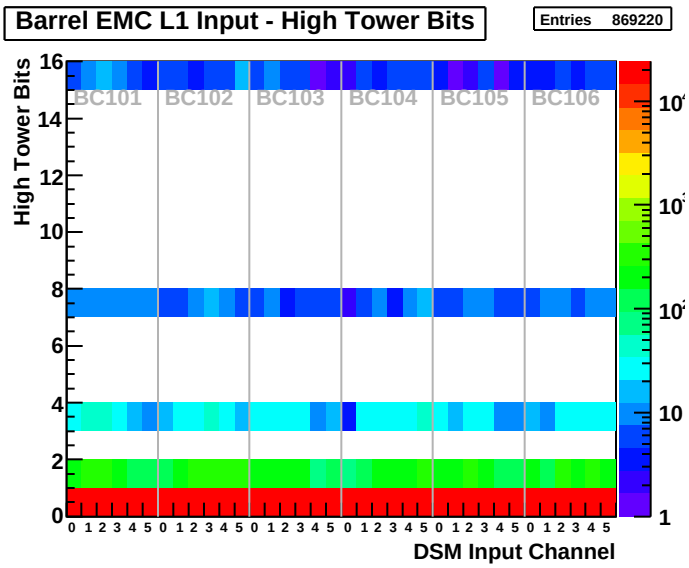
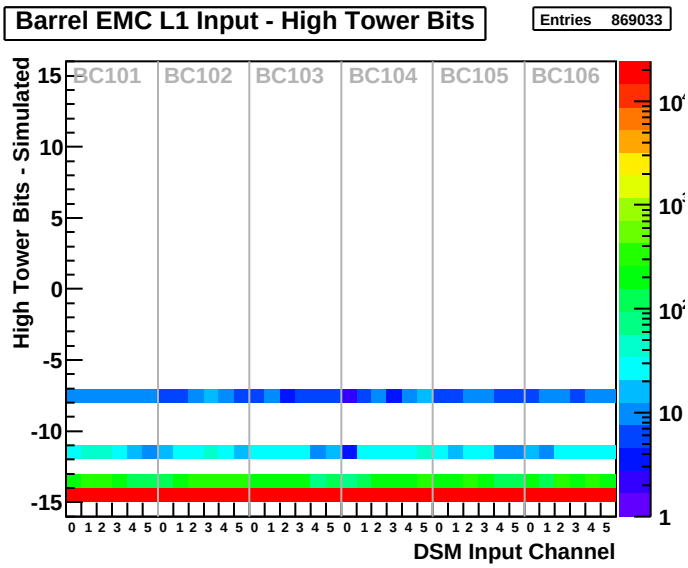

Barrel EMC L1 Input - Low Eta Sum
Entries 869220
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

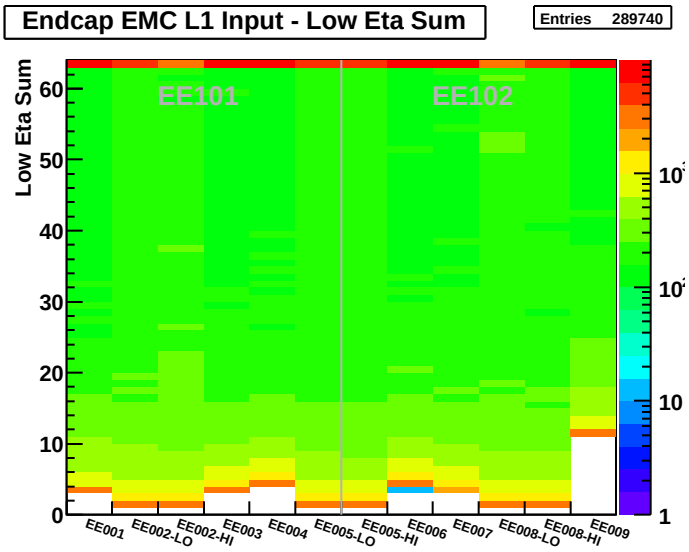

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

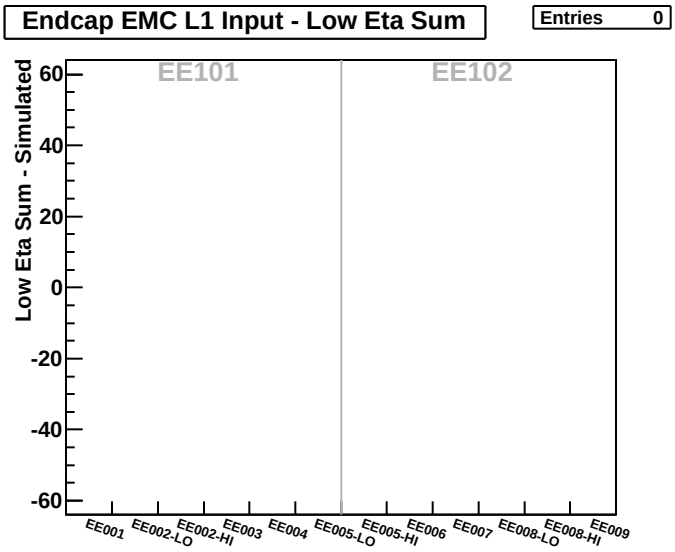

Barrel EMC L1 Input - High Eta Sum
Entries 869220
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

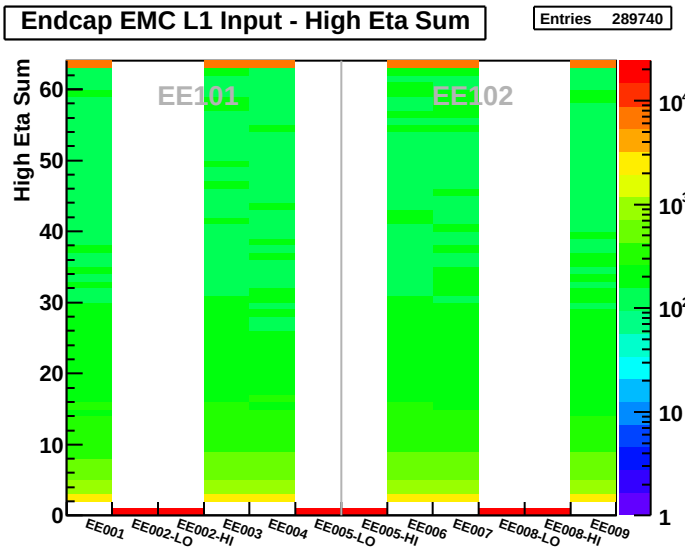

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

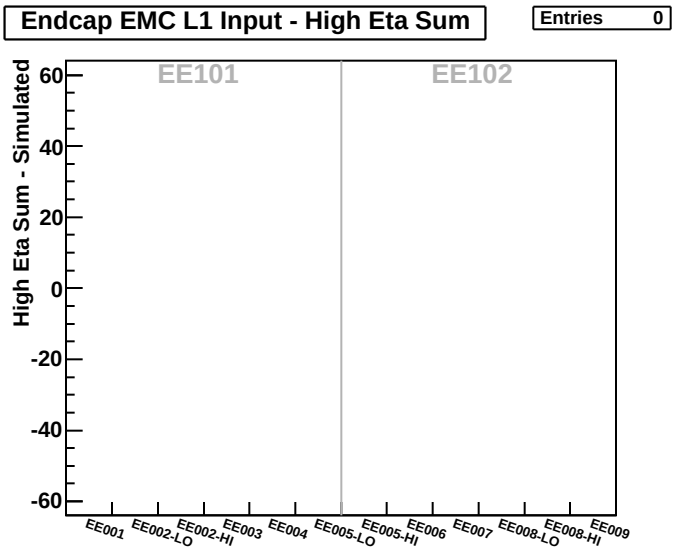

Barrel EMC L1 Input - High Tower Bits
Entries 869220
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

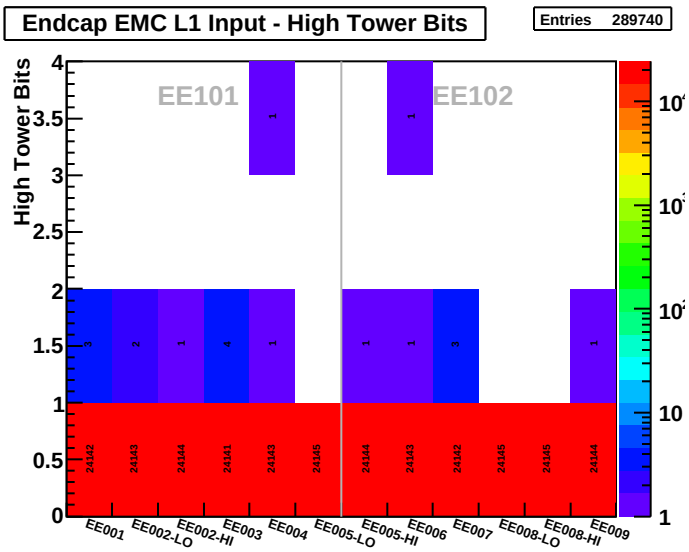

Barrel EMC L1 Input - High Tower Bits
Entries 869033
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

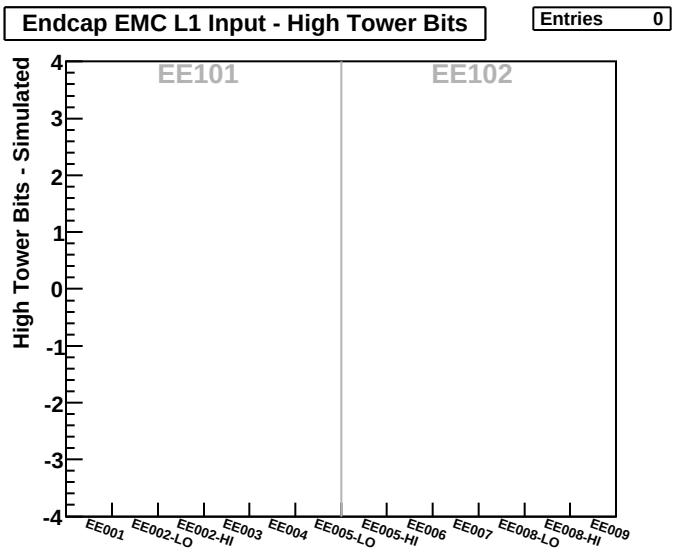

Endcap EMC L1 Input - Low Eta Sum
Entries 289740
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

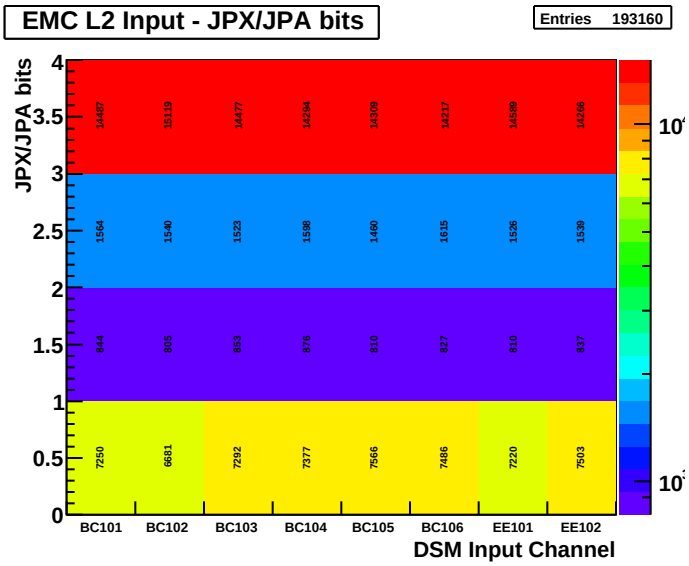

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

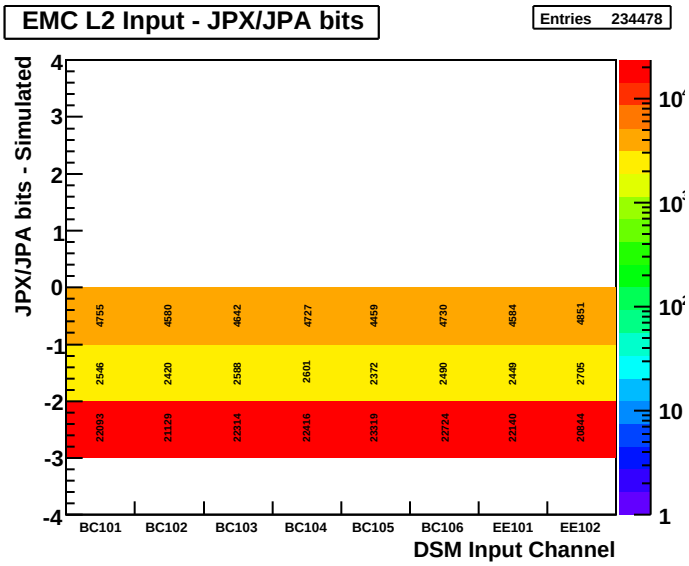

Endcap EMC L1 Input - High Eta Sum
Entries 289740
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

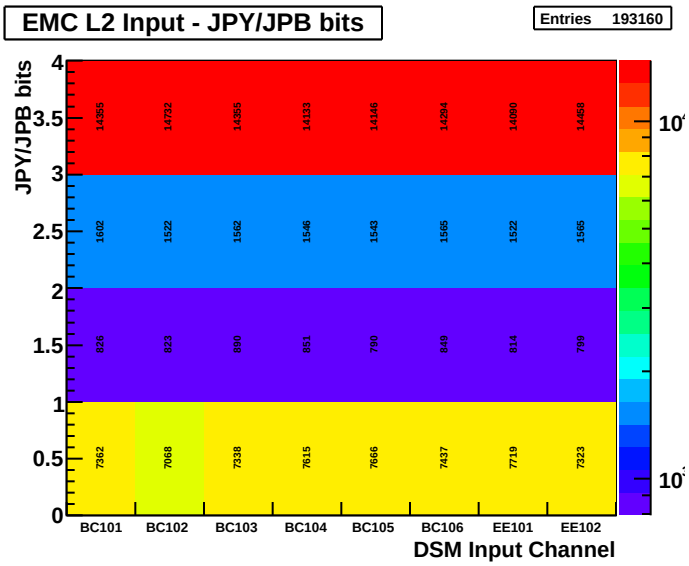

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

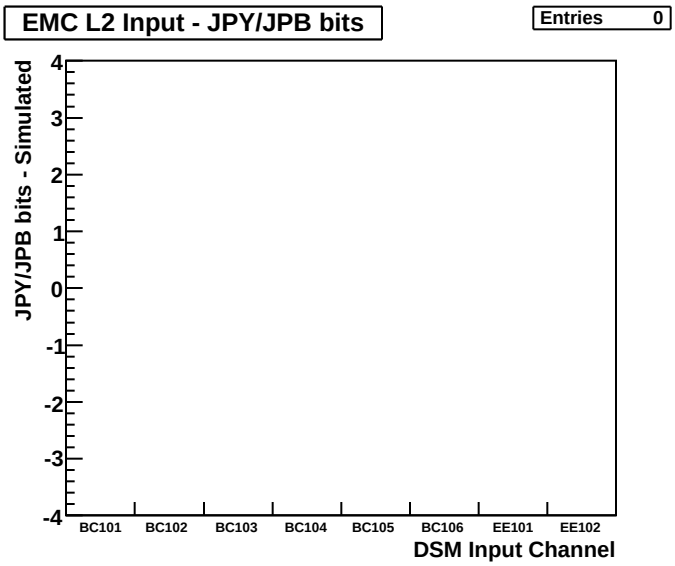

Endcap EMC L1 Input - High Tower Bits
Entries 289740
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

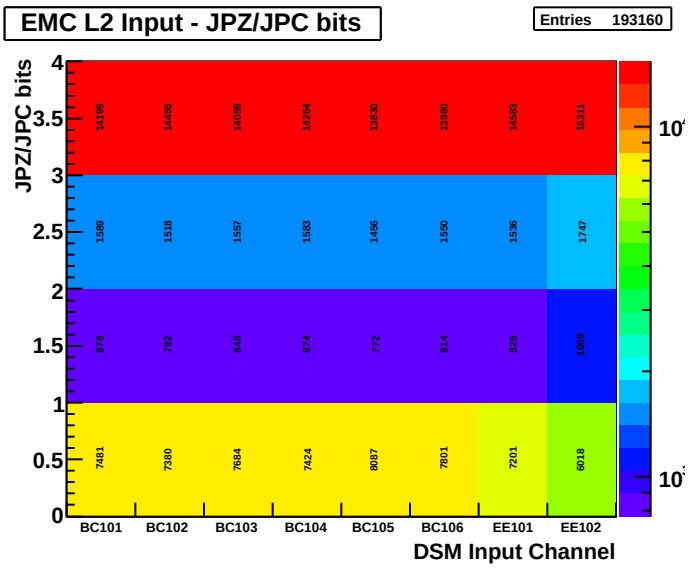

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

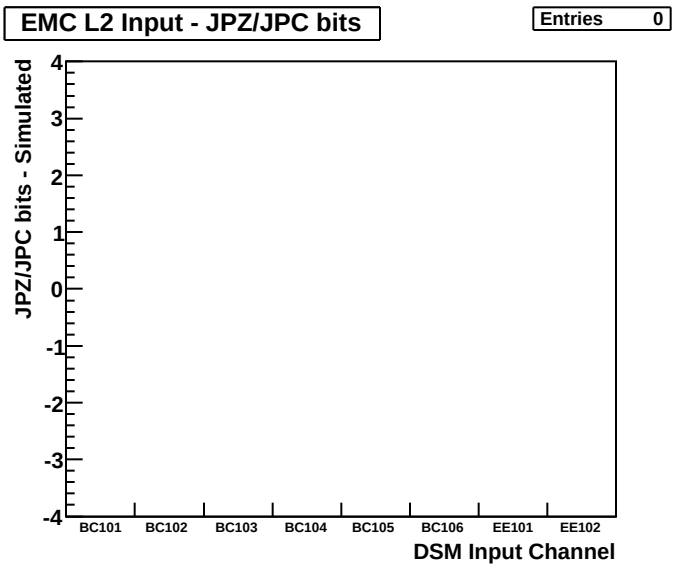

EMC L2 Input - JPX/JPA bits
Entries 193160
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
14487 15119 14477 14294 14309 14217 14589 14266
1564 1540 1523 1598 1460 1615 1526 1539
844 805 853 876 810 827 810 837
7250 6681 7292 7377 7566 7486 7220 7503

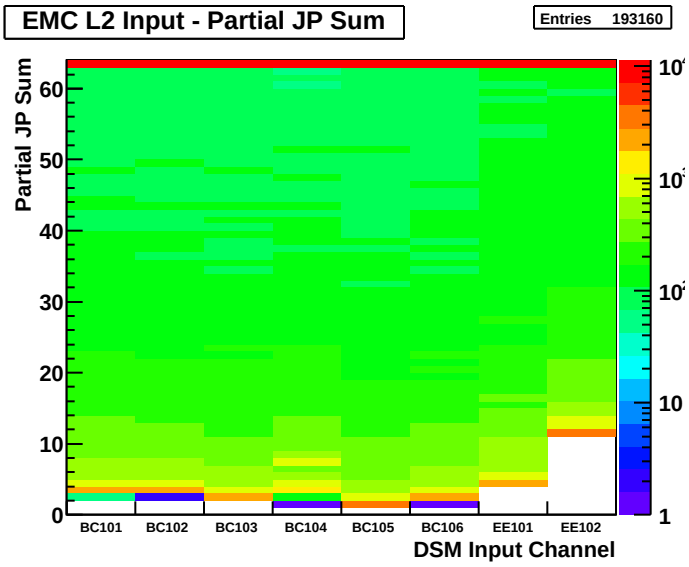

EMC L2 Input - JPX/JPA bits
Entries 234478
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4755 4580 4642 4727 4459 4730 4584 4851
2546 2420 2588 2601 2372 2490 2449 2705
22093 21129 22314 22416 23319 22724 22140 20844

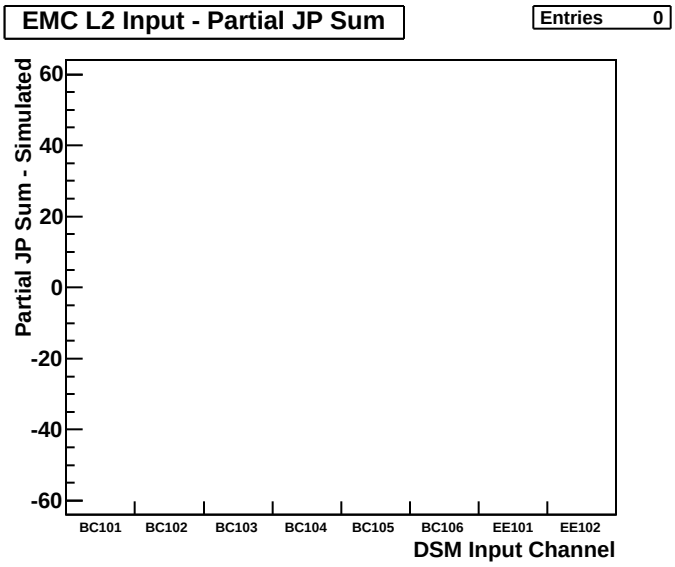

EMC L2 Input - JPY/JPB bits
Entries 193160
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
14355 14732 14355 14133 14146 14294 14090 14458
1602 1522 1562 1546 1543 1565 1522 1565
826 823 890 851 790 849 814 799
7362 7068 7338 7615 7666 7437 7719 7323

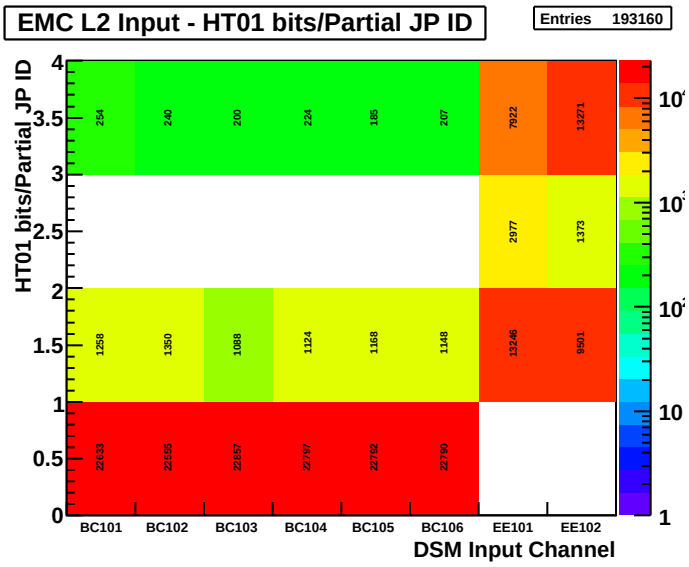

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

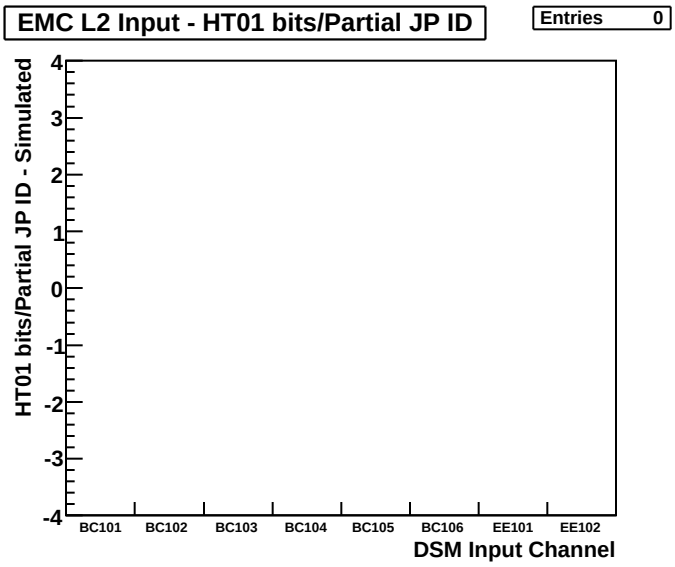

EMC L2 Input - JPZ/JPC bits
Entries 193160
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
14199 14455 14059 14264 13830 13980 14583 15311
1589 1518 1567 1583 1456 1590 1536 1747
876 792 845 874 772 814 825 1069
7481 7380 7684 7424 8087 7801 7201 6018

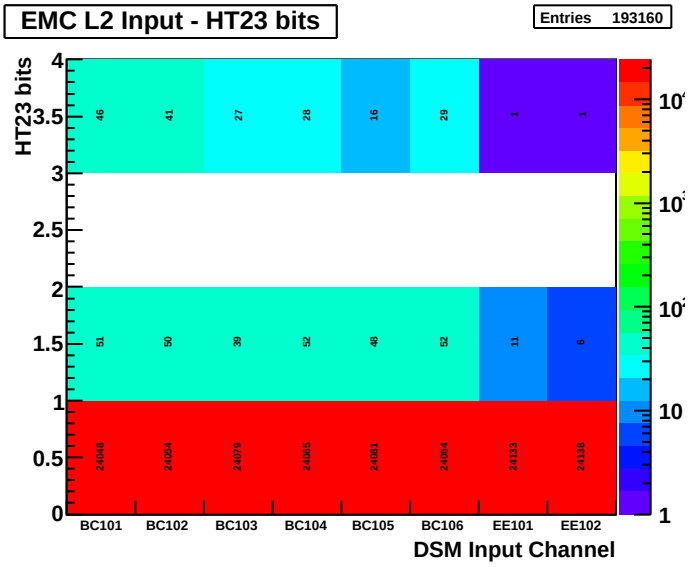

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

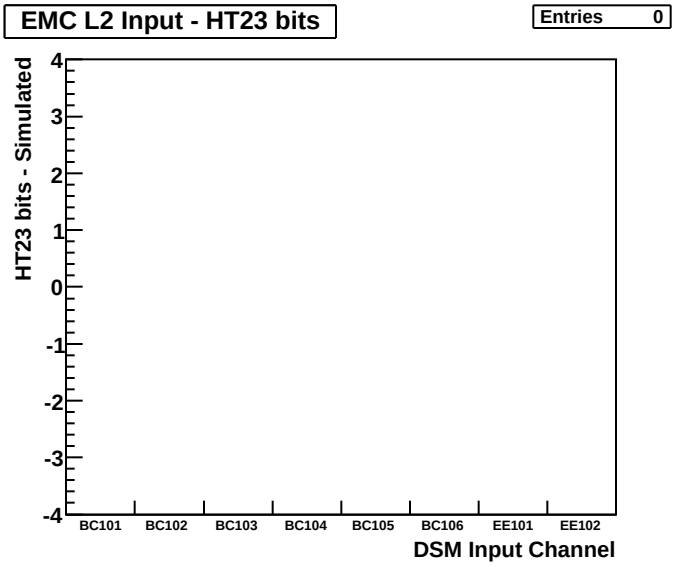

EMC L2 Input - Partial JP Sum
Entries 193160
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

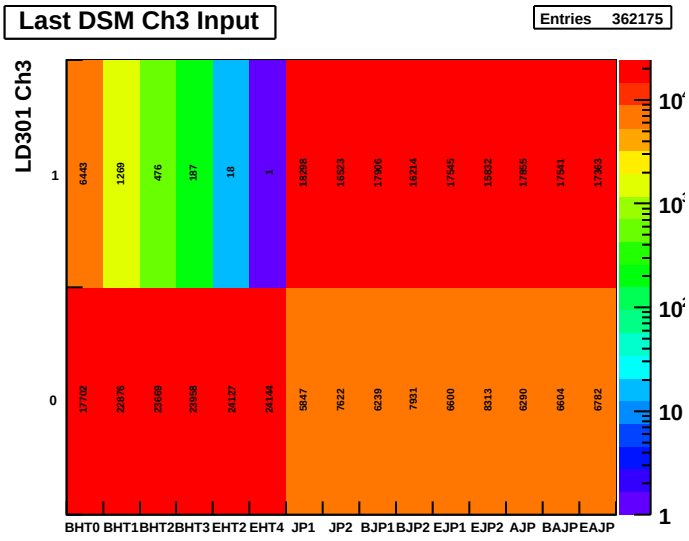

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

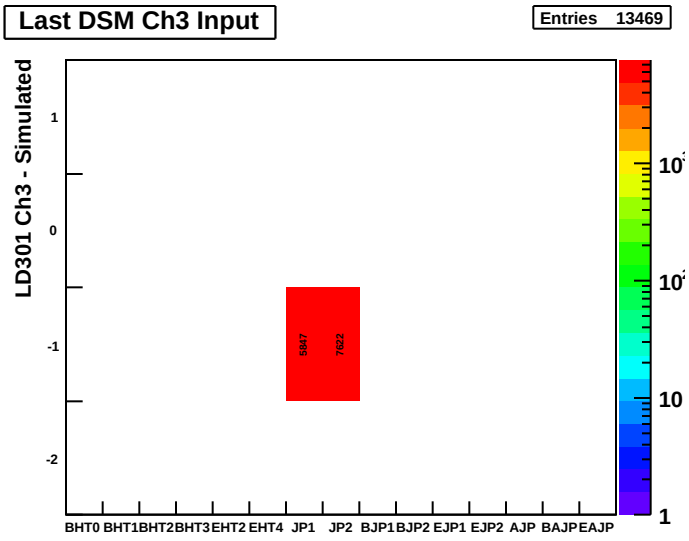

EMC L2 Input - HT01 bits/Partial JP ID
Entries 193160
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

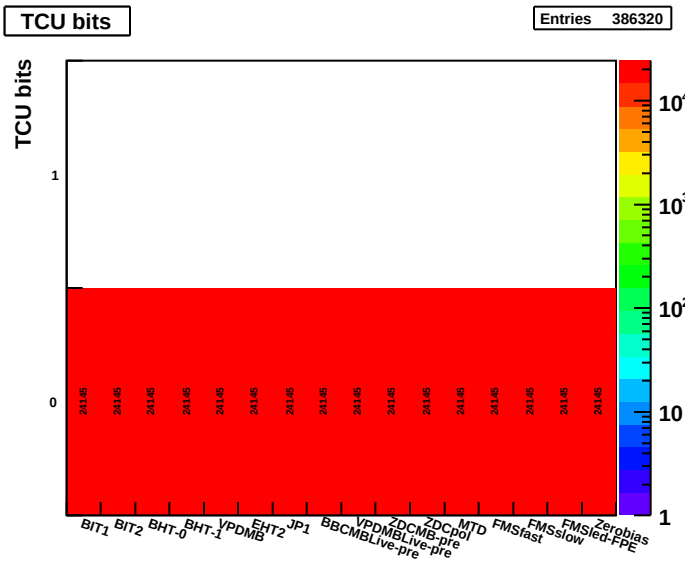

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

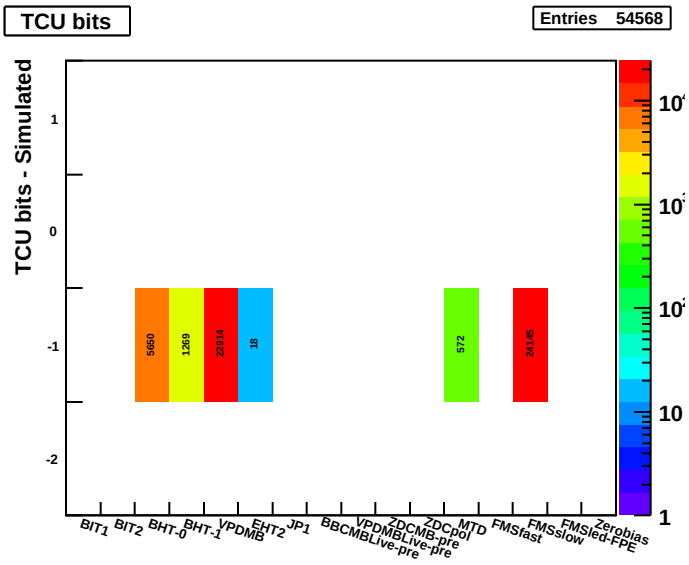

EMC L2 Input - HT23 bits
Entries 193160
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

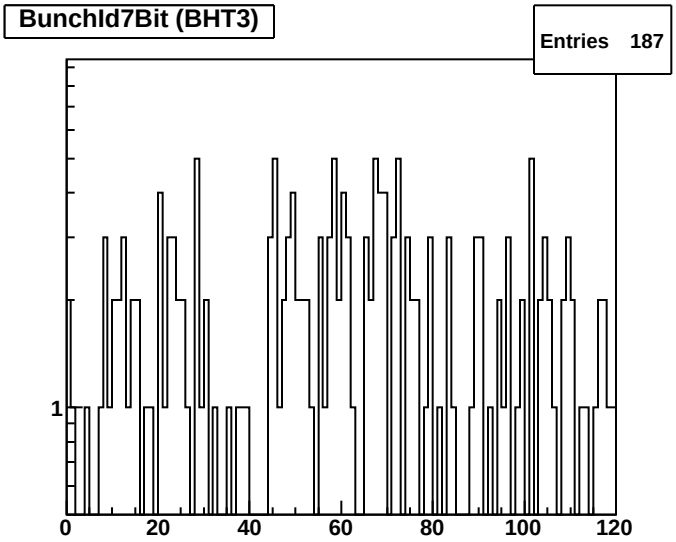

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

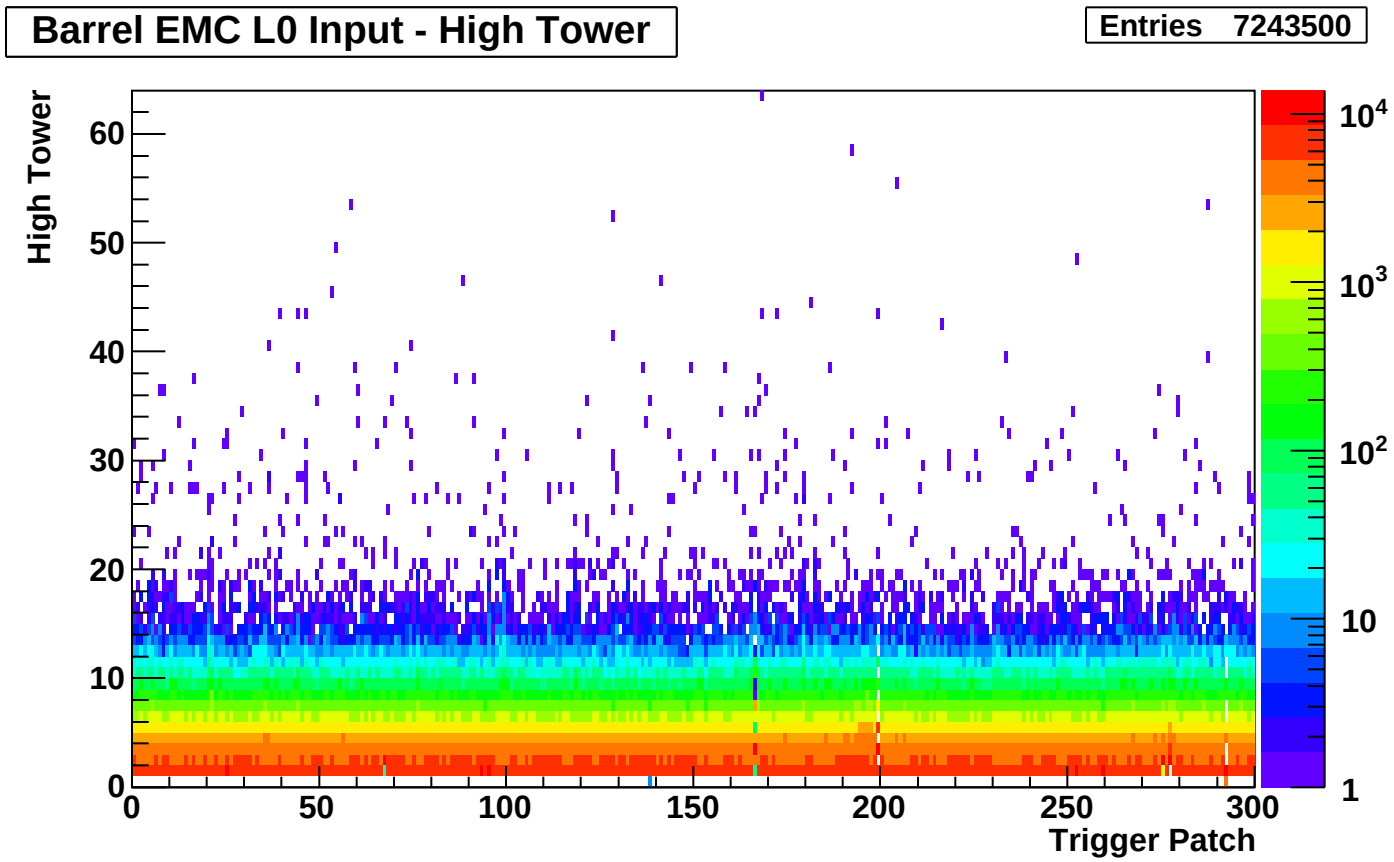

Last DSM Ch3 Input
Entries 362175
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

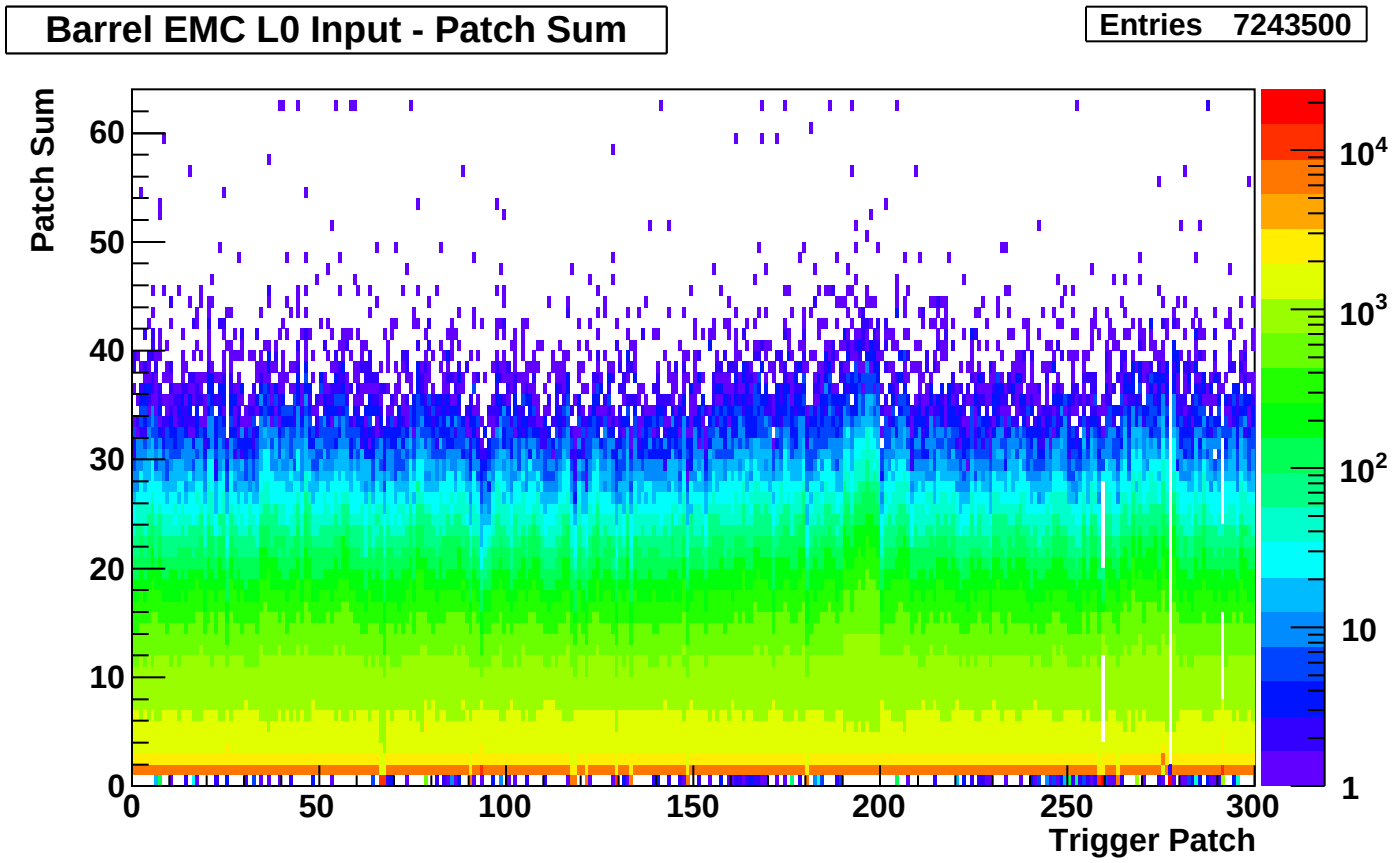

Last DSM Ch3 Input
Entries 13469
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

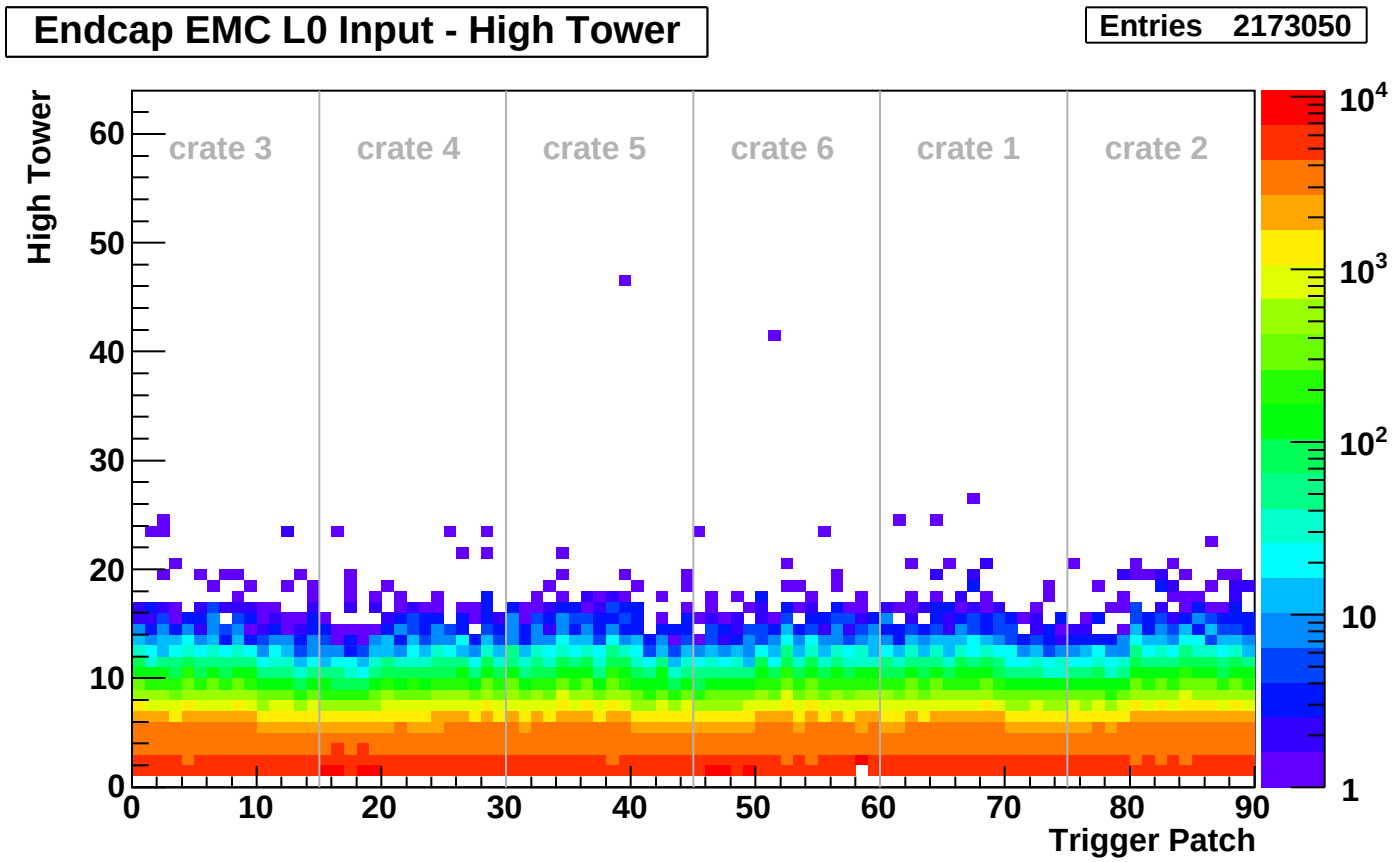

TCU bits
Entries 386320
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

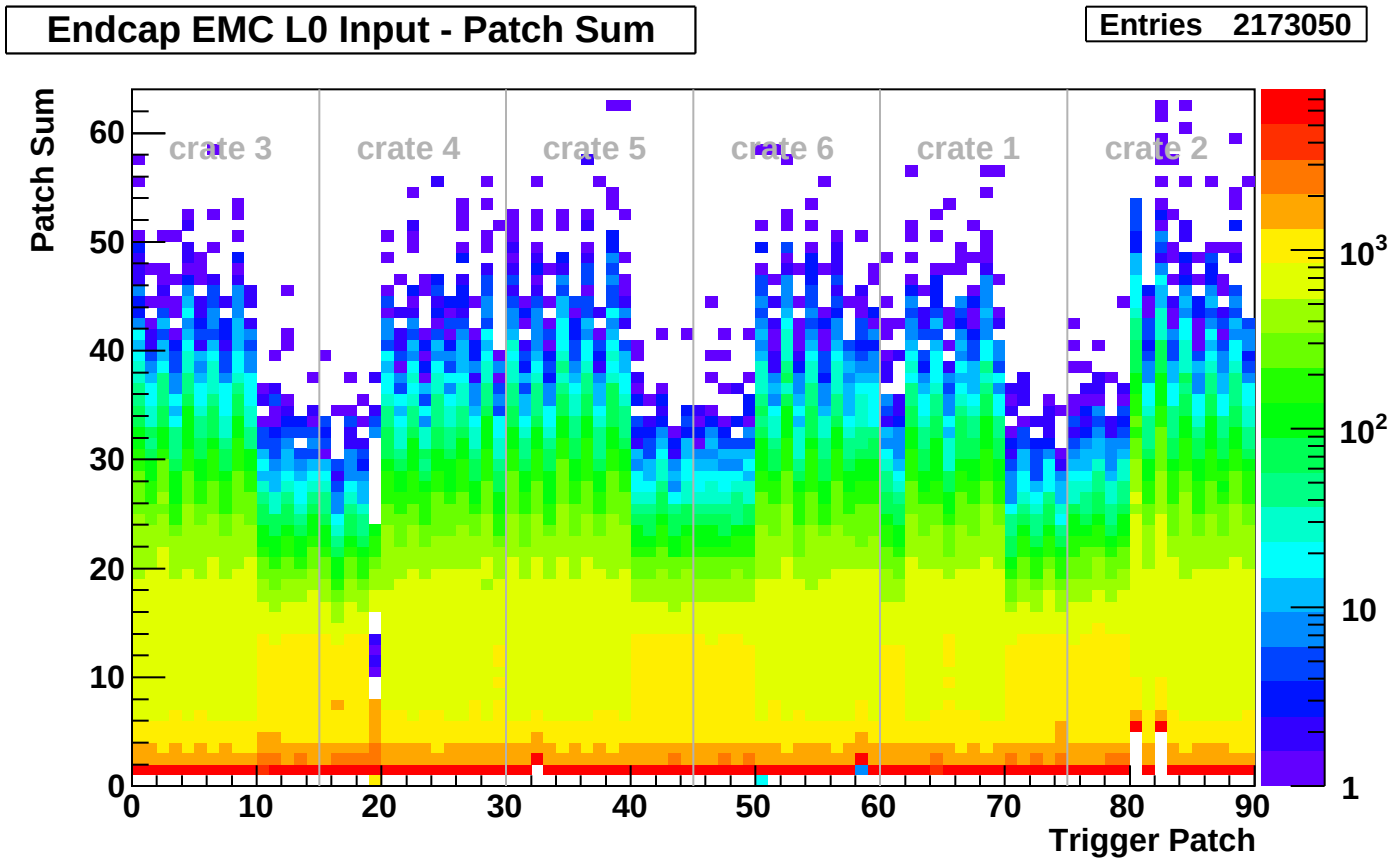

TCU bits
Entries 54568
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

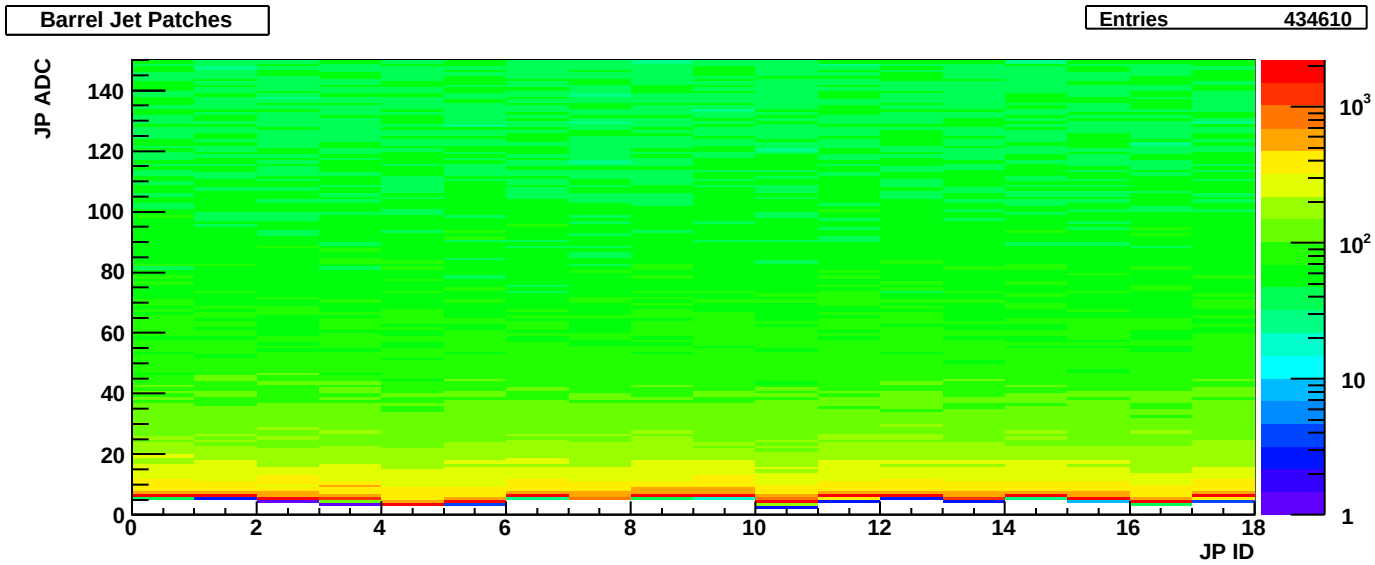

BunchId7Bit (BHT3)
Entries 187

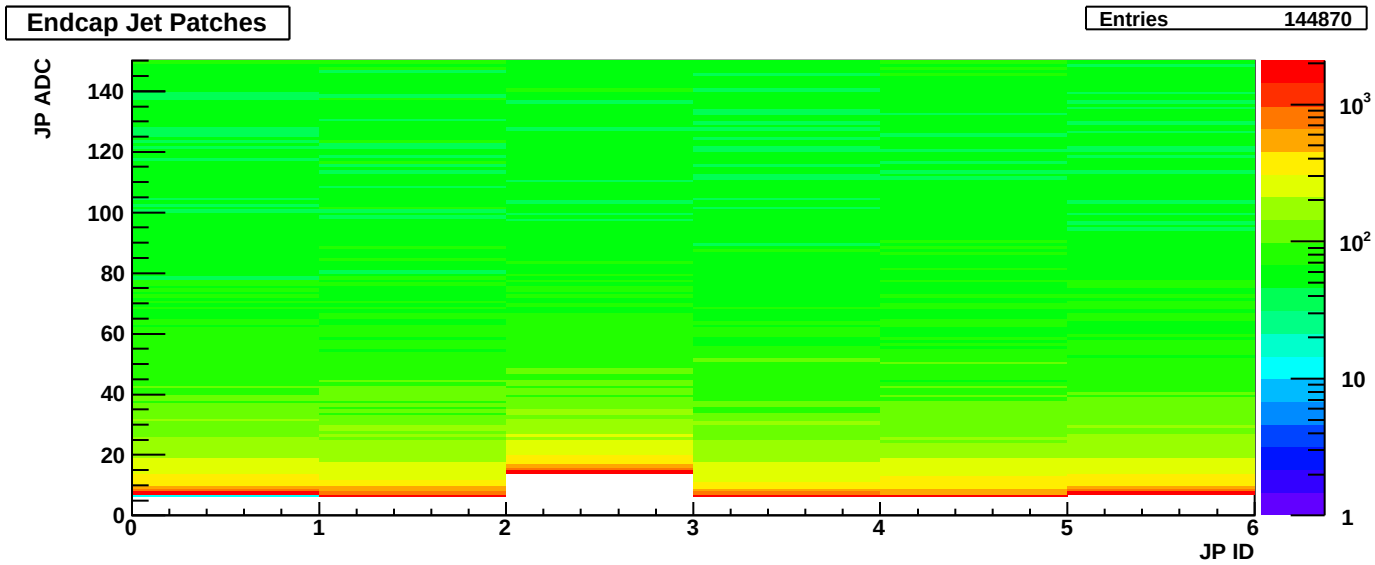

Barrel EMC L0 Input - High Tower
Entries 7243500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

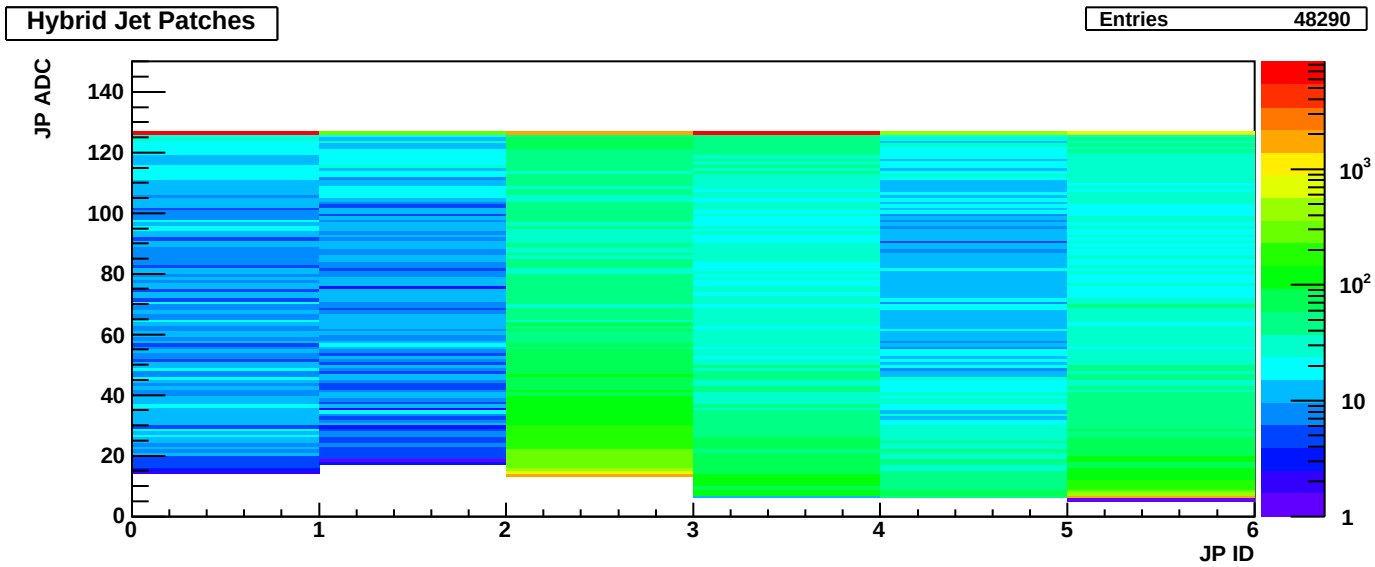

Barrel EMC L0 Input - Patch Sum
Entries 7243500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

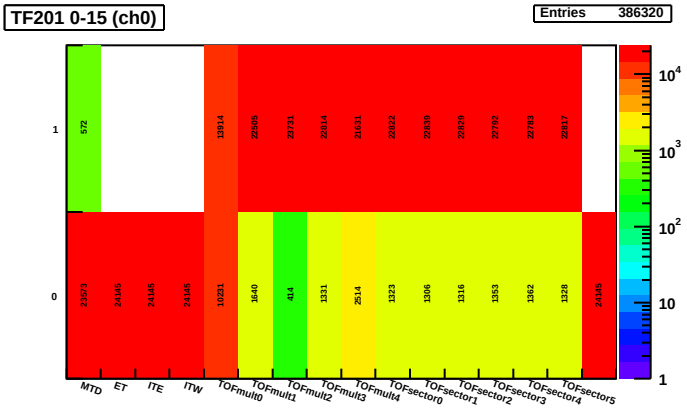

Endcap EMC L0 Input - High Tower
Entries 2173050
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

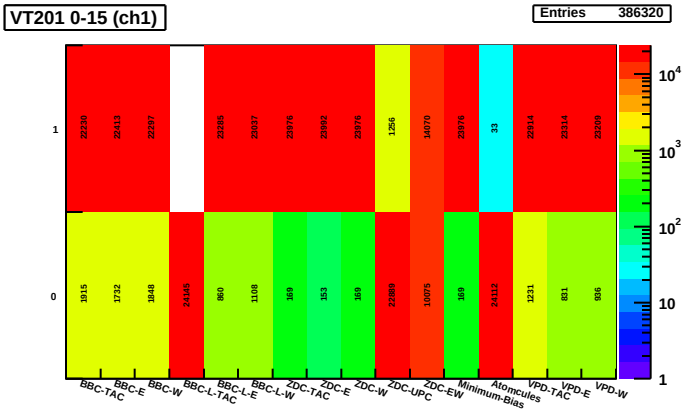

Endcap EMC L0 Input - Patch Sum
Entries 2173050
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

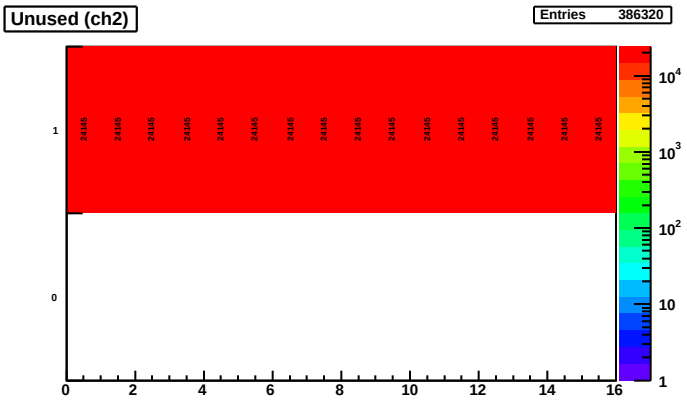

Barrel Jet Patches
Entries 434610
JP ADC
JP ID

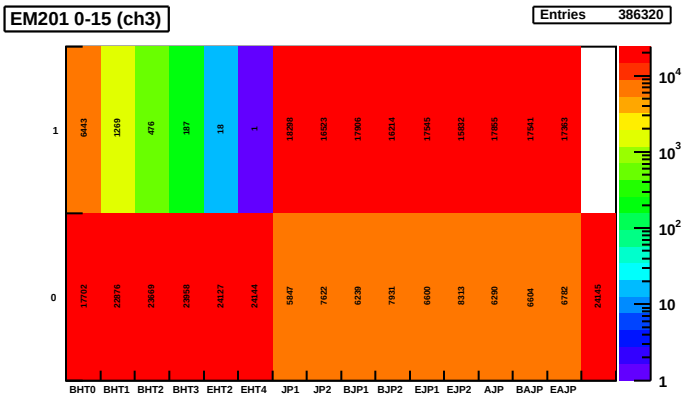

Endcap Jet Patches
Entries 144870
JP ADC
JP ID

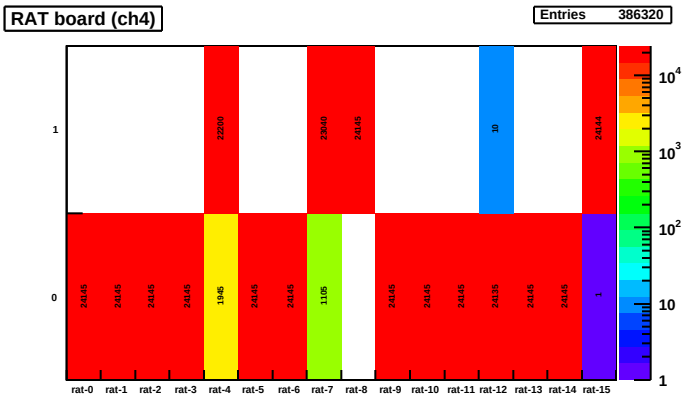

Hybrid Jet Patches
Entries 48290
JP ADC
JP ID

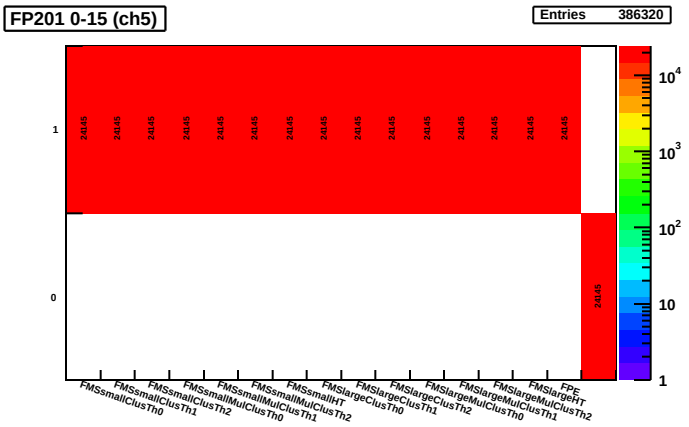

TF201 0-15 (ch0)
Entries 386320
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

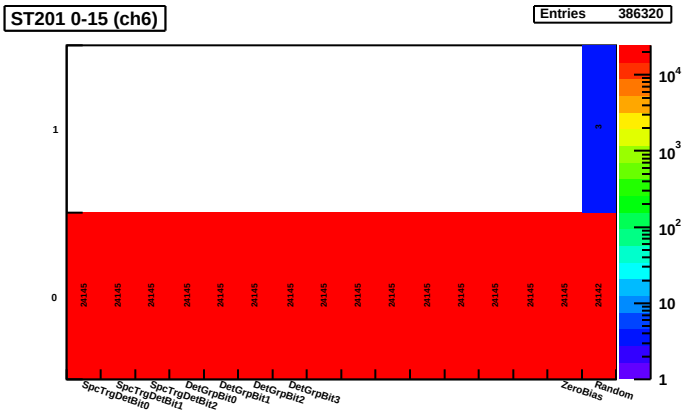

VT201 0-15 (ch1)
Entries 386320
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

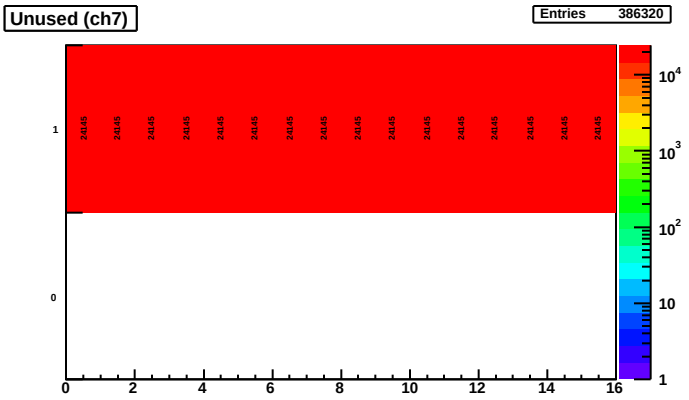

Unused (ch2)
Entries 386320

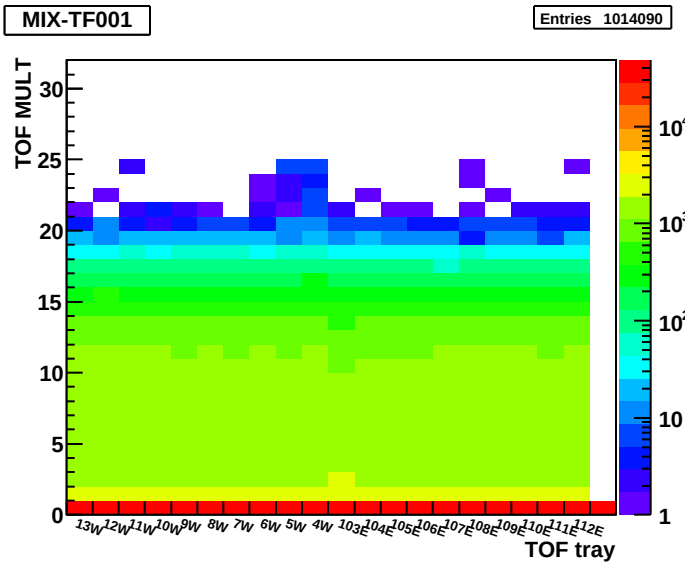

EM201 0-15 (ch3)
Entries 386320
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

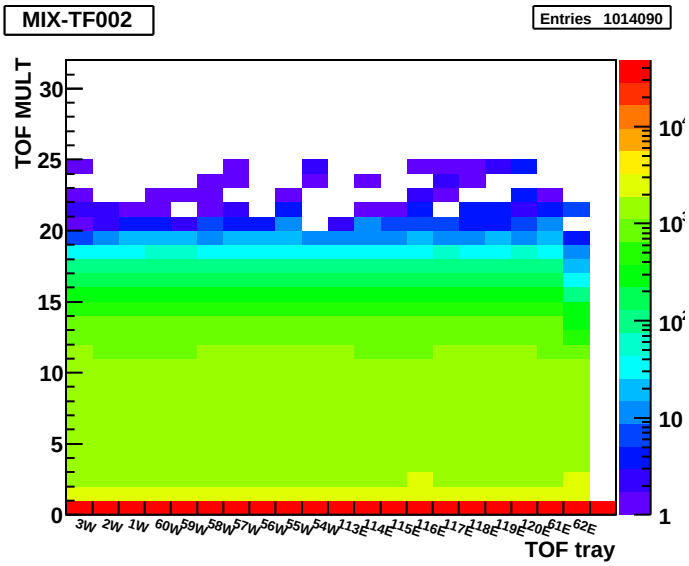

RAT board (ch4)
Entries 386320
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

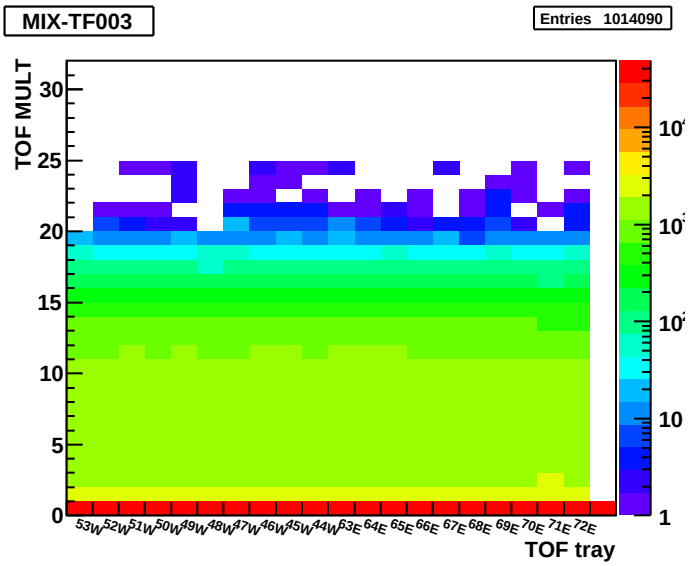

FP201 0-15 (ch5)
Entries 386320
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

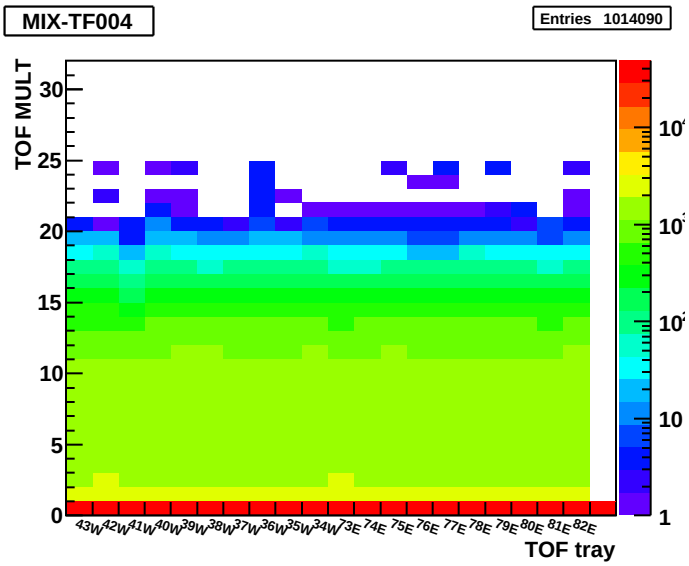

ST201 0-15 (ch6)
Entries 386320
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

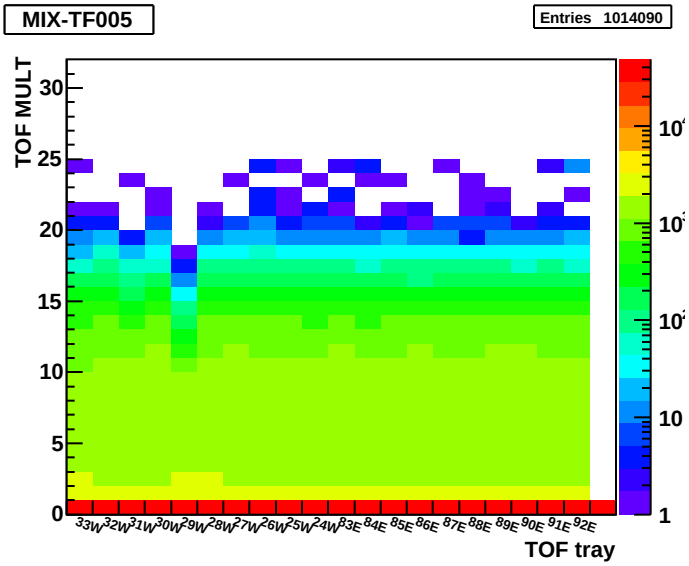

Unused (ch7)
Entries 386320

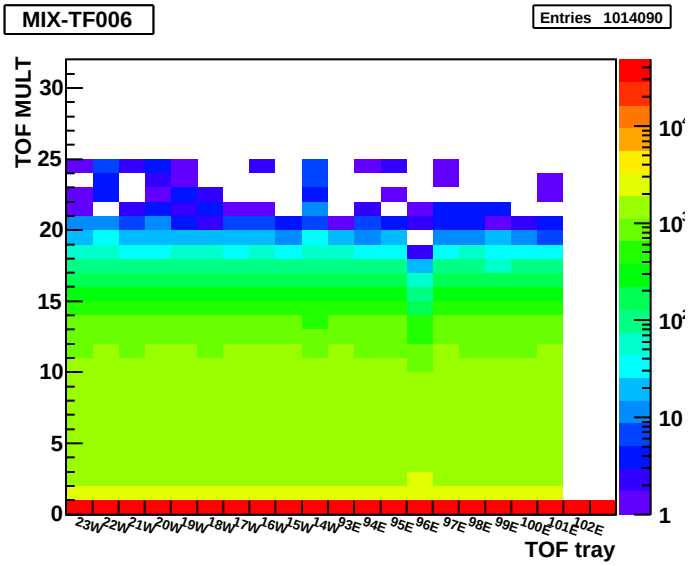

MIX-TF001
Entries 1014090
TOF MULT
TOF tray

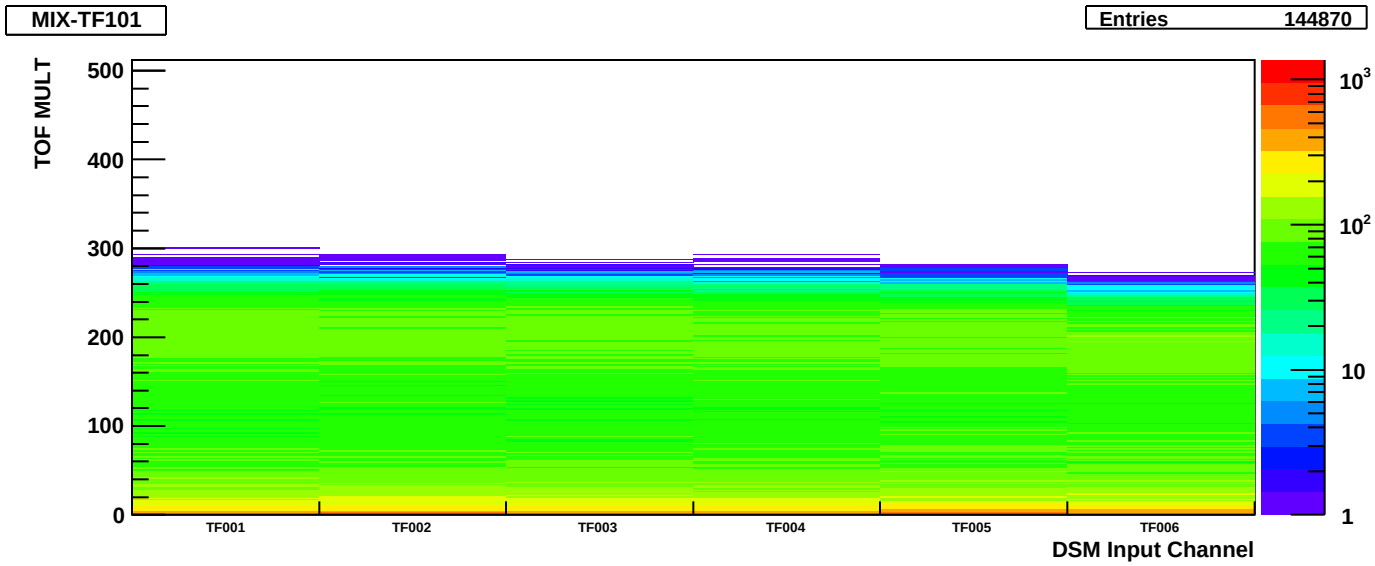

MIX-TF002
Entries 1014090
TOF MULT
TOF tray

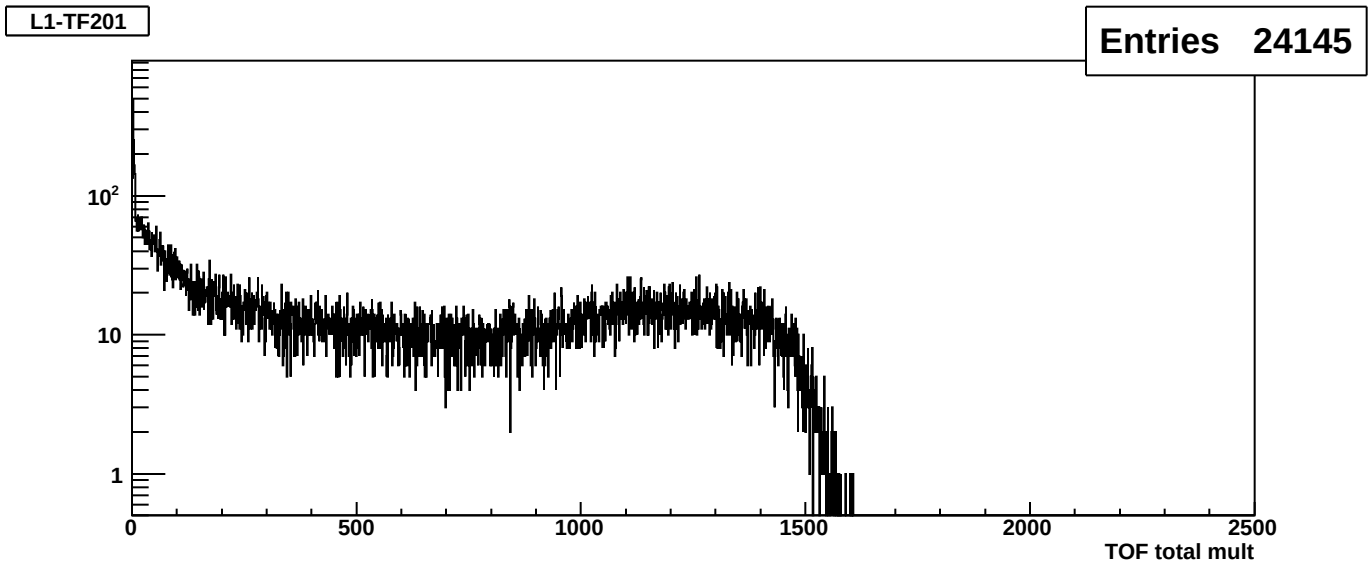

MIX-TF003
Entries 1014090
TOF MULT
TOF tray

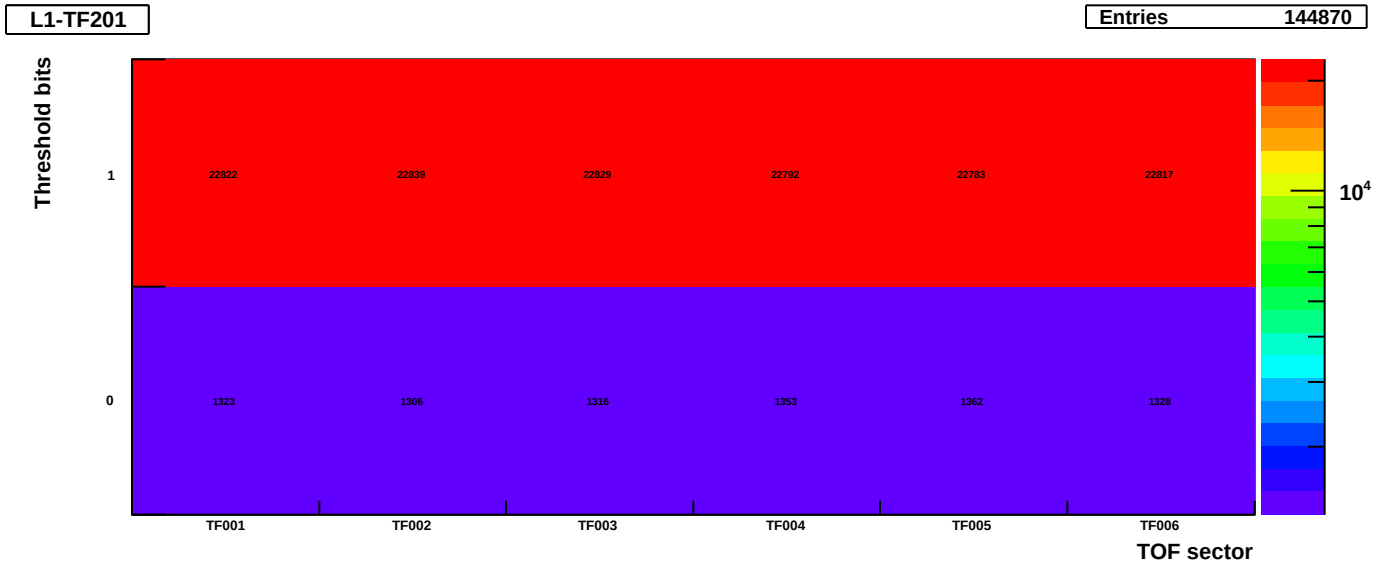

MIX-TF004
Entries 1014090
TOF MULT
TOF tray

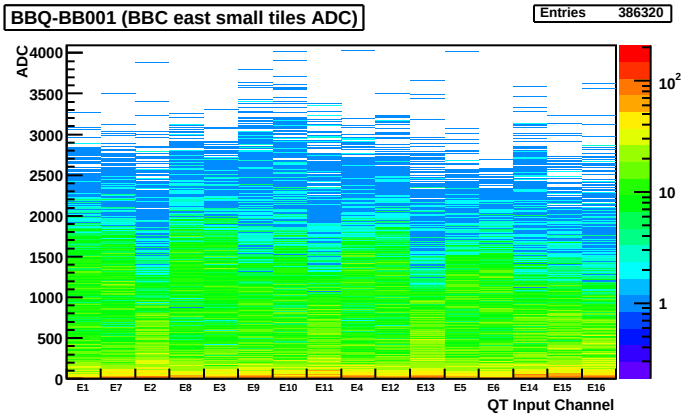

MIX-TF005
Entries 1014090
TOF MULT
TOF tray

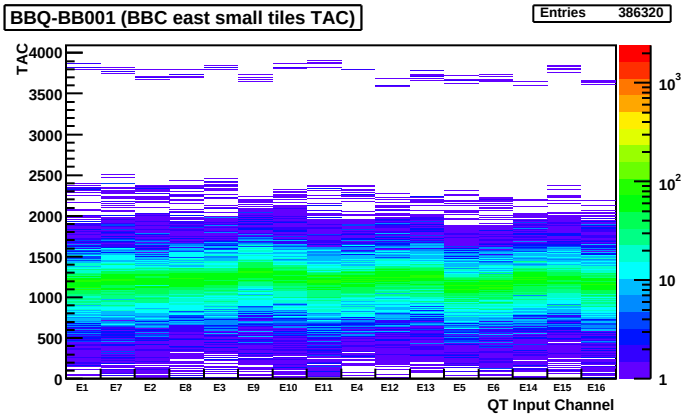

MIX-TF006
Entries 1014090
TOF MULT
TOF tray

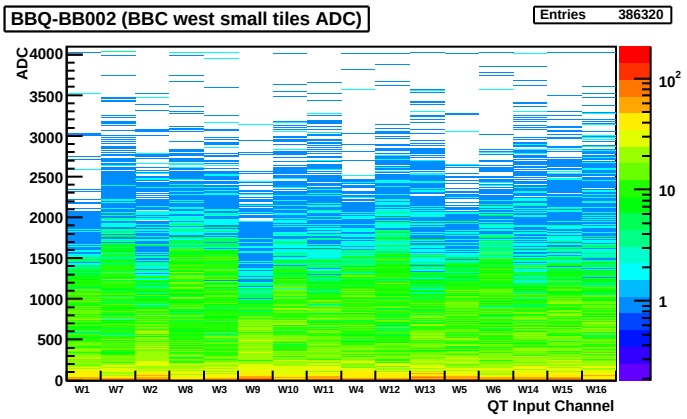

MIX-TF101
Entries 144870
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^3$
$10^2$
10
1

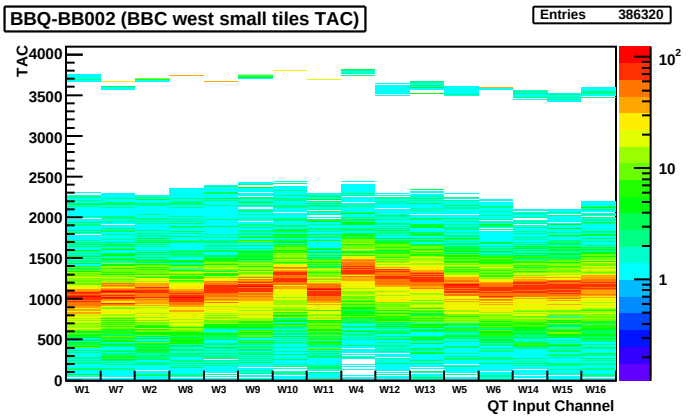

L1-TF201
Entries 24145
$10^2$
10
1
0
500
1000
1500
2000
2500
TOF total mult

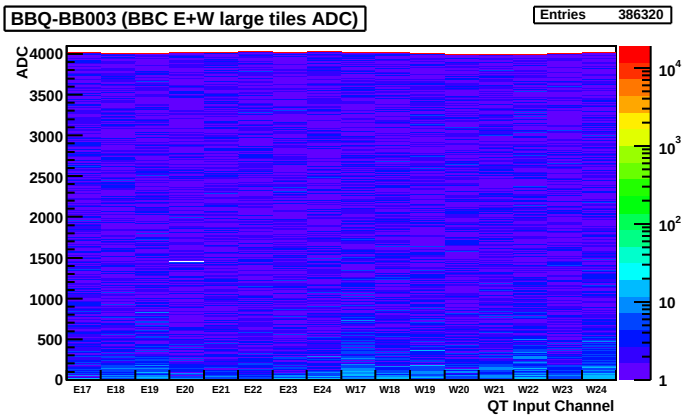

L1-TF201
Entries 144870
Threshold bits
1
0
22822
22839
22829
22792
22783
22817
1323
1306
1316
1353
1362
1328
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^4$

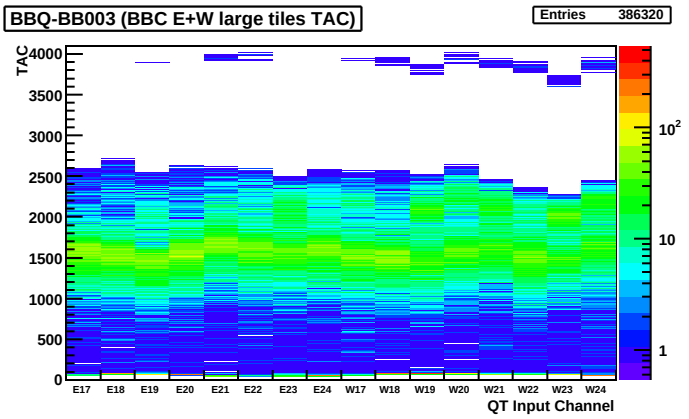

BBQ-BB001 (BBC east small tiles ADC)
Entries 386320
ADC
QT Input Channel

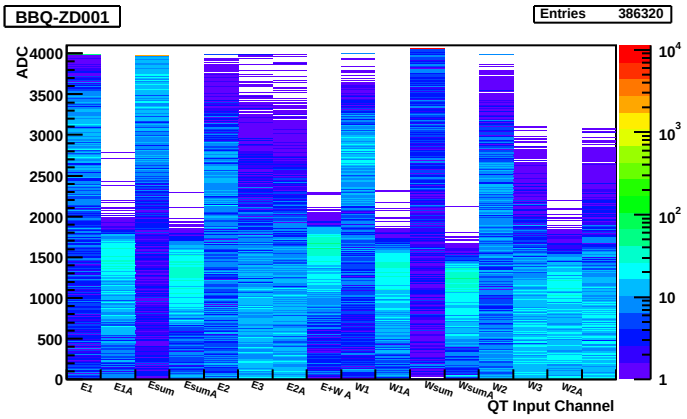

BBQ-BB001 (BBC east small tiles TAC)
Entries 386320
TAC
QT Input Channel

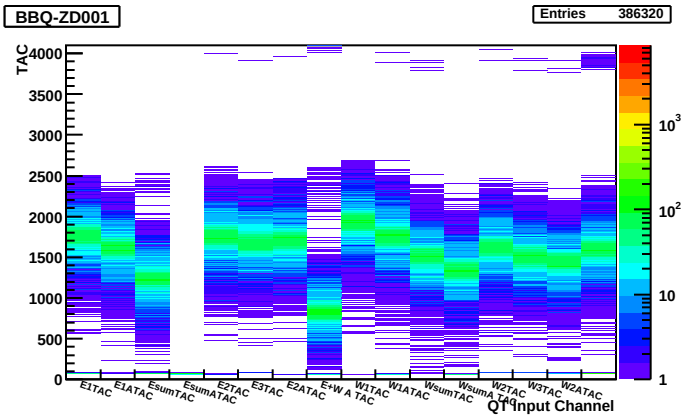

BBQ-BB002 (BBC west small tiles ADC)
Entries 386320
ADC
QT Input Channel

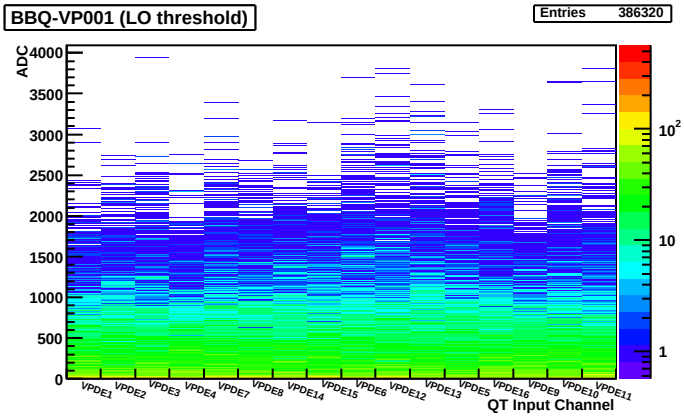

BBQ-BB002 (BBC west small tiles TAC)
Entries 386320
TAC
QT Input Channel

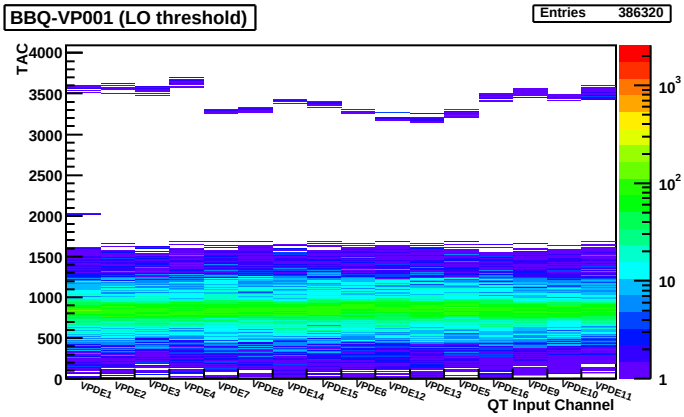

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 386320
ADC
QT Input Channel

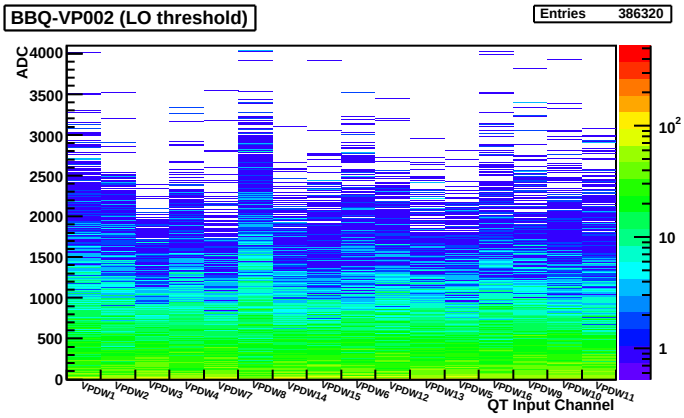

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 386320
TAC
QT Input Channel

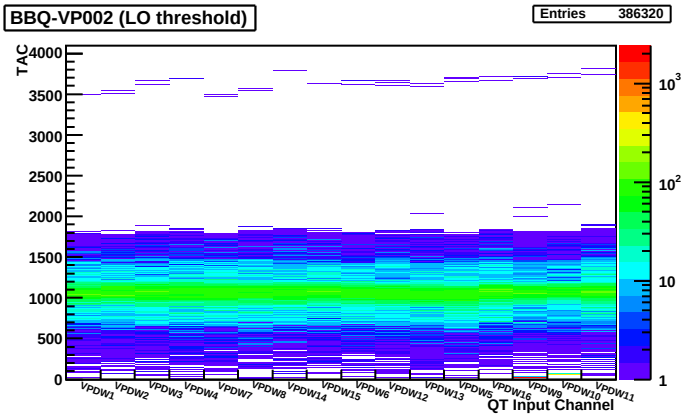

BBQ-ZD001
Entries 386320
ADC
QT Input Channel

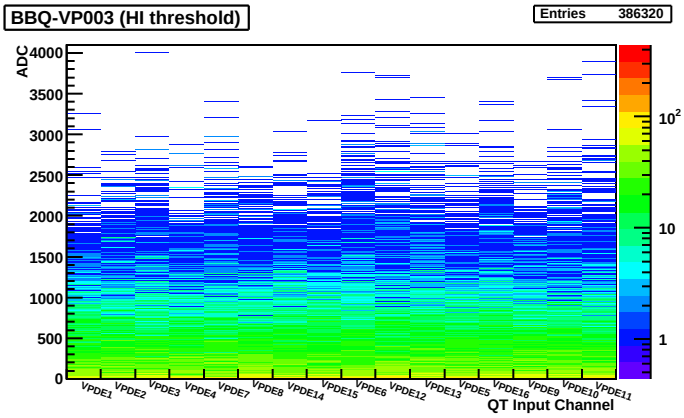

BBQ-ZD001
Entries 386320
TAC
QT Input Channel

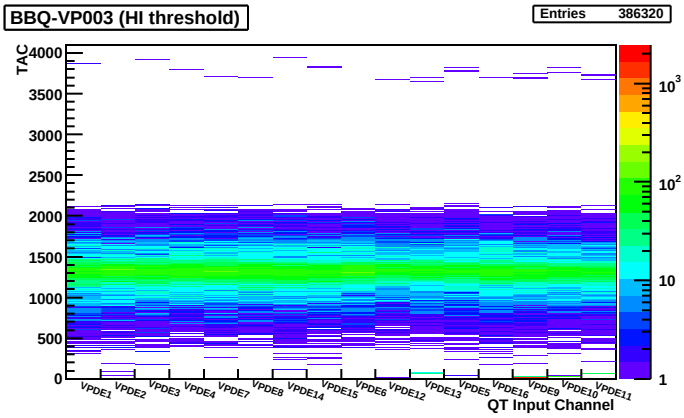

BBQ-VP001 (LO threshold)
Entries 386320
ADC
QT Input Channel

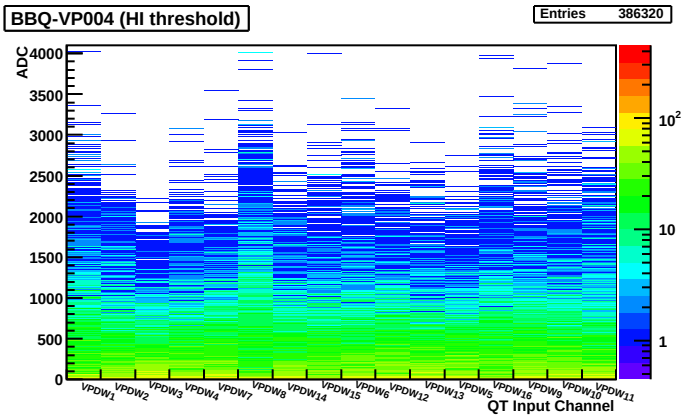

BBQ-VP001 (LO threshold)
Entries 386320
TAC
QT Input Channel

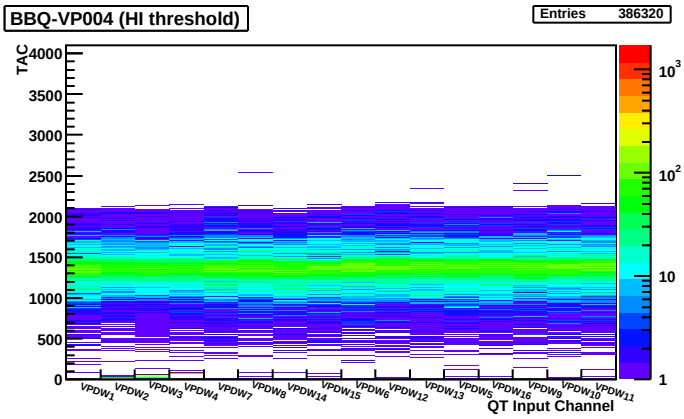

BBQ-VP002 (LO threshold)
Entries 386320
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 386320
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 386320
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 386320
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 386320
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 386320
TAC
QT Input Channel