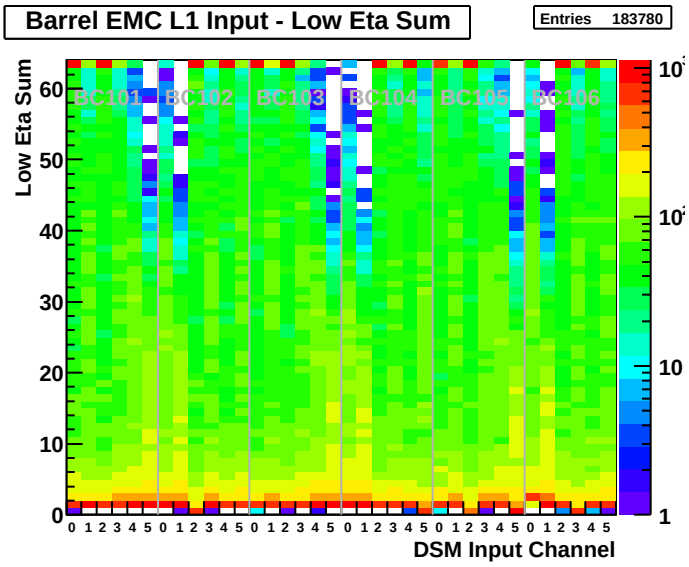
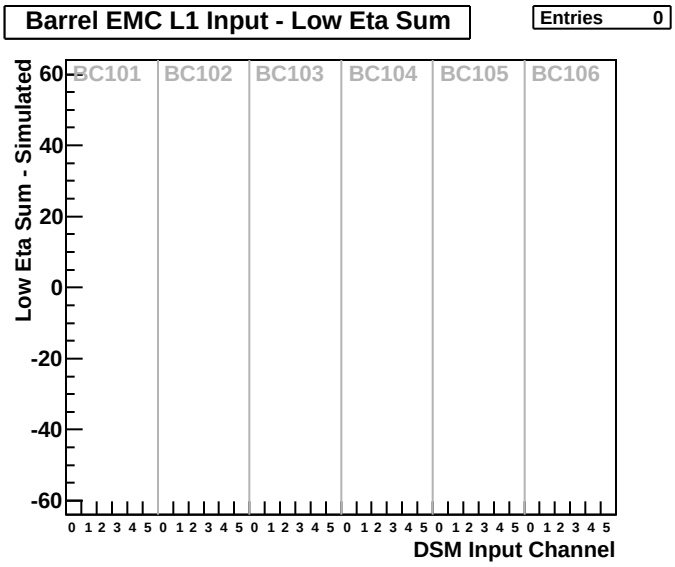
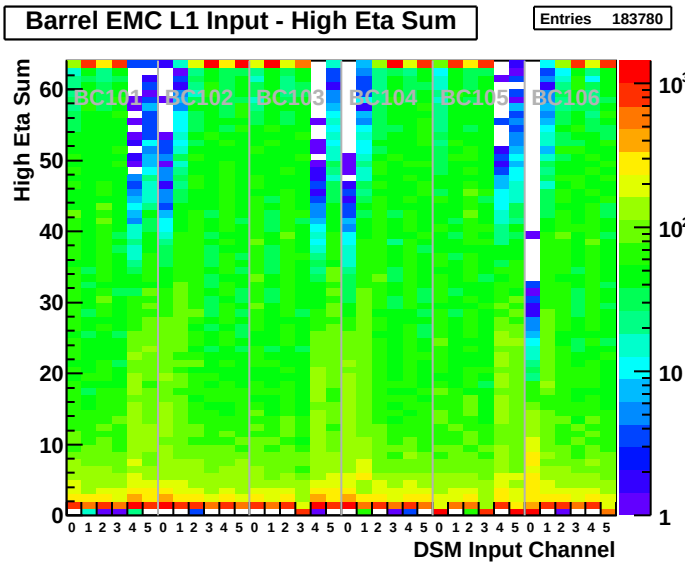
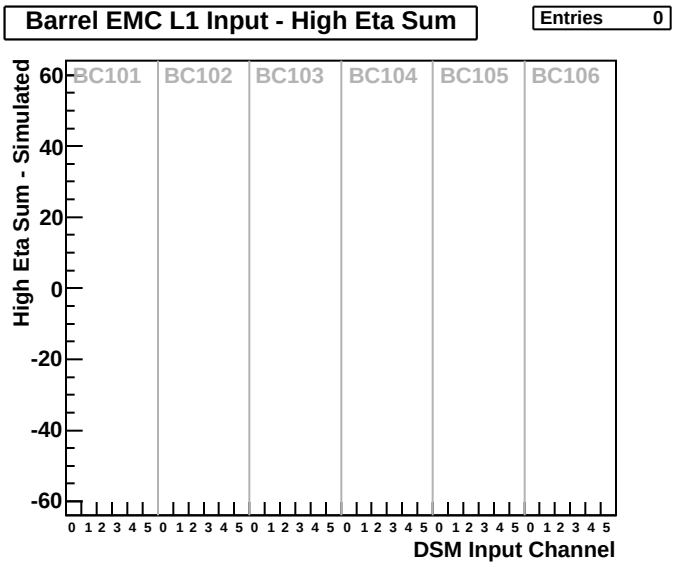
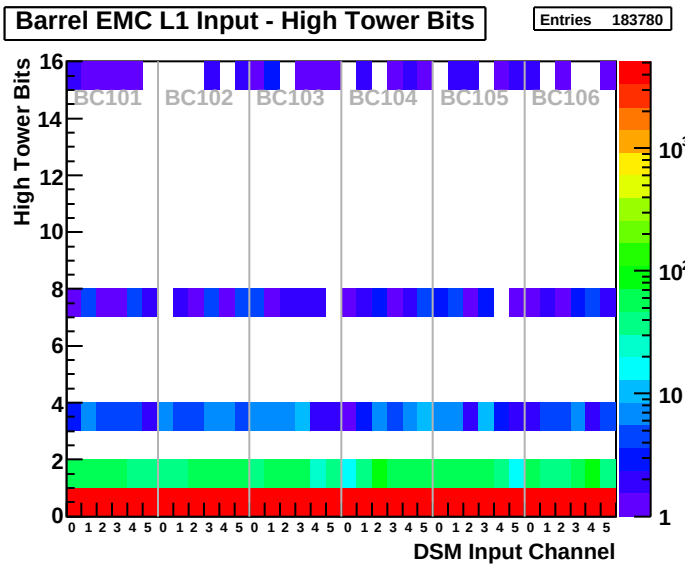
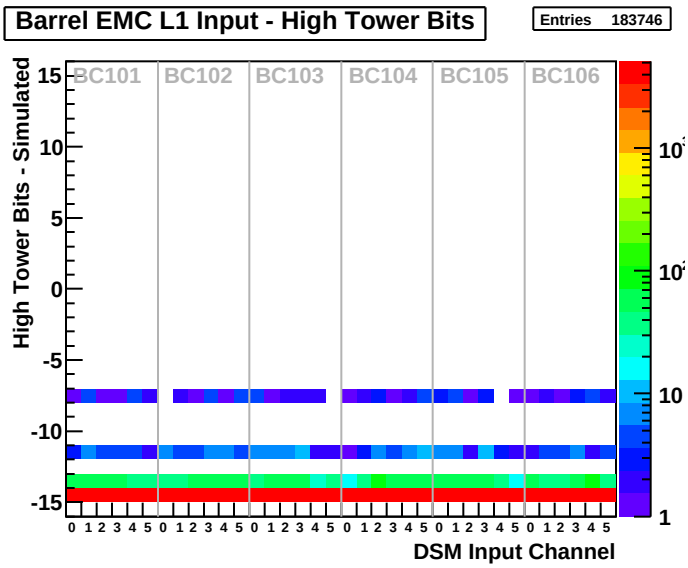

Barrel EMC L1 Input - Low Eta Sum
Entries 183780
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

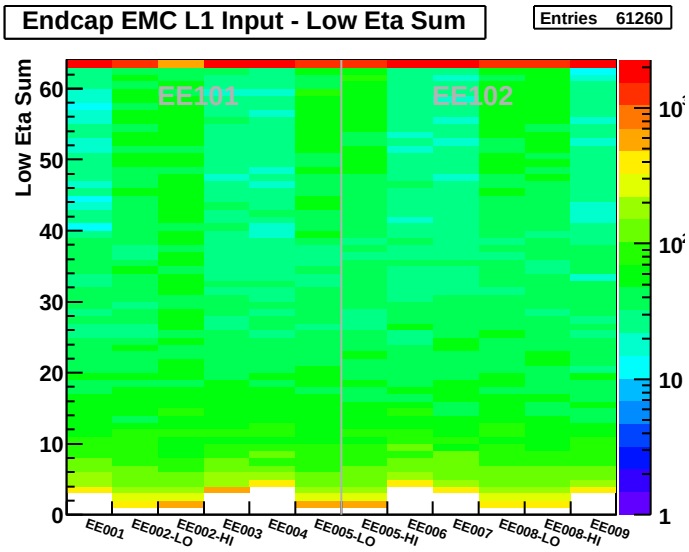

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

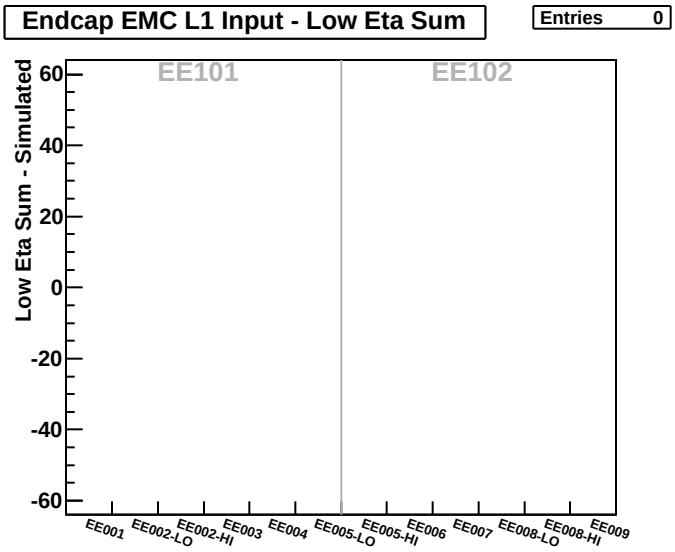

Barrel EMC L1 Input - High Eta Sum
Entries 183780
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

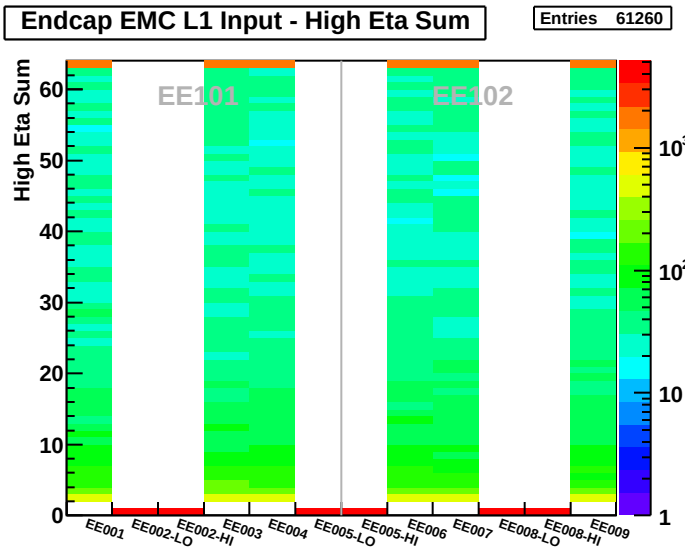

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

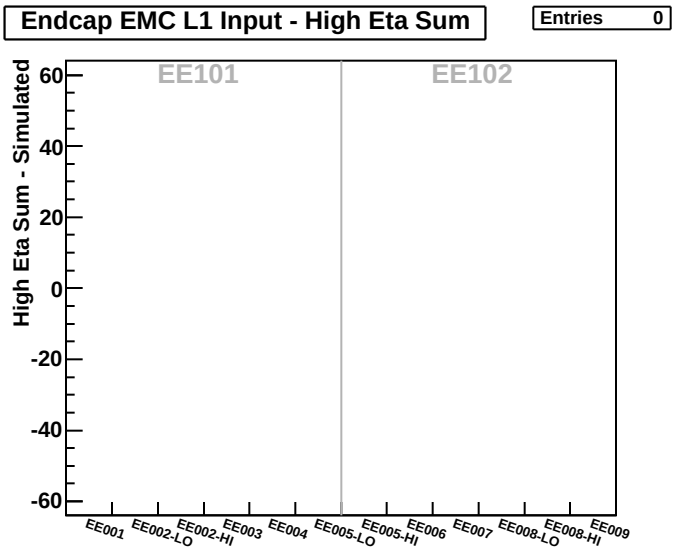

Barrel EMC L1 Input - High Tower Bits
Entries 183780
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

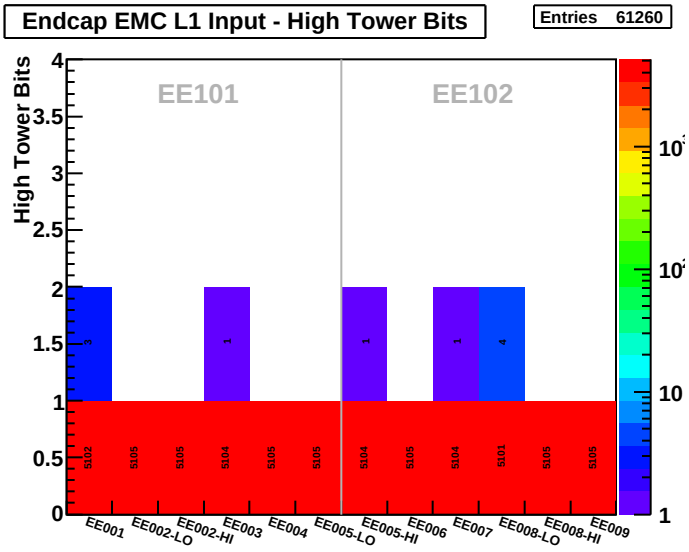

Barrel EMC L1 Input - High Tower Bits
Entries 183746
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

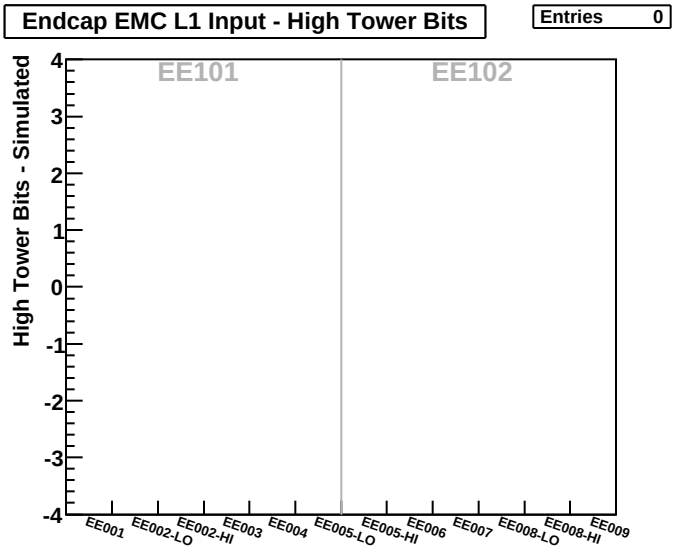

Endcap EMC L1 Input - Low Eta Sum
Entries 61260
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

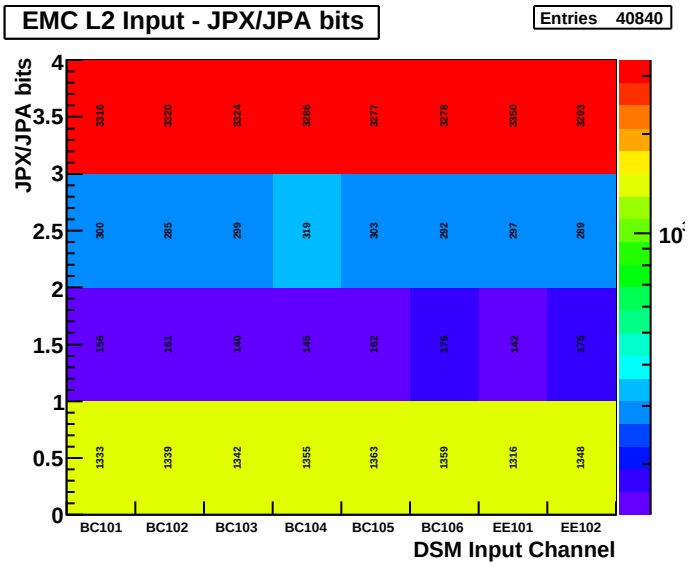

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

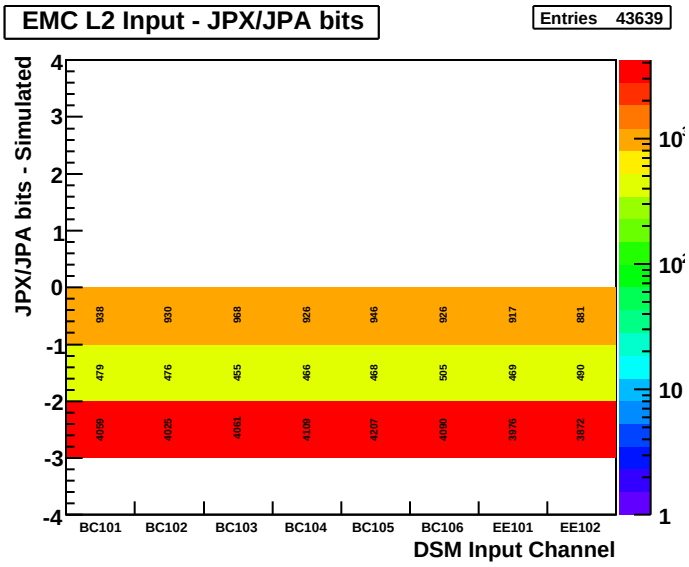

Endcap EMC L1 Input - High Eta Sum
Entries 61260
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

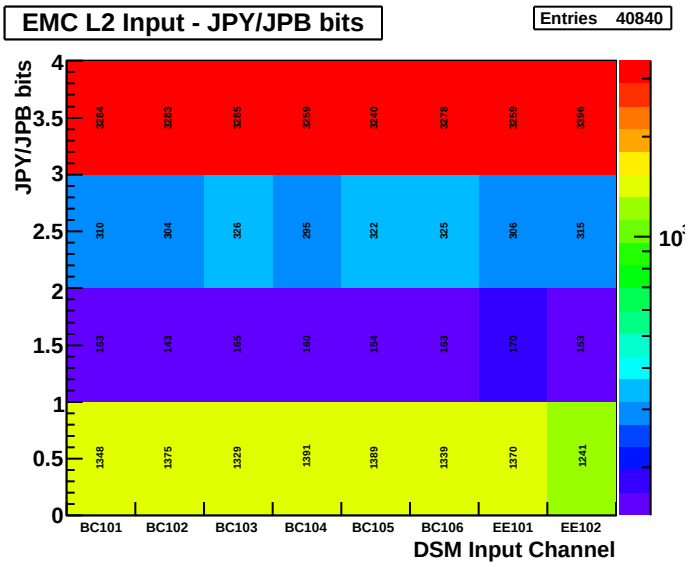

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

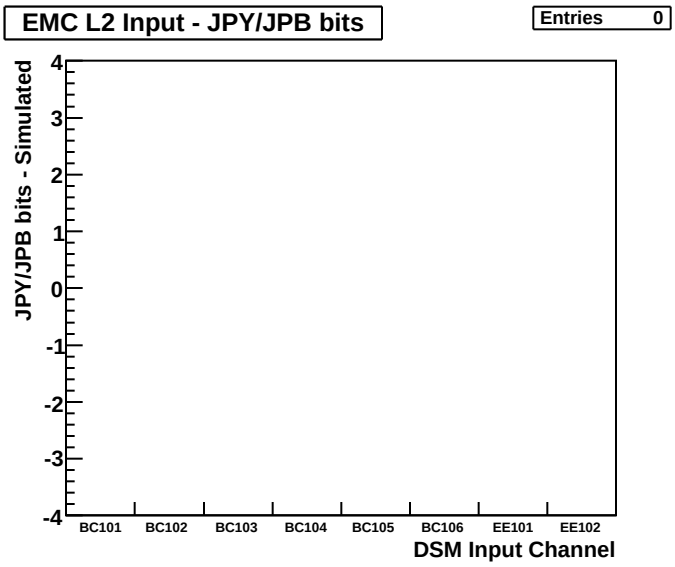

Endcap EMC L1 Input - High Tower Bits
Entries 61260
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

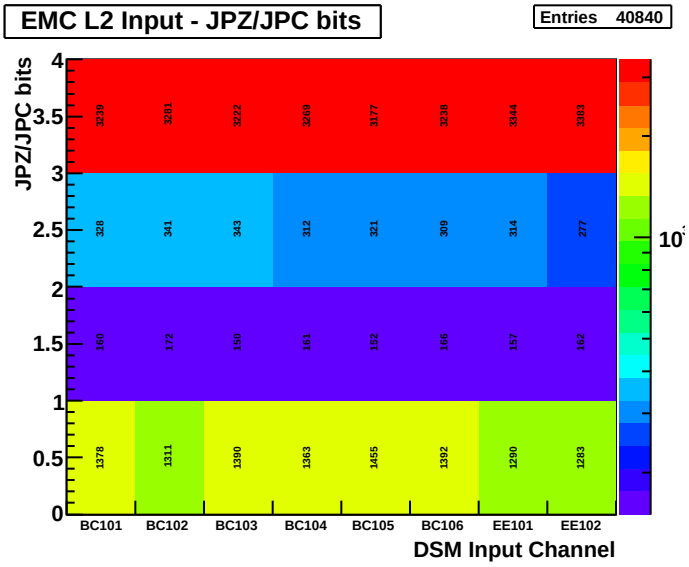

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

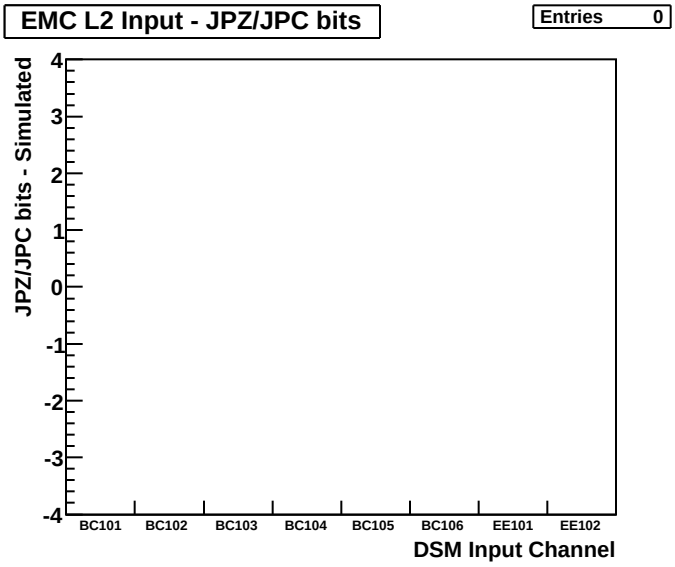

EMC L2 Input - JPX/JPA bits
Entries 40840
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3316 3320 3324 3286 3277 3278 3350 3293
300 295 299 319 303 292 297 289
156 161 140 145 162 176 142 175
1333 1339 1342 1355 1363 1359 1316 1348

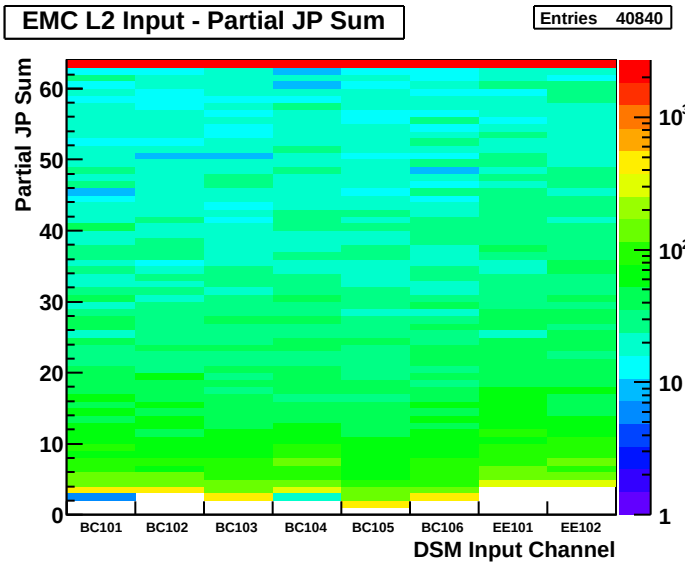

EMC L2 Input - JPX/JPA bits
Entries 43639
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
938 930 968 926 946 926 917 881
479 476 455 466 468 505 469 490
4099 4025 4061 4109 4207 4090 3976 3872

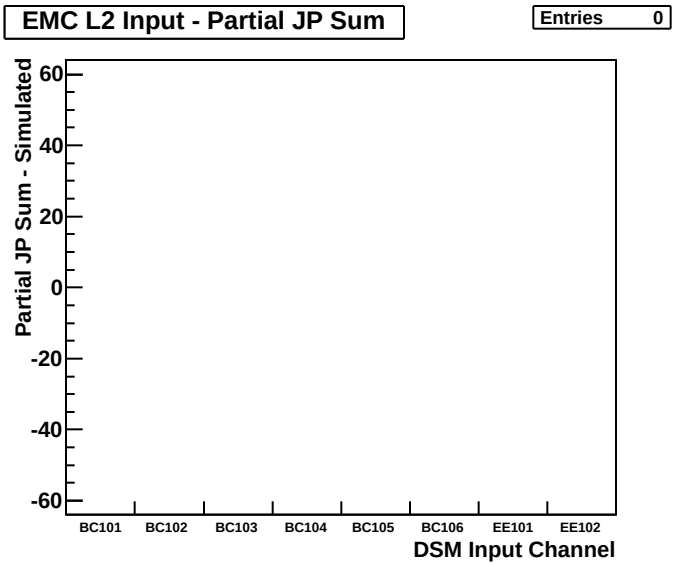

EMC L2 Input - JPY/JPB bits
Entries 40840
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3284 3283 3285 3259 3240 3278 3259 3396
310 304 326 295 322 325 306 315
163 143 165 160 154 163 170 153
1348 1375 1329 1391 1389 1339 1370 1241

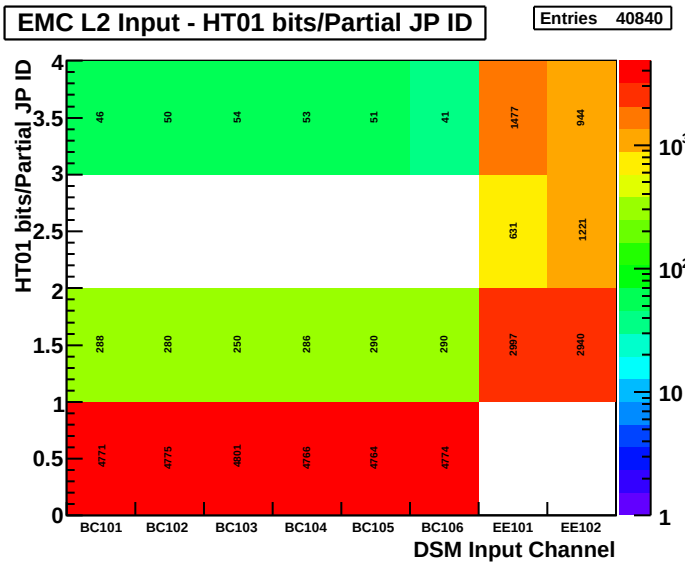

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

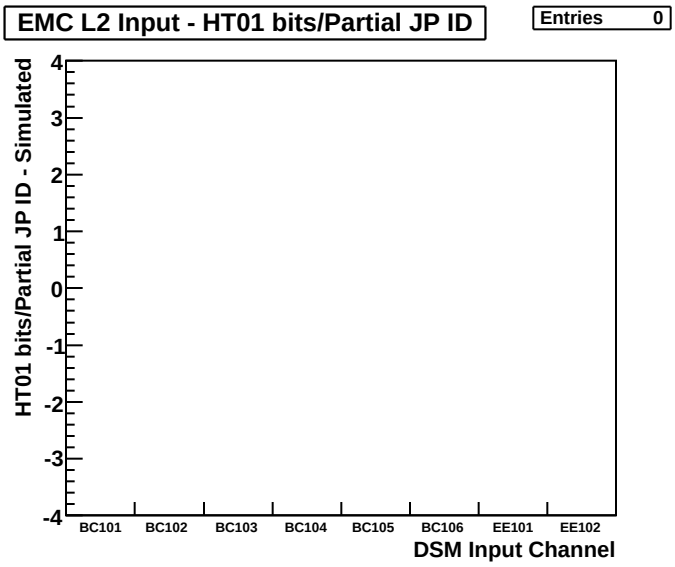

EMC L2 Input - JPZ/JPC bits
Entries 40840
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3239 3281 3222 3269 3177 3238 3344 3383
328 341 343 312 321 309 314 277
160 172 150 161 152 166 157 162
1378 1311 1390 1363 1455 1392 1290 1283

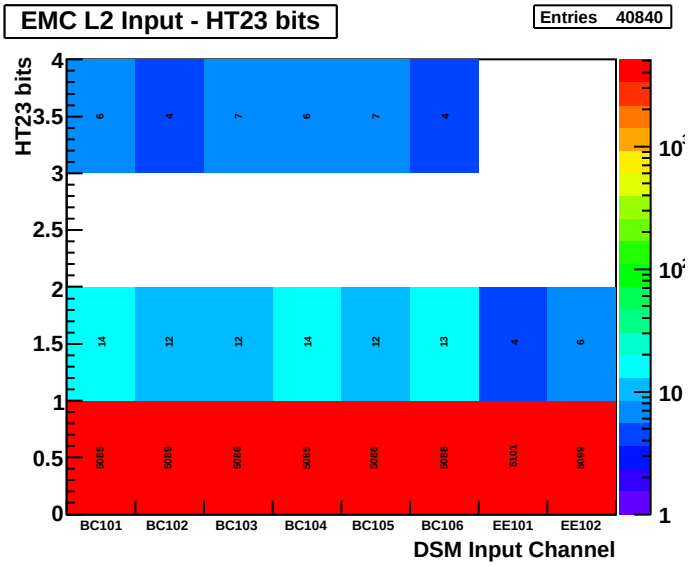

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

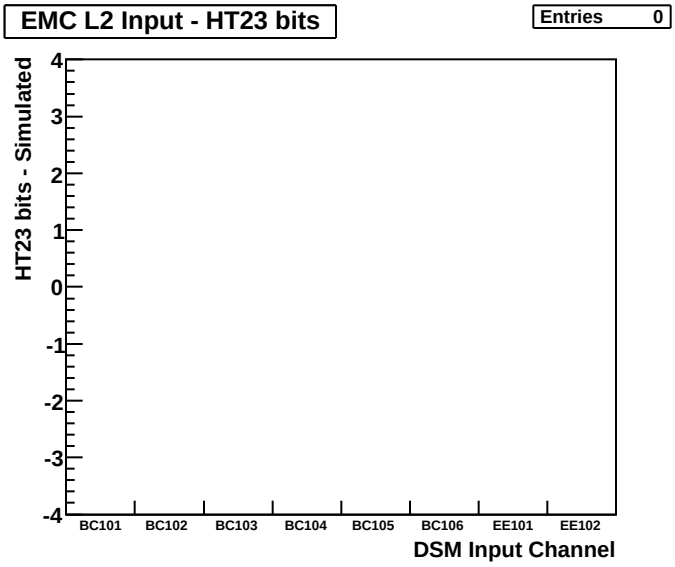

EMC L2 Input - Partial JP Sum
Entries 40840
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

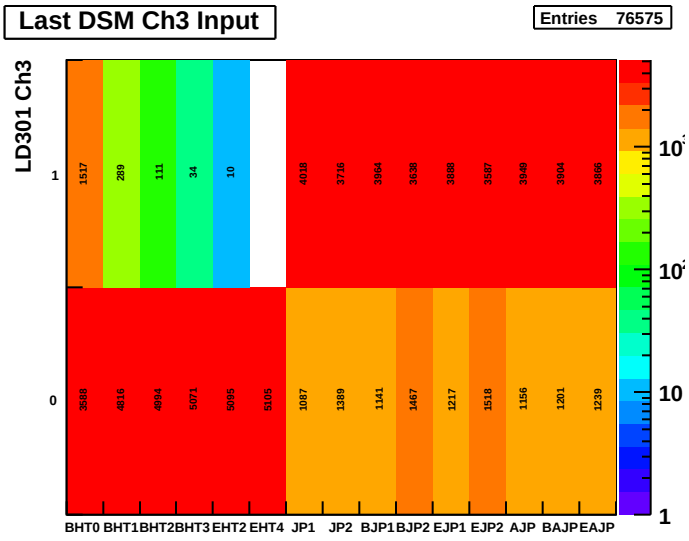

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

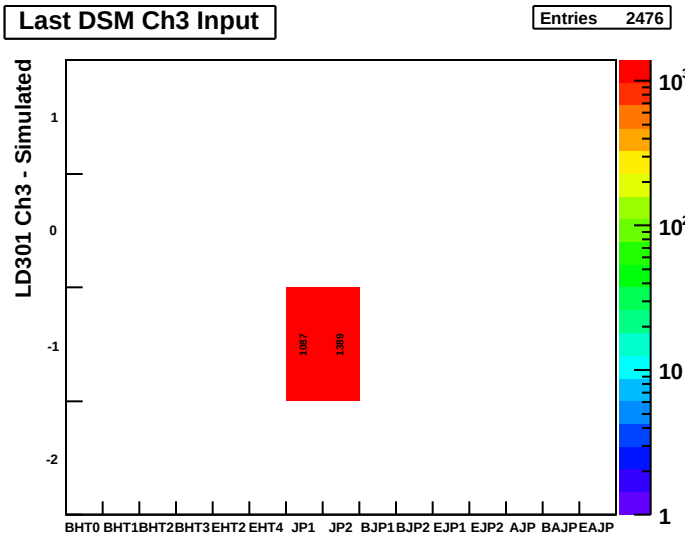

EMC L2 Input - HT01 bits/Partial JP ID
Entries 40840
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

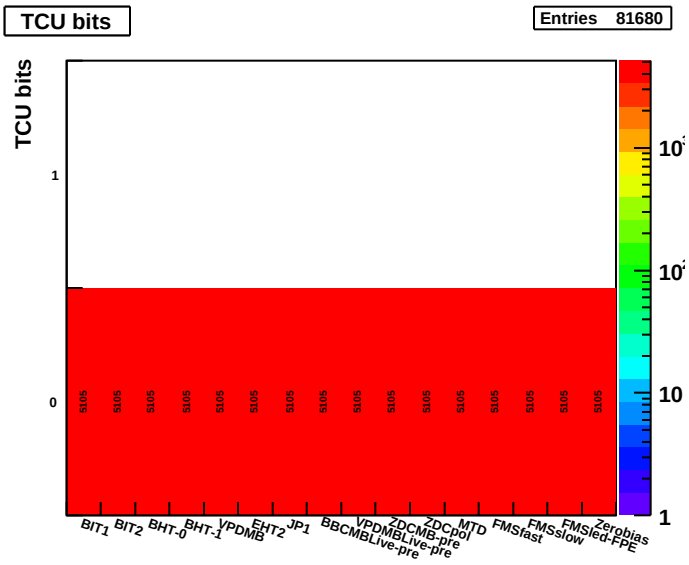

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

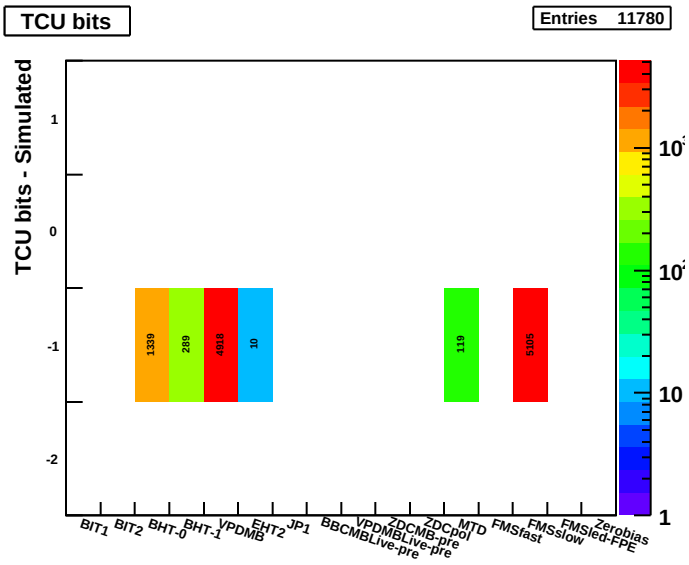

EMC L2 Input - HT23 bits
Entries 40840
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

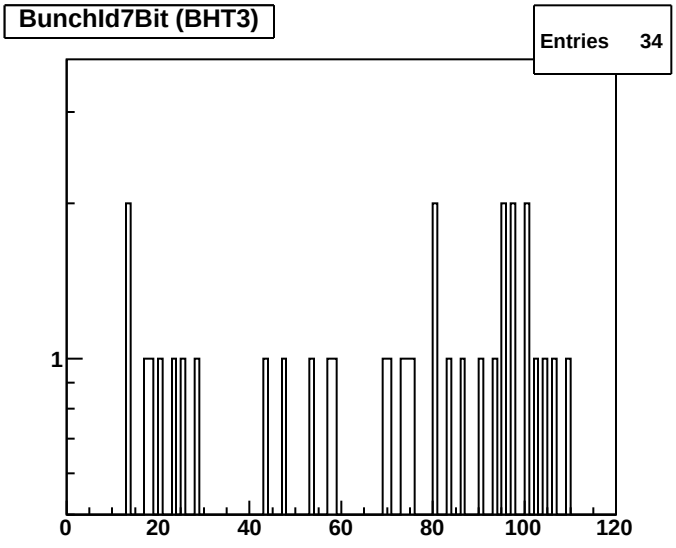

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

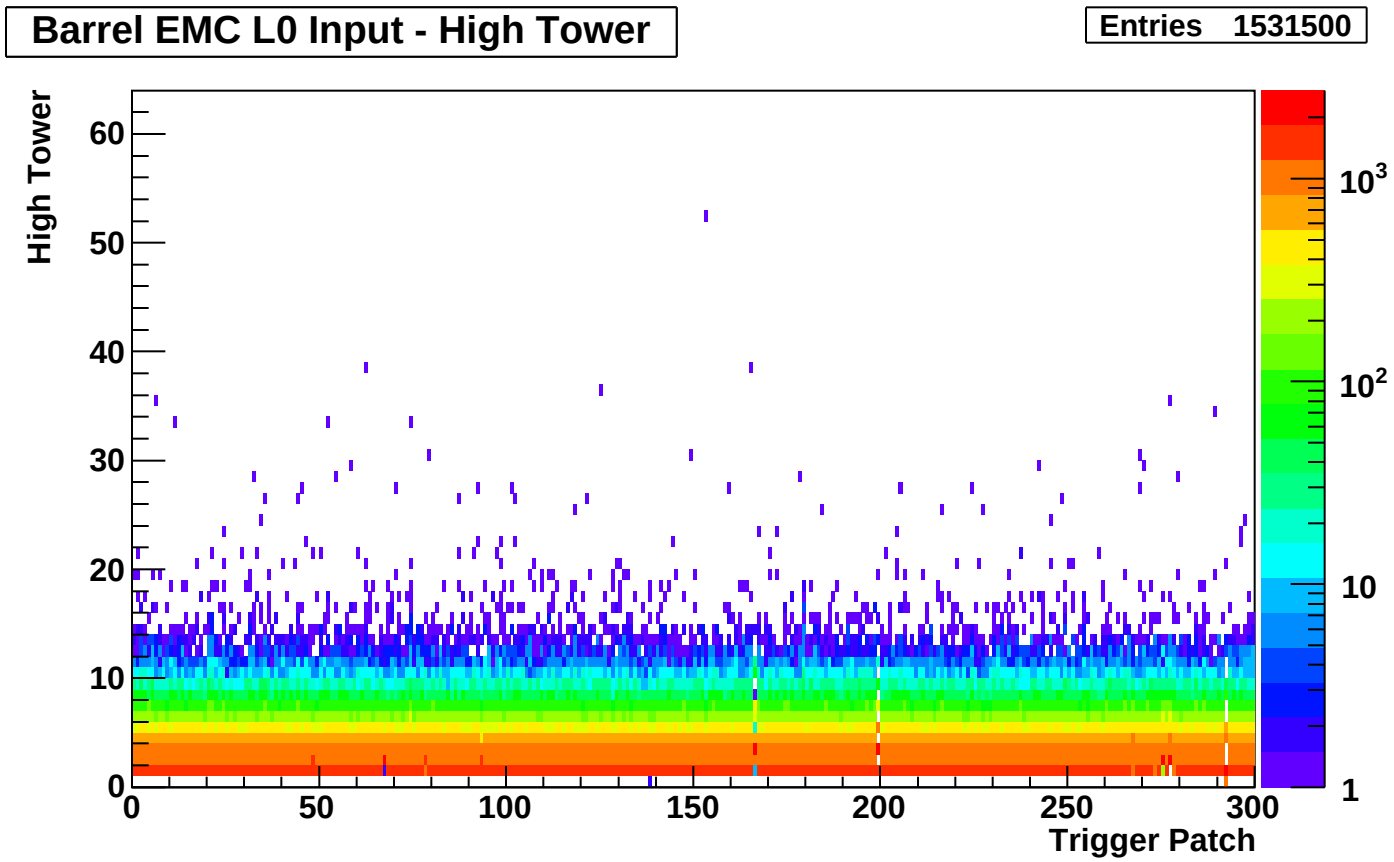

Last DSM Ch3 Input
Entries 76575
LD301 Ch3
1517 289 111 34 10 4018 3716 3964 3638 3888 3587 3949 3904 3866
3588 4816 4994 5071 5095 5105 1087 1389 1141 1467 1217 1518 1156 1201 1239
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP1 EJP2 AJP BAJP EAJP

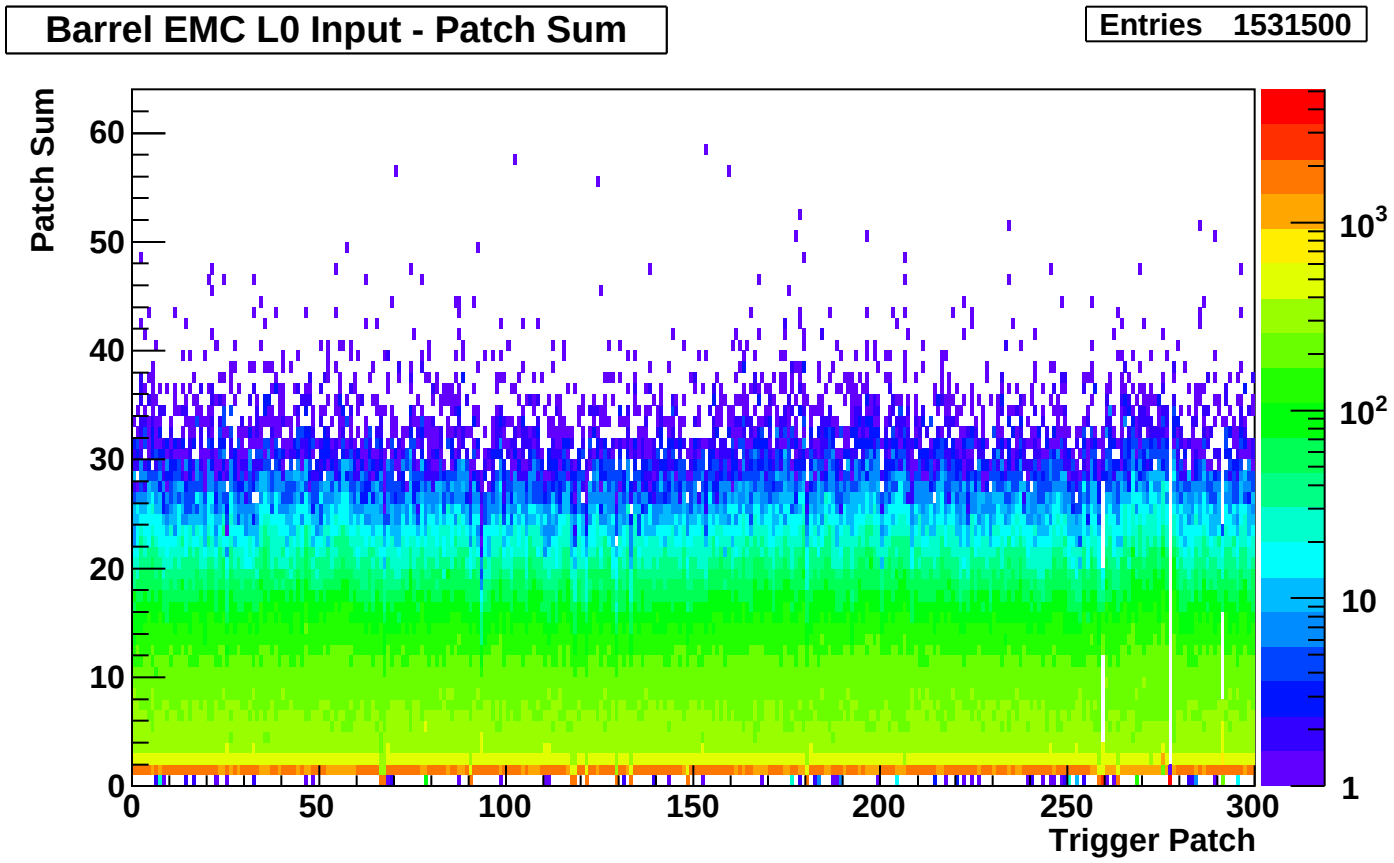

Last DSM Ch3 Input
Entries 2476
LD301 Ch3 - Simulated
1087 1389
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

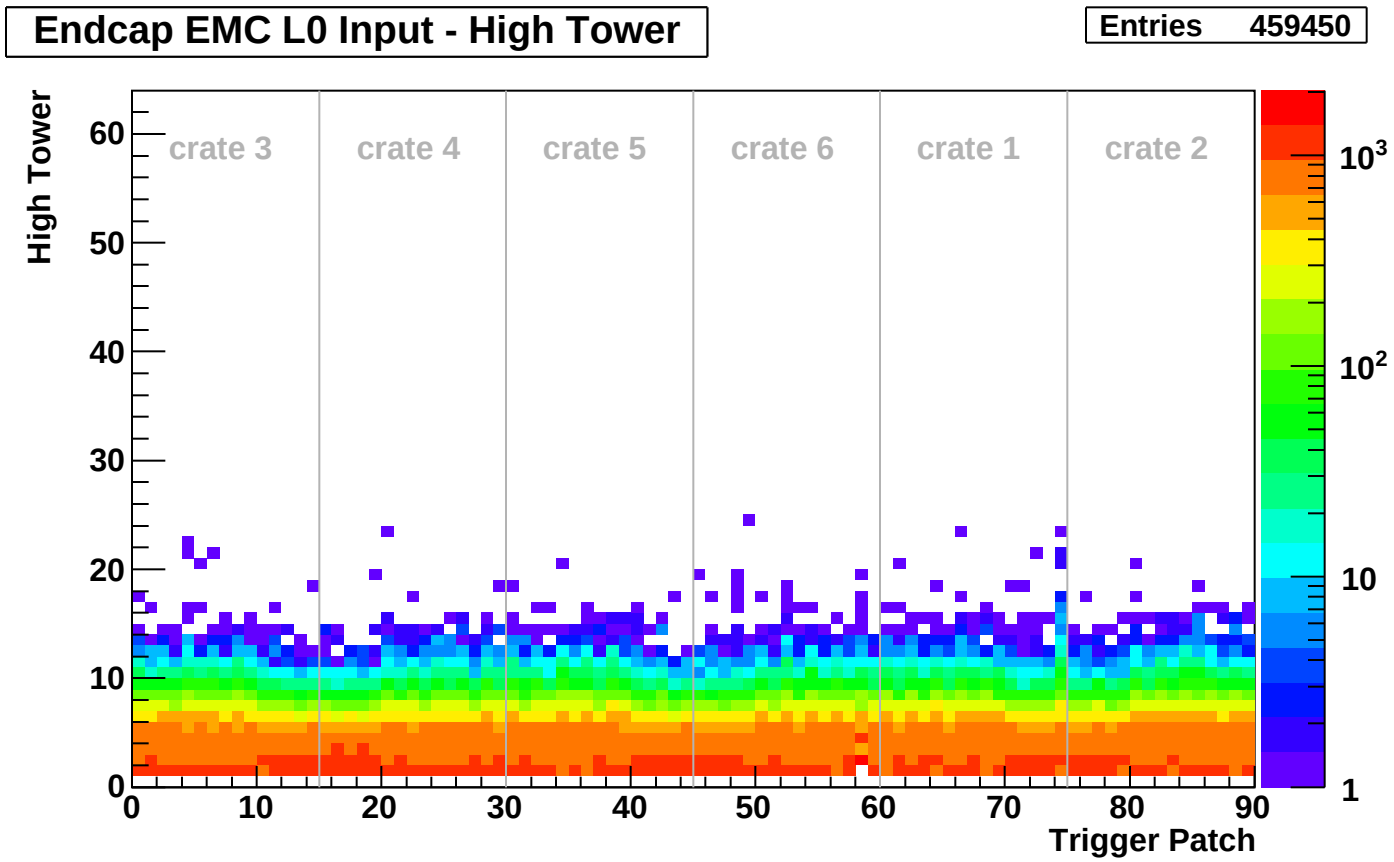

TCU bits
Entries 81680
TCU bits
5105 5105 5105 5105 5105 5105 5105 5105 5105 5105 5105 5105 5105 5105 5105 5105
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

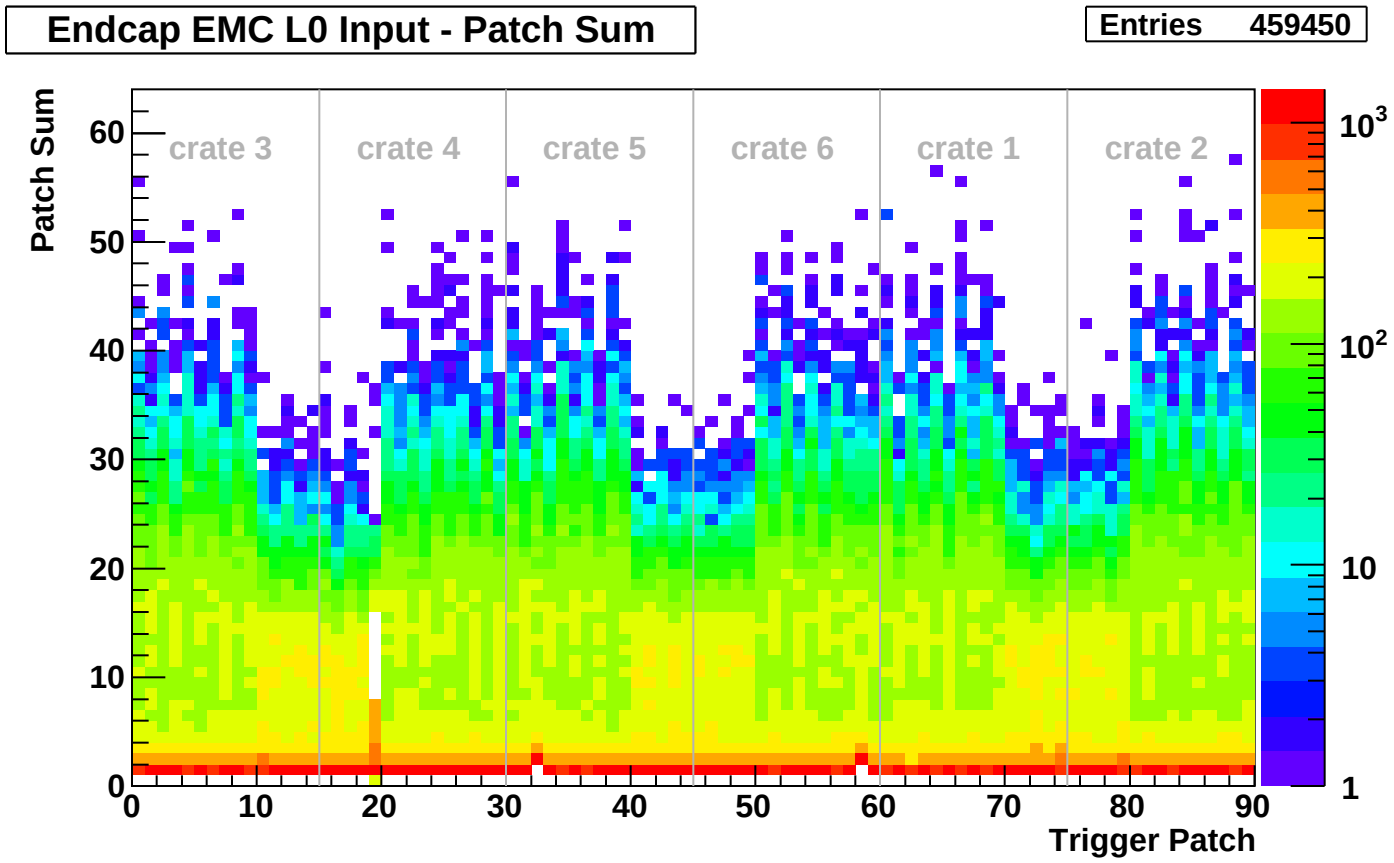

TCU bits
Entries 11780
TCU bits - Simulated
1339 289 4918 10 119 5105
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

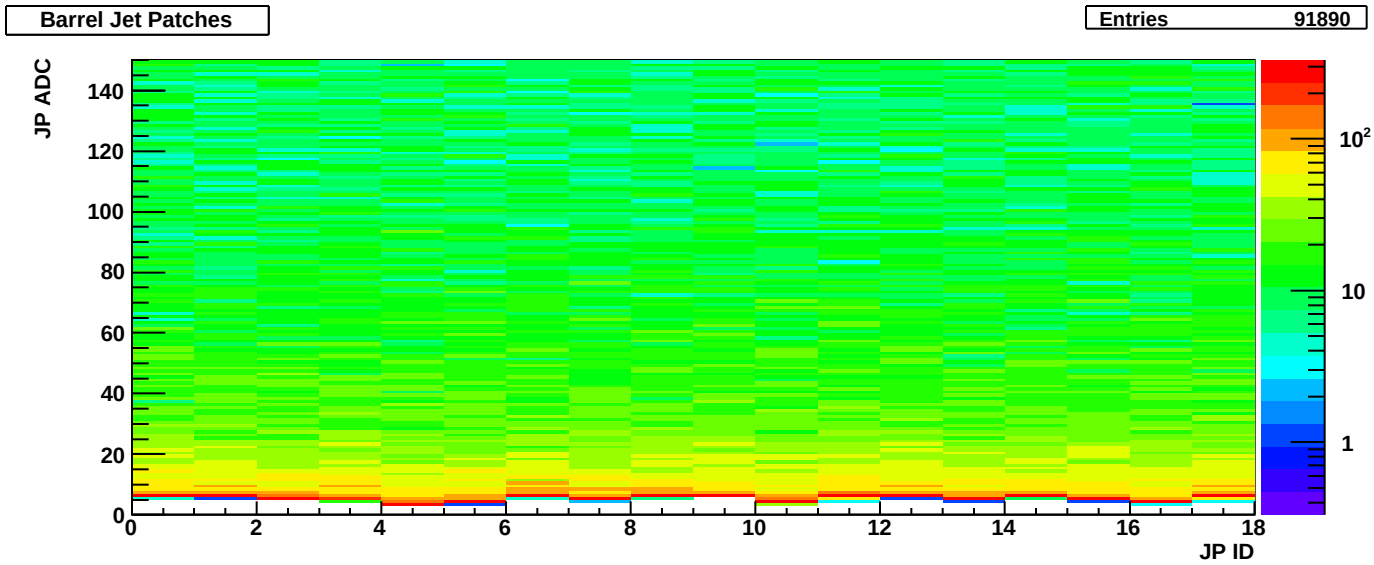

BunchId7Bit (BHT3)
Entries 34
0 20 40 60 80 100 120

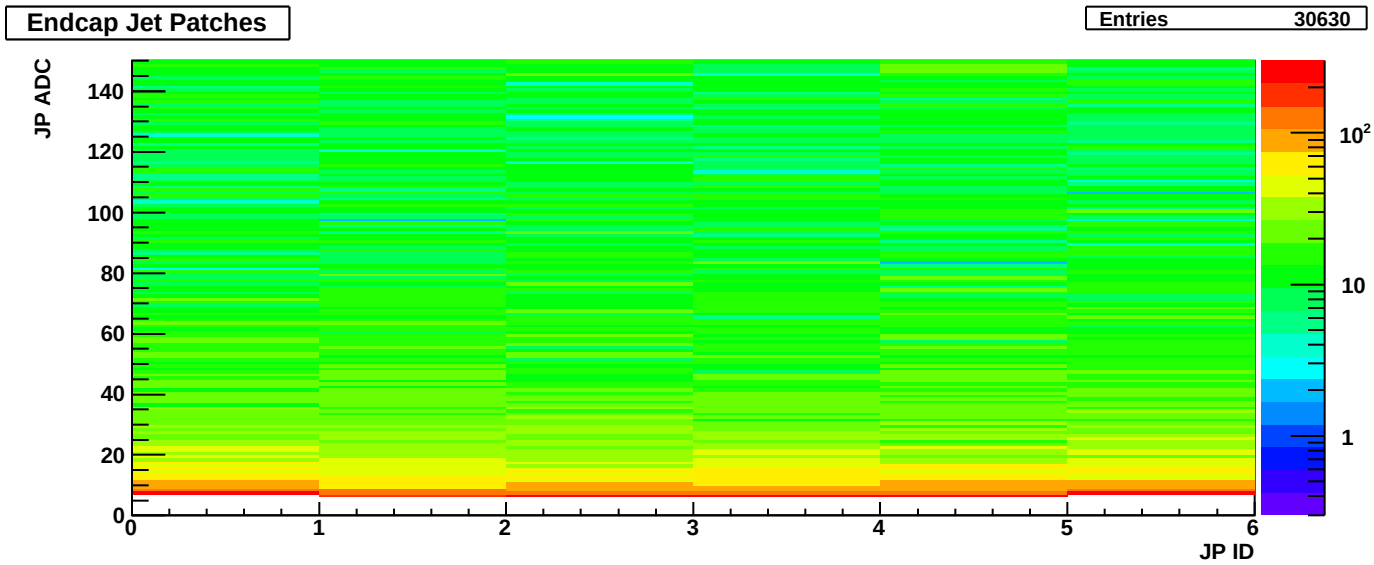

Barrel EMC L0 Input - High Tower
Entries 1531500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

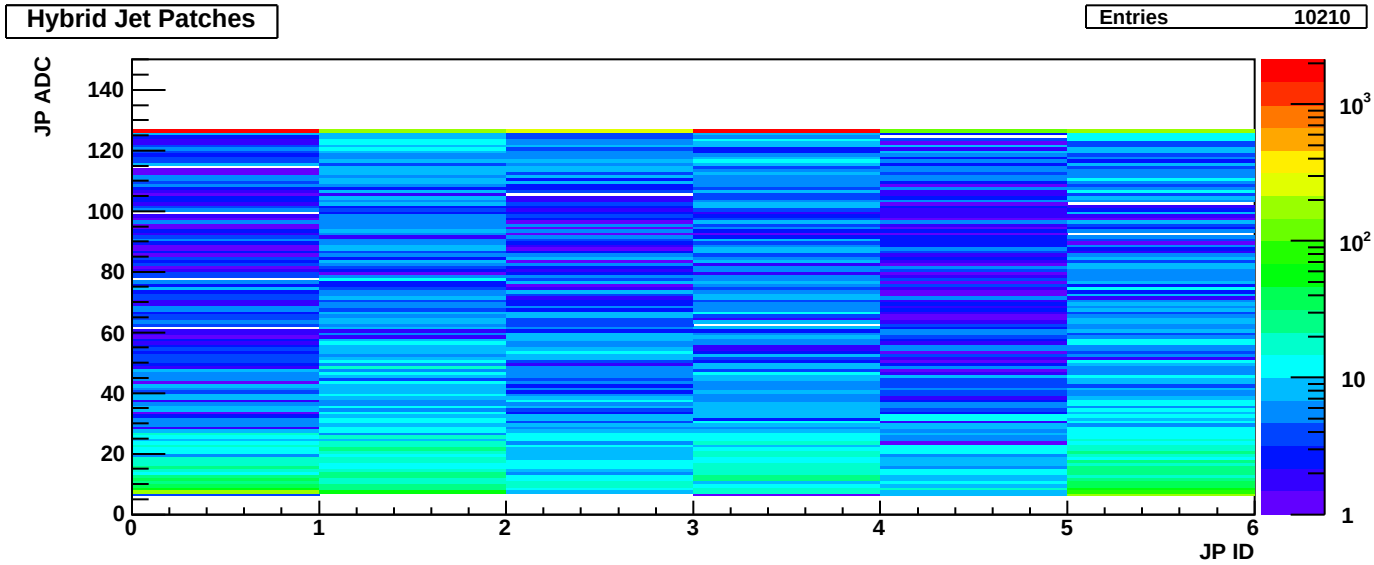

Barrel EMC L0 Input - Patch Sum
Entries 1531500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

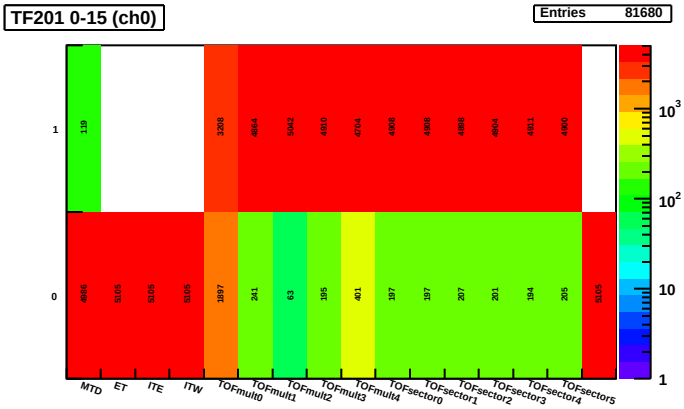

Endcap EMC L0 Input - High Tower
Entries 459450
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

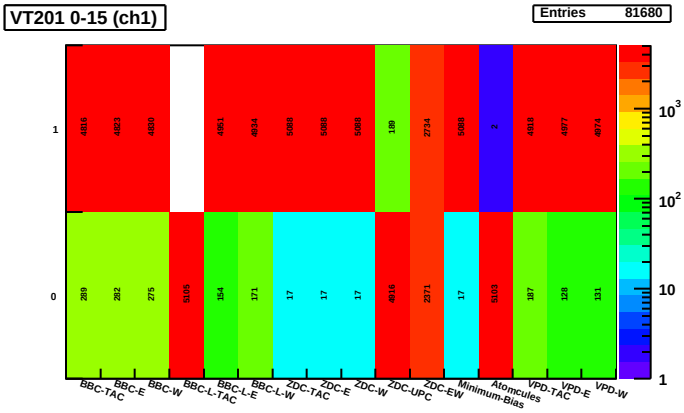

Endcap EMC L0 Input - Patch Sum
Entries 459450
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

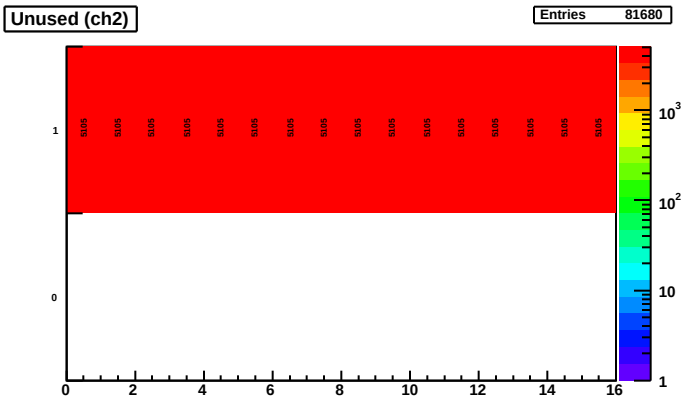

Barrel Jet Patches
Entries 91890
JP ADC
JP ID

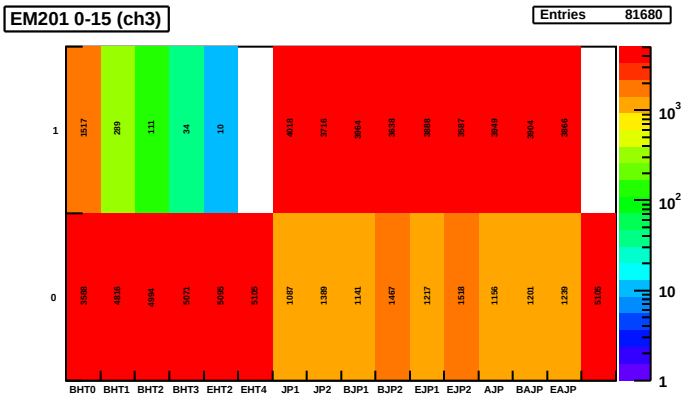

Endcap Jet Patches
Entries 30630
JP ADC
JP ID

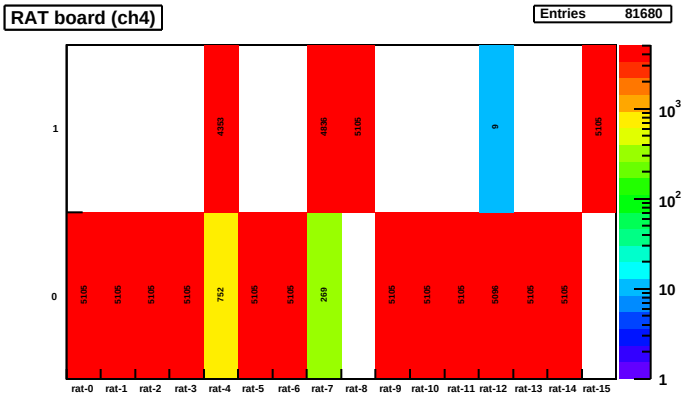

Hybrid Jet Patches
Entries 10210
JP ADC
JP ID

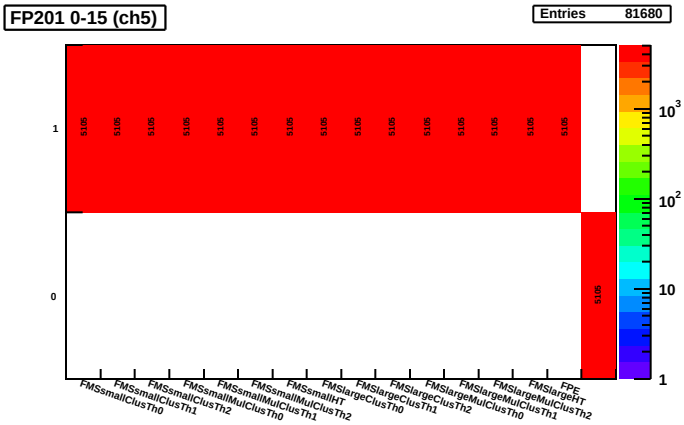

TF201 0-15 (ch0)
Entries 81680
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

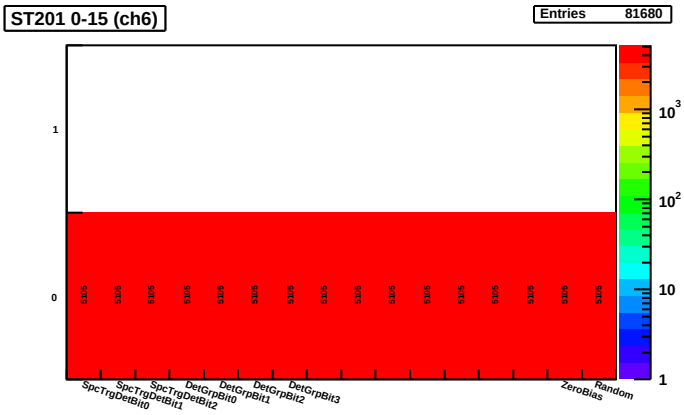

VT201 0-15 (ch1)
Entries 81680
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

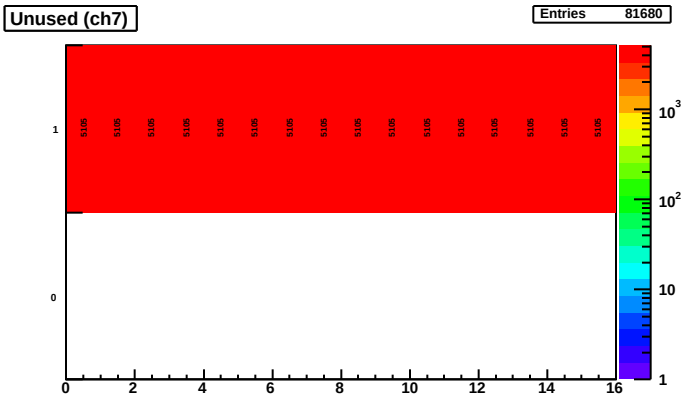

Unused (ch2)
Entries 81680

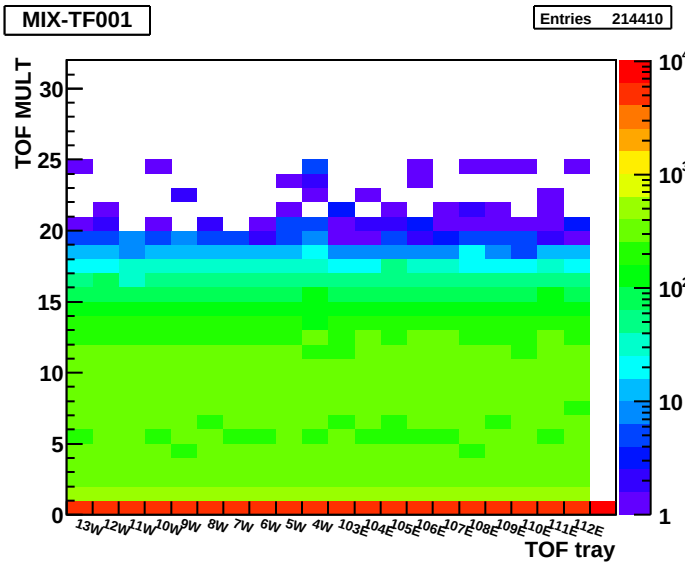

EM201 0-15 (ch3)
Entries 81680
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

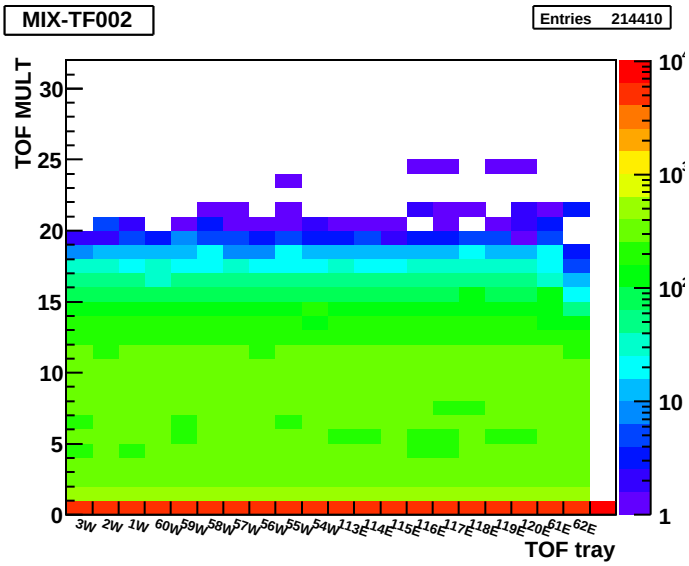

RAT board (ch4)
Entries 81680
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

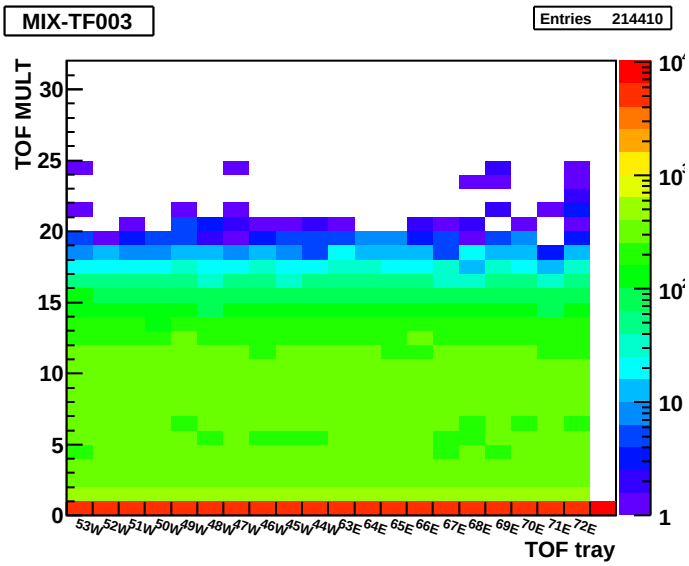

FP201 0-15 (ch5)
Entries 81680
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

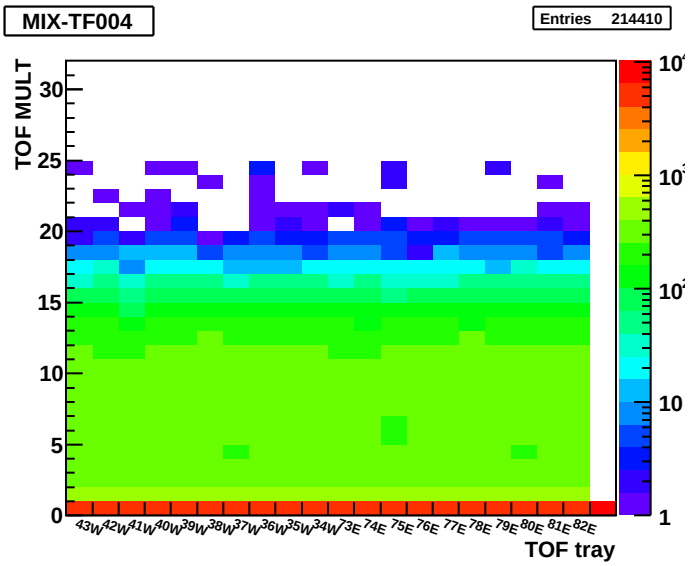

ST201 0-15 (ch6)
Entries 81680
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

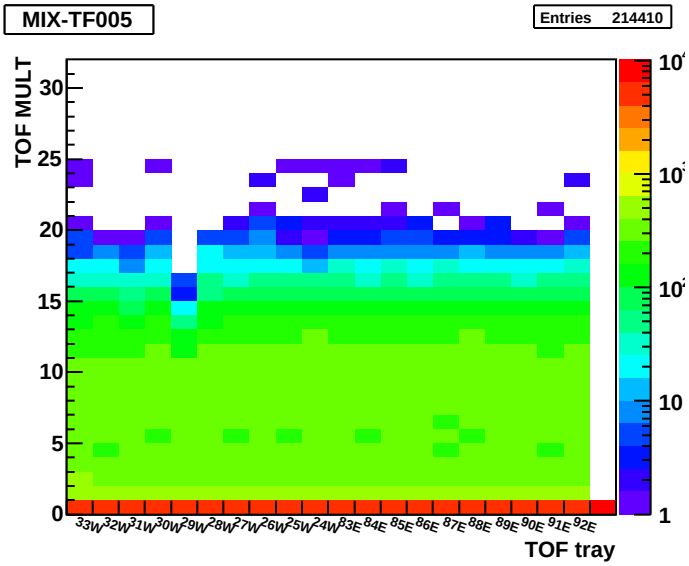

Unused (ch7)
Entries 81680

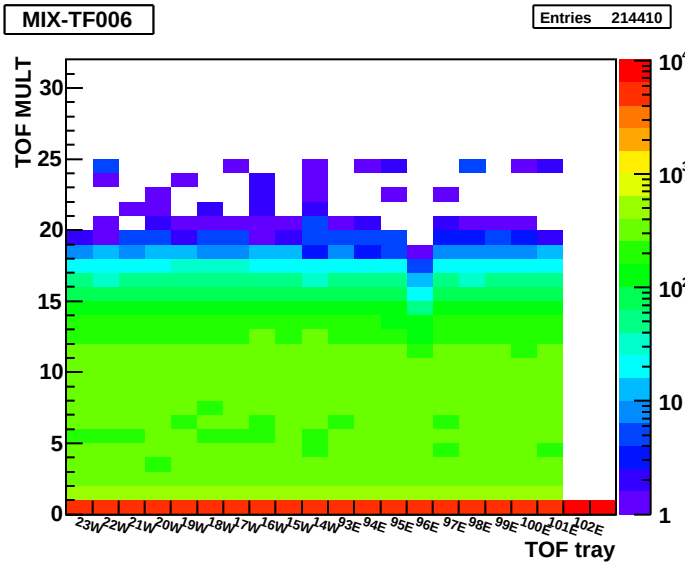

MIX-TF001
Entries 214410
TOF MULT
TOF tray

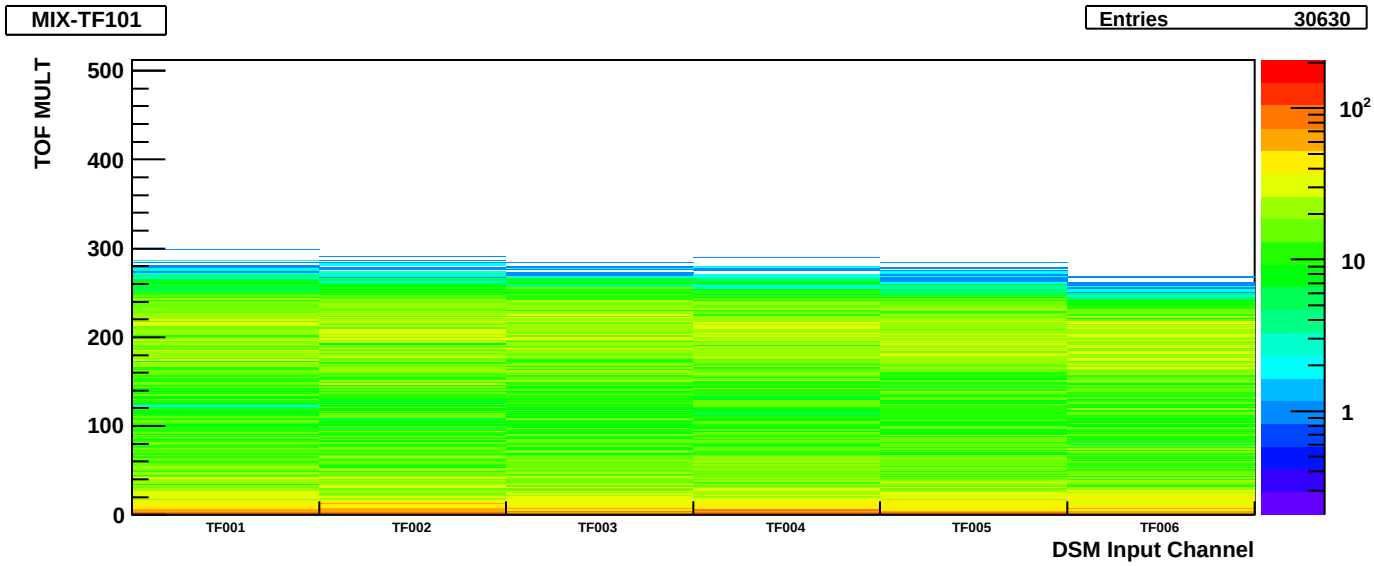

MIX-TF002
Entries 214410
TOF MULT
TOF tray

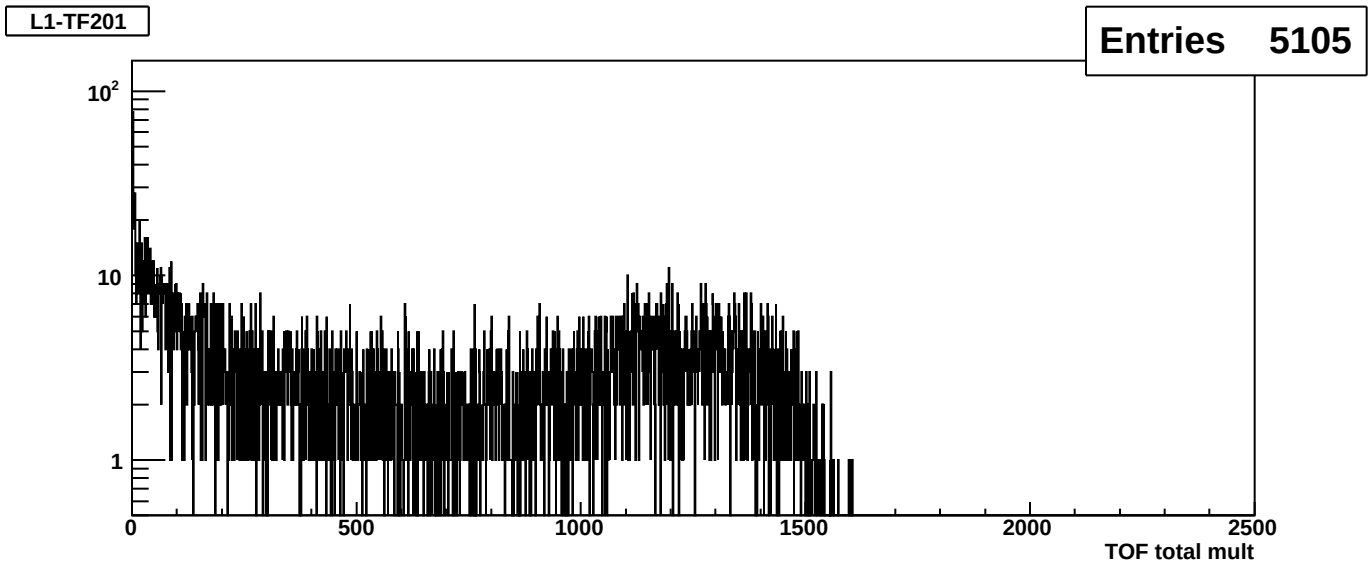

MIX-TF003
Entries 214410
TOF MULT
TOF tray

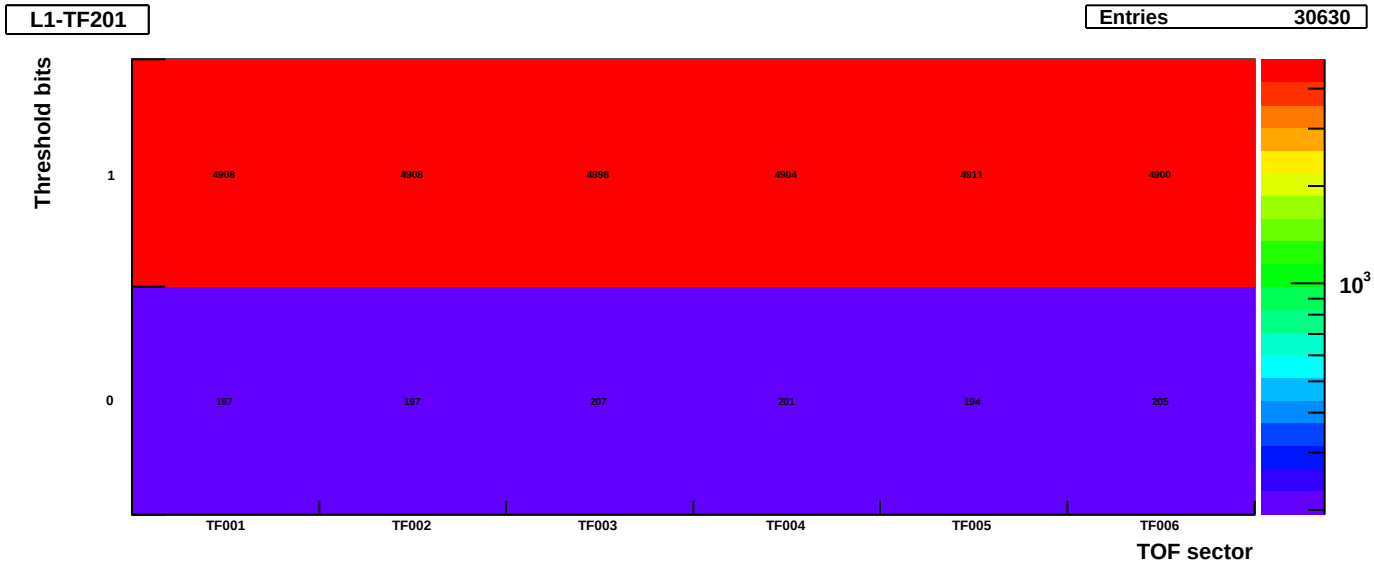

MIX-TF004
Entries 214410
TOF MULT
TOF tray

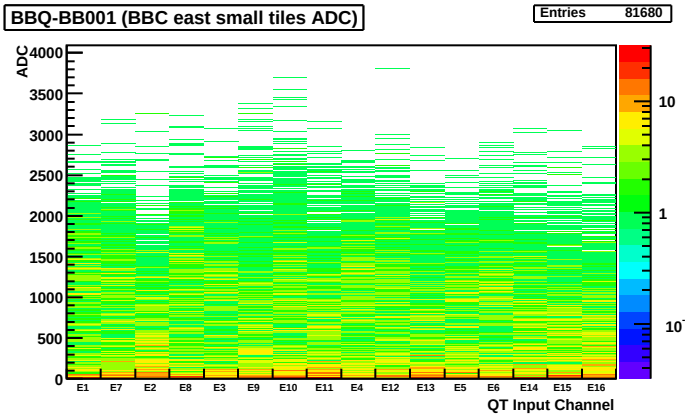

MIX-TF005
Entries 214410
TOF MULT
TOF tray

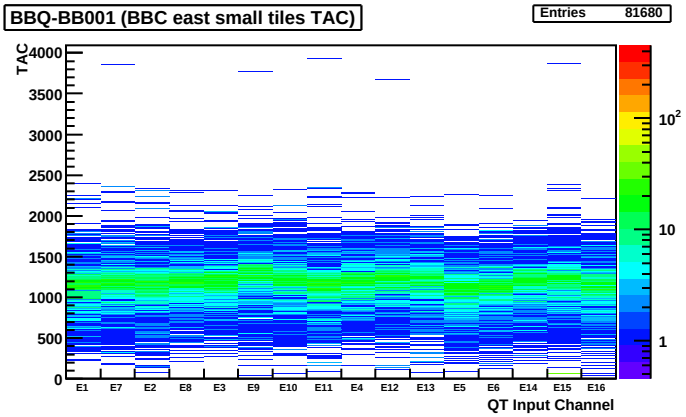

MIX-TF006
Entries 214410
TOF MULT
TOF tray

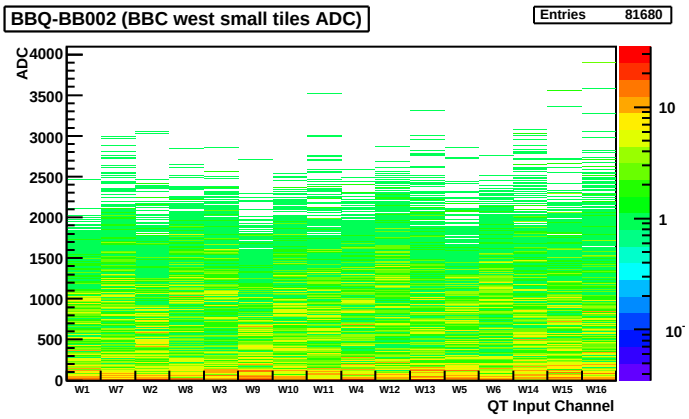

MIX-TF101
Entries 30630
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

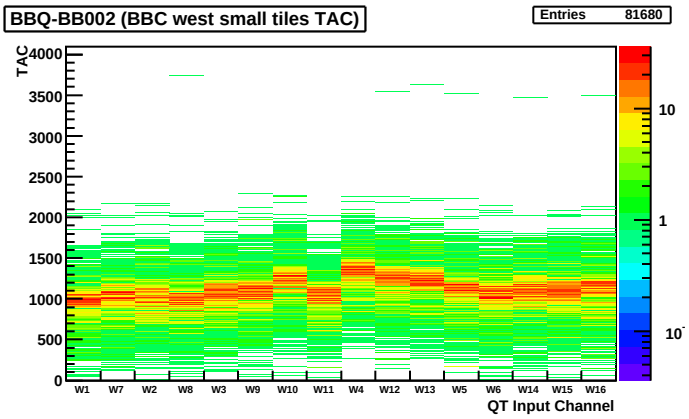

L1-TF201
Entries 5105
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

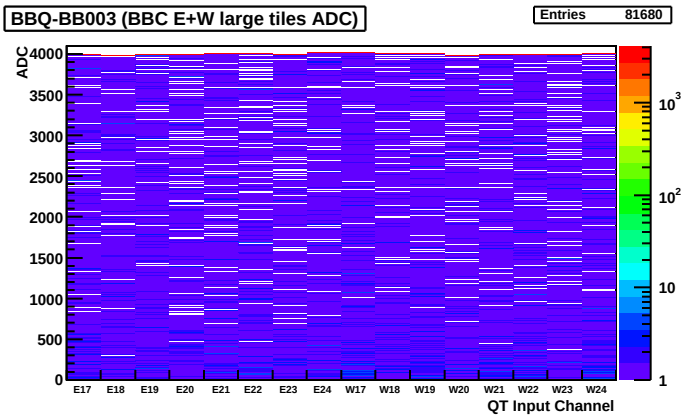

L1-TF201
Entries 30630
Threshold bits
1
0
4908
4908
4898
4904
4911
4900
197
197
207
201
194
205
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3

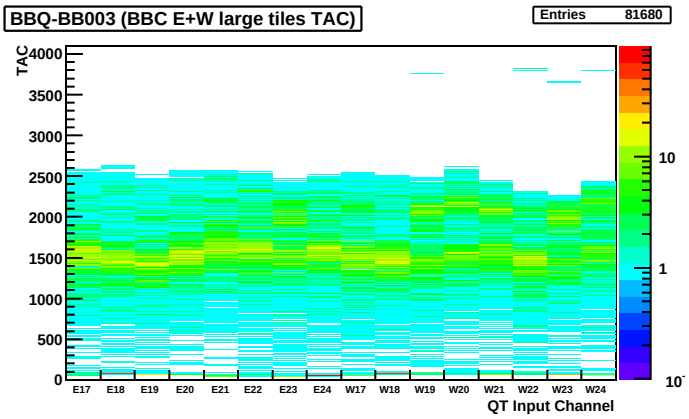

BBQ-BB001 (BBC east small tiles ADC)
Entries 81680
ADC
QT Input Channel

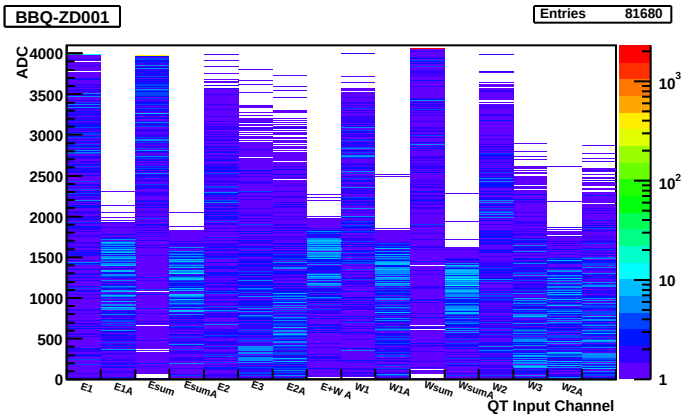

BBQ-BB001 (BBC east small tiles TAC)
Entries 81680
TAC
QT Input Channel

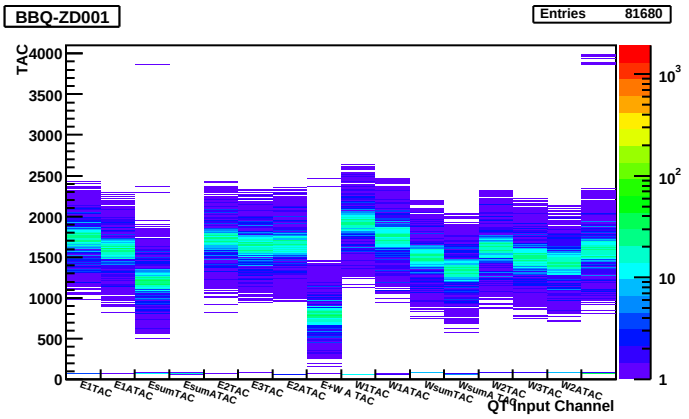

BBQ-BB002 (BBC west small tiles ADC)
Entries 81680
ADC
QT Input Channel

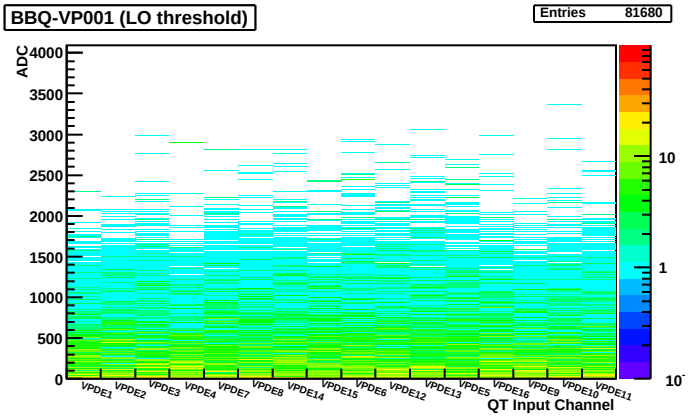

BBQ-BB002 (BBC west small tiles TAC)
Entries 81680
TAC
QT Input Channel

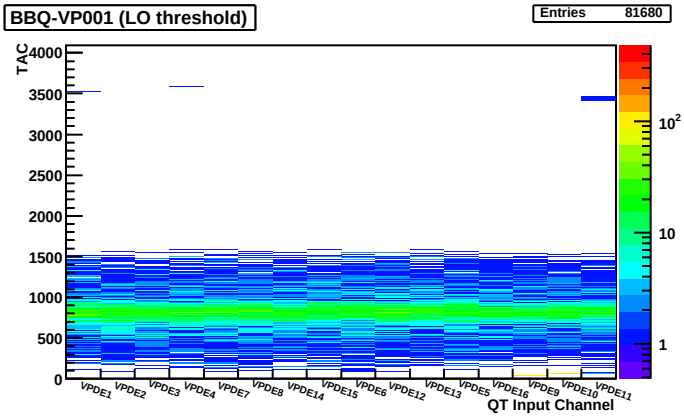

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 81680
ADC
QT Input Channel

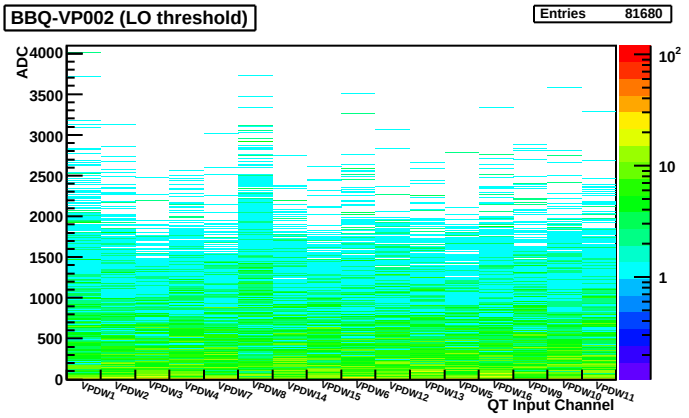

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 81680
TAC
QT Input Channel

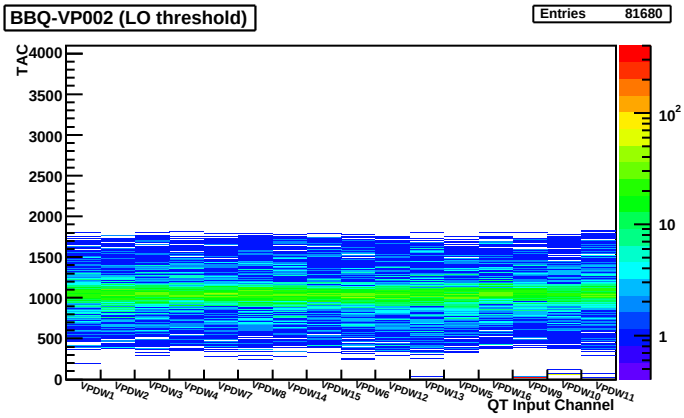

BBQ-ZD001
Entries 81680
ADC
QT Input Channel

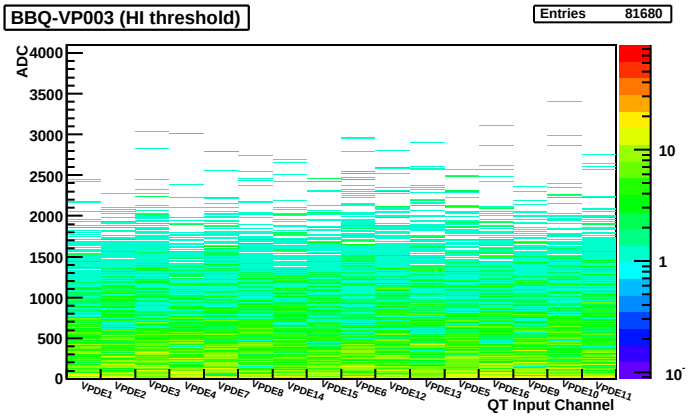

BBQ-ZD001
Entries 81680
TAC
QT Input Channel

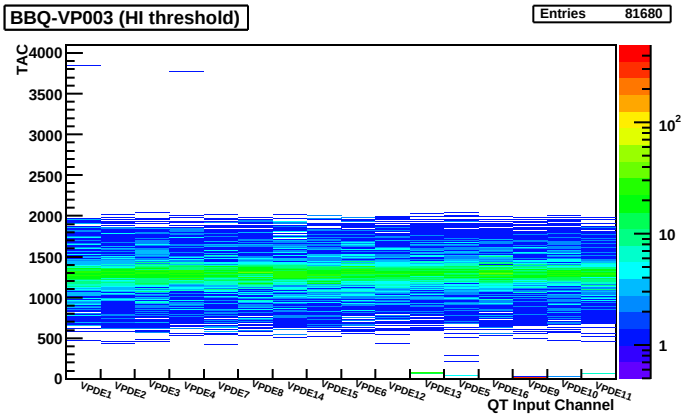

BBQ-VP001 (LO threshold)
Entries 81680
ADC
QT Input Channel

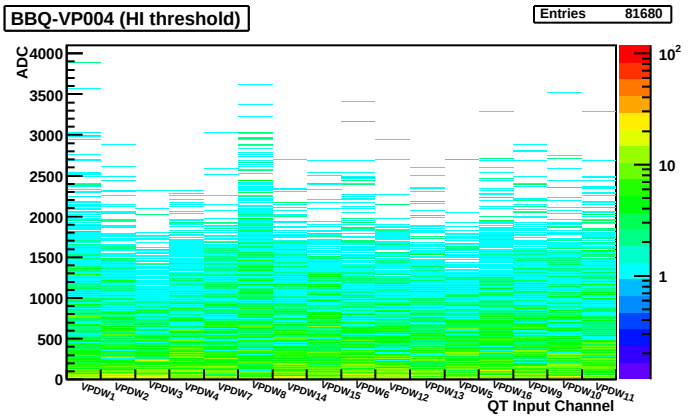

BBQ-VP001 (LO threshold)
Entries 81680
TAC
QT Input Channel

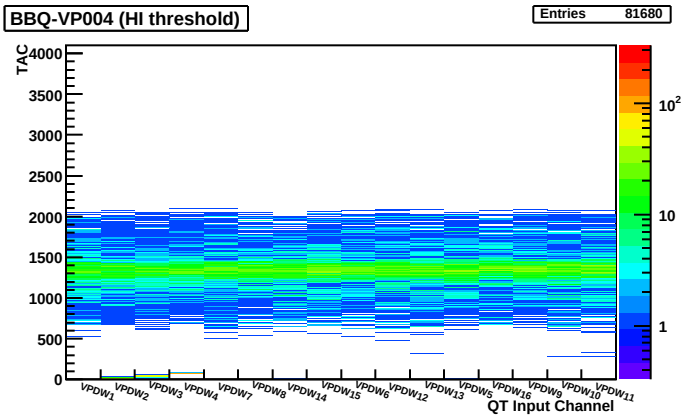

BBQ-VP002 (LO threshold)
Entries 81680
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 81680
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 81680
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 81680
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 81680
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 81680
TAC
QT Input Channel