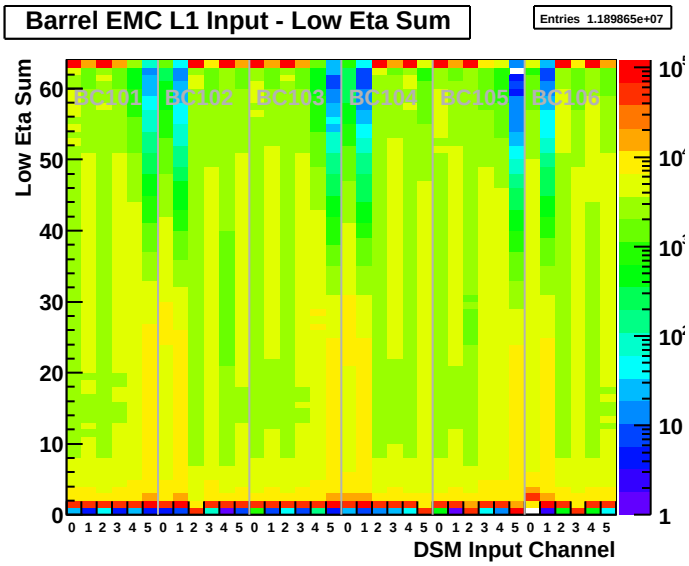
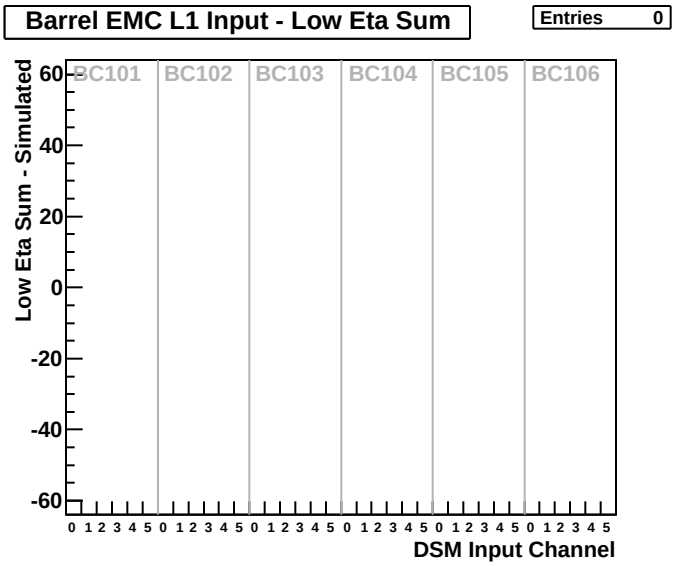
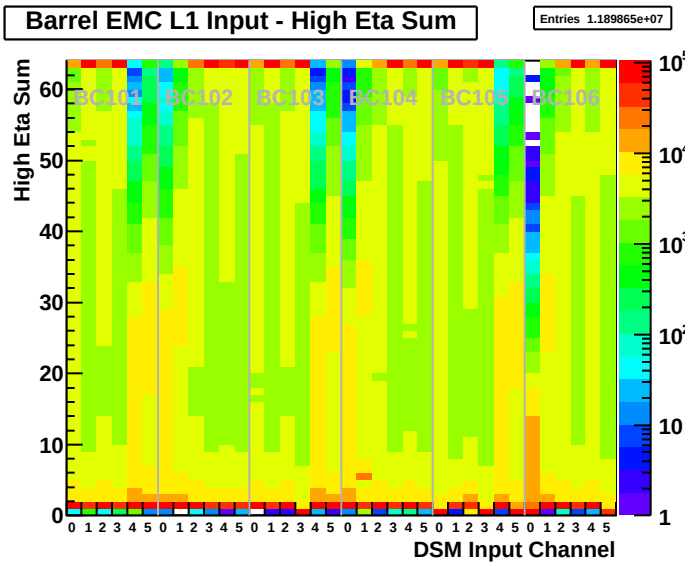
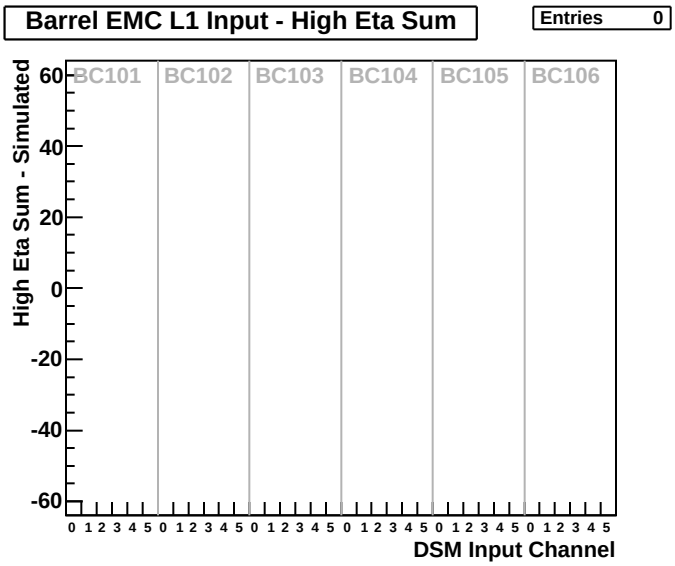
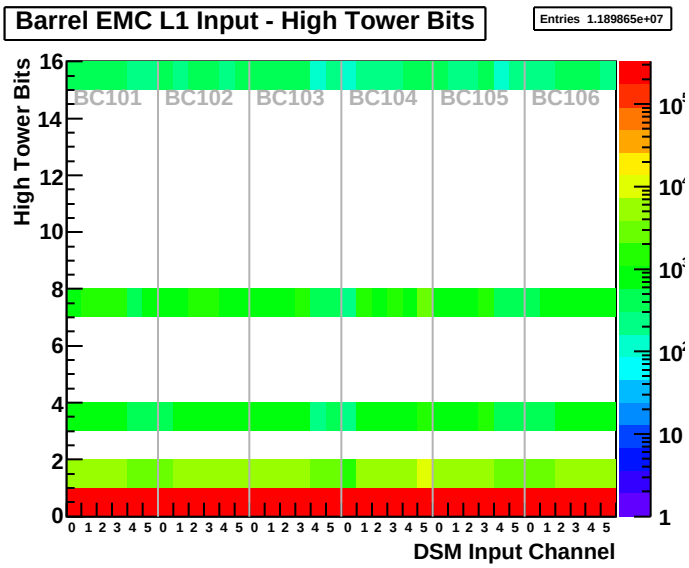
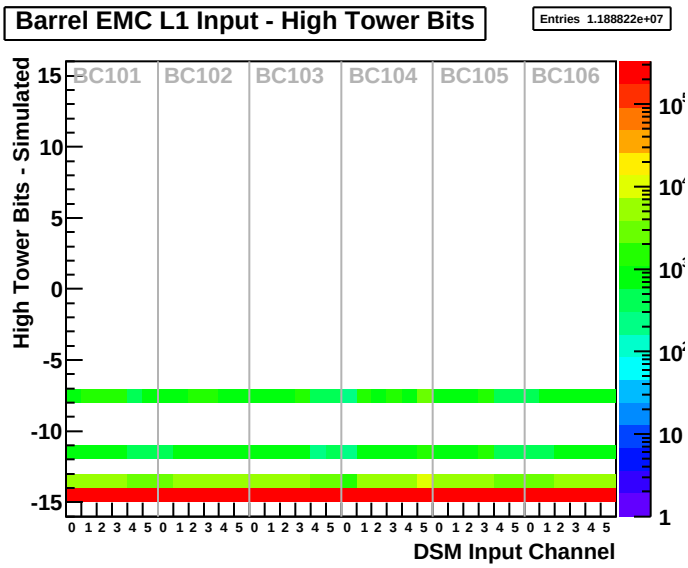

Barrel EMC L1 Input - Low Eta Sum
Entries 1.189865e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

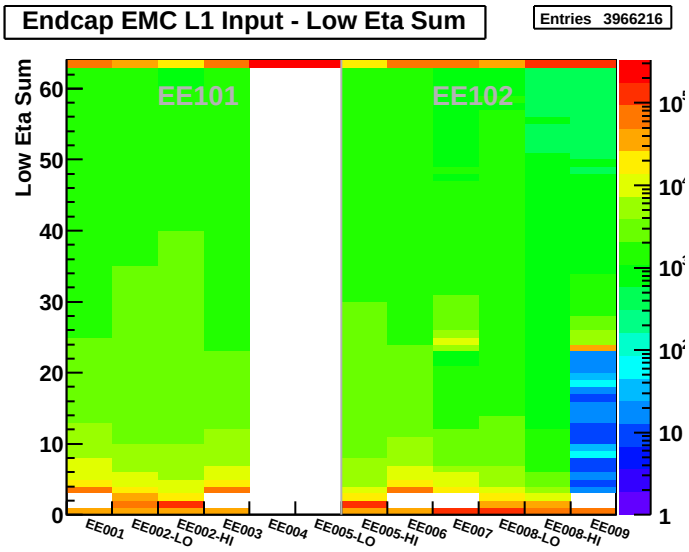

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

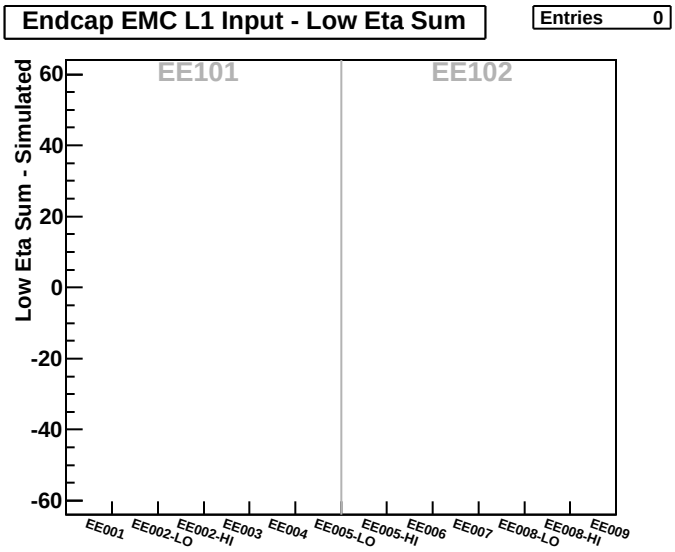

Barrel EMC L1 Input - High Eta Sum
Entries 1.189865e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

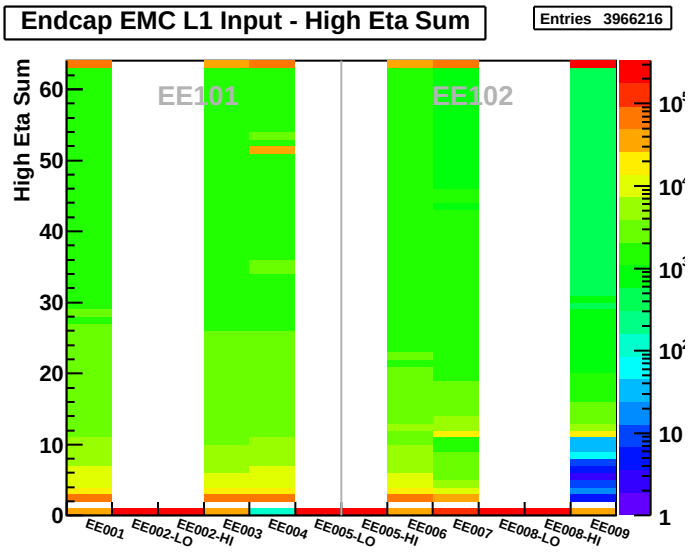

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

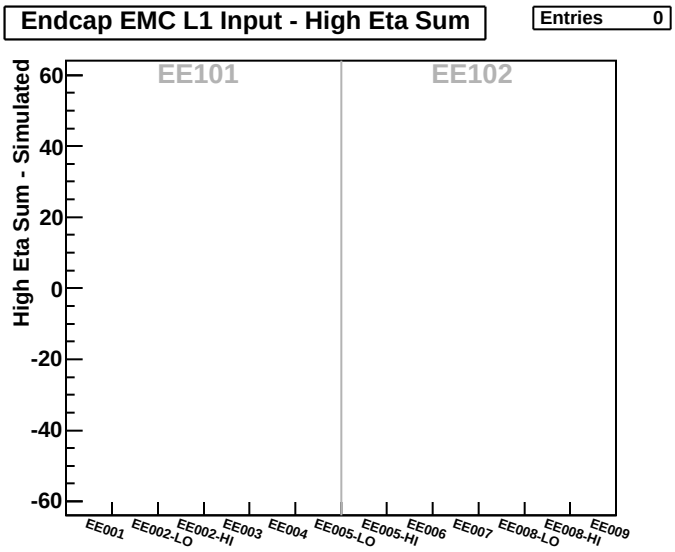

Barrel EMC L1 Input - High Tower Bits
Entries 1.189865e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

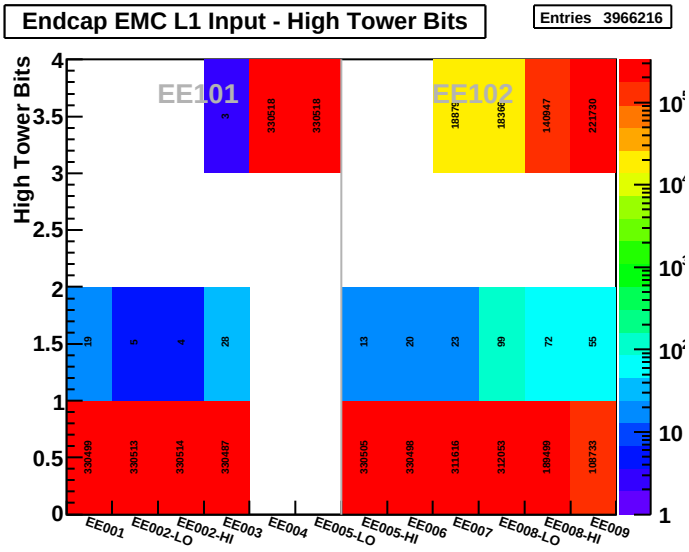

Barrel EMC L1 Input - High Tower Bits
Entries 1.188822e+07
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

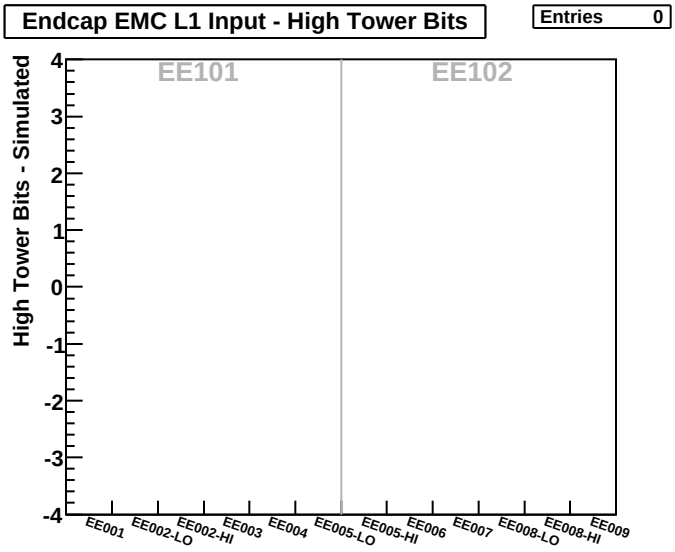

Endcap EMC L1 Input - Low Eta Sum
Entries 3966216
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

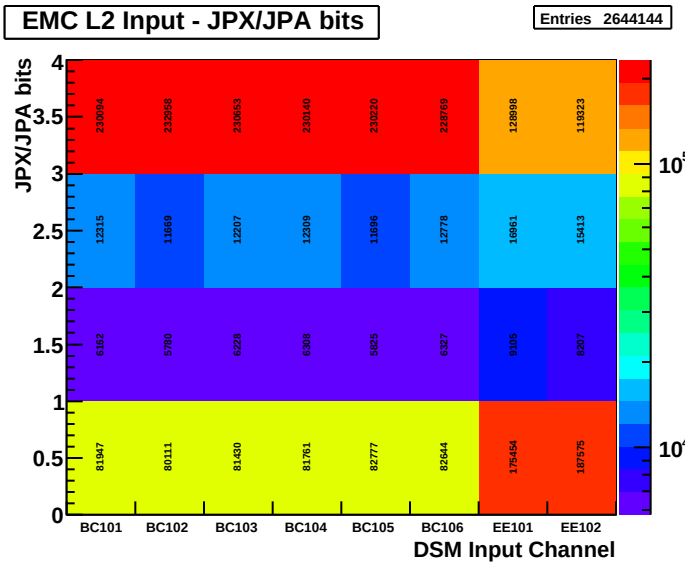

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

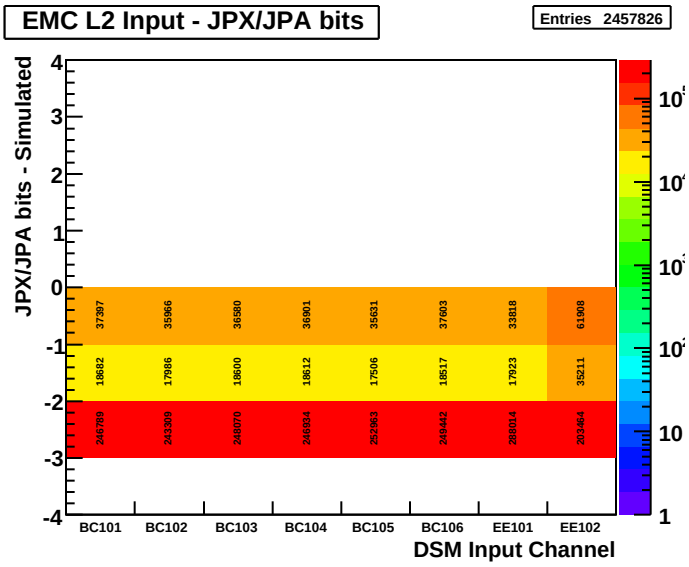

Endcap EMC L1 Input - High Eta Sum
Entries 3966216
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

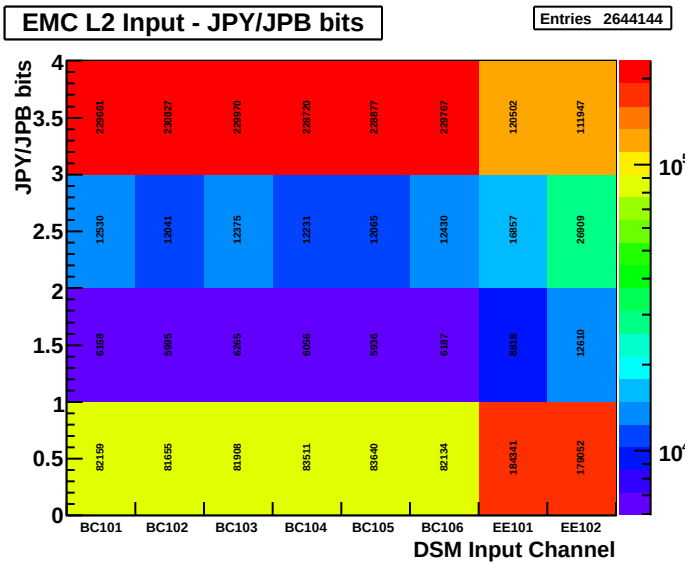

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

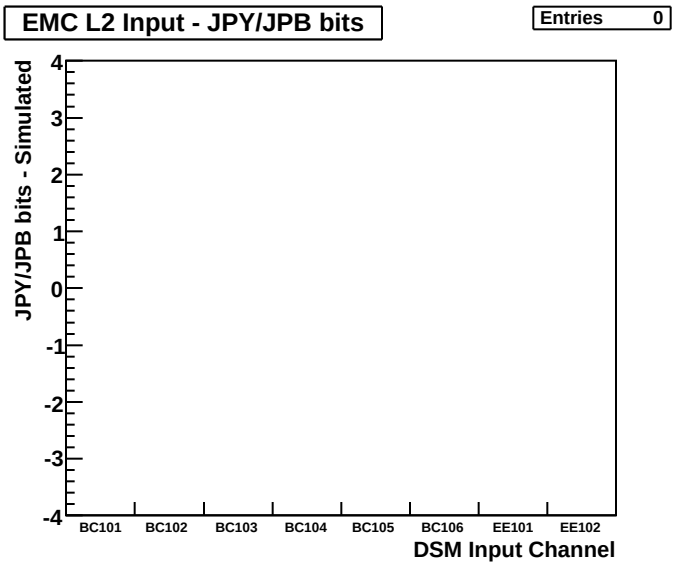

Endcap EMC L1 Input - High Tower Bits
Entries 3966216
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

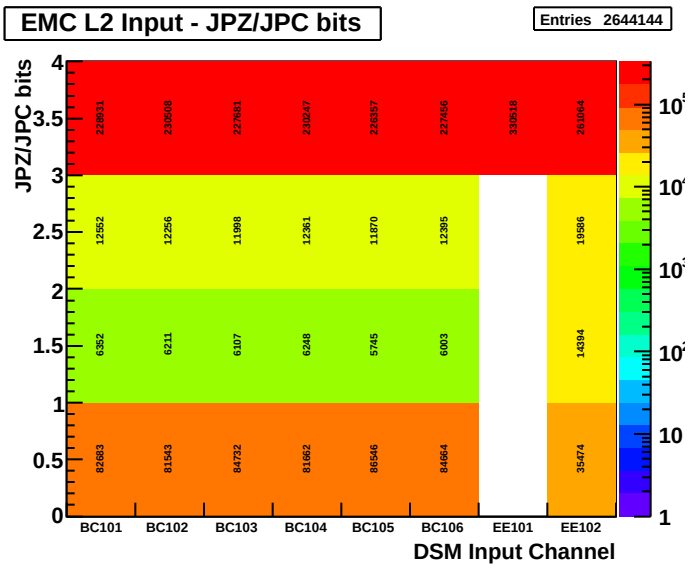

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

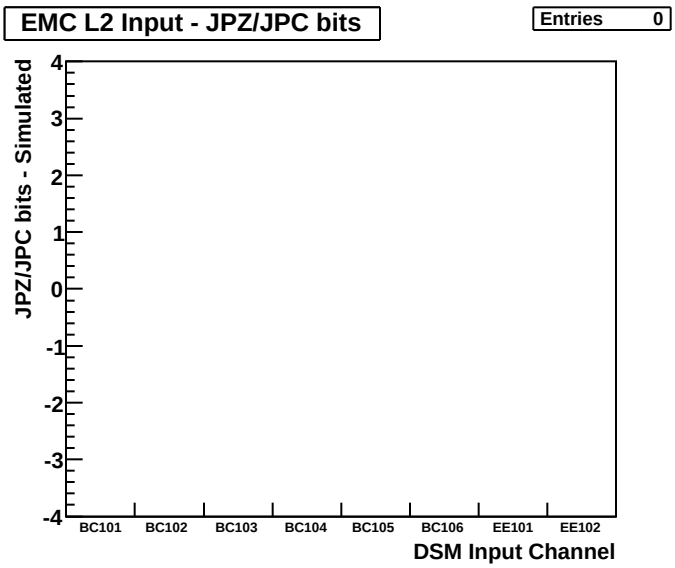

EMC L2 Input - JPX/JPA bits
Entries 2644144
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
230094 232358 230653 230140 230220 228769 128898 119323
12315 11669 12207 12309 11686 12778 16861 15413
6162 5780 6228 6308 5825 6327 9105 8207
81947 80111 81430 81761 82777 82644 175454 187575

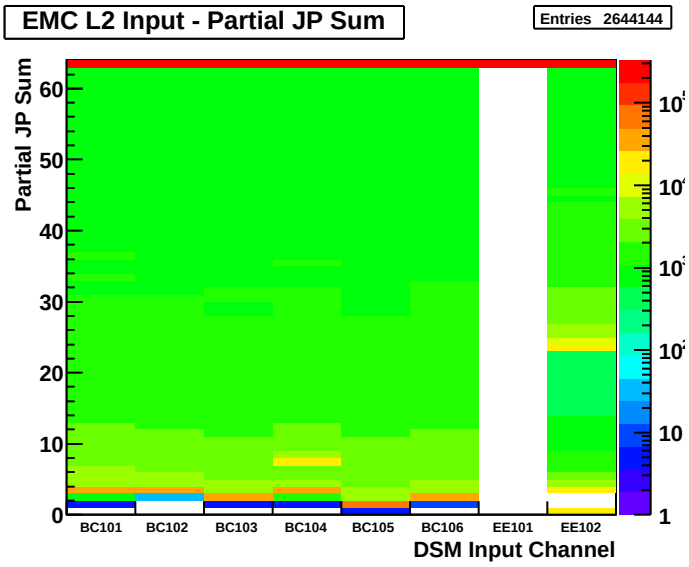

EMC L2 Input - JPX/JPA bits
Entries 2457826
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
37397 35966 36580 36901 35631 37603 33818 61908
18682 17986 18600 18612 17506 18617 17923 35211
246789 243309 249070 246934 252963 249442 238014 203464

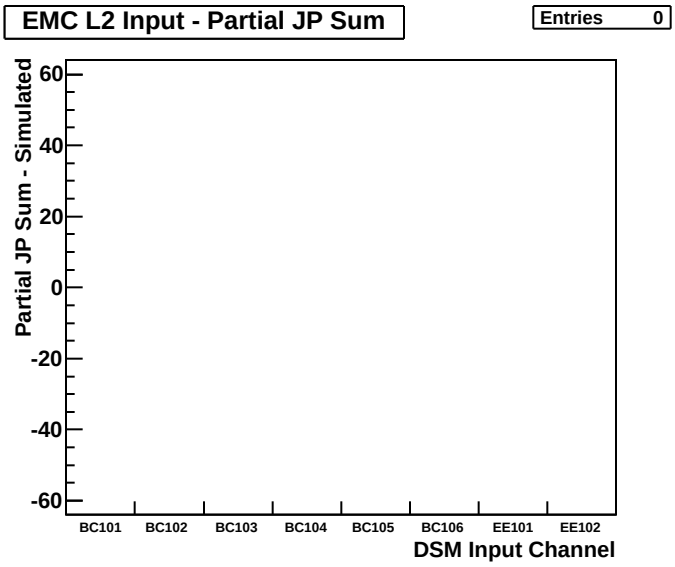

EMC L2 Input - JPY/JPB bits
Entries 2644144
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
229661 230827 229970 229720 229877 229767 120502 111947
12530 12041 12375 12231 12065 12430 16857 26909
6168 5995 6255 6056 5936 6197 8818 12610
82159 81655 81908 83511 83640 82134 184341 179052

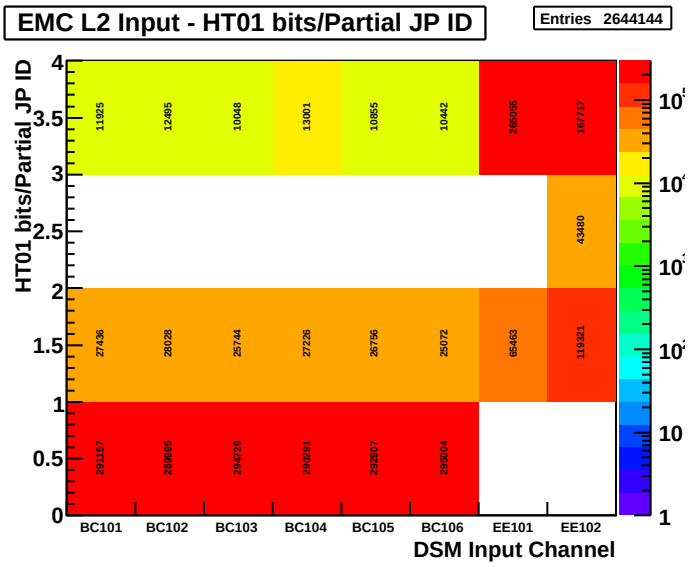

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

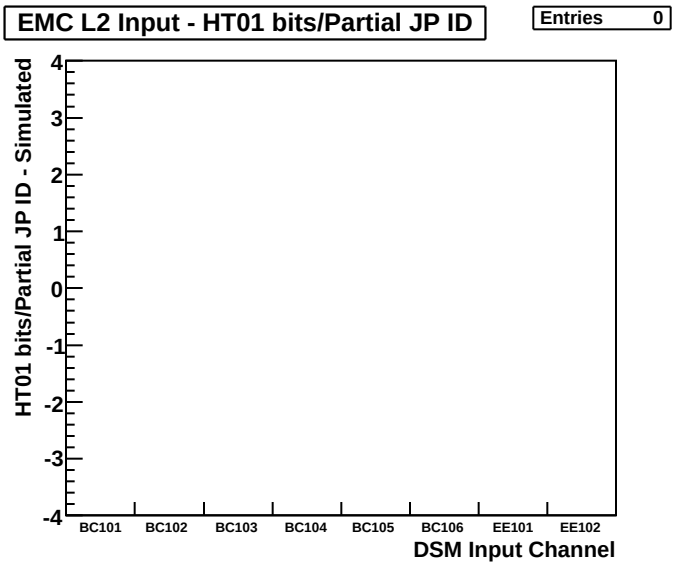

EMC L2 Input - JPZ/JPC bits
Entries 2644144
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
228931 230508 227681 230247 226357 227456 330518 261064
12552 12256 11998 12361 11870 12395 19586
6352 6211 6107 6248 5745 6003 14394
82683 81543 84732 81662 86546 84664 35474

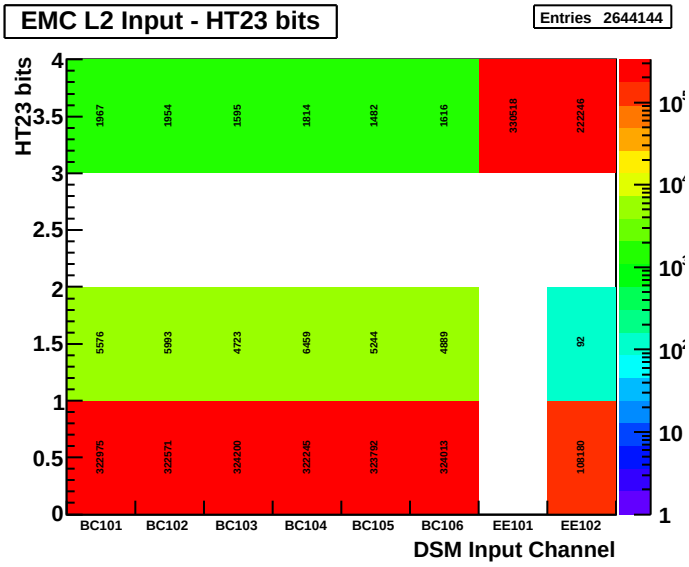

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

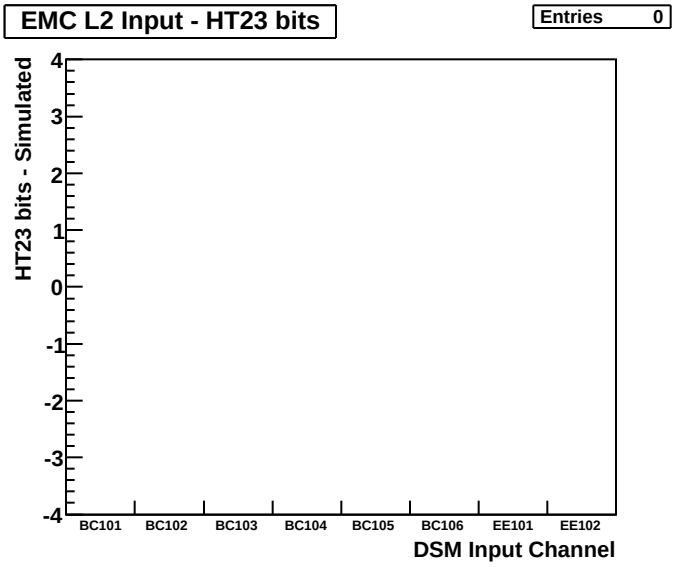

EMC L2 Input - Partial JP Sum
Entries 2644144
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

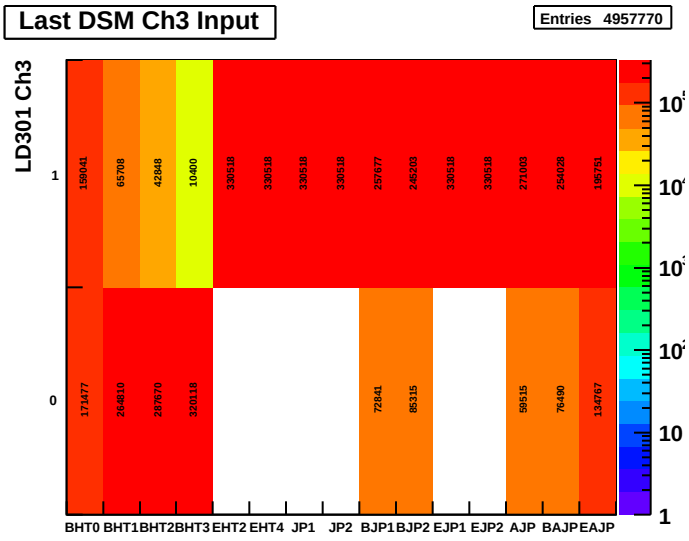

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

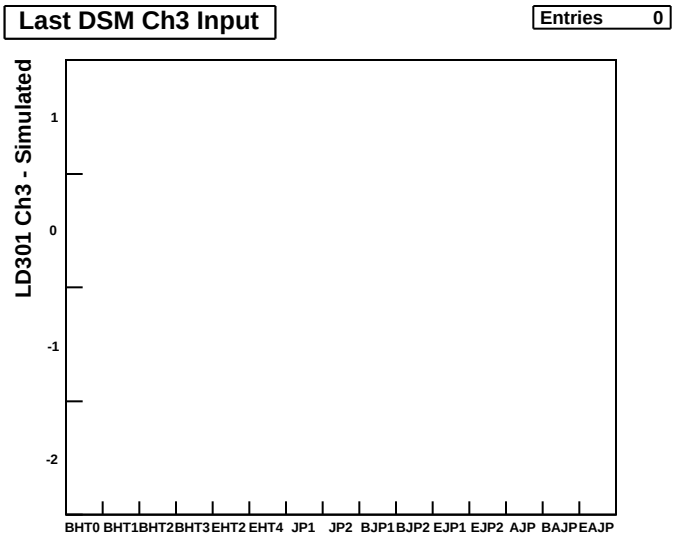

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2644144
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

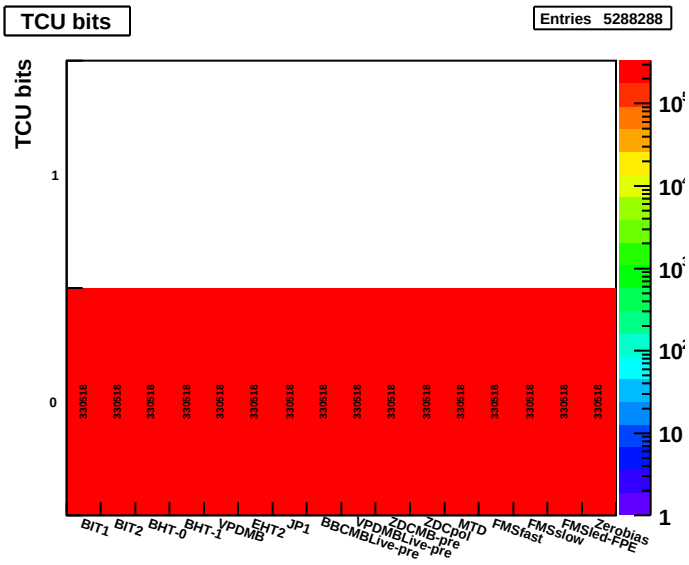

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

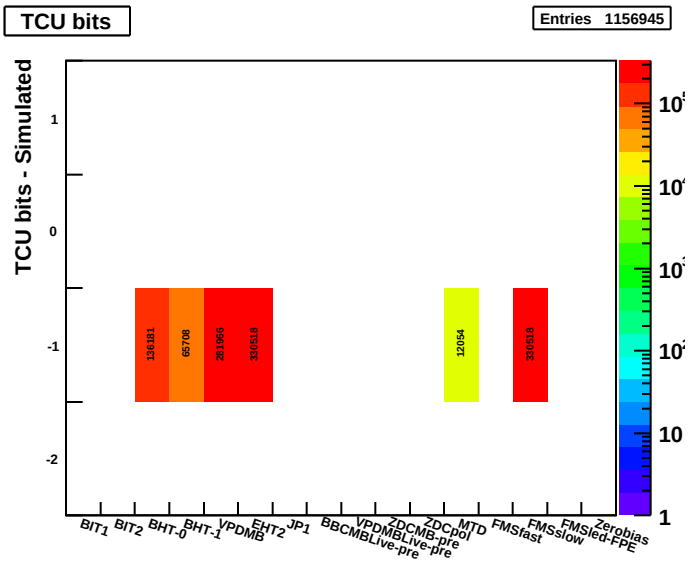

EMC L2 Input - HT23 bits
Entries 2644144
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

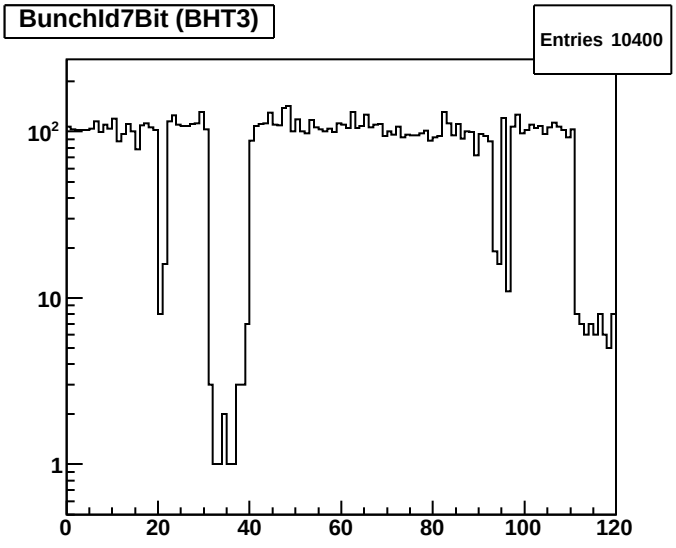

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

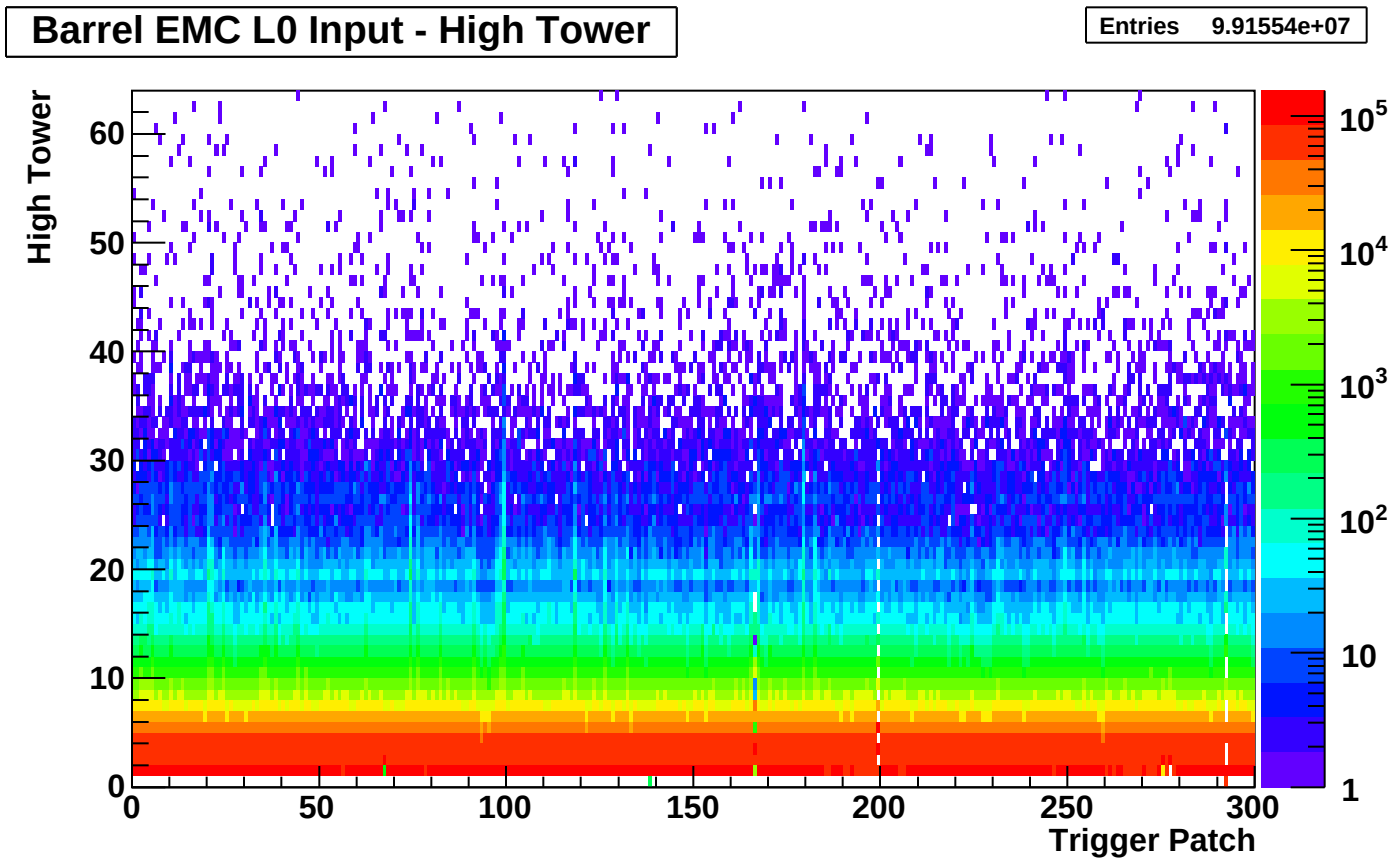

Last DSM Ch3 Input
Entries 4957770
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

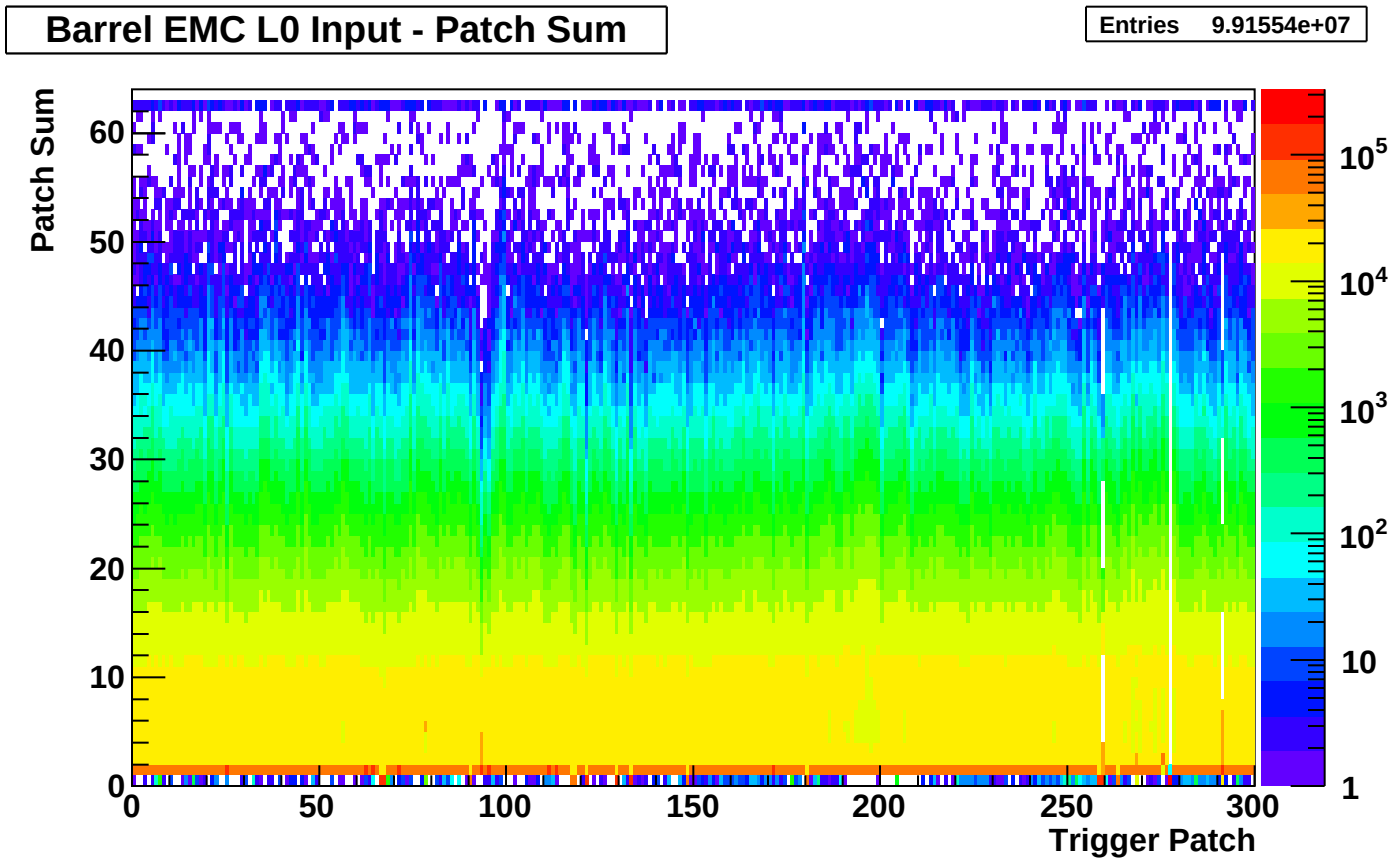

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

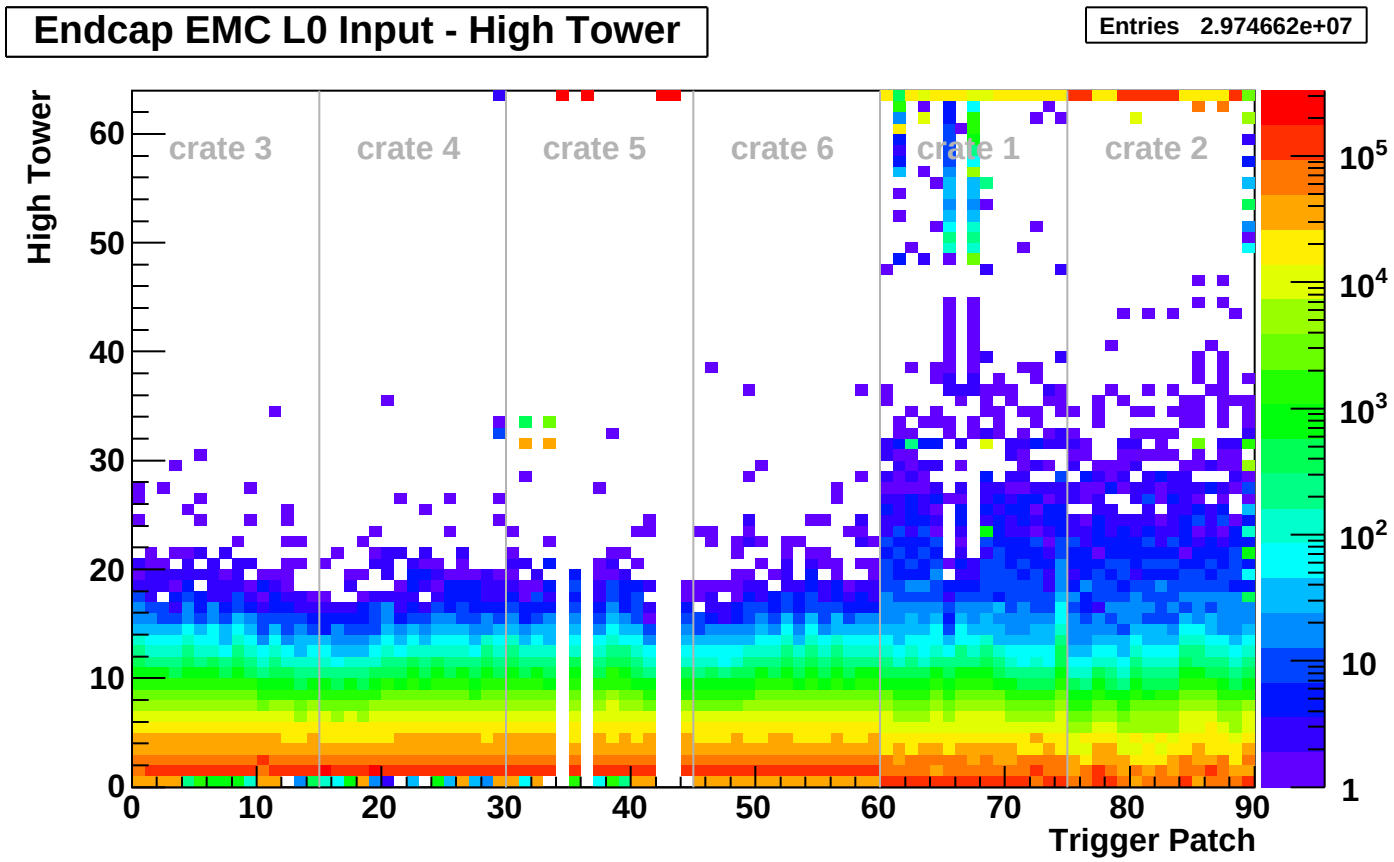

TCU bits
Entries 5288288
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

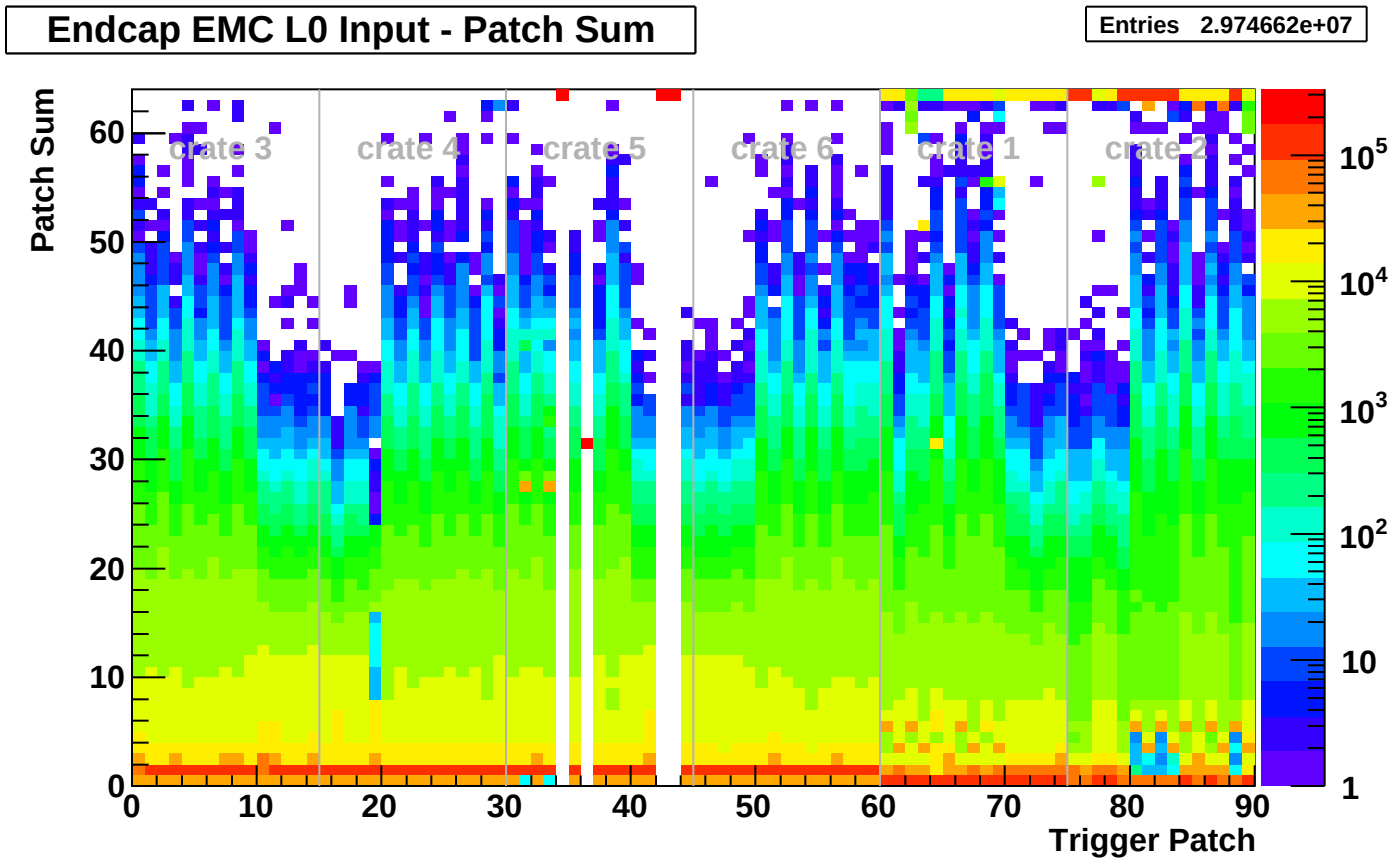

TCU bits
Entries 1156945
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

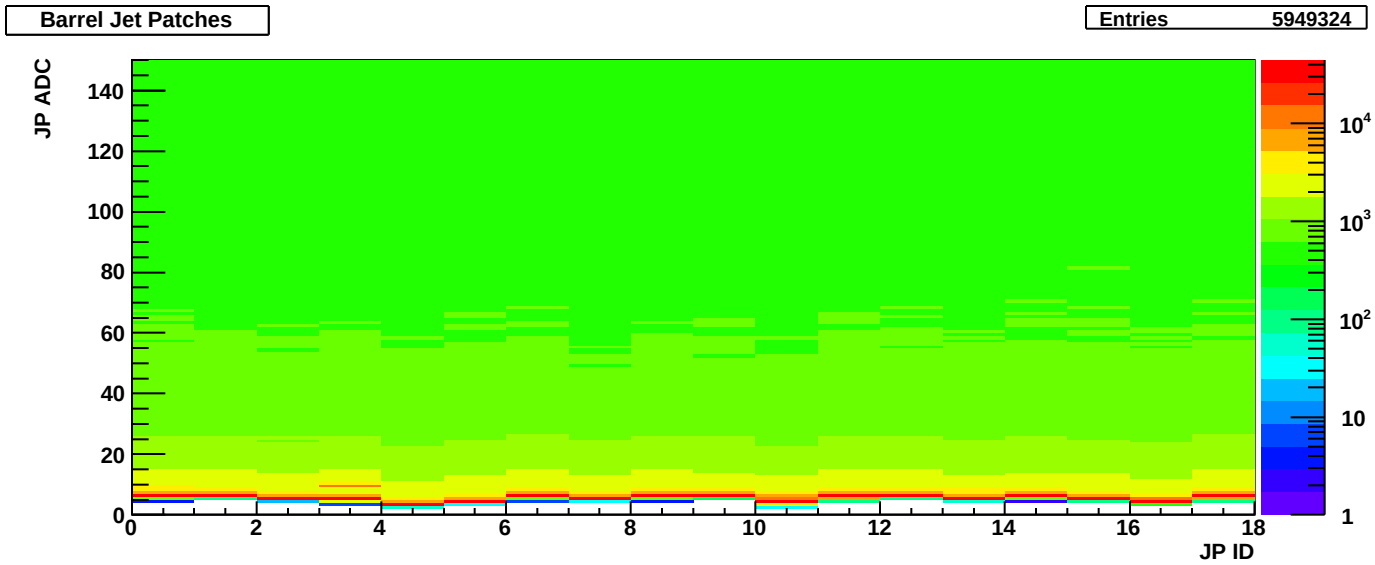

BunchId7Bit (BHT3)
Entries 10400

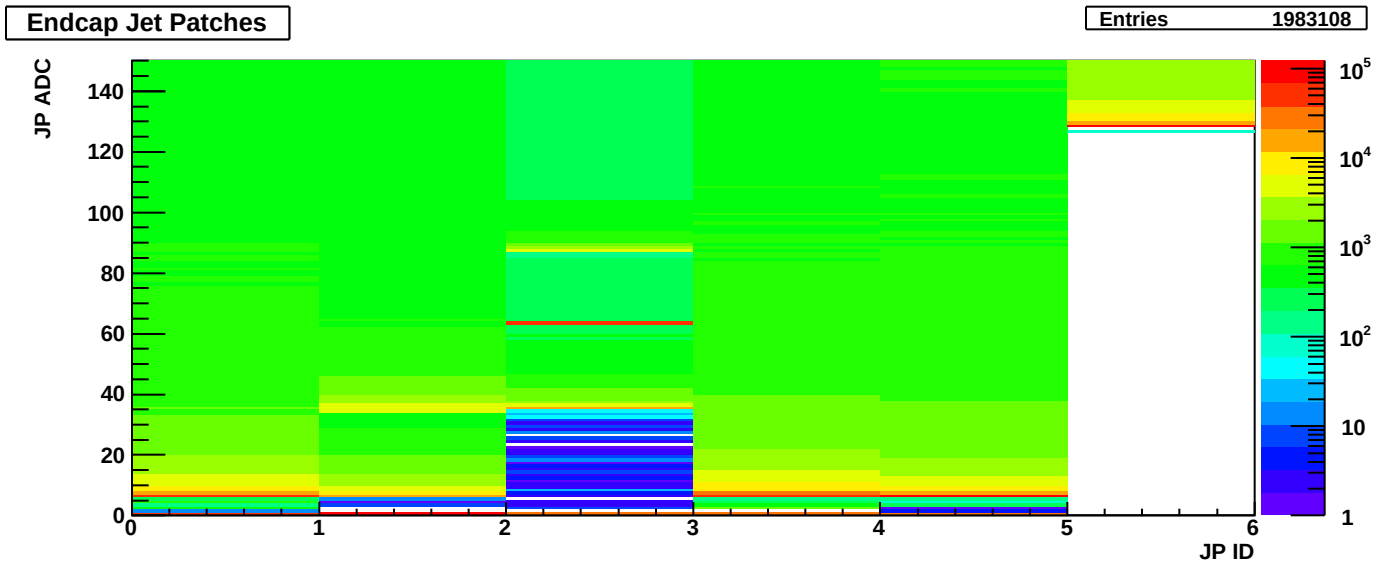

Barrel EMC L0 Input - High Tower
Entries 9.91554e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

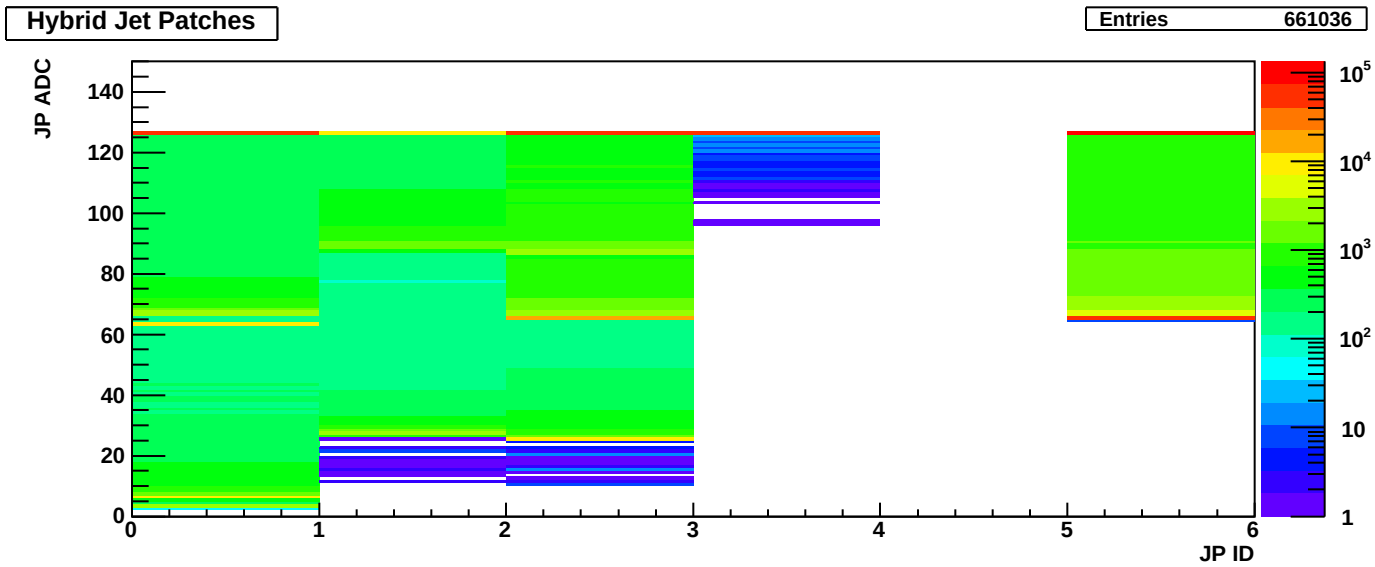

Barrel EMC L0 Input - Patch Sum
Entries 9.91554e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

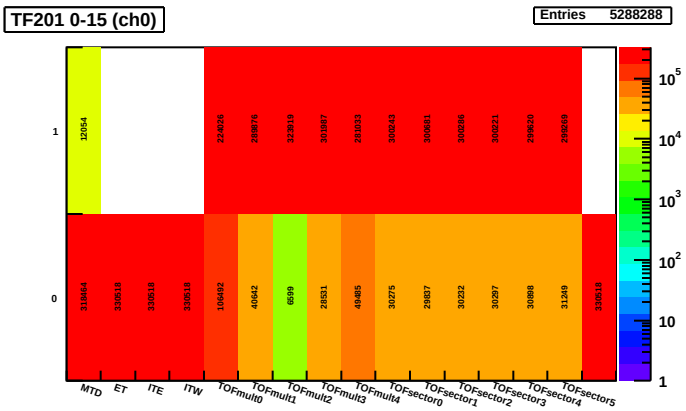

Endcap EMC L0 Input - High Tower
Entries 2.974662e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

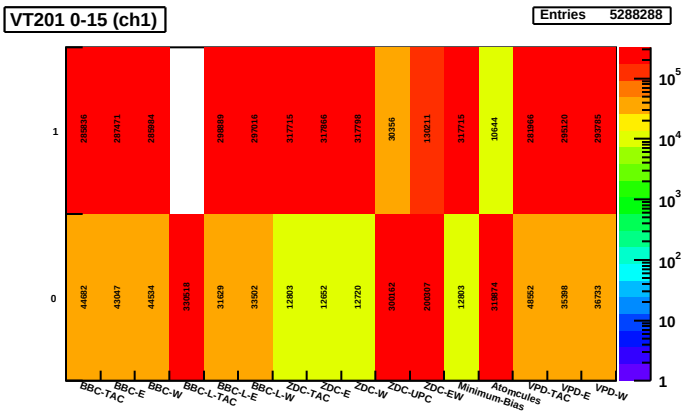

Endcap EMC L0 Input - Patch Sum
Entries 2.974662e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

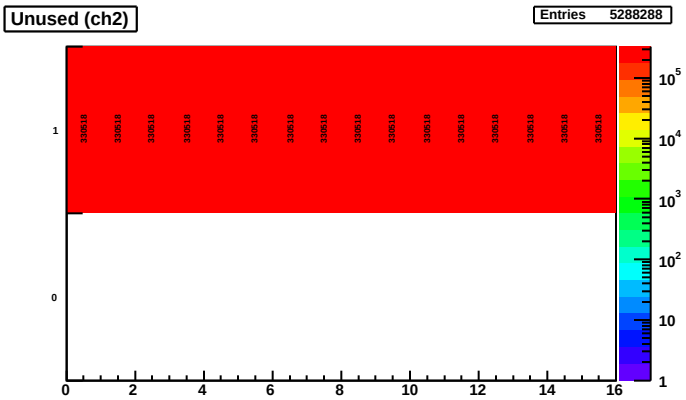

Barrel Jet Patches
Entries 5949324
JP ADC
JP ID

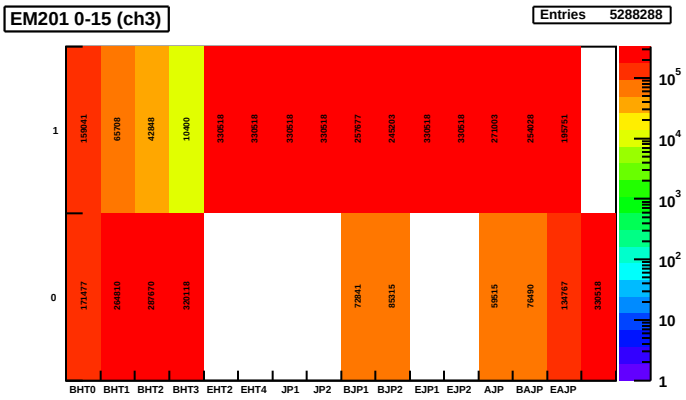

Endcap Jet Patches
Entries 1983108
JP ADC
JP ID

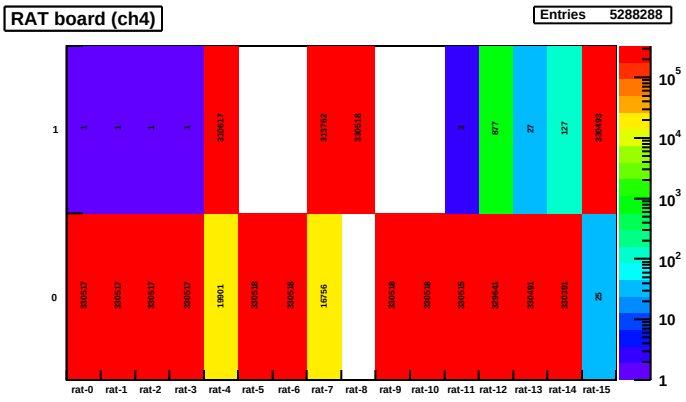

Hybrid Jet Patches
Entries 661036
JP ADC
JP ID

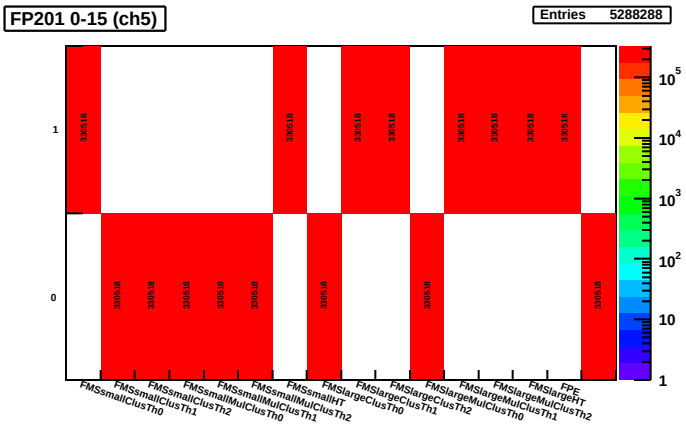

TF201 0-15 (ch0)
Entries 5288288
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

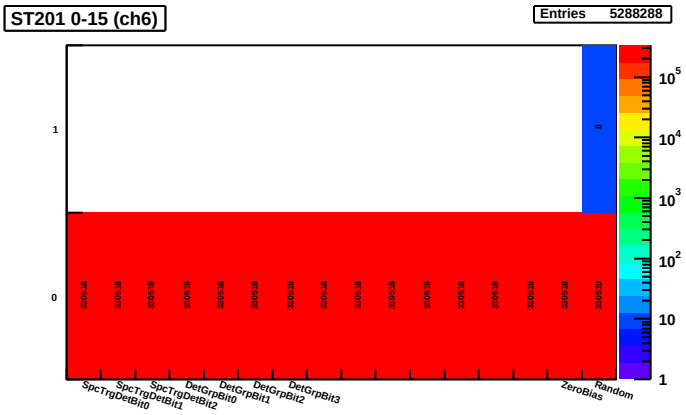

VT201 0-15 (ch1)
Entries 5288288
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

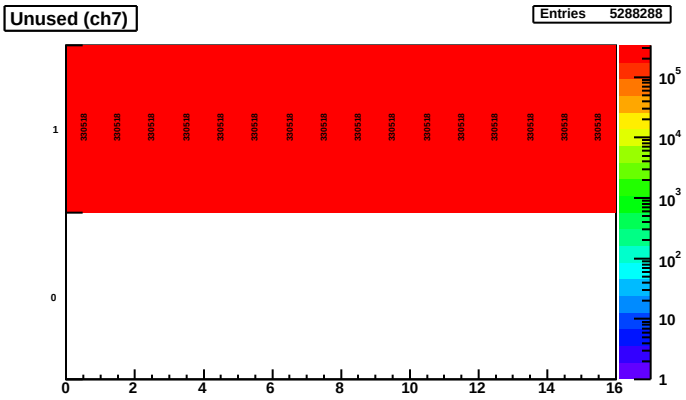

Unused (ch2)
Entries 5288288

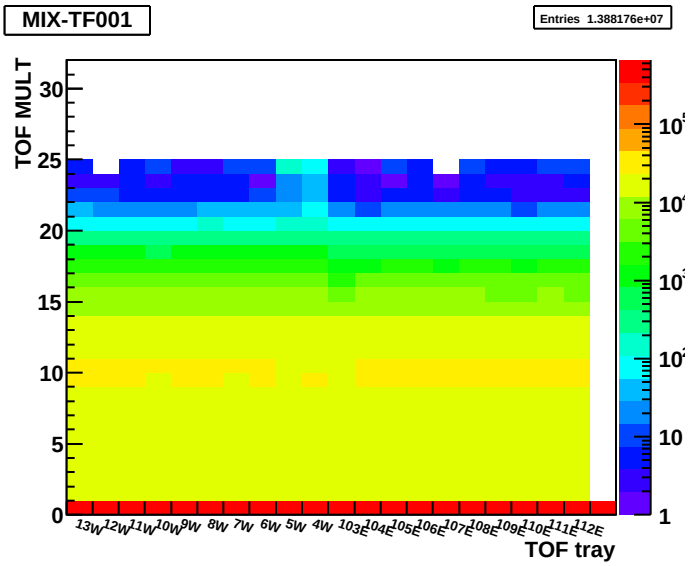

EM201 0-15 (ch3)
Entries 5288288
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP

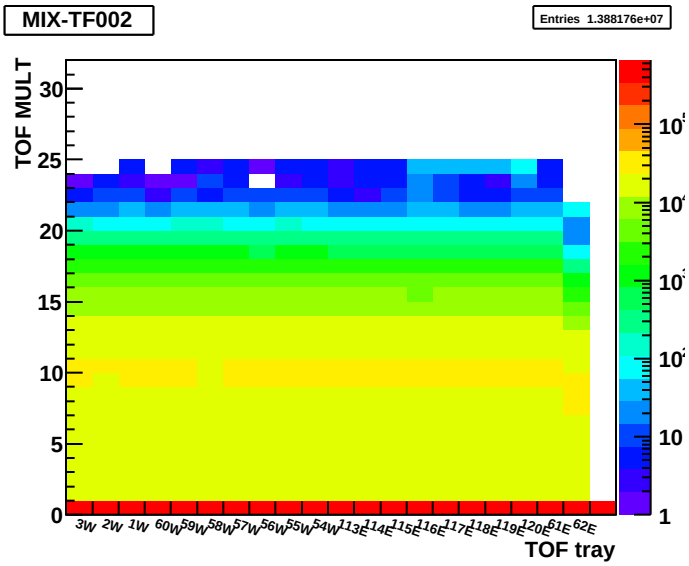

RAT board (ch4)
Entries 5288288
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

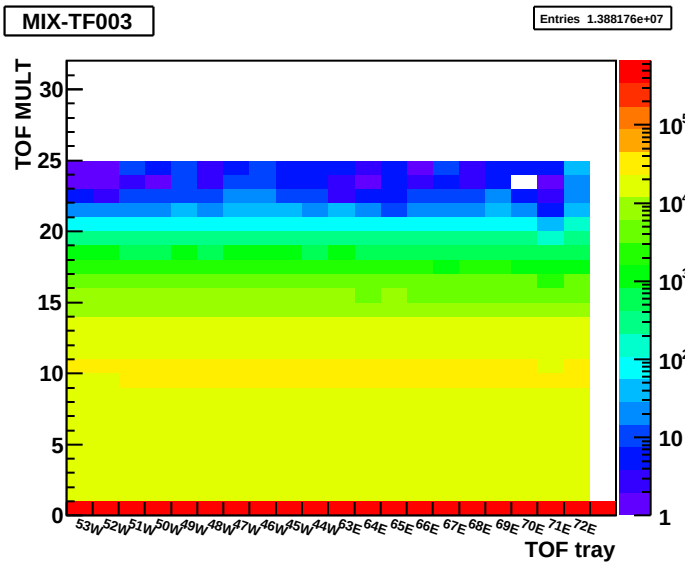

FP201 0-15 (ch5)
Entries 5288288
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FPE
FMSlargeHT

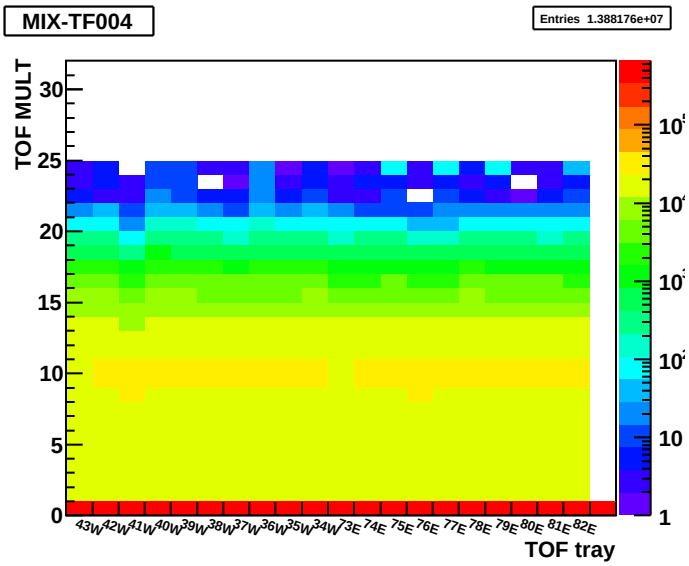

ST201 0-15 (ch6)
Entries 5288288
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

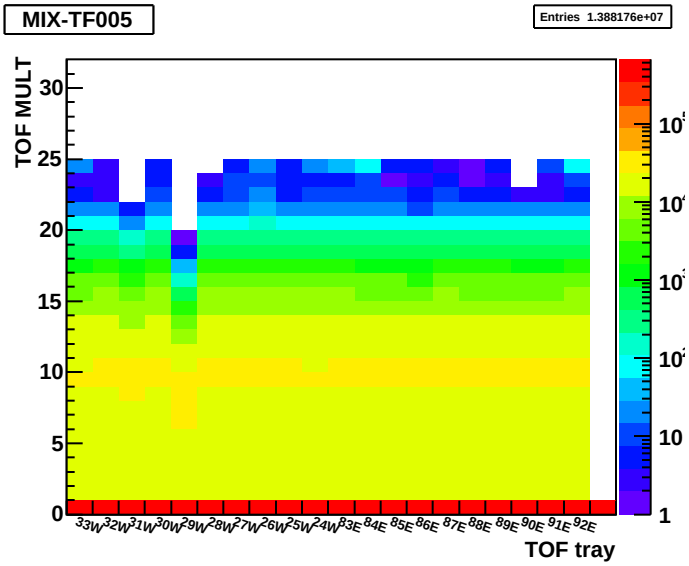

Unused (ch7)
Entries 5288288

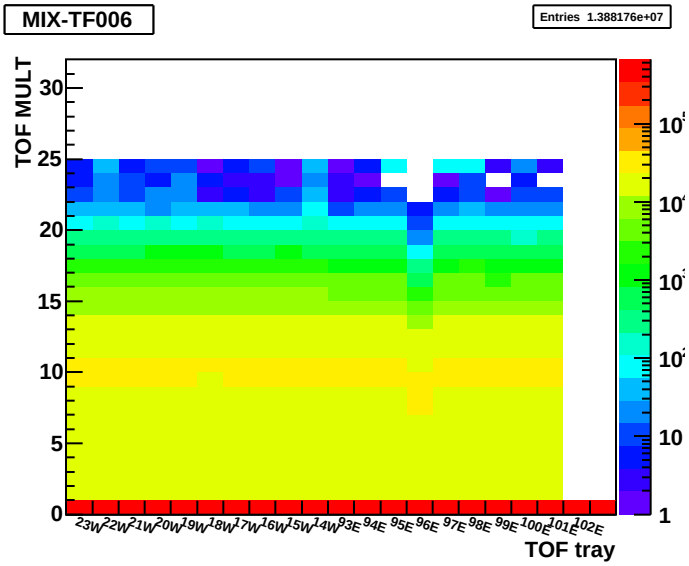

MIX-TF001
Entries 1.388176e+07
TOF MULT
TOF tray

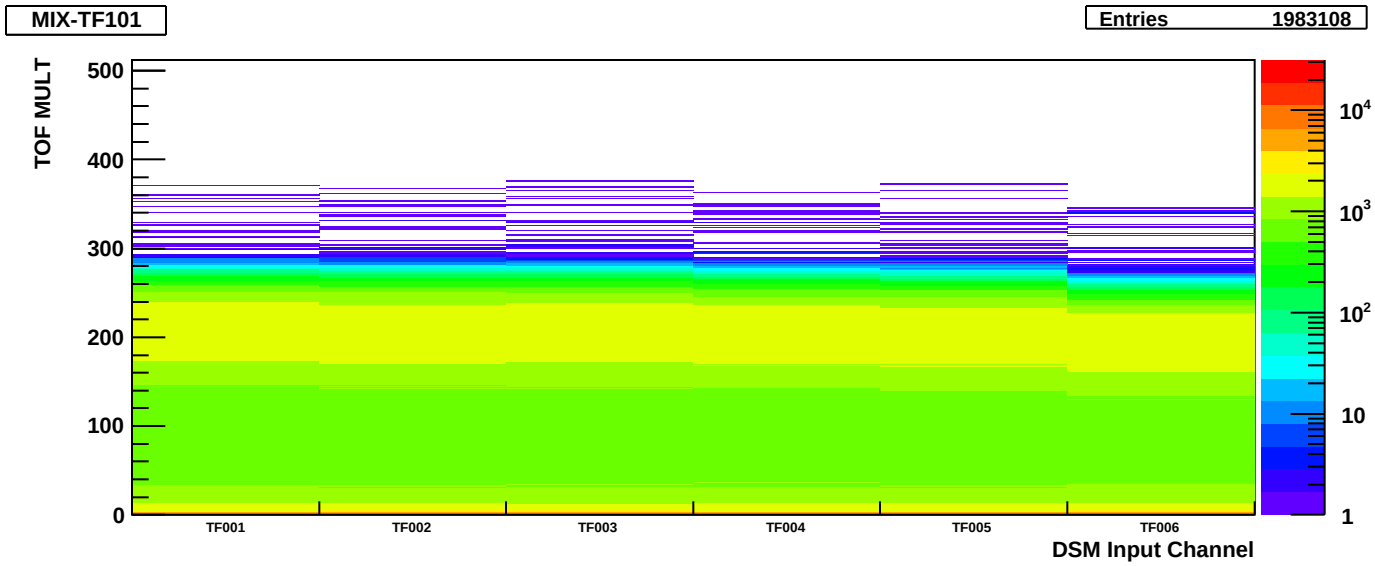

MIX-TF002
Entries 1.388176e+07
TOF MULT
TOF tray

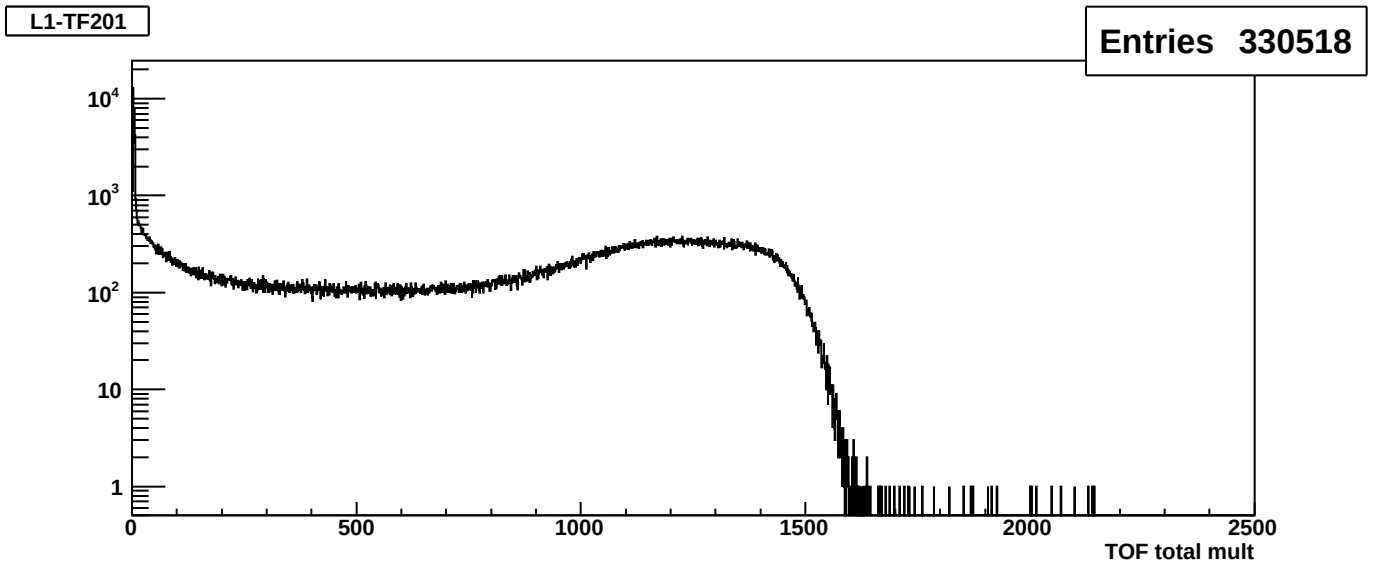

MIX-TF003
Entries 1.388176e+07
TOF MULT
TOF tray

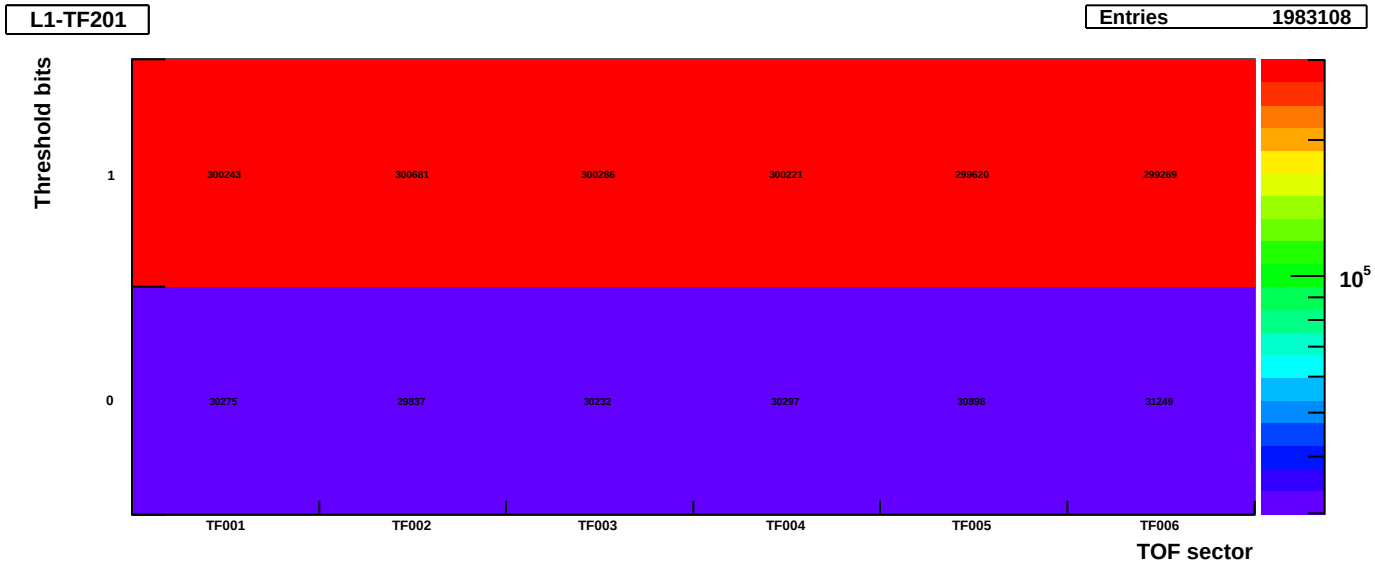

MIX-TF004
Entries 1.388176e+07
TOF MULT
TOF tray

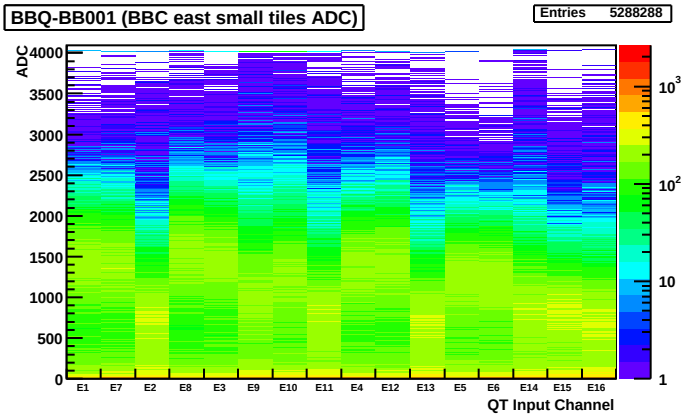

MIX-TF005
Entries 1.388176e+07
TOF MULT
TOF tray

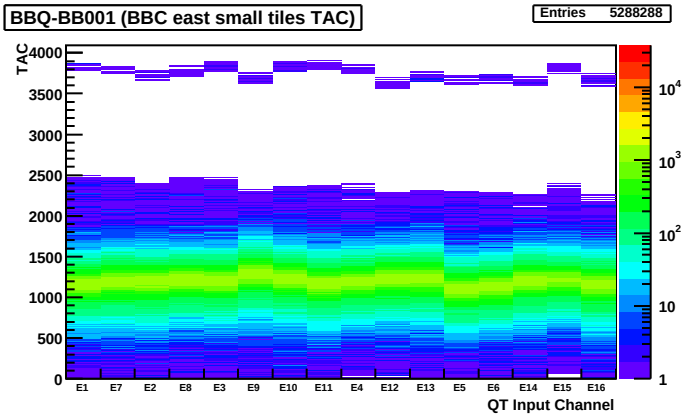

MIX-TF006
Entries 1.388176e+07
TOF MULT
TOF tray

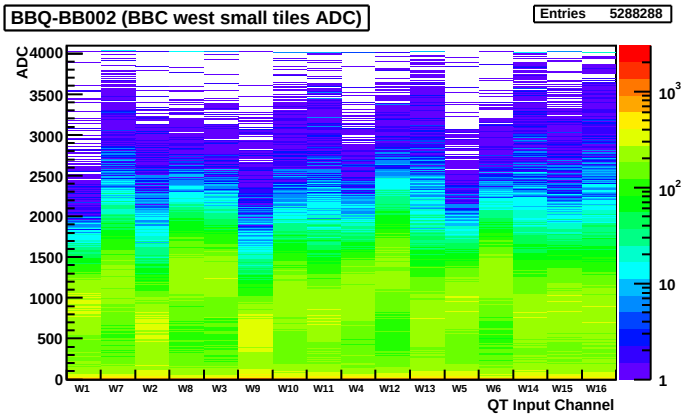

MIX-TF101
Entries 1983108
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

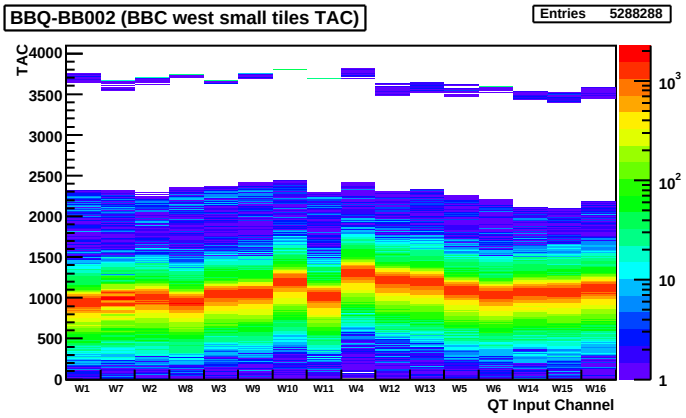

L1-TF201
Entries 330518
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

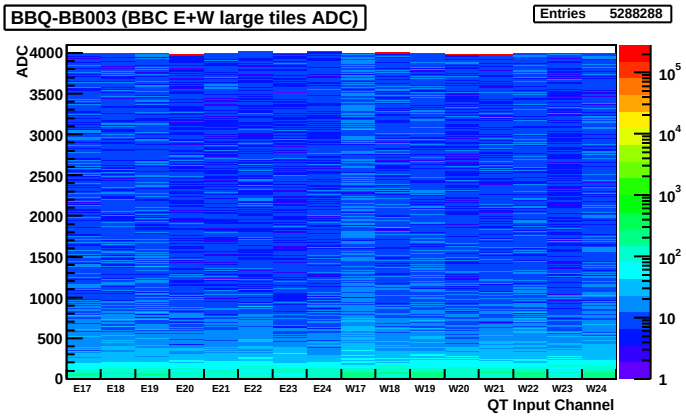

L1-TF201
Entries 1983108
Threshold bits
1
0
300243
300681
300286
300221
299620
299269
30275
29837
30232
30297
30898
31249
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

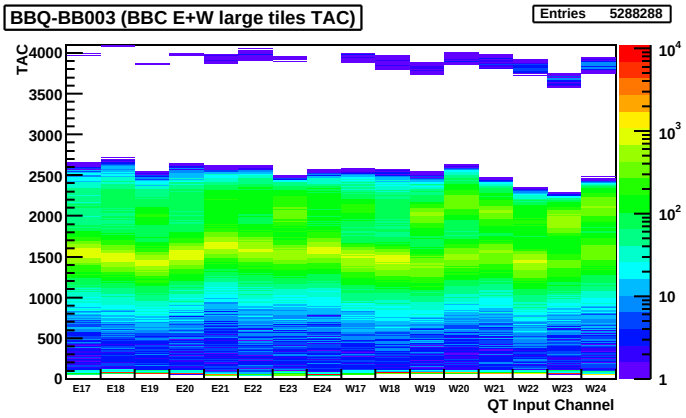

BBQ-BB001 (BBC east small tiles ADC)
Entries 5288288
ADC
QT Input Channel

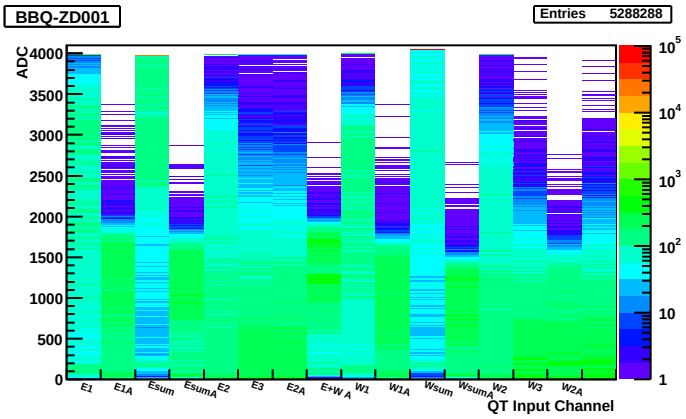

BBQ-BB001 (BBC east small tiles TAC)
Entries 5288288
TAC
QT Input Channel

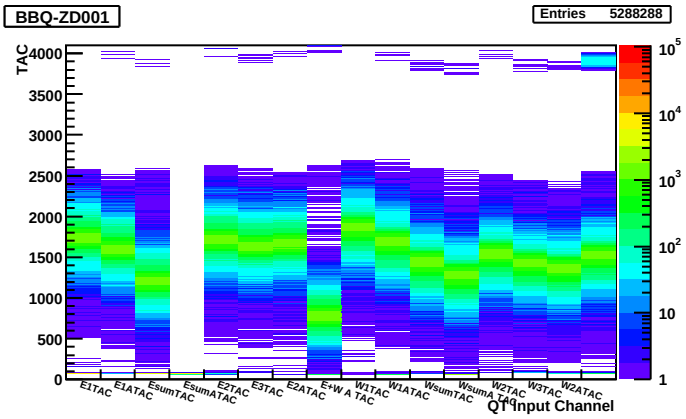

BBQ-BB002 (BBC west small tiles ADC)
Entries 5288288
ADC
QT Input Channel

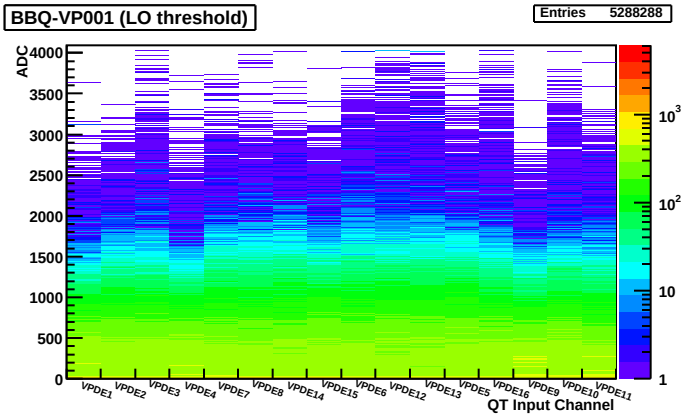

BBQ-BB002 (BBC west small tiles TAC)
Entries 5288288
TAC
QT Input Channel

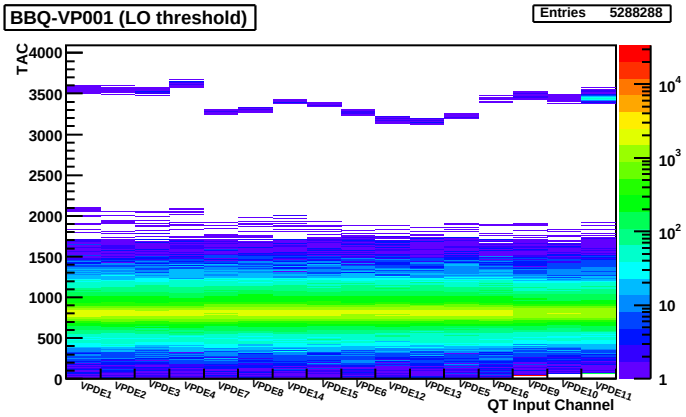

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5288288
ADC
QT Input Channel

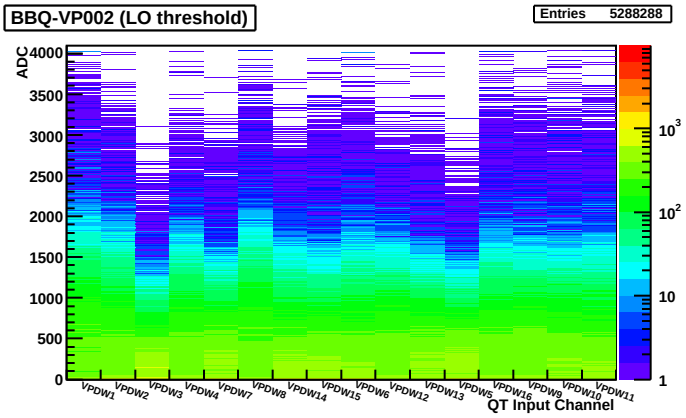

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5288288
TAC
QT Input Channel

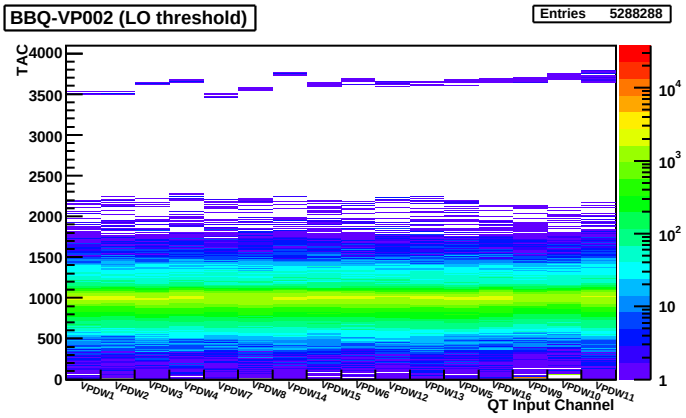

BBQ-ZD001
Entries 5288288
ADC
QT Input Channel

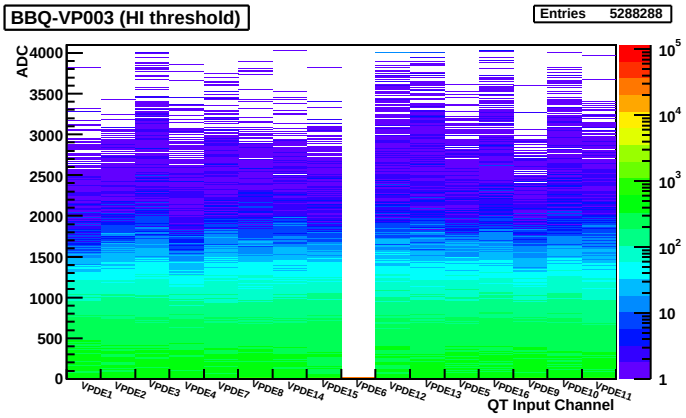

BBQ-ZD001
Entries 5288288
TAC
QT Input Channel

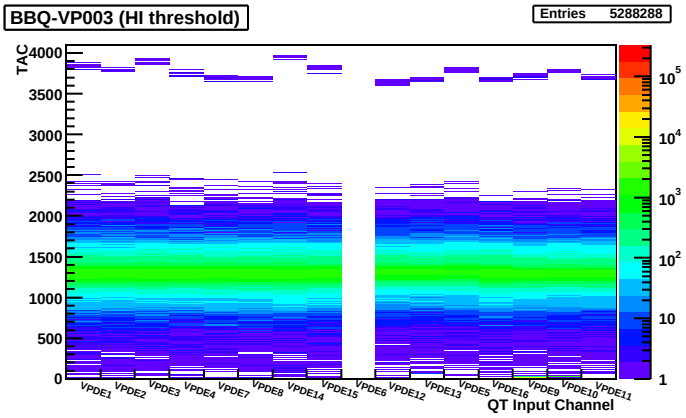

BBQ-VP001 (LO threshold)
Entries 5288288
ADC
QT Input Channel

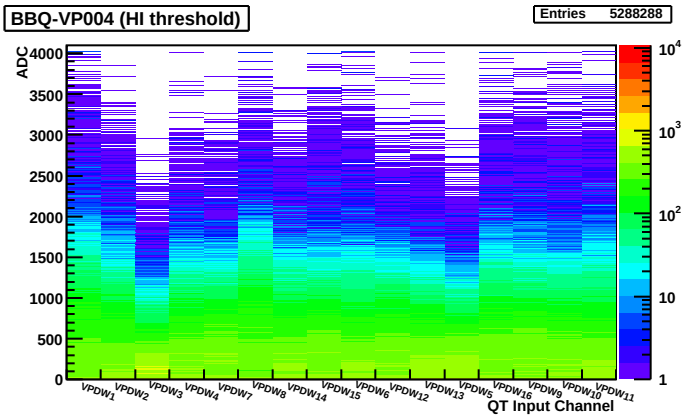

BBQ-VP001 (LO threshold)
Entries 5288288
TAC
QT Input Channel

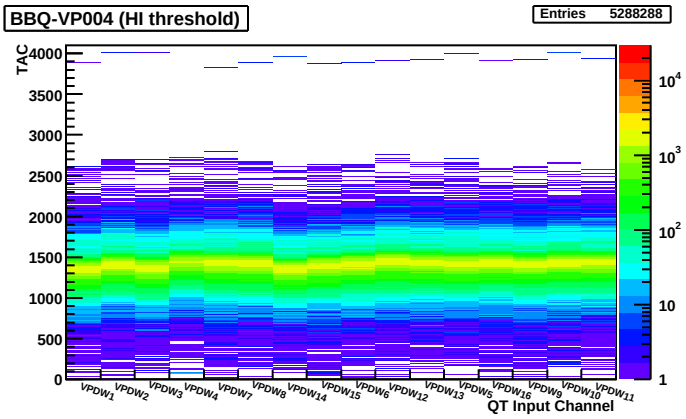

BBQ-VP002 (LO threshold)
Entries 5288288
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 5288288
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 5288288
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 5288288
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 5288288
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 5288288
TAC
QT Input Channel