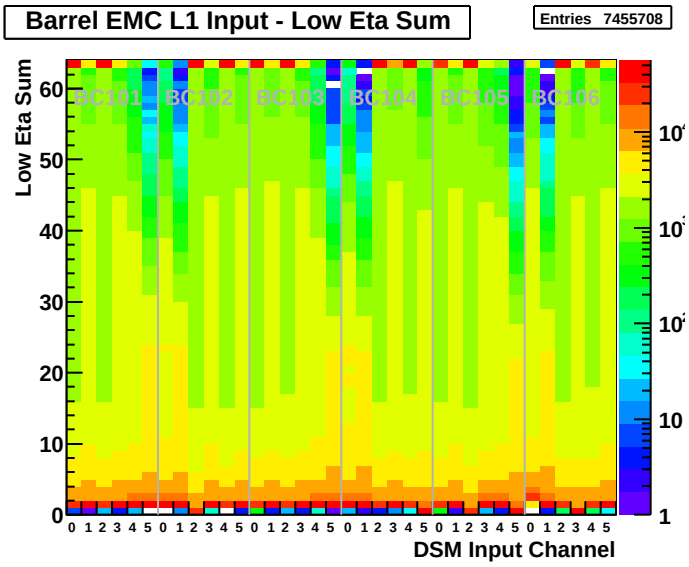
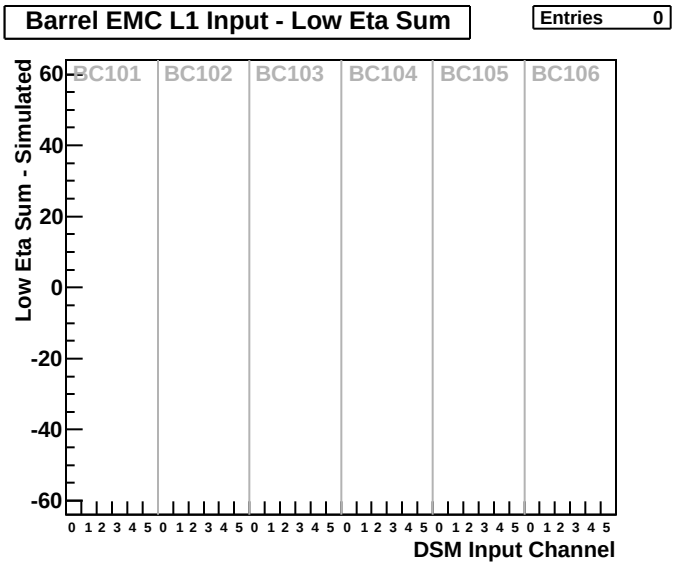
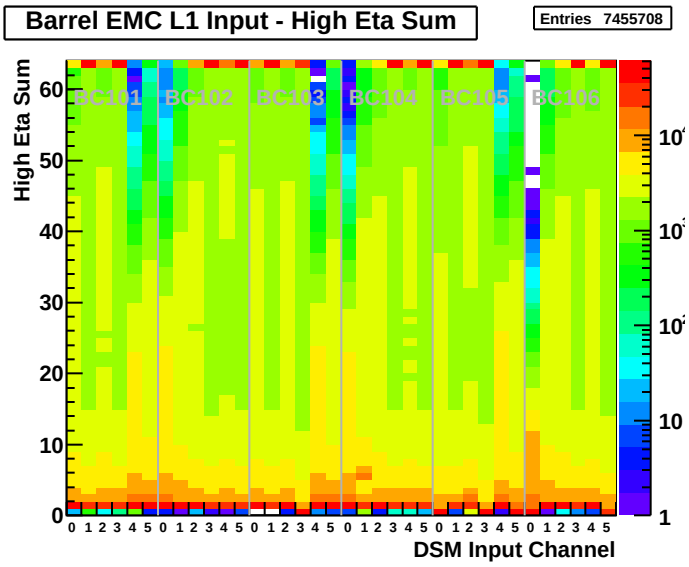
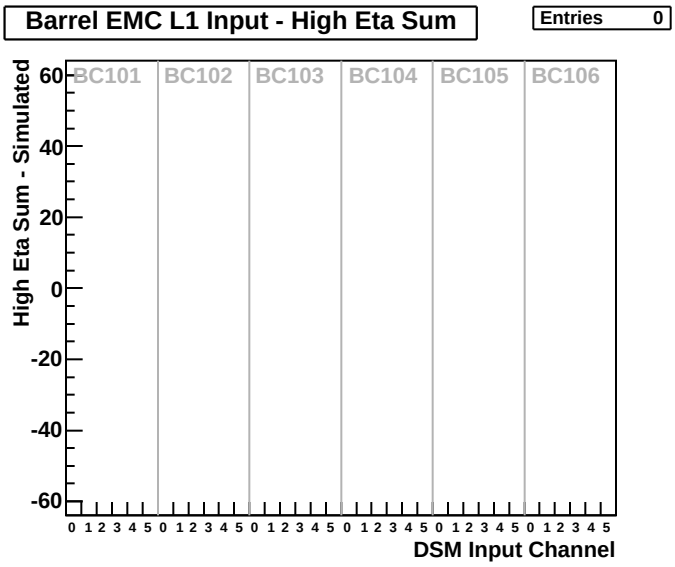
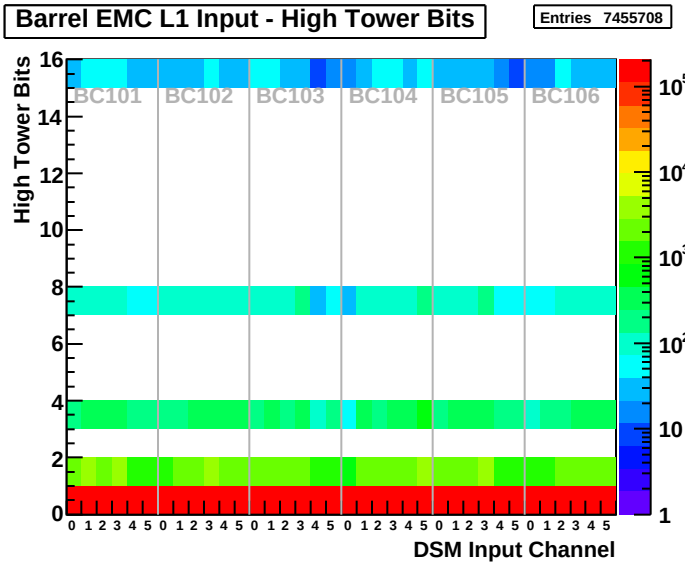
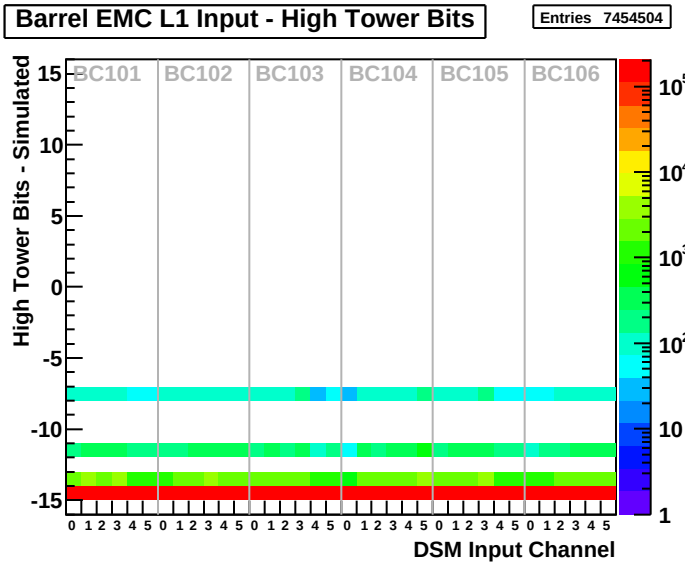

Barrel EMC L1 Input - Low Eta Sum
Entries 7455708
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

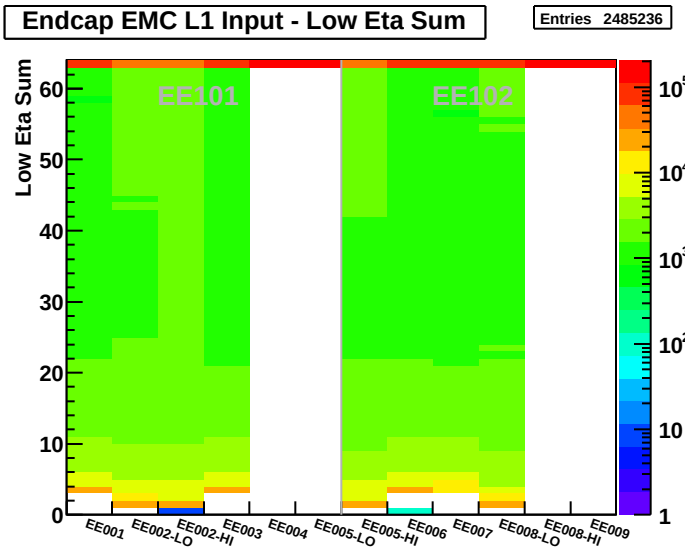

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

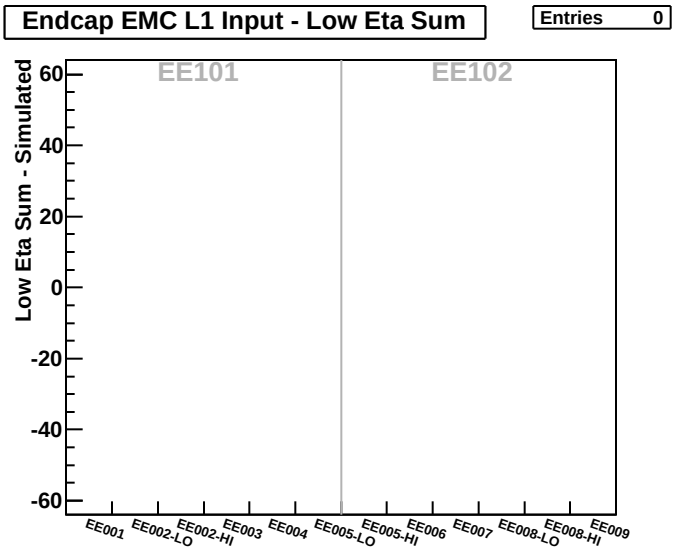

Barrel EMC L1 Input - High Eta Sum
Entries 7455708
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

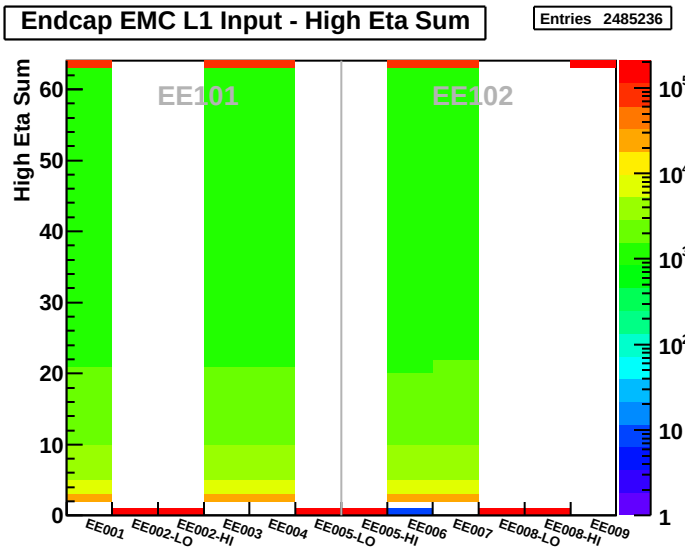

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

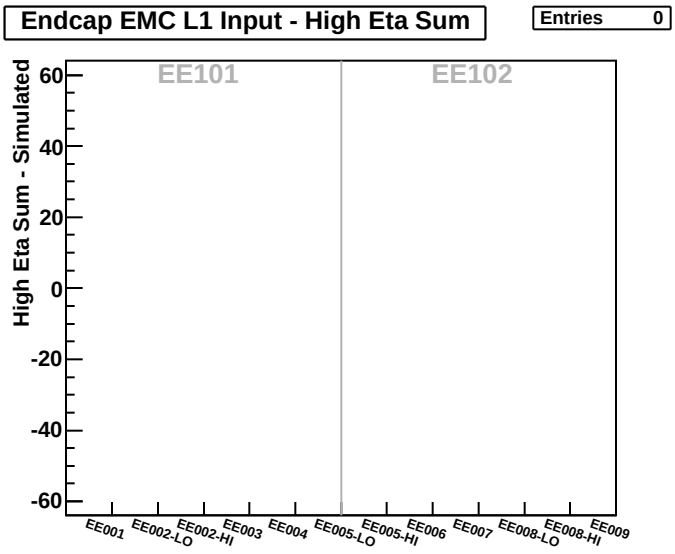

Barrel EMC L1 Input - High Tower Bits
Entries 7455708
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

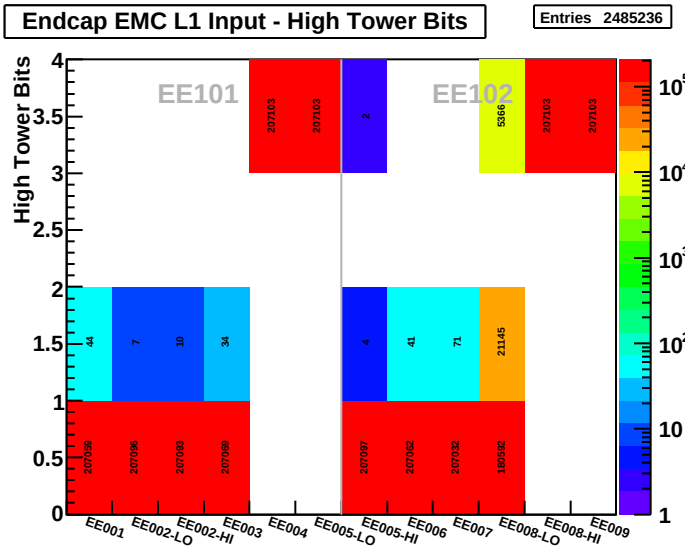

Barrel EMC L1 Input - High Tower Bits
Entries 7454504
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

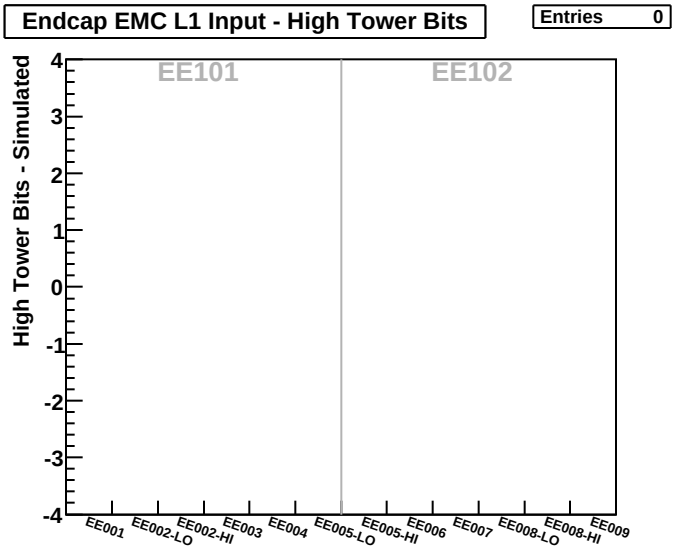

Endcap EMC L1 Input - Low Eta Sum
Entries 2485236
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

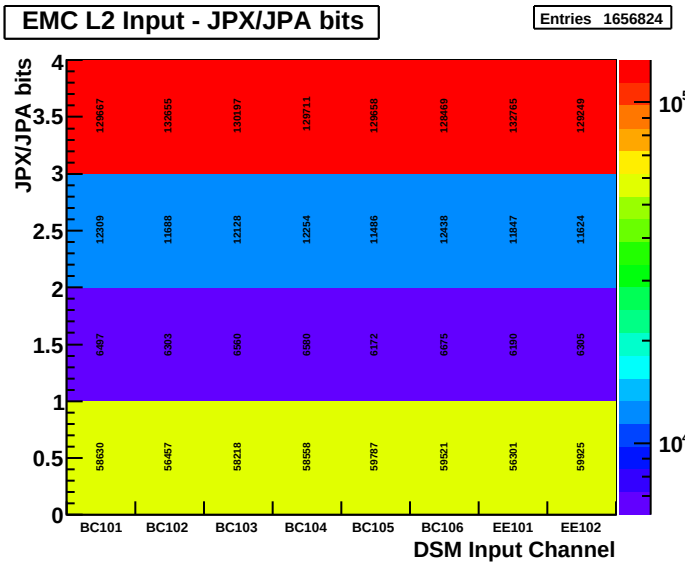

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

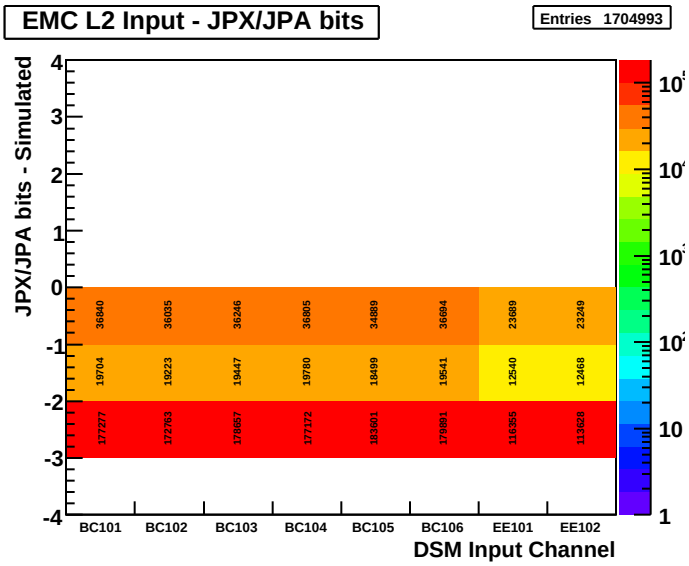

Endcap EMC L1 Input - High Eta Sum
Entries 2485236
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

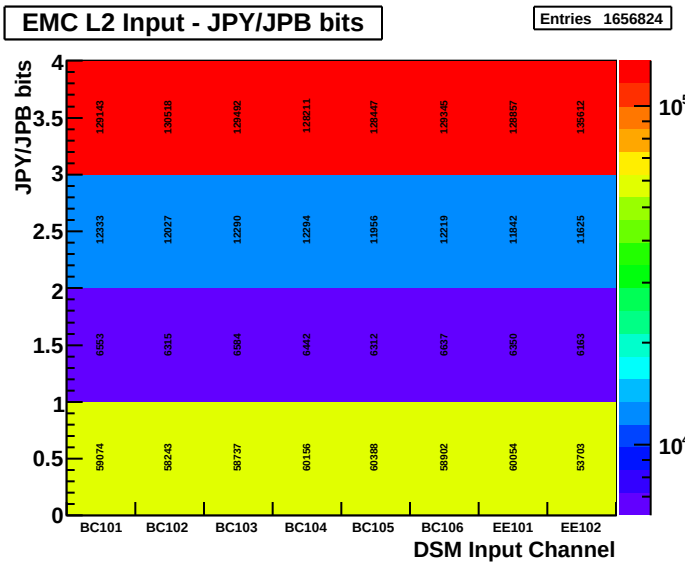

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

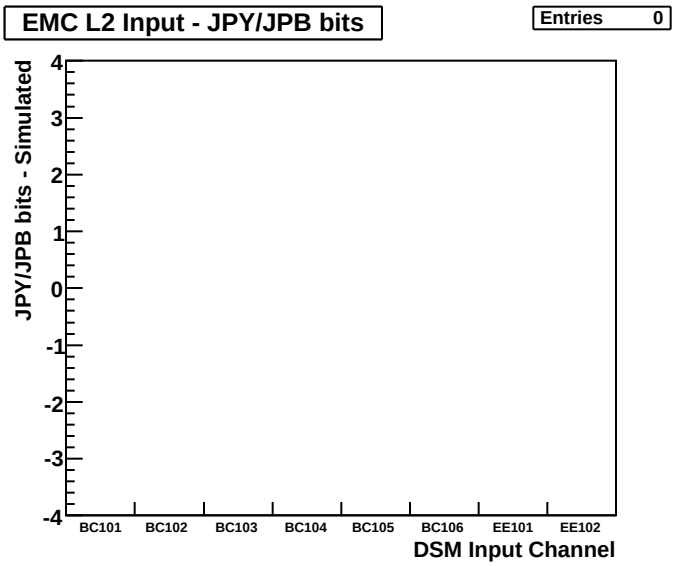

Endcap EMC L1 Input - High Tower Bits
Entries 2485236
High Tower Bits
EE101
EE102
207103
207103
2
5366
207103
207103
44
7
10
34
4
41
71
21145
207099
207096
207093
207069
207097
207062
207032
180592
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

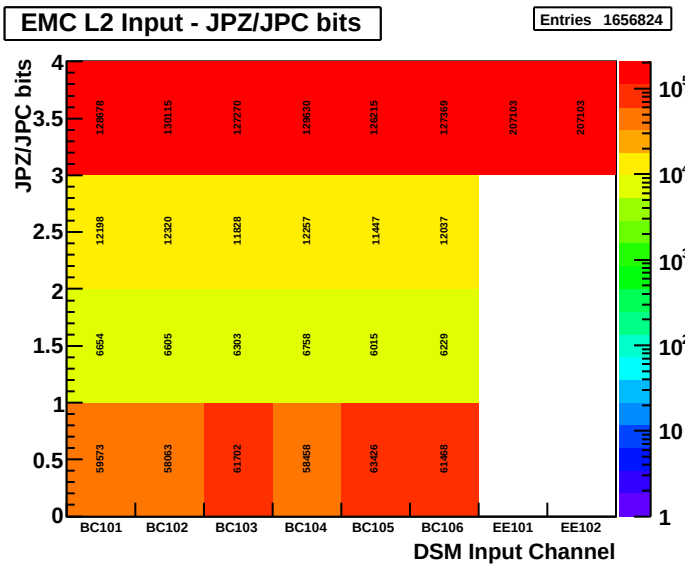

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

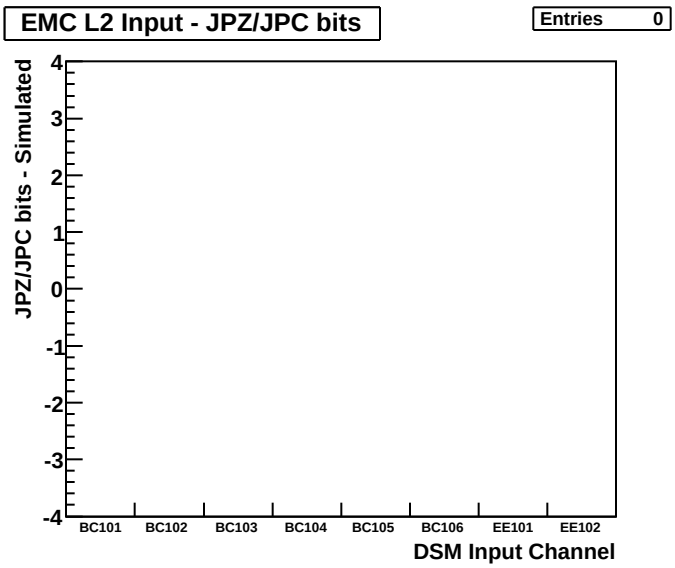

EMC L2 Input - JPX/JPA bits
Entries 1656824
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
129667 132655 130197 129711 129658 129469 132765 129249
12309 11608 12128 12254 11486 12438 11847 11624
6497 6303 6560 6580 6172 6675 6190 6305
58630 56457 58218 58558 59787 59521 56301 59925

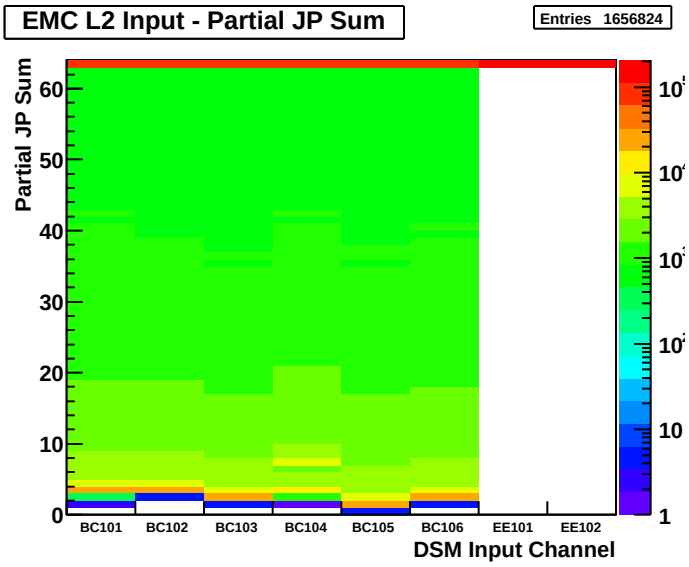

EMC L2 Input - JPX/JPA bits
Entries 1704993
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
36840 36035 36246 36805 34889 36694 23689 23249
19704 19223 19447 19780 18499 19541 12540 12468
177277 172763 178657 177172 183601 179891 116355 113628

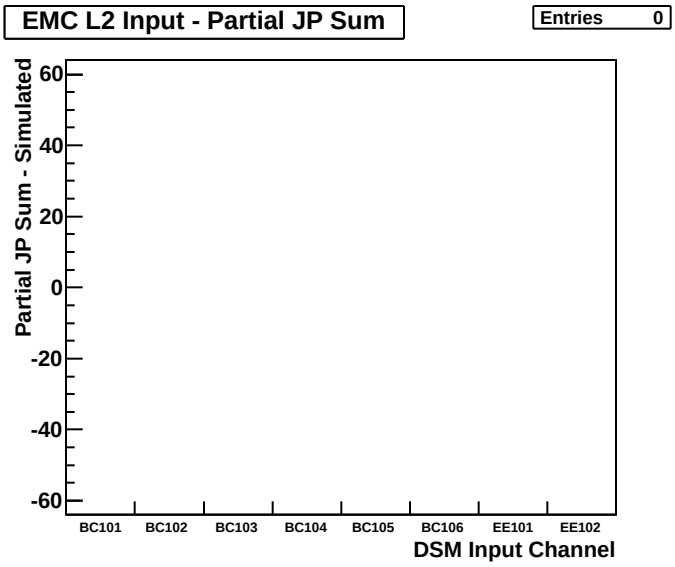

EMC L2 Input - JPY/JPB bits
Entries 1656824
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
129143 130518 129402 128211 128447 129345 128857 135612
12333 12027 12290 12294 11956 12219 11842 11625
6553 6315 6584 6412 6312 6637 6350 6183
59074 58243 58737 60156 60388 58902 60054 53703

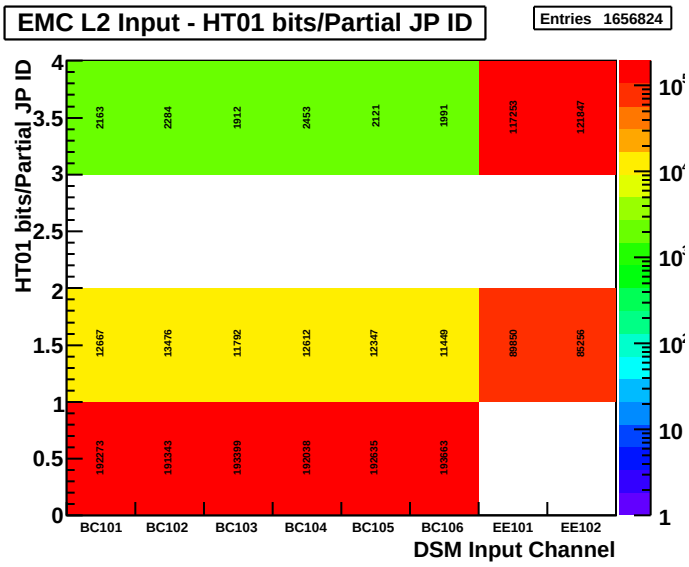

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

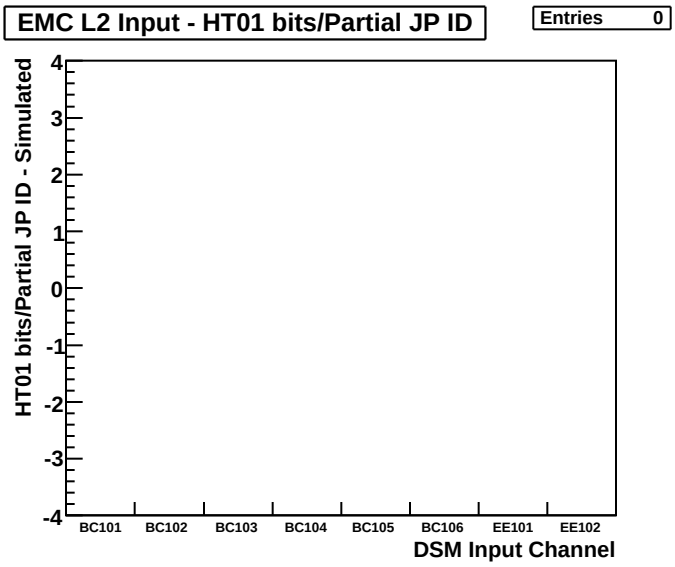

EMC L2 Input - JPZ/JPC bits
Entries 1656824
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
128678 130115 127270 128630 126215 127369 207103 207103
12198 12320 11828 12257 11447 12037
6654 6605 6303 6758 6015 6229
59573 58063 61702 58458 63426 61468

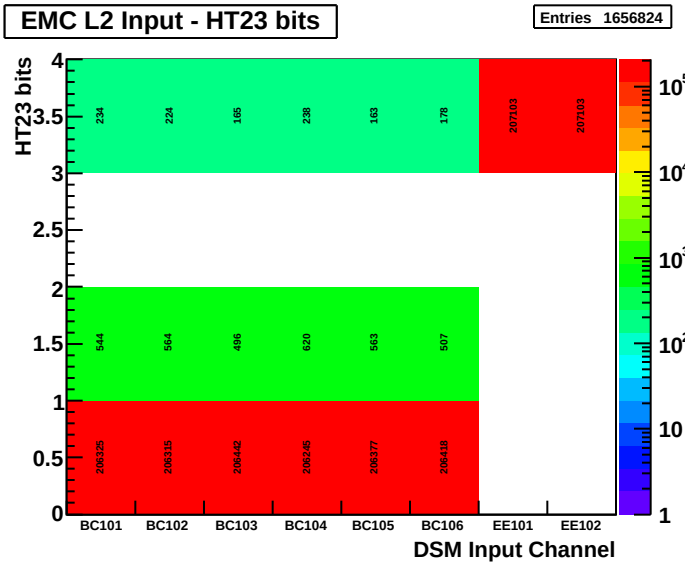

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

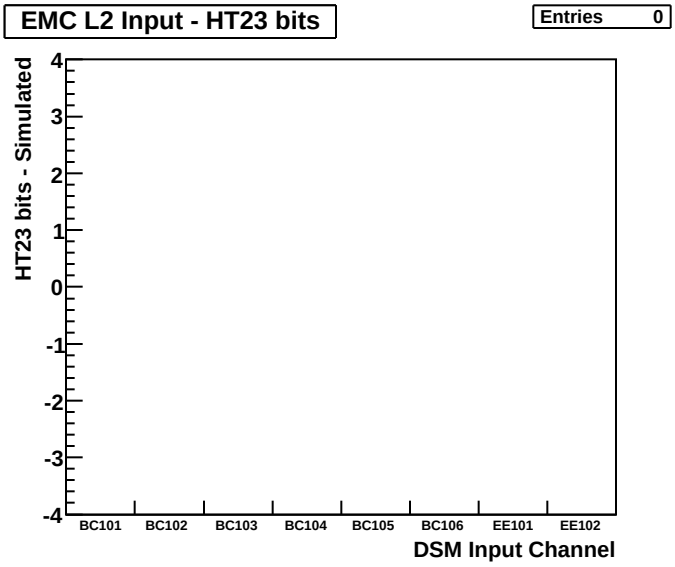

EMC L2 Input - Partial JP Sum
Entries 1656824
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

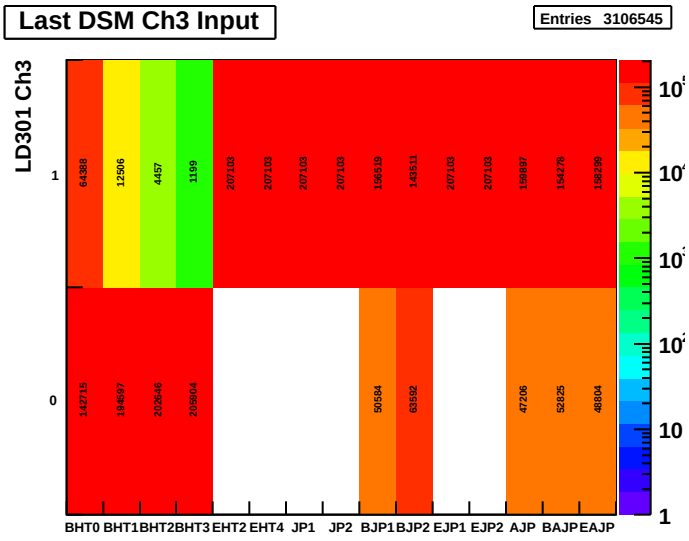

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

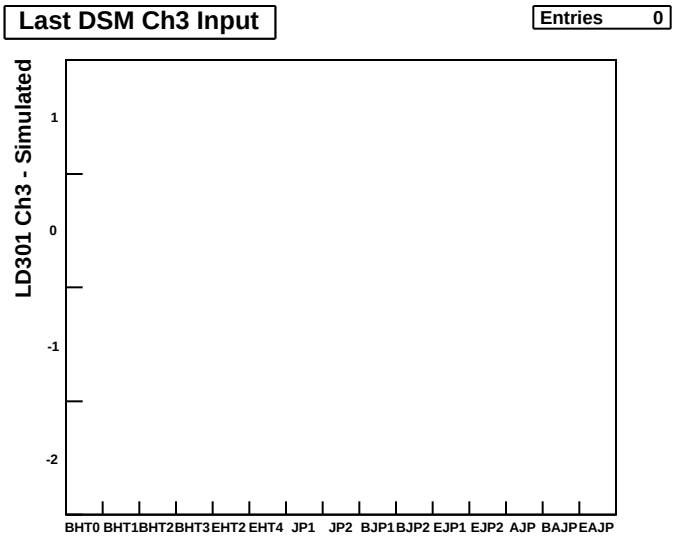

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1656824
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

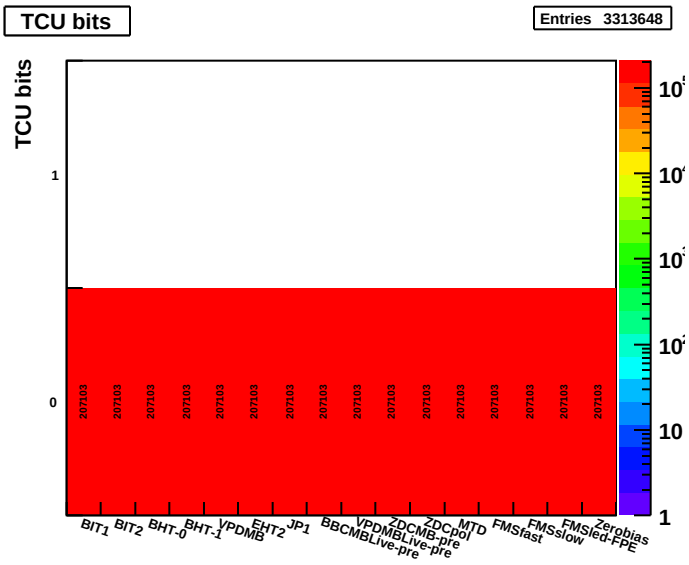

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

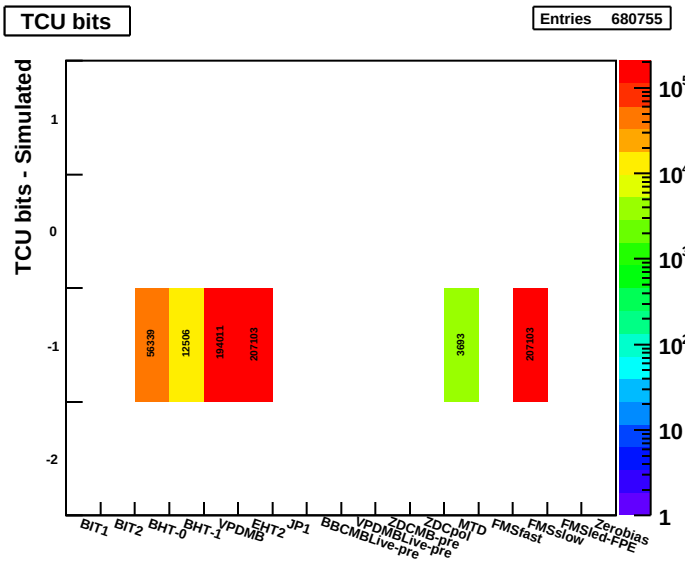

EMC L2 Input - HT23 bits
Entries 1656824
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

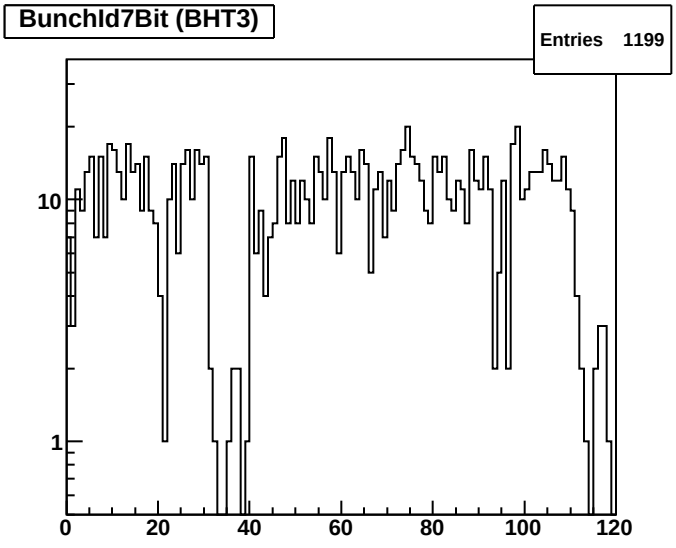

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

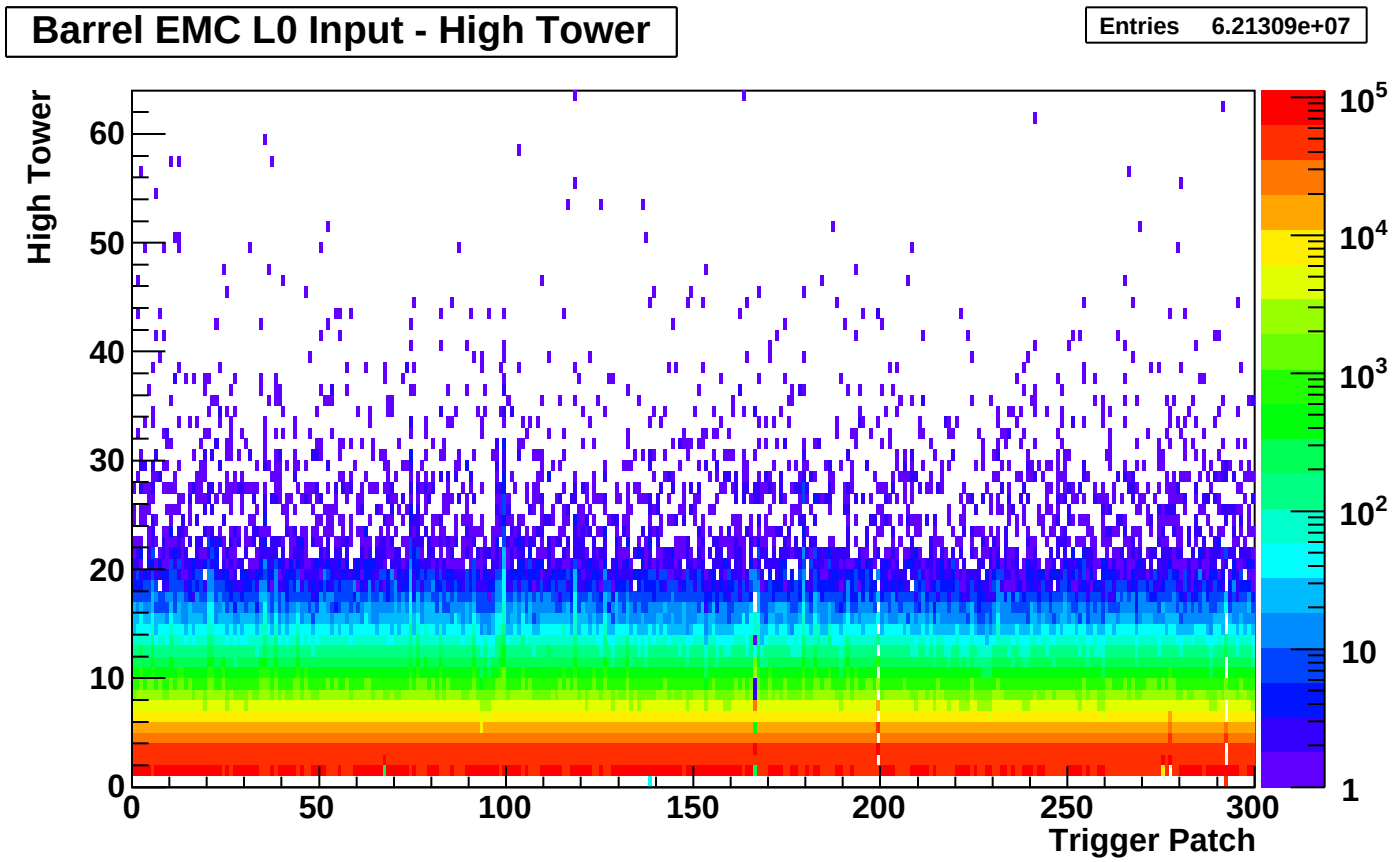

Last DSM Ch3 Input
Entries 3106545
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

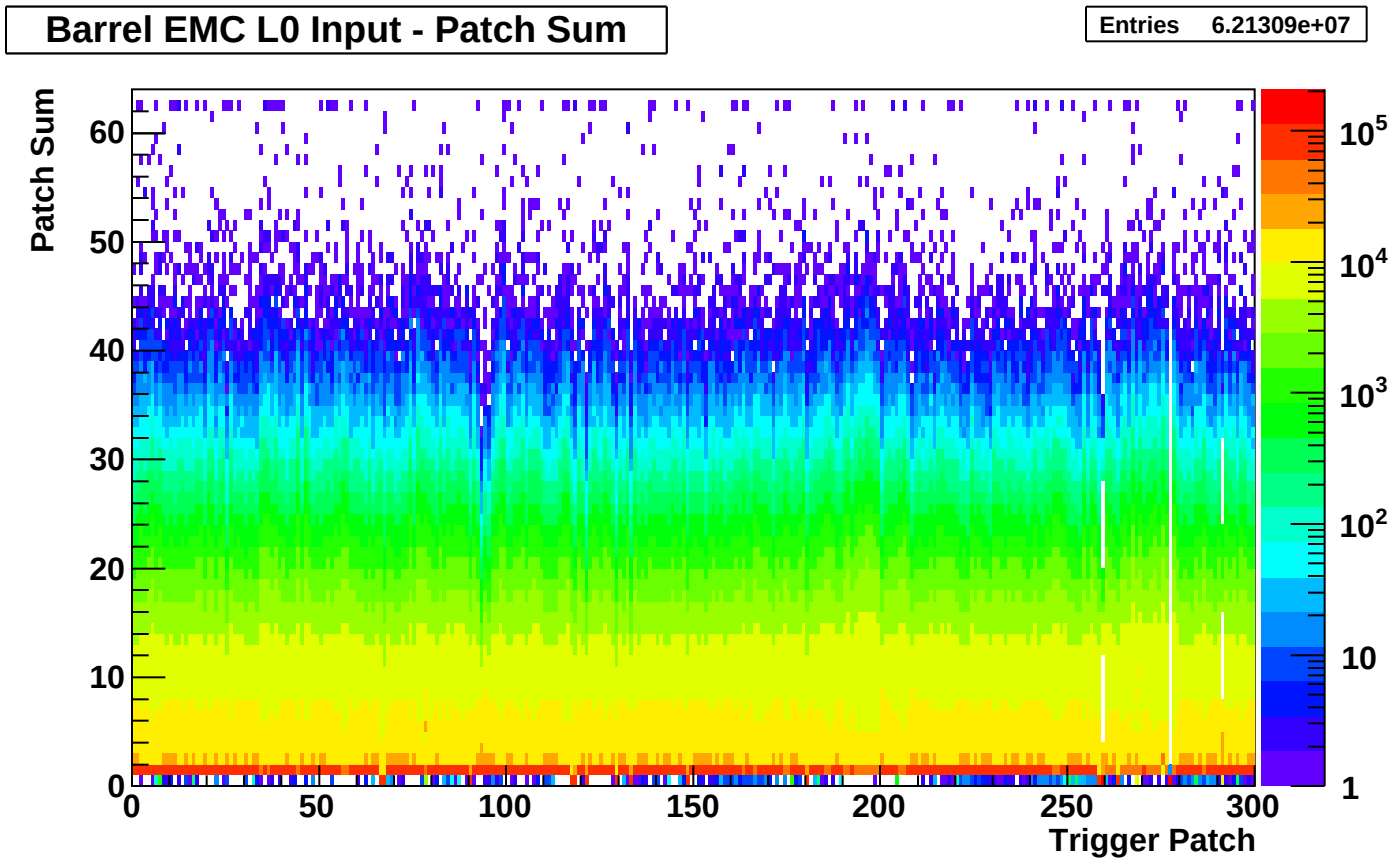

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

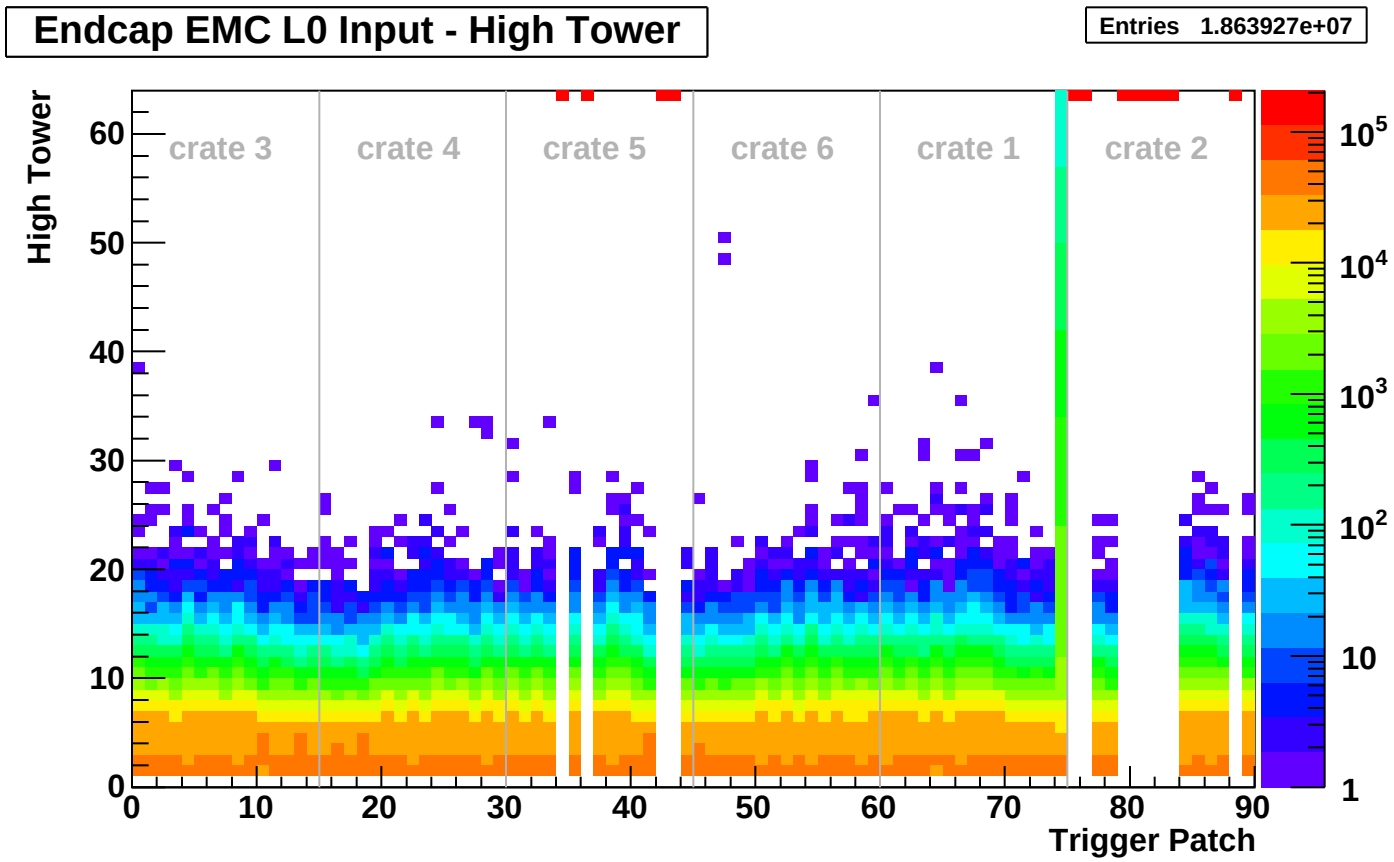

TCU bits
Entries 3313648
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

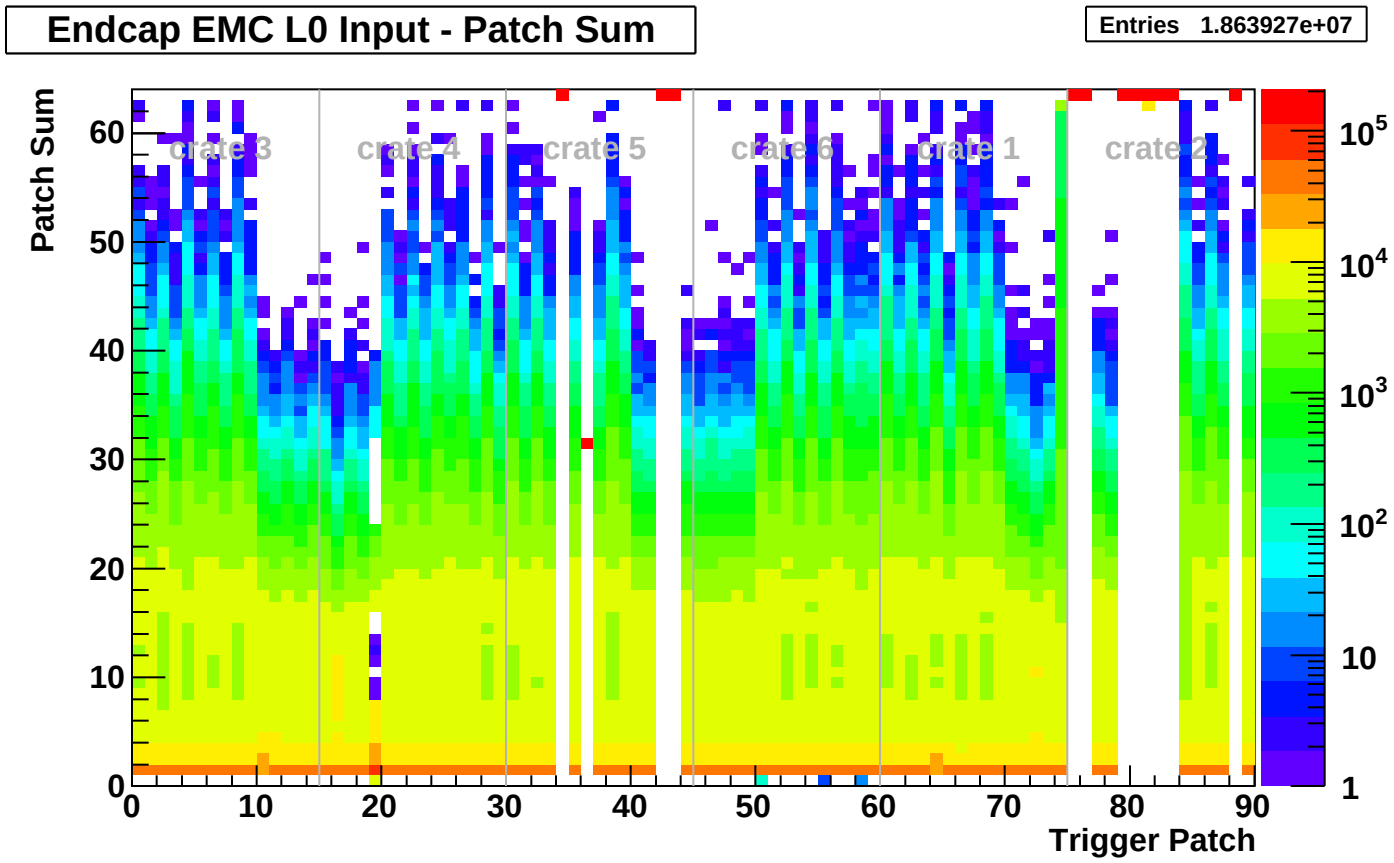

TCU bits
Entries 680755
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

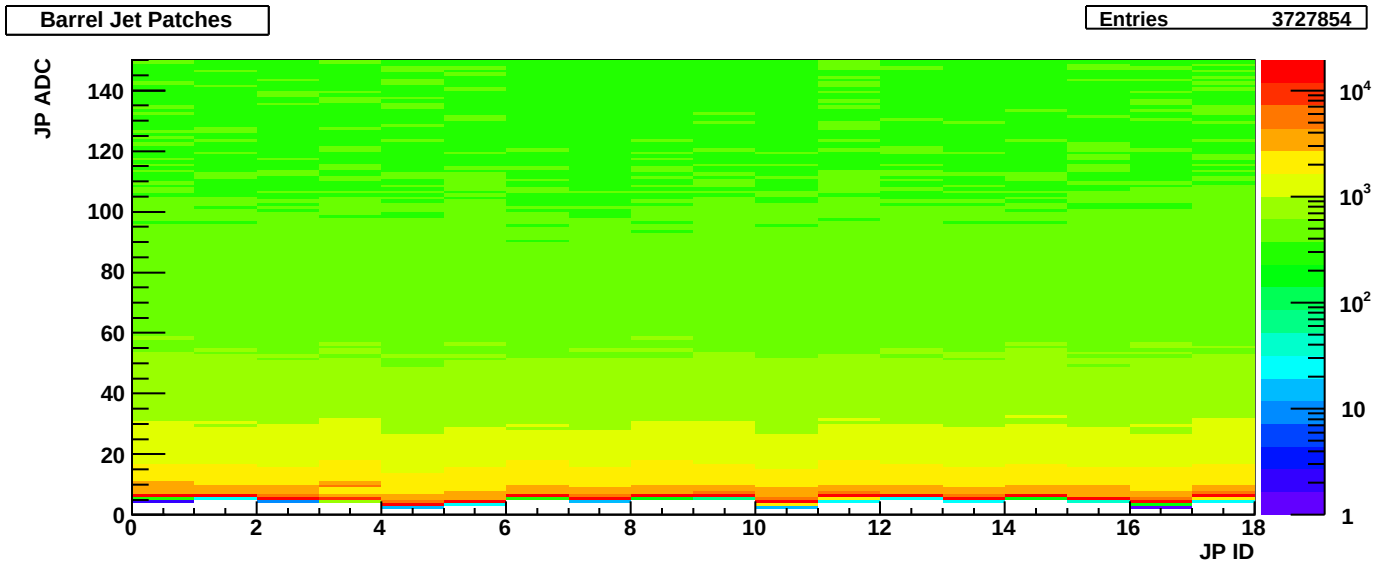

BunchId7Bit (BHT3)
Entries 1199

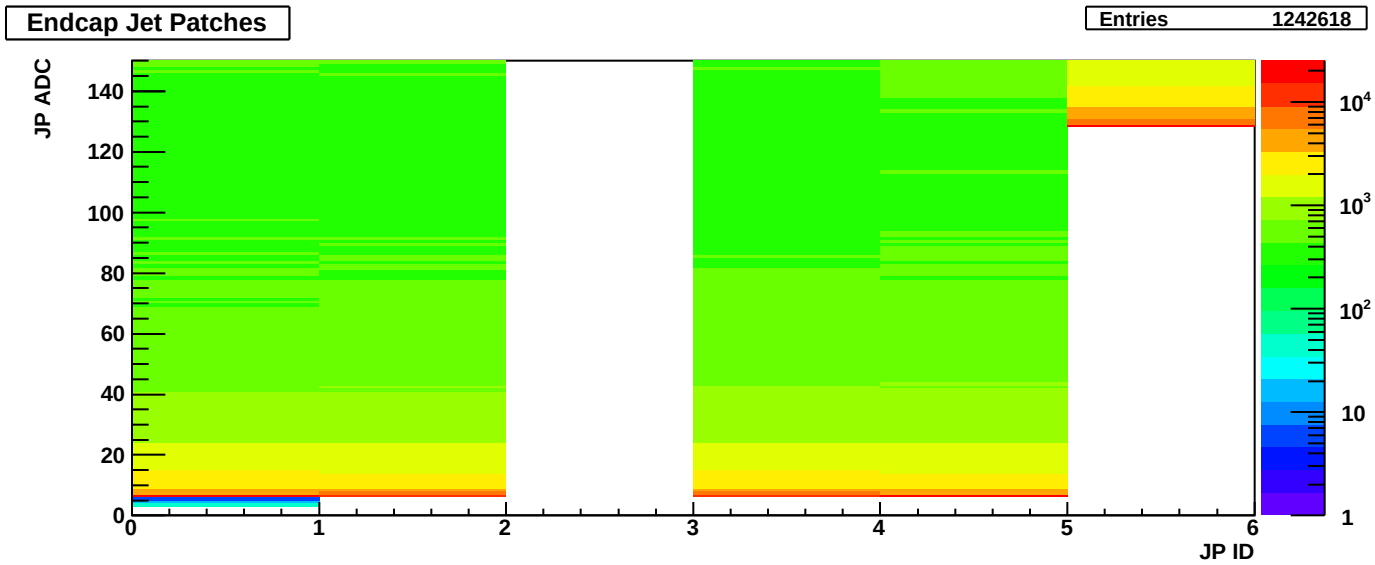

Barrel EMC L0 Input - High Tower
Entries 6.21309e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

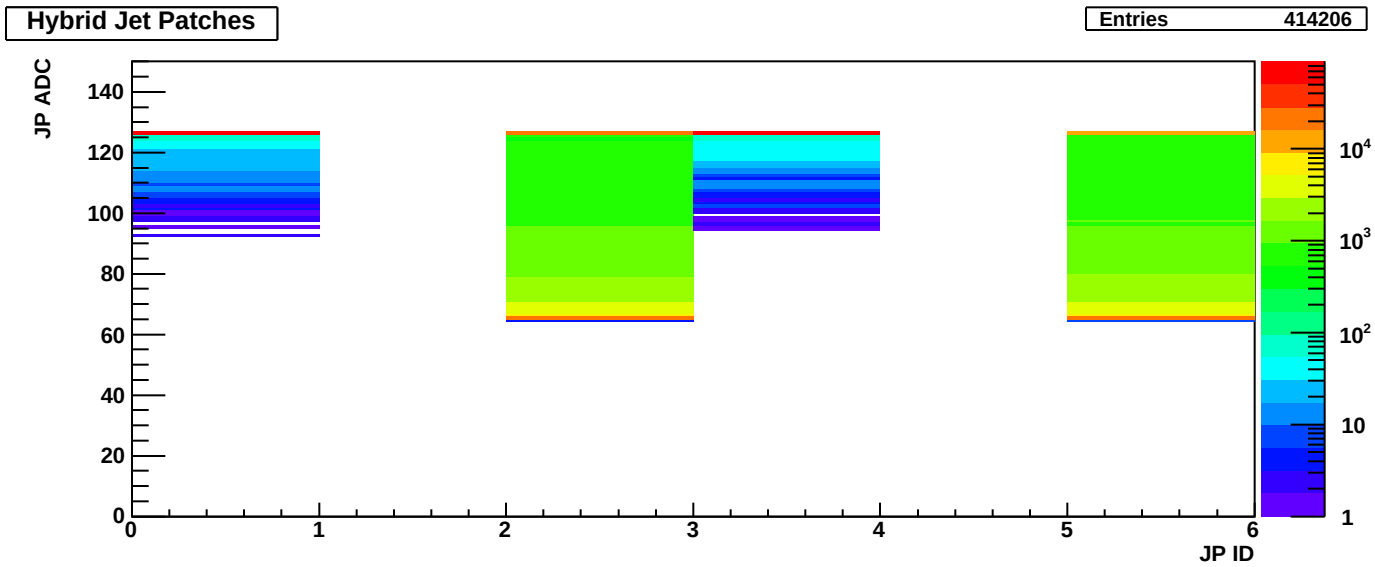

Barrel EMC L0 Input - Patch Sum
Entries 6.21309e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 1.863927e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch


Endcap EMC L0 Input - Patch Sum
Entries 1.863927e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

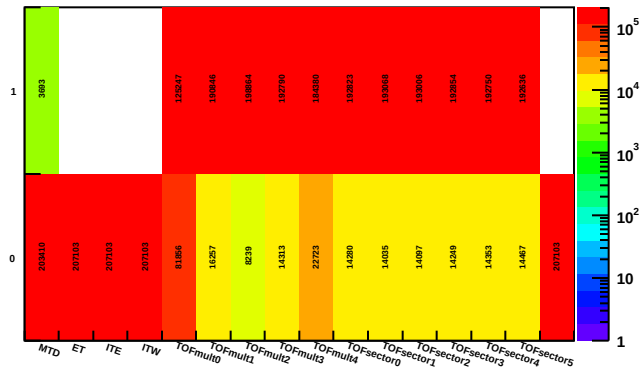

Barrel Jet Patches
Entries 3727854
JP ADC
JP ID


Endcap Jet Patches
Entries 1242618
JP ADC
JP ID


Hybrid Jet Patches
Entries 414206
JP ADC
JP ID

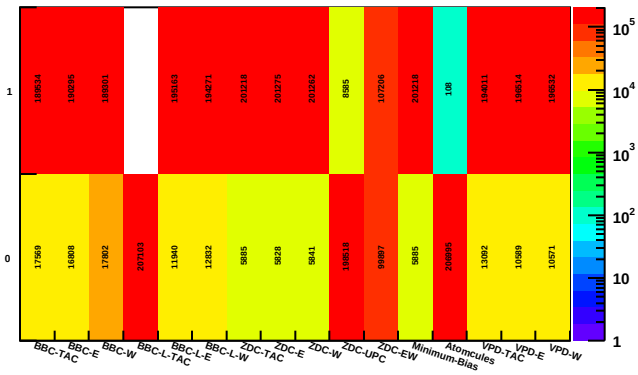
TF201 0-15 (ch0)

Entries 3313648



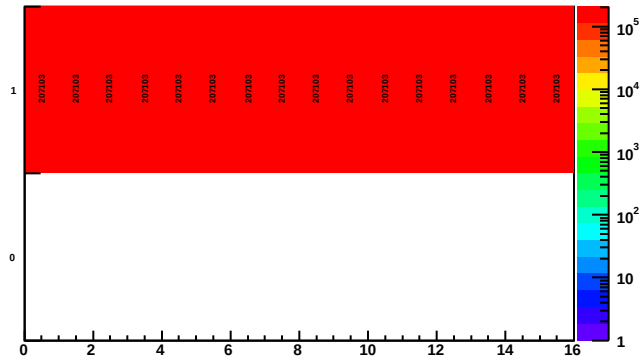
VT201 0-15 (ch1)

Entries 3313648



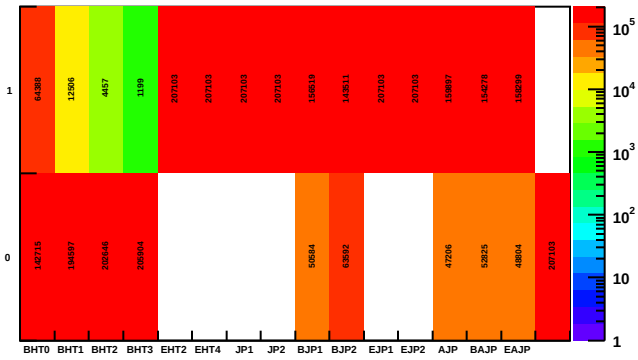
Unused (ch2)

Entries 3313648



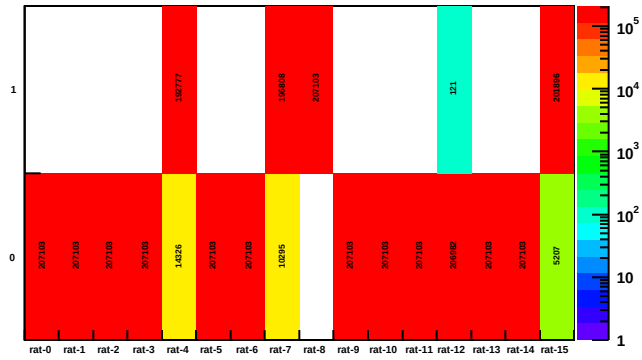
EM201 0-15 (ch3)

Entries 3313648



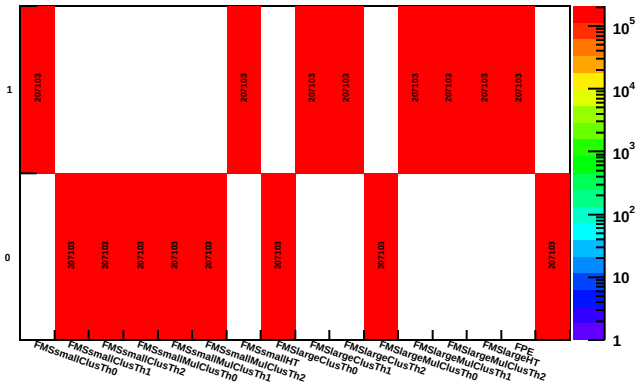
RAT board (ch4)

Entries 3313648



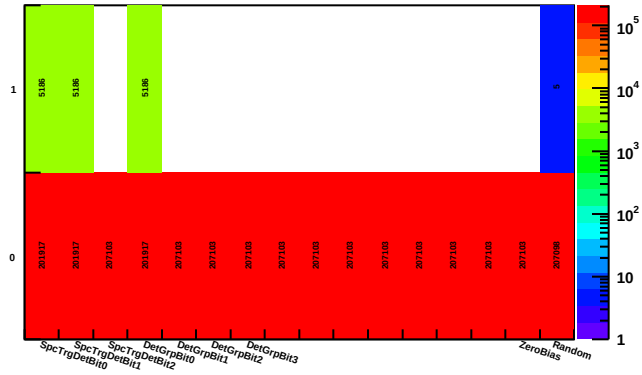
FP201 0-15 (ch5)

Entries 3313648



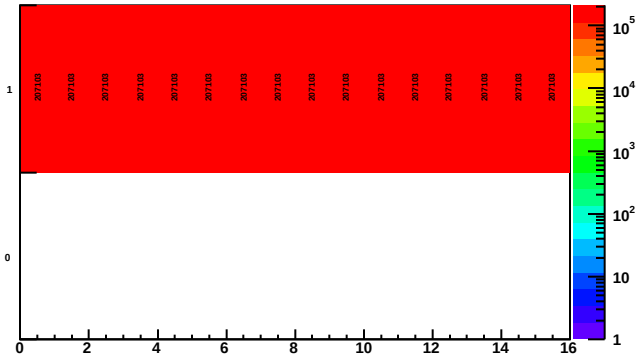
ST201 0-15 (ch6)

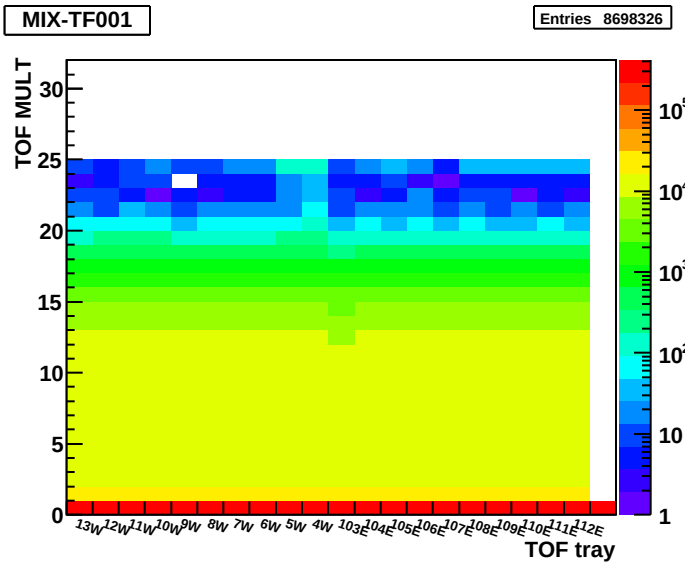
Entries 3313648

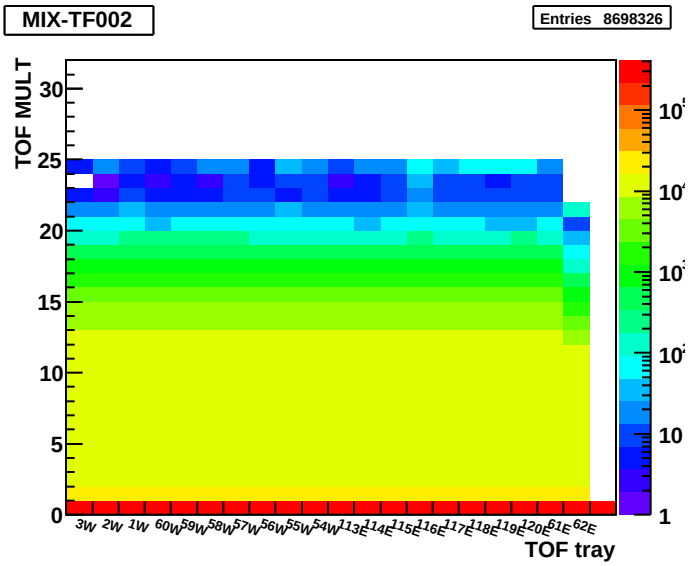


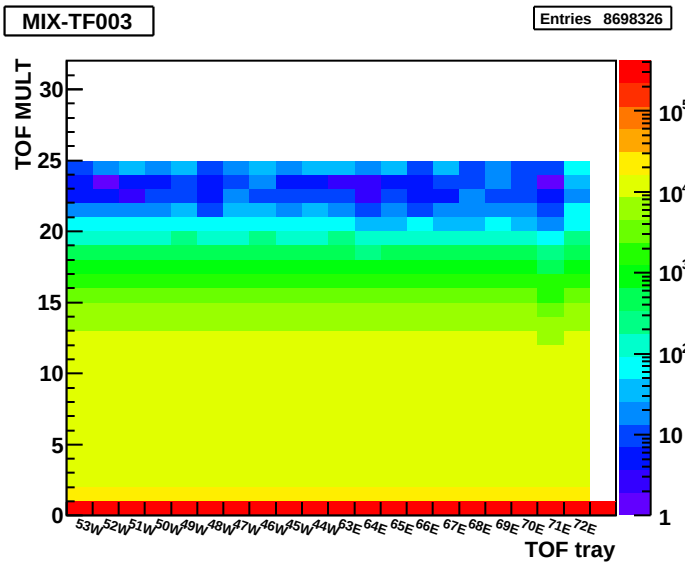
Unused (ch7)

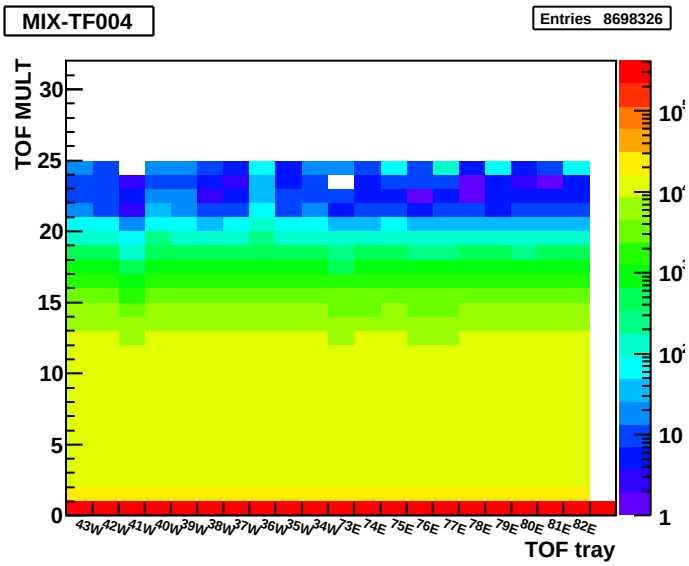
Entries 3313648

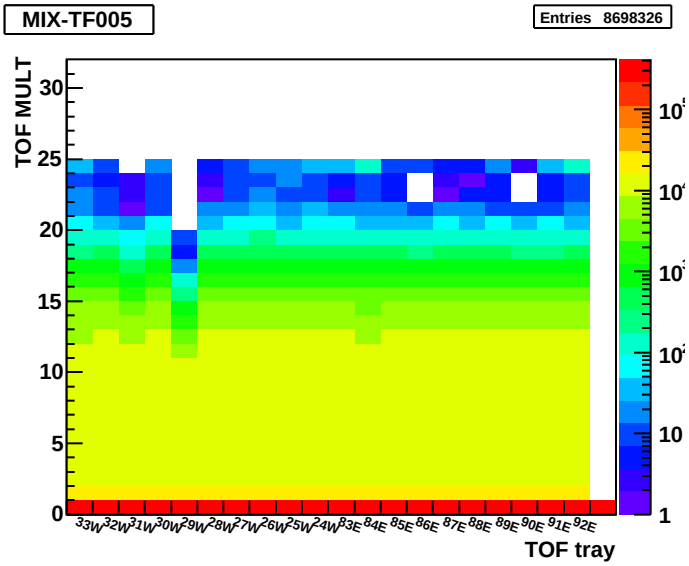


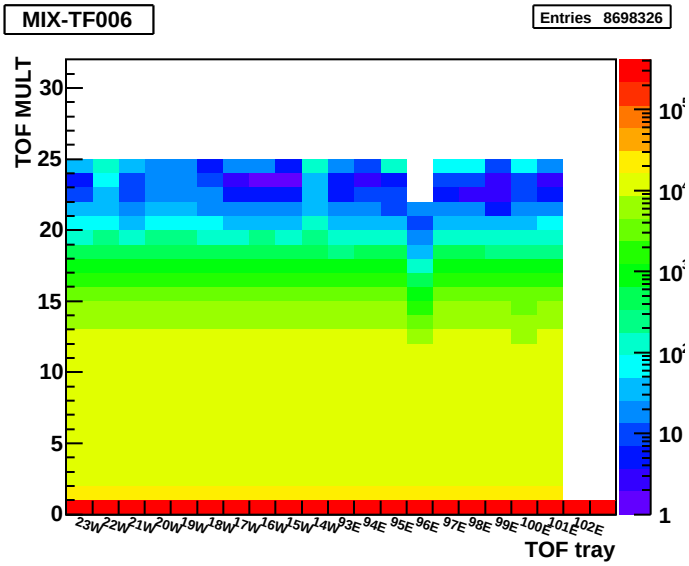

MIX-TF001
Entries 8698326
TOF MULT
TOF tray

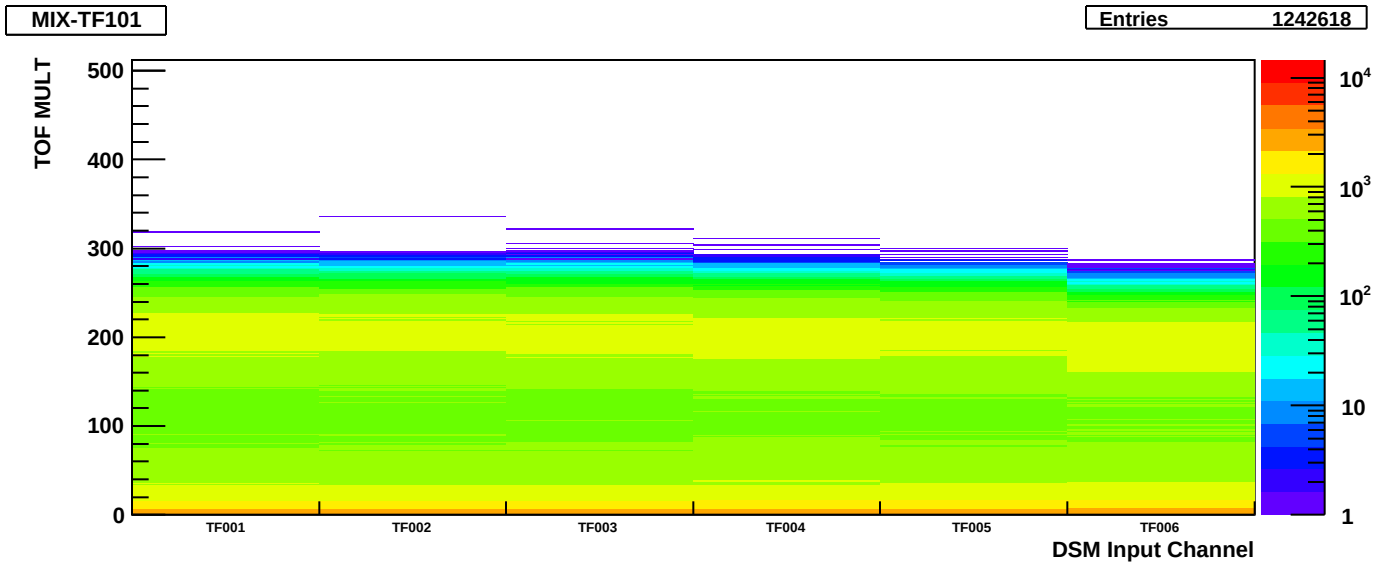

MIX-TF002
Entries 8698326
TOF MULT
TOF tray

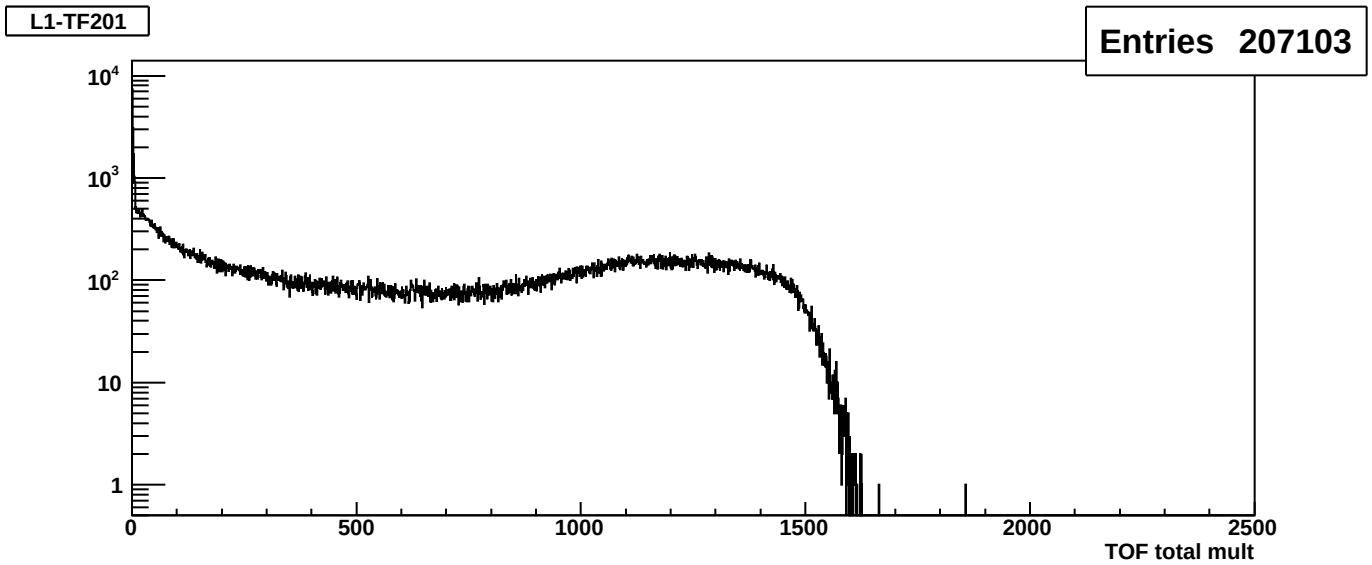

MIX-TF003
Entries 8698326
TOF MULT
TOF tray

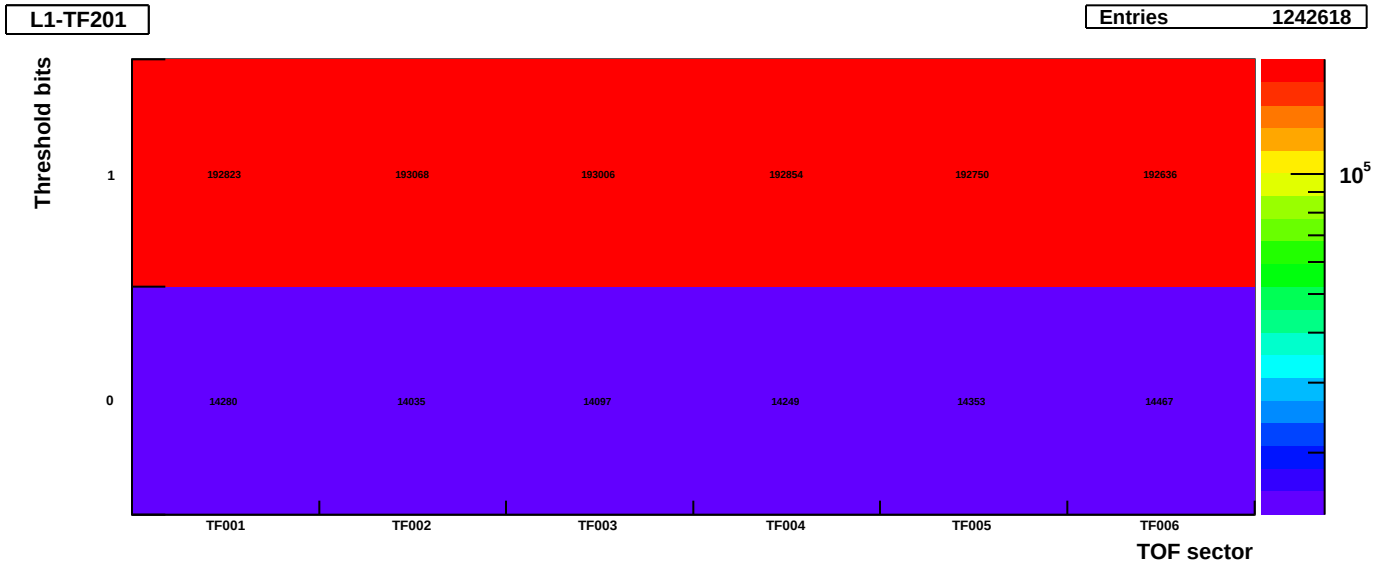

MIX-TF004
Entries 8698326
TOF MULT
TOF tray

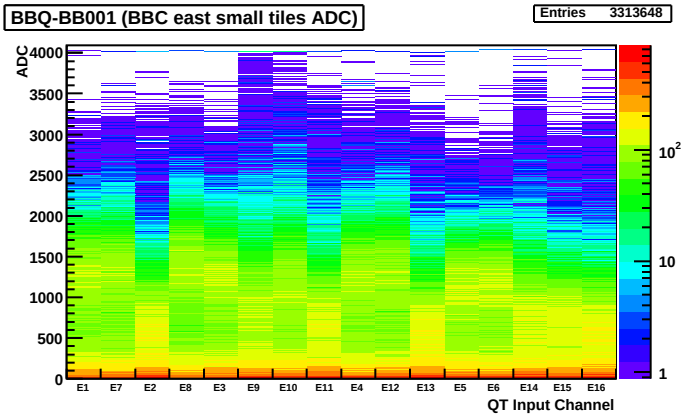

MIX-TF005
Entries 8698326
TOF MULT
TOF tray

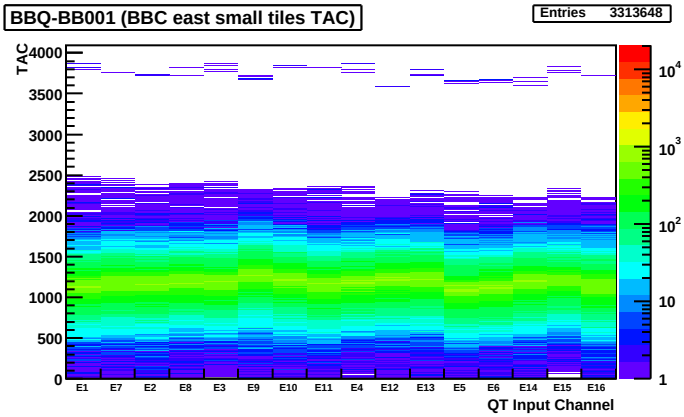

MIX-TF006
Entries 8698326
TOF MULT
TOF tray

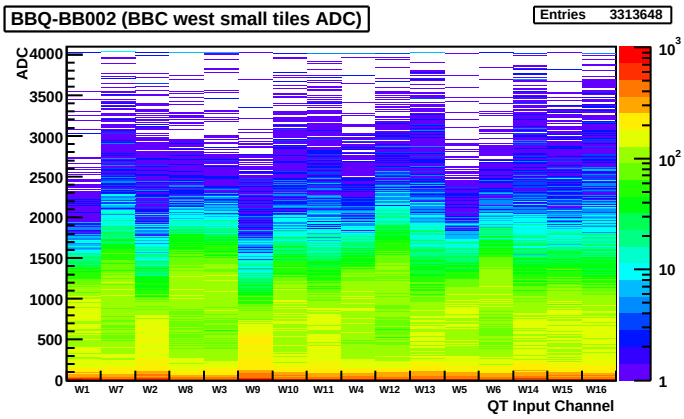

MIX-TF101
Entries 1242618
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

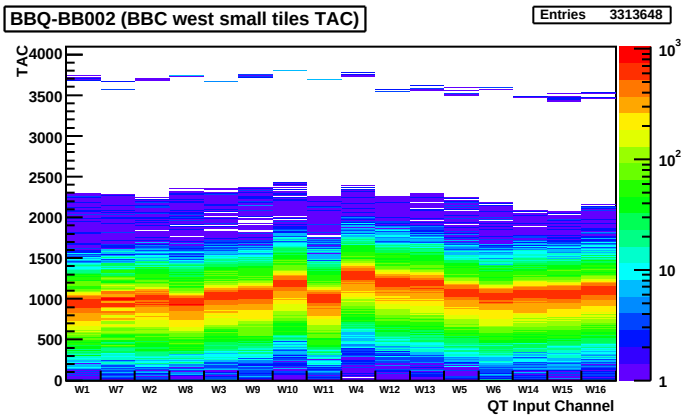

L1-TF201
Entries 207103
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

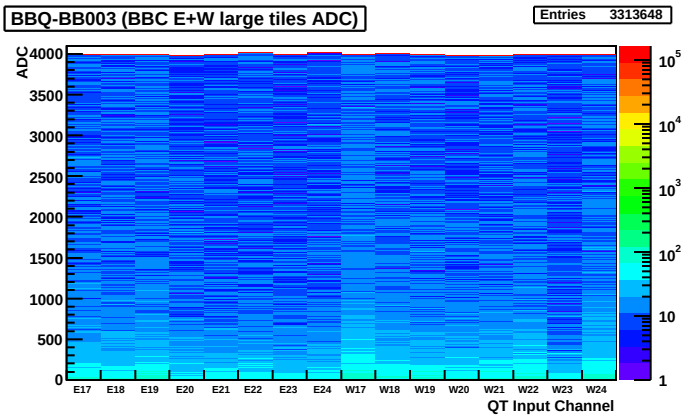

L1-TF201
Entries 1242618
Threshold bits
1
0
192823
193068
193006
192854
192750
192636
14280
14035
14097
14249
14353
14467
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

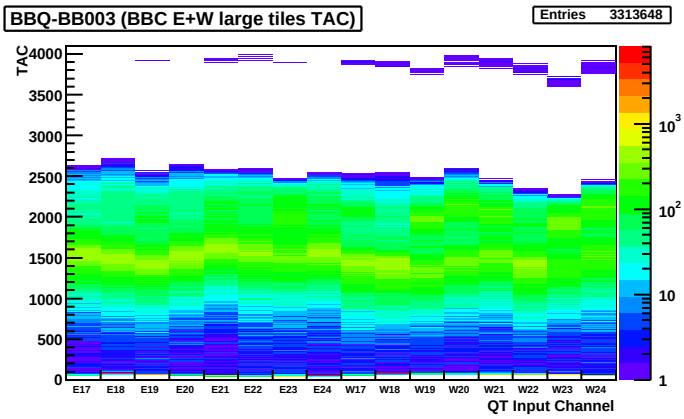

BBQ-BB001 (BBC east small tiles ADC)
Entries 3313648
ADC
QT Input Channel

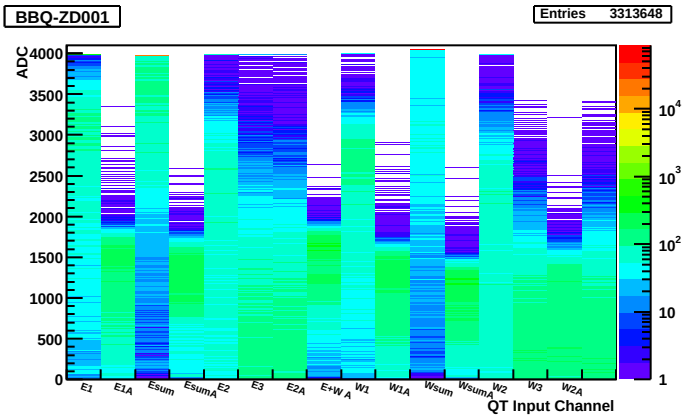

BBQ-BB001 (BBC east small tiles TAC)
Entries 3313648
TAC
QT Input Channel

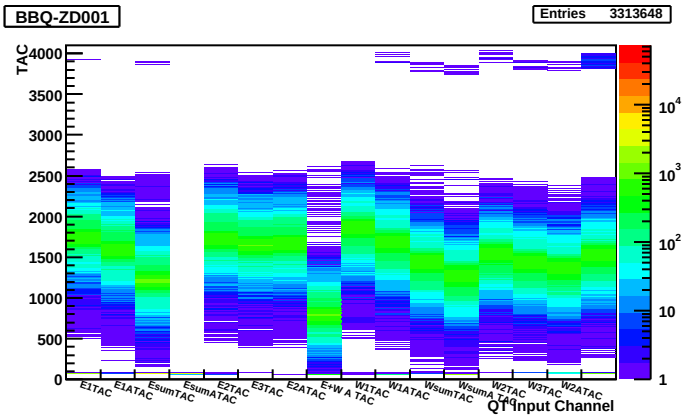

BBQ-BB002 (BBC west small tiles ADC)
Entries 3313648
ADC
QT Input Channel

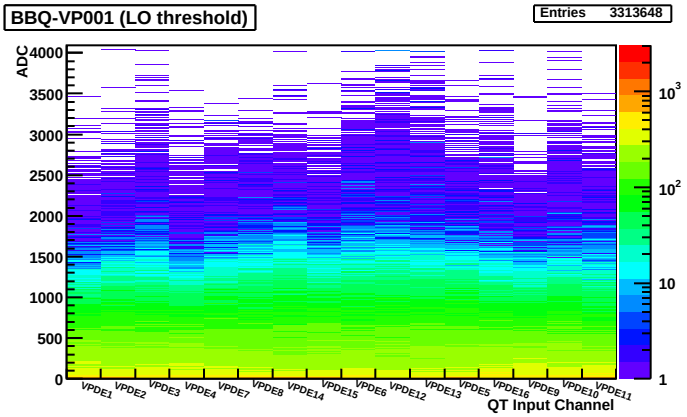

BBQ-BB002 (BBC west small tiles TAC)
Entries 3313648
TAC
QT Input Channel

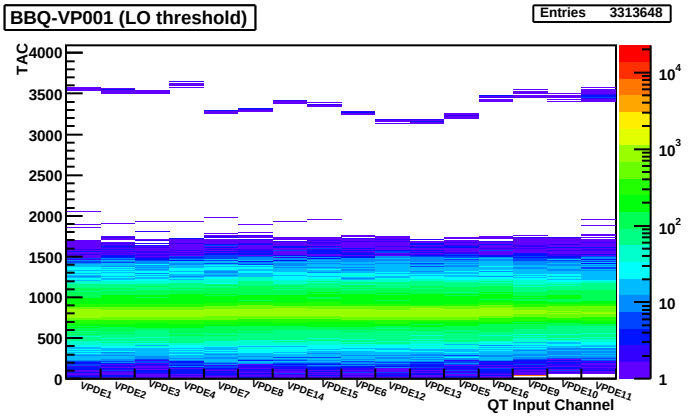

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3313648
ADC
QT Input Channel

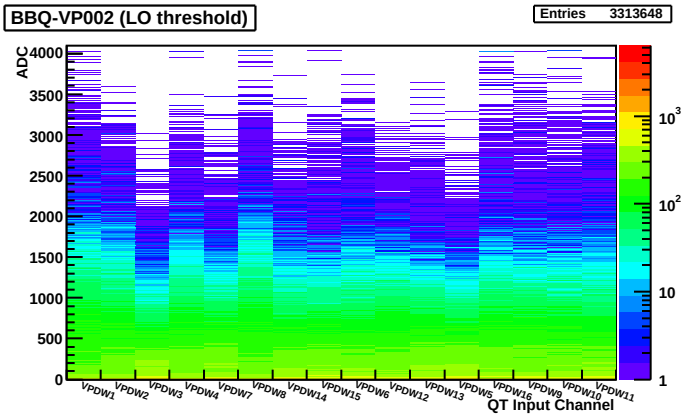

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3313648
TAC
QT Input Channel

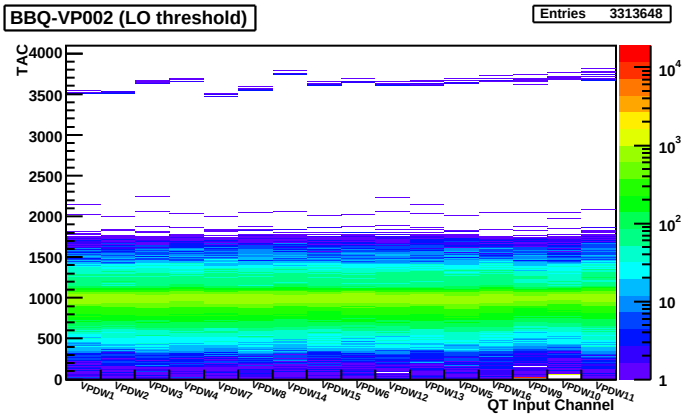

BBQ-ZD001
Entries 3313648
ADC
QT Input Channel

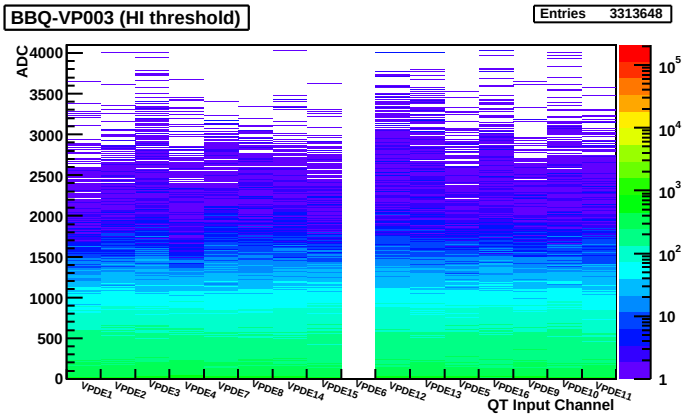

BBQ-ZD001
Entries 3313648
TAC
QT Input Channel

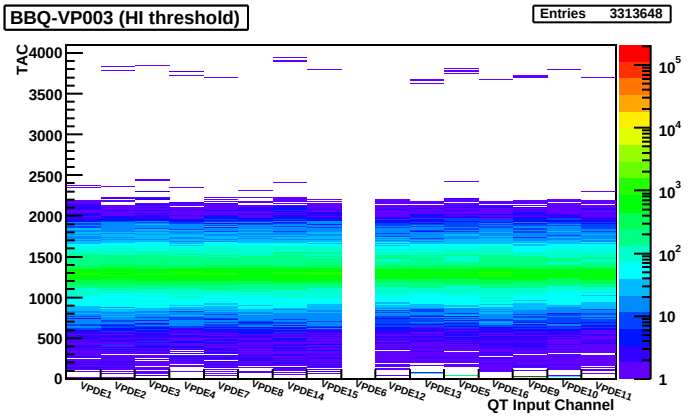

BBQ-VP001 (LO threshold)
Entries 3313648
ADC
QT Input Channel

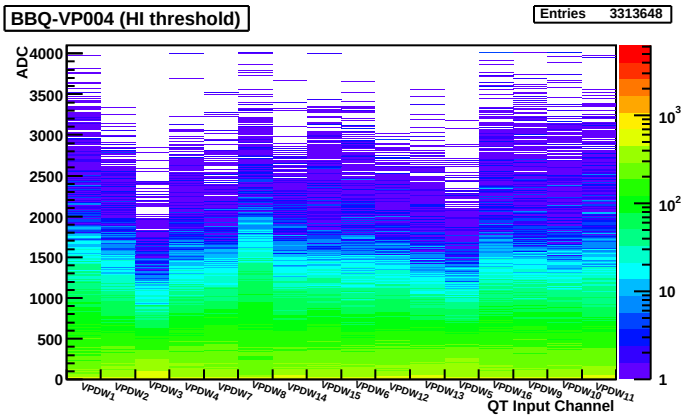

BBQ-VP001 (LO threshold)
Entries 3313648
TAC
QT Input Channel

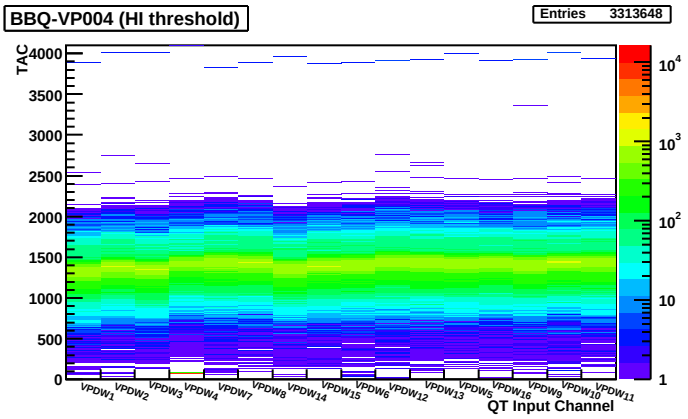

BBQ-VP002 (LO threshold)
Entries 3313648
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 3313648
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 3313648
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 3313648
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 3313648
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 3313648
TAC
QT Input Channel