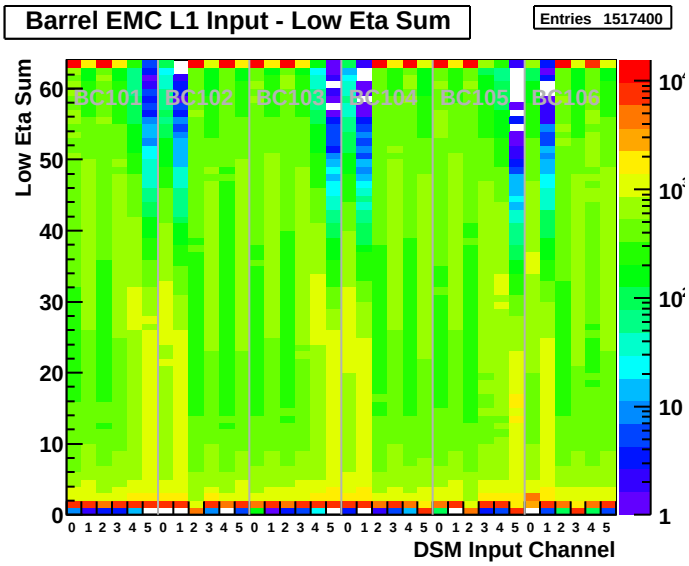
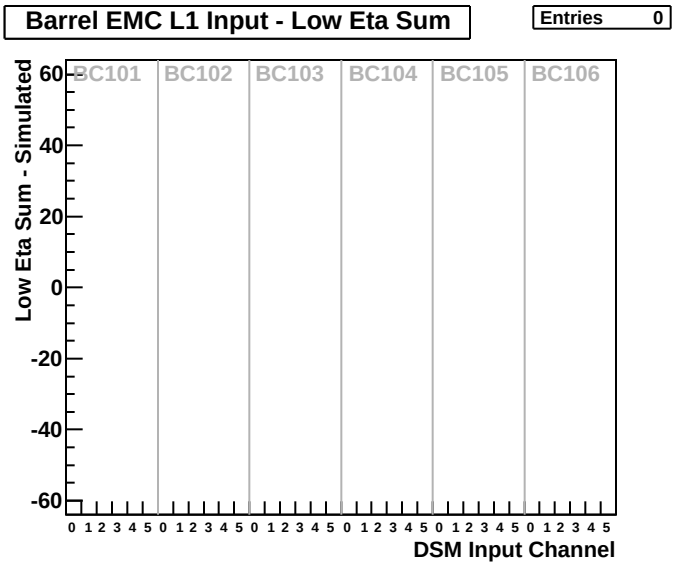
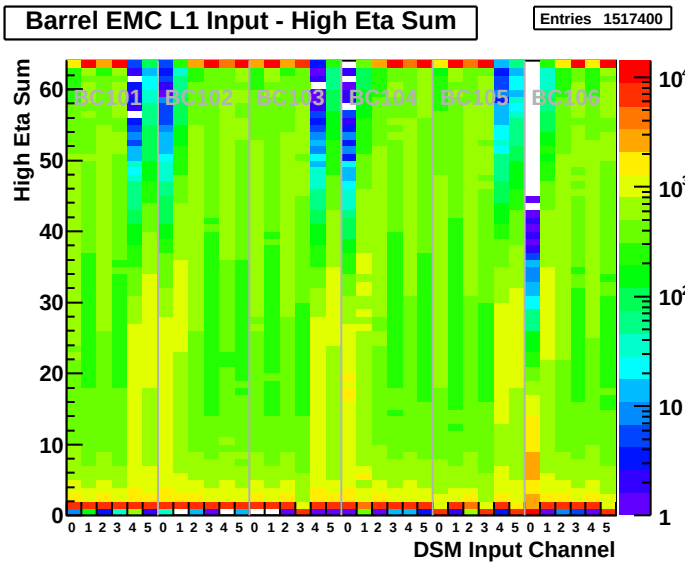
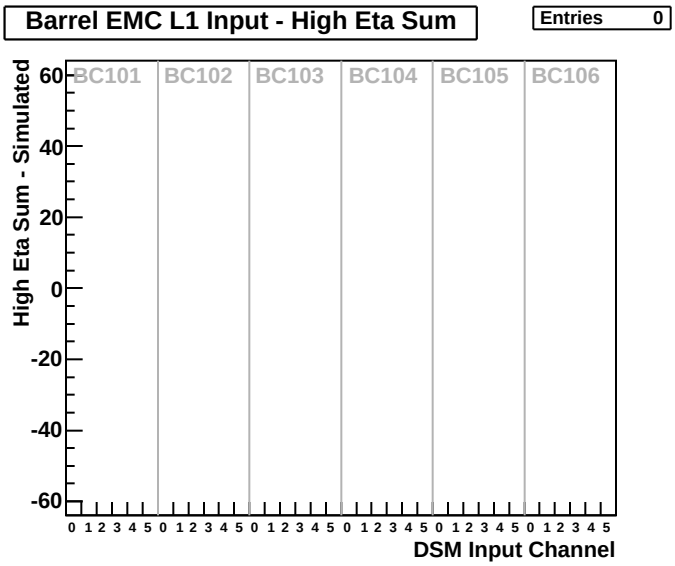
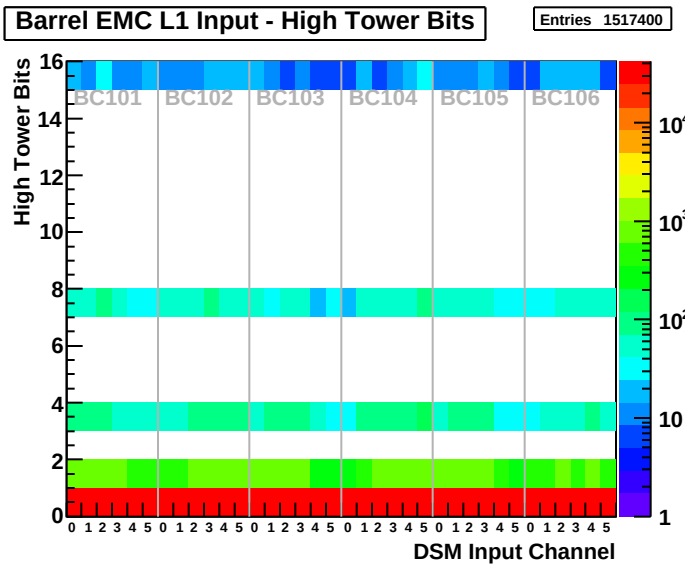
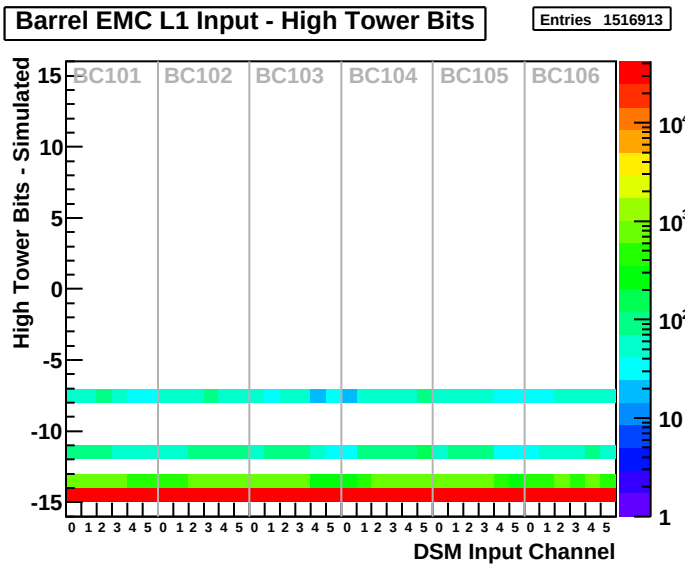

Barrel EMC L1 Input - Low Eta Sum
Entries 1517400
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

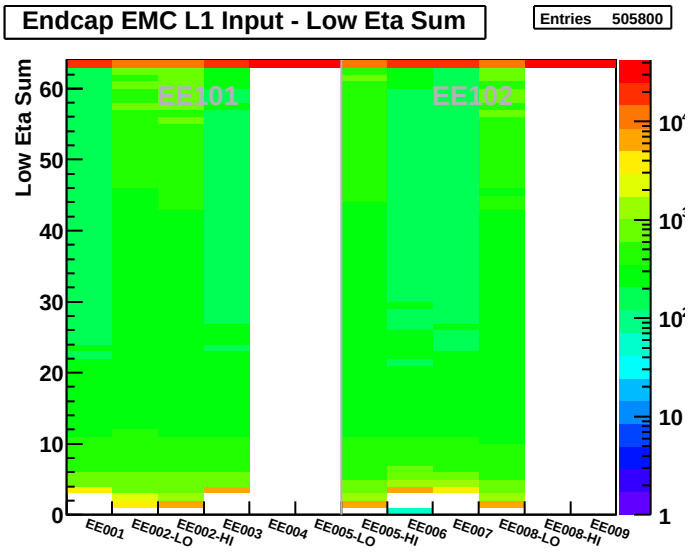

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

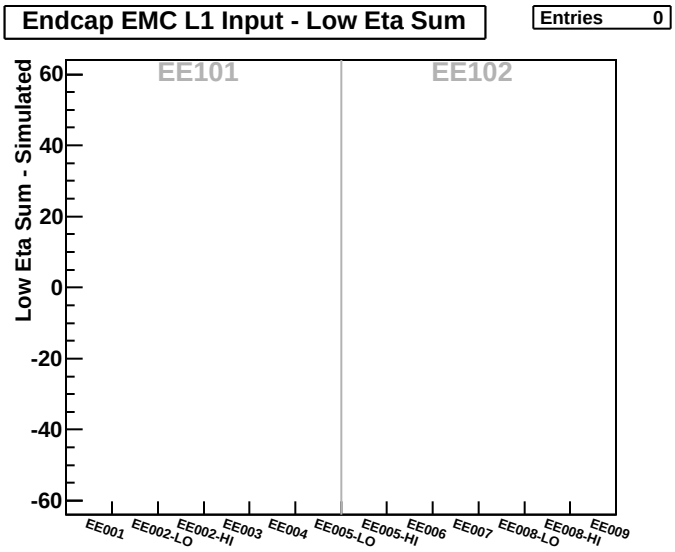

Barrel EMC L1 Input - High Eta Sum
Entries 1517400
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

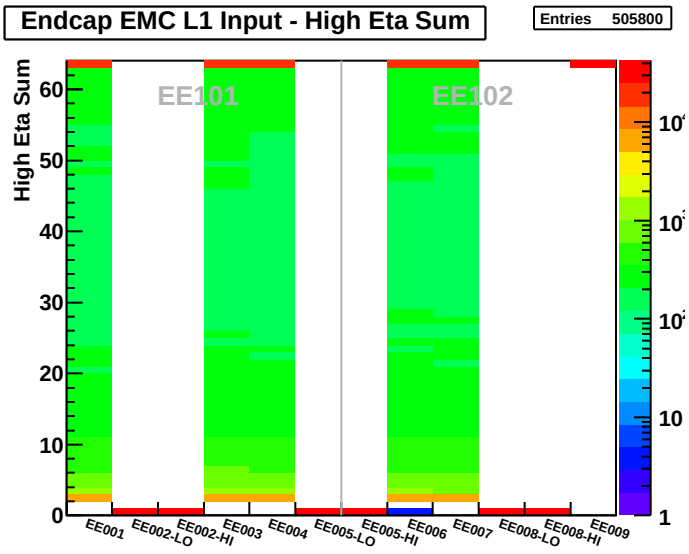

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

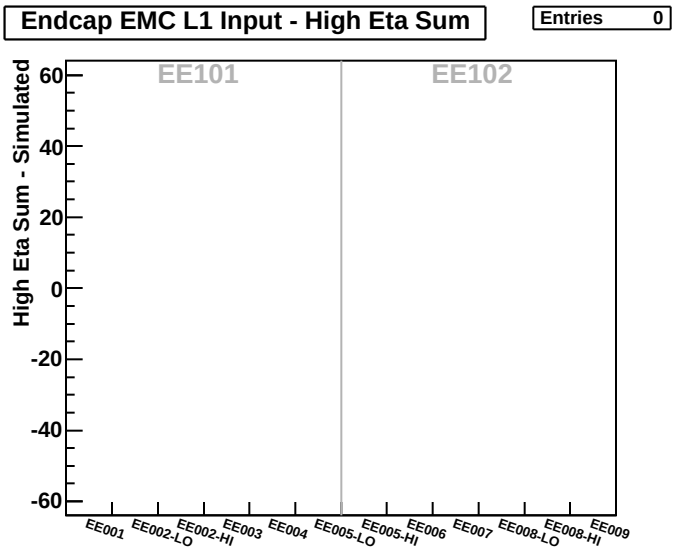

Barrel EMC L1 Input - High Tower Bits
Entries 1517400
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

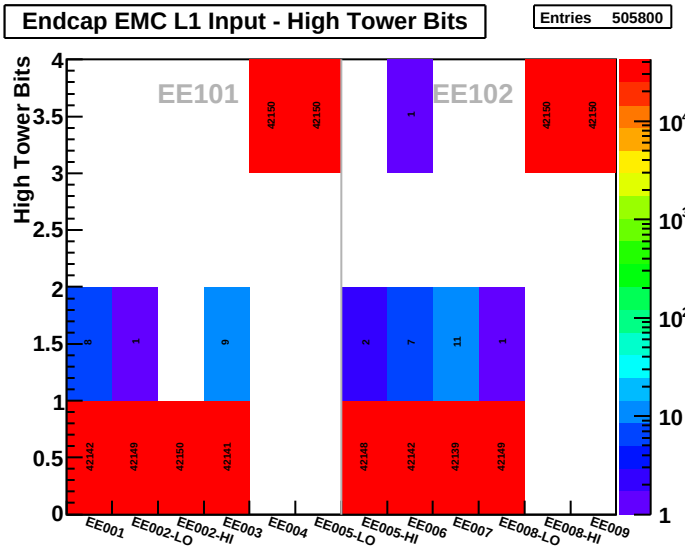

Barrel EMC L1 Input - High Tower Bits
Entries 1516913
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

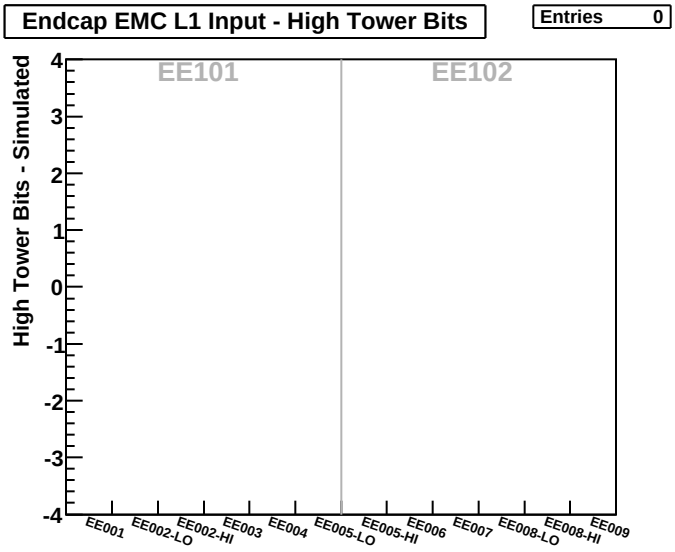

Endcap EMC L1 Input - Low Eta Sum
Entries 505800
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

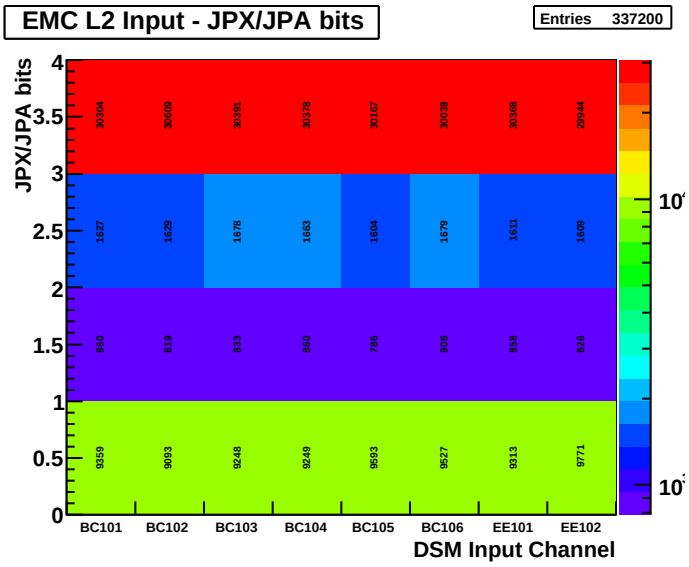

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

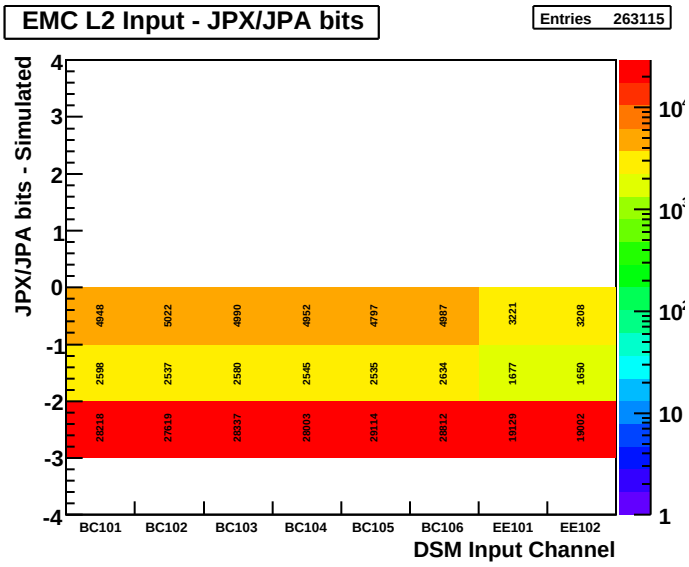

Endcap EMC L1 Input - High Eta Sum
Entries 505800
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

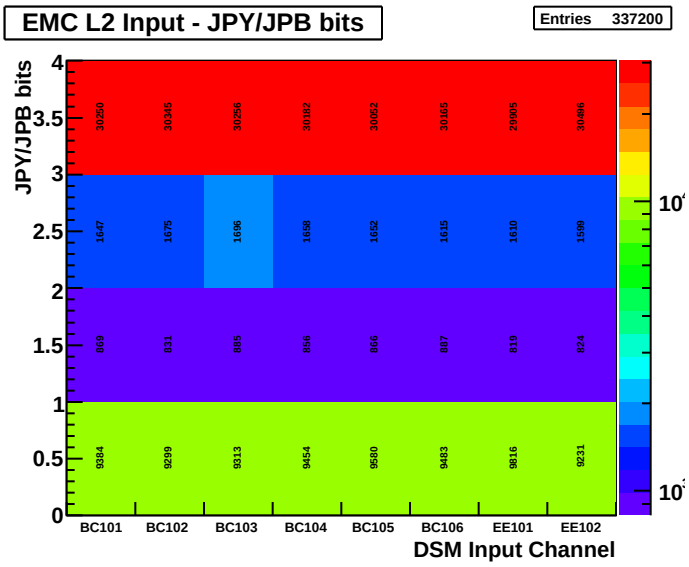

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

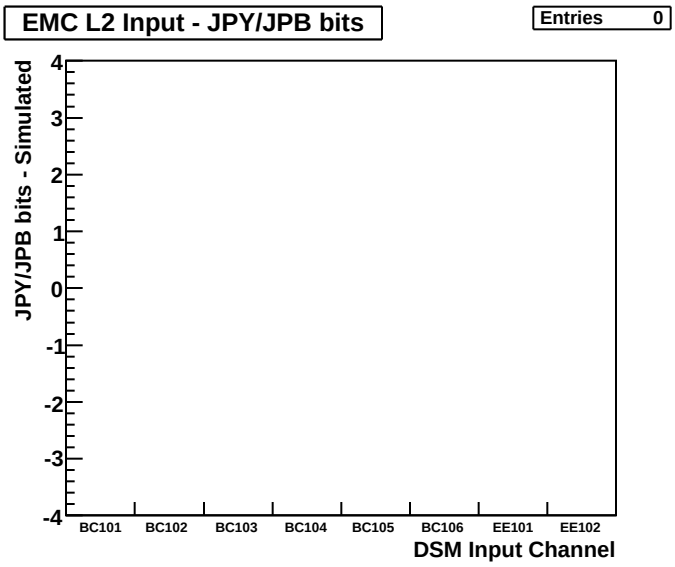

Endcap EMC L1 Input - High Tower Bits
Entries 505800
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

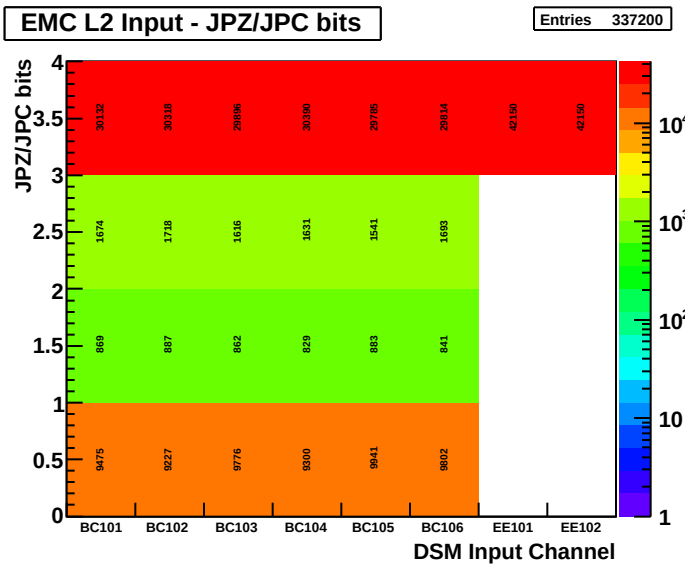

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

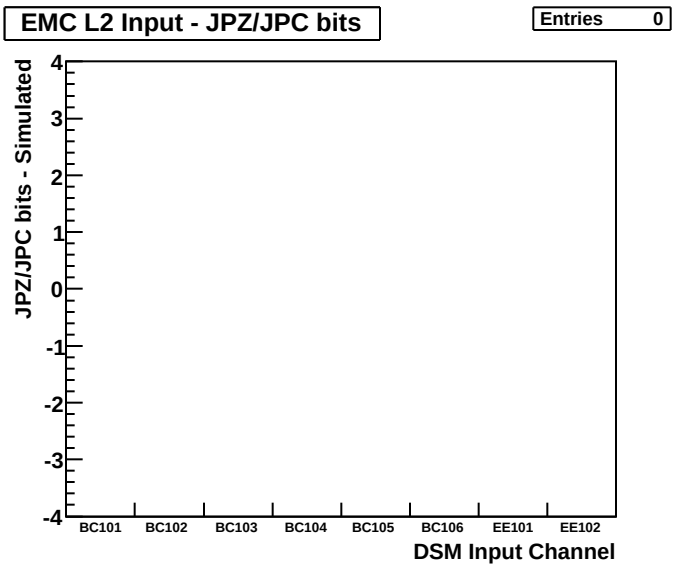

EMC L2 Input - JPX/JPA bits
Entries 337200
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
30304 30609 30391 30378 30167 30038 30368 29944
1627 1629 1678 1663 1604 1679 1611 1609
860 819 833 860 786 906 858 826
9359 9093 9248 9249 9593 9527 9313 9771

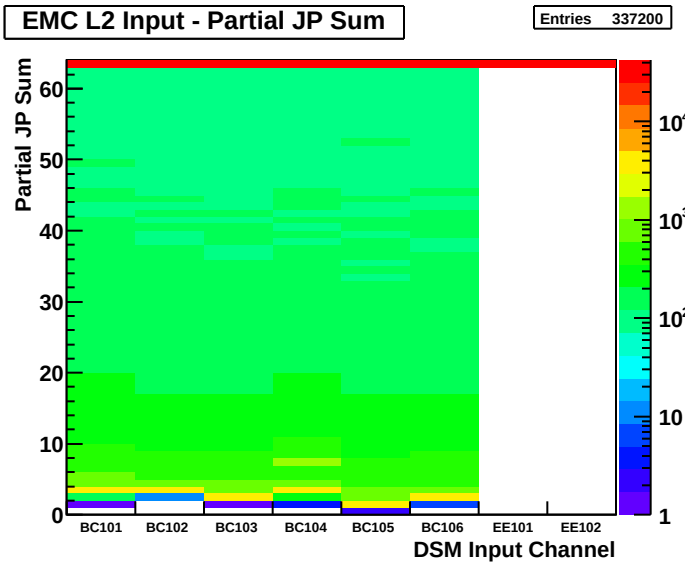

EMC L2 Input - JPX/JPA bits
Entries 263115
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4948 5022 4990 4952 4797 4987 3221 3208
2598 2537 2580 2545 2535 2634 1677 1650
25218 27619 28337 28003 29114 28812 19129 19002

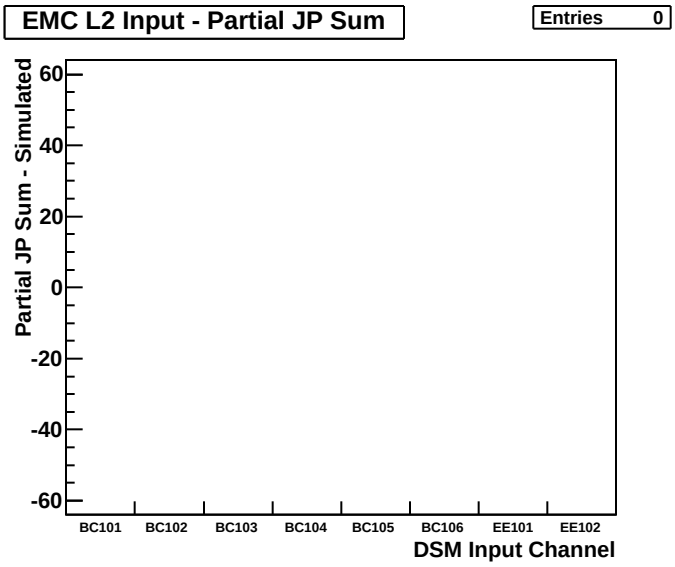

EMC L2 Input - JPY/JPB bits
Entries 337200
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
30250 30345 30256 30182 30052 30165 29905 30496
1647 1675 1696 1658 1652 1615 1610 1599
869 831 885 856 866 887 819 824
9384 9299 9313 9454 9580 9483 9816 9231

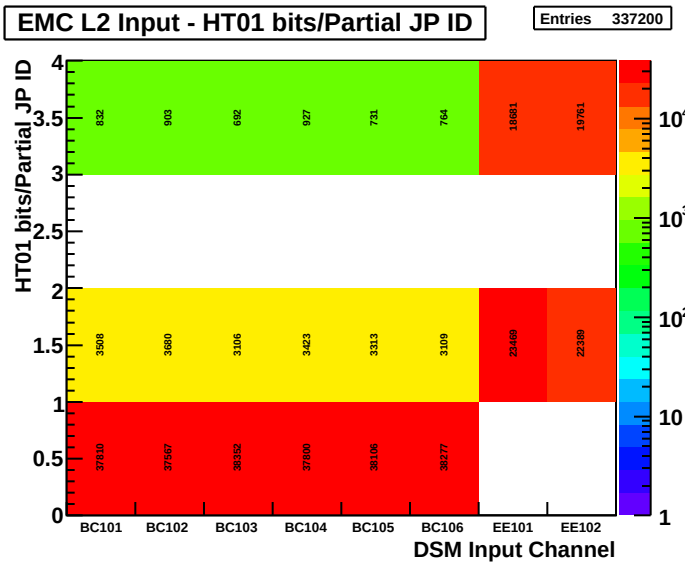

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

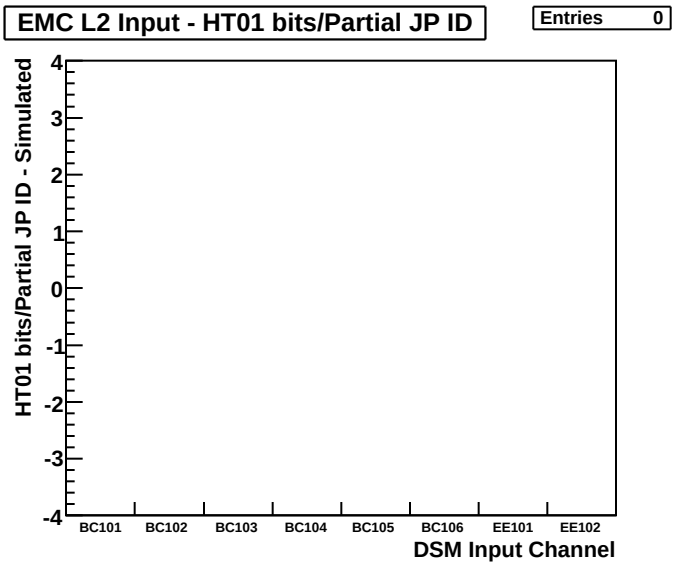

EMC L2 Input - JPZ/JPC bits
Entries 337200
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
30132 30318 29896 30390 29785 29814 42150 42150
1674 1718 1616 1631 1541 1693
869 887 862 829 883 841
9475 9227 9776 9300 9941 9802

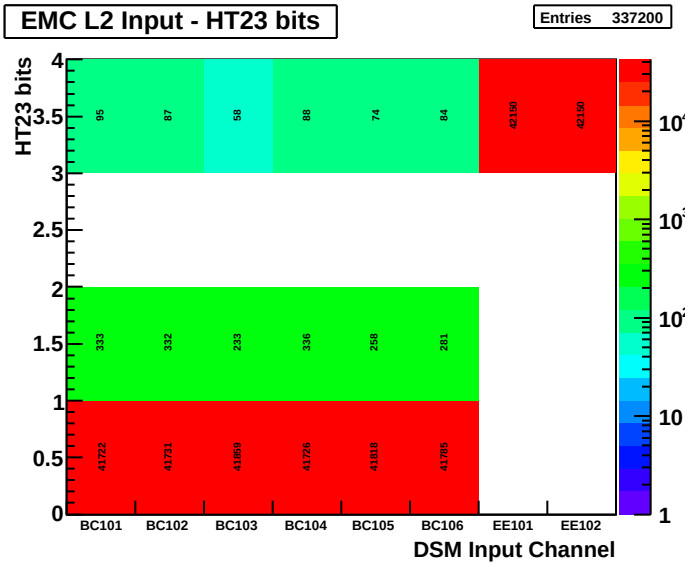

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

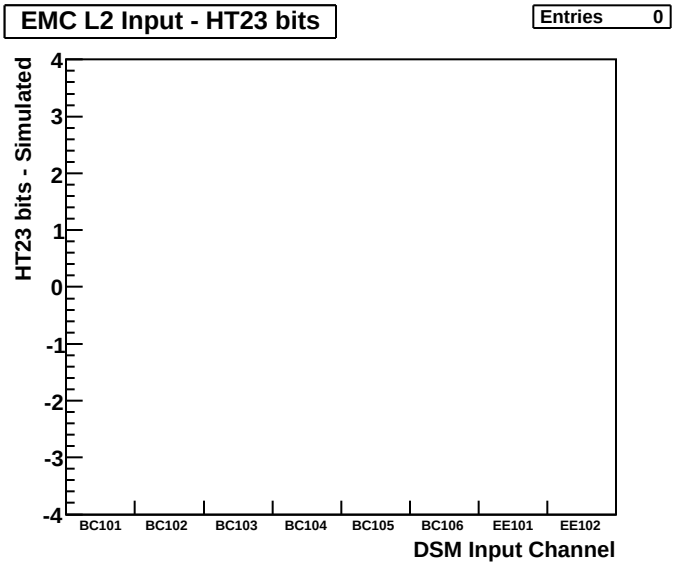

EMC L2 Input - Partial JP Sum
Entries 337200
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

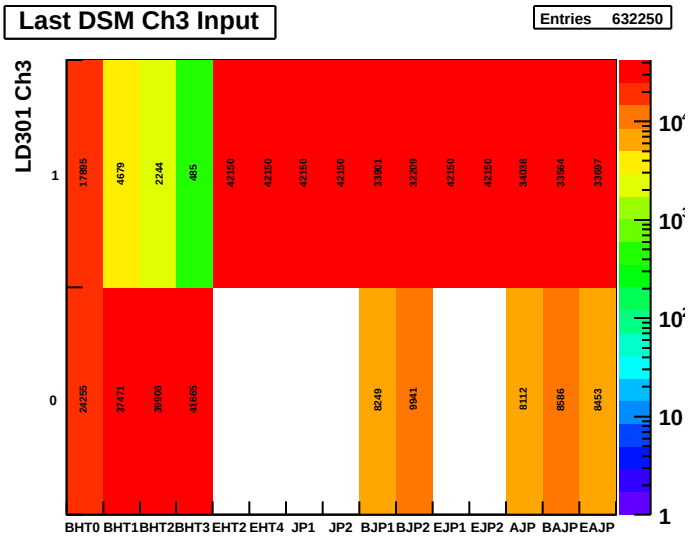

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

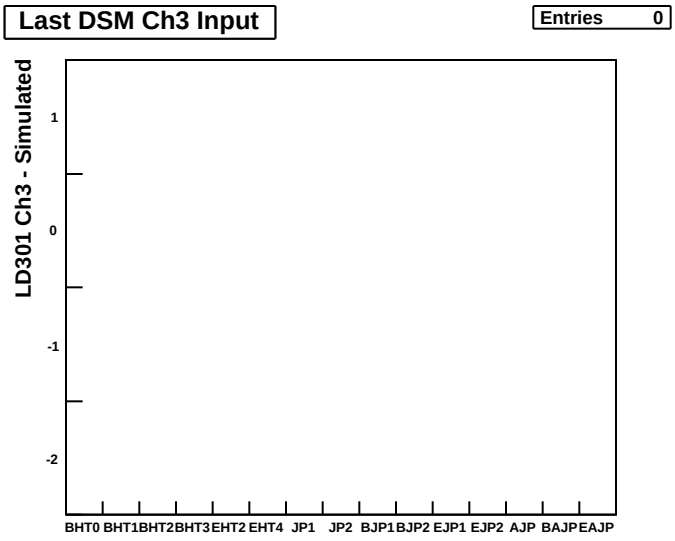

EMC L2 Input - HT01 bits/Partial JP ID
Entries 337200
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

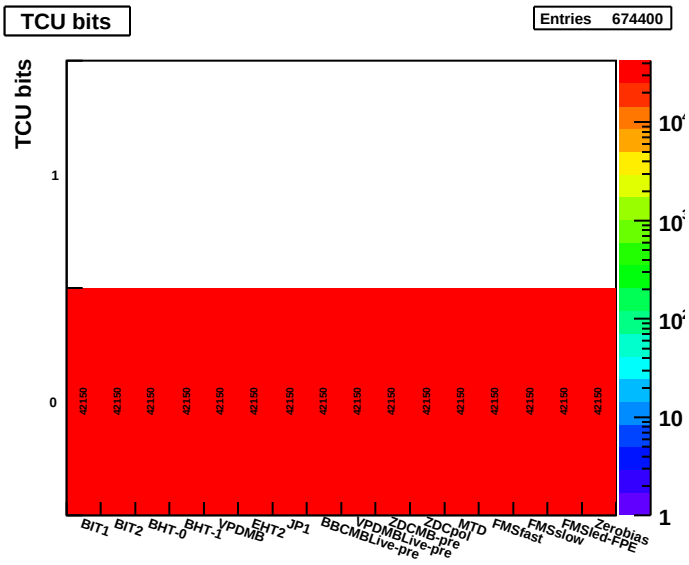

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

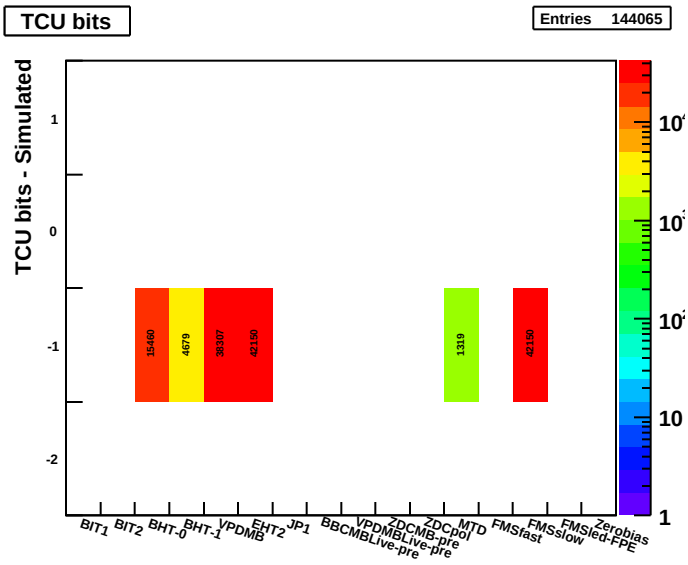

EMC L2 Input - HT23 bits
Entries 337200
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

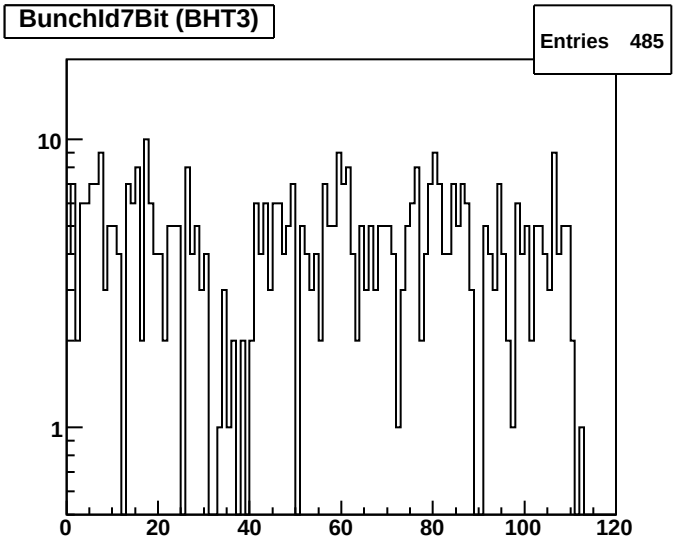

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

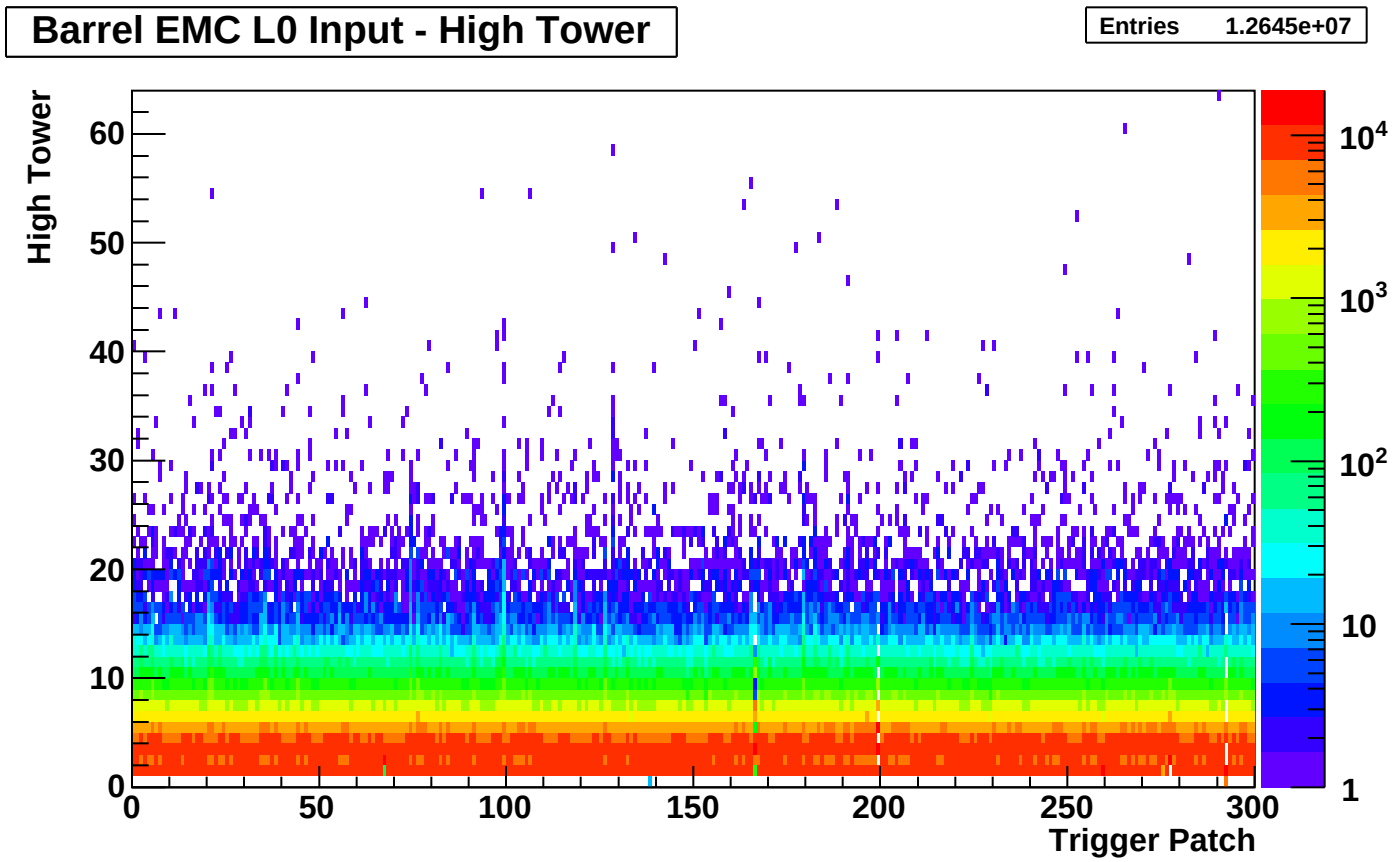

Last DSM Ch3 Input
Entries 632250
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

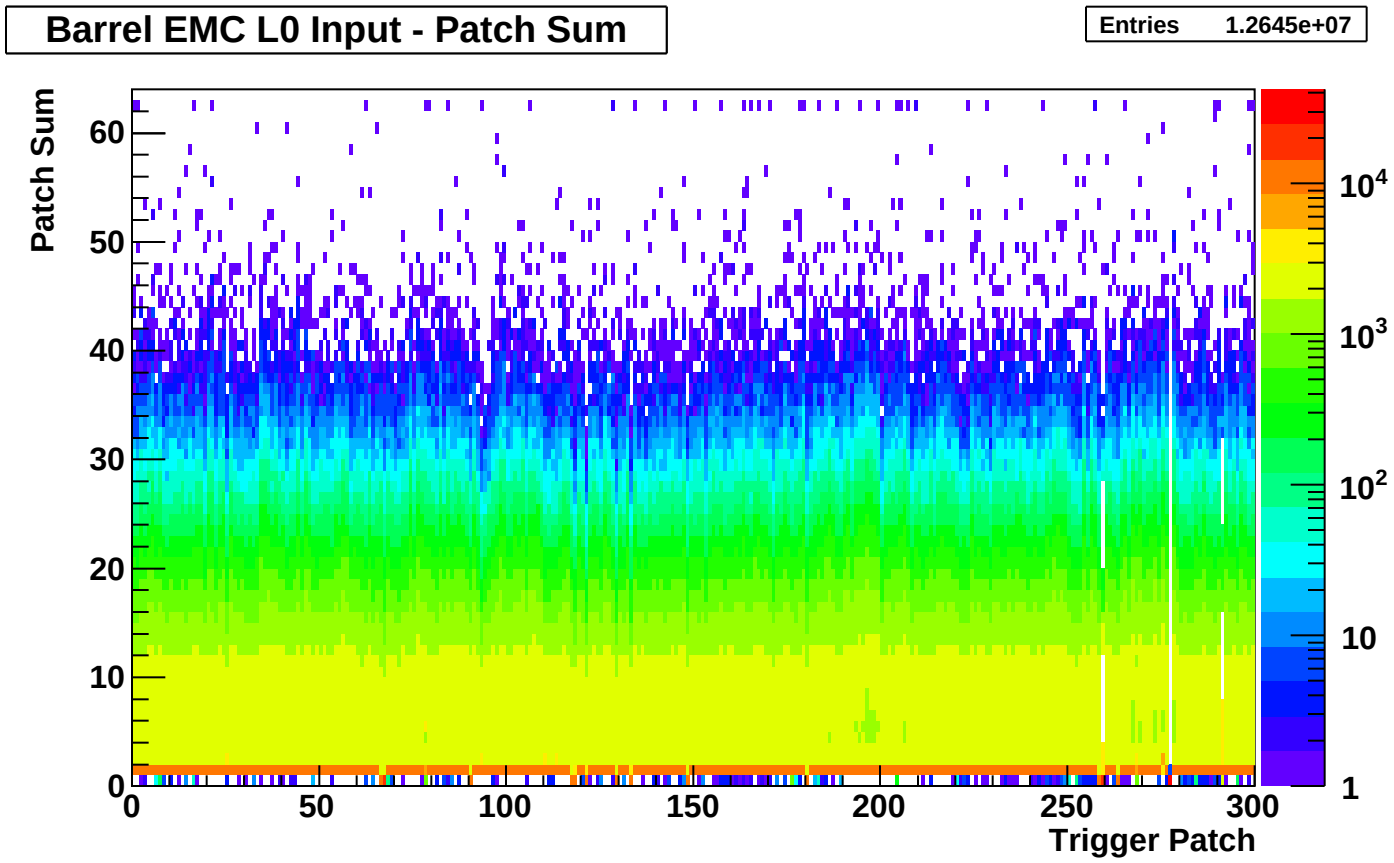

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

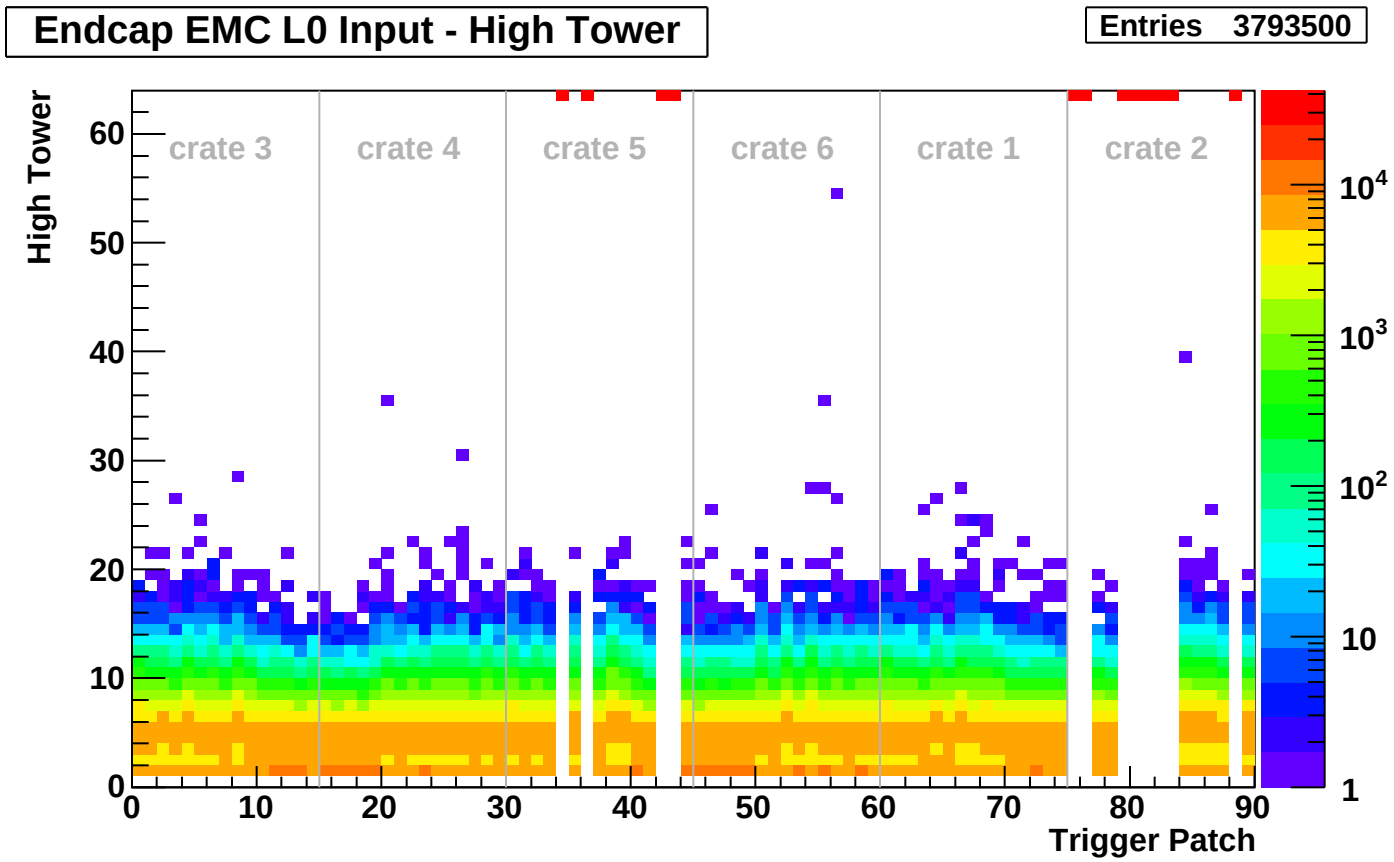

TCU bits
Entries 674400
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

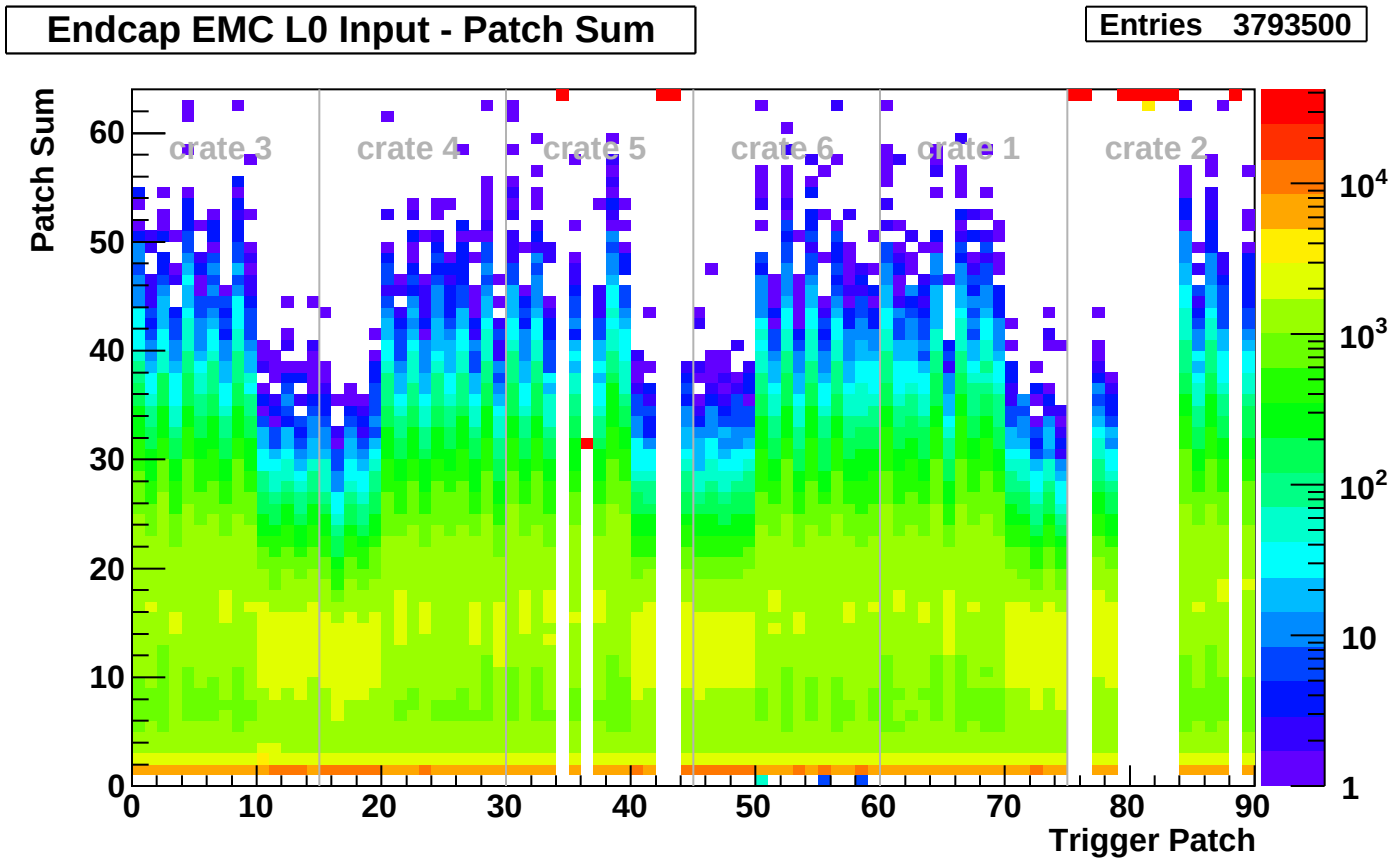

TCU bits
Entries 144065
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

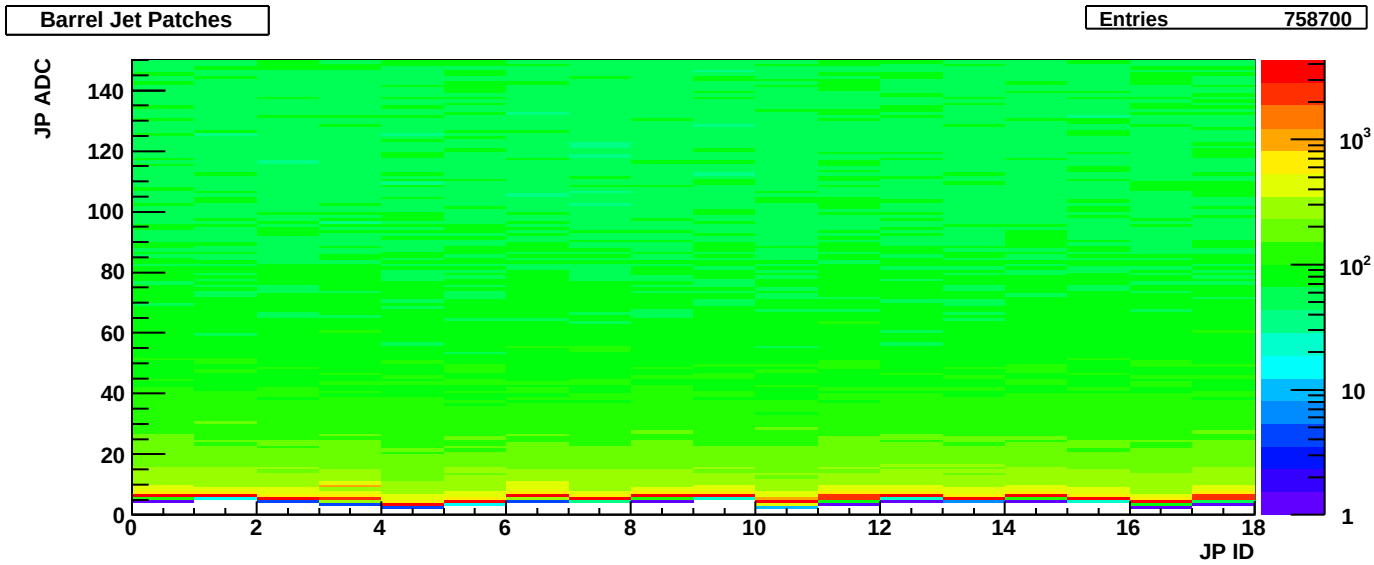

BunchId7Bit (BHT3)
Entries 485

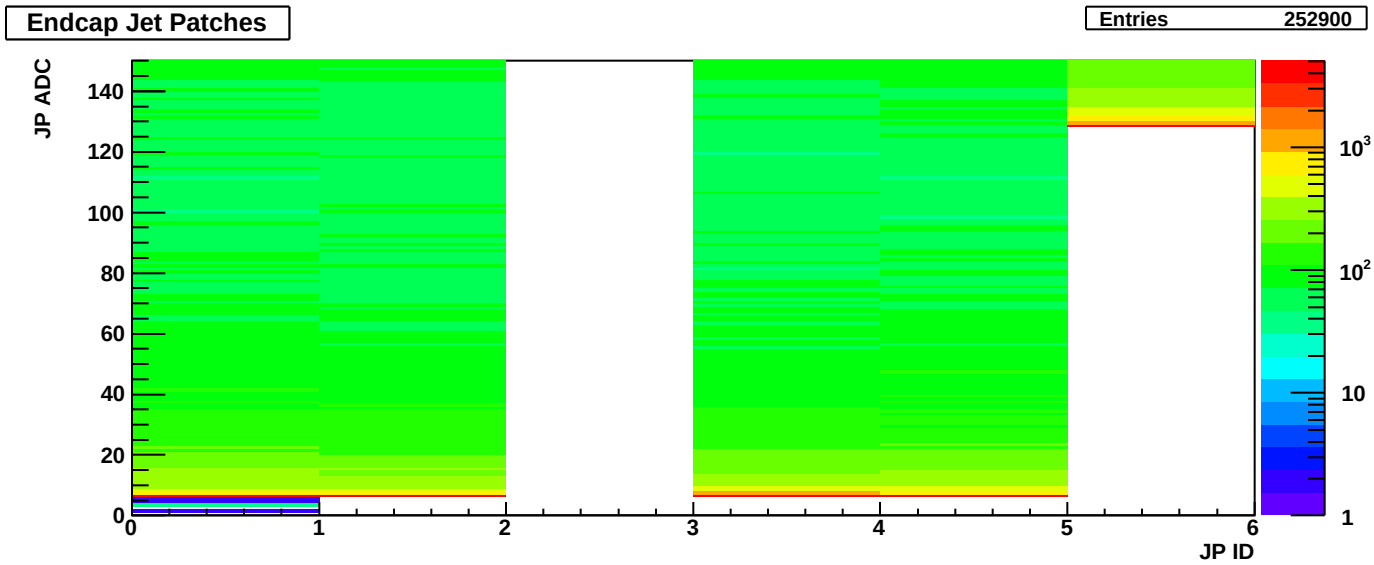

Barrel EMC L0 Input - High Tower
Entries 1.2645e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

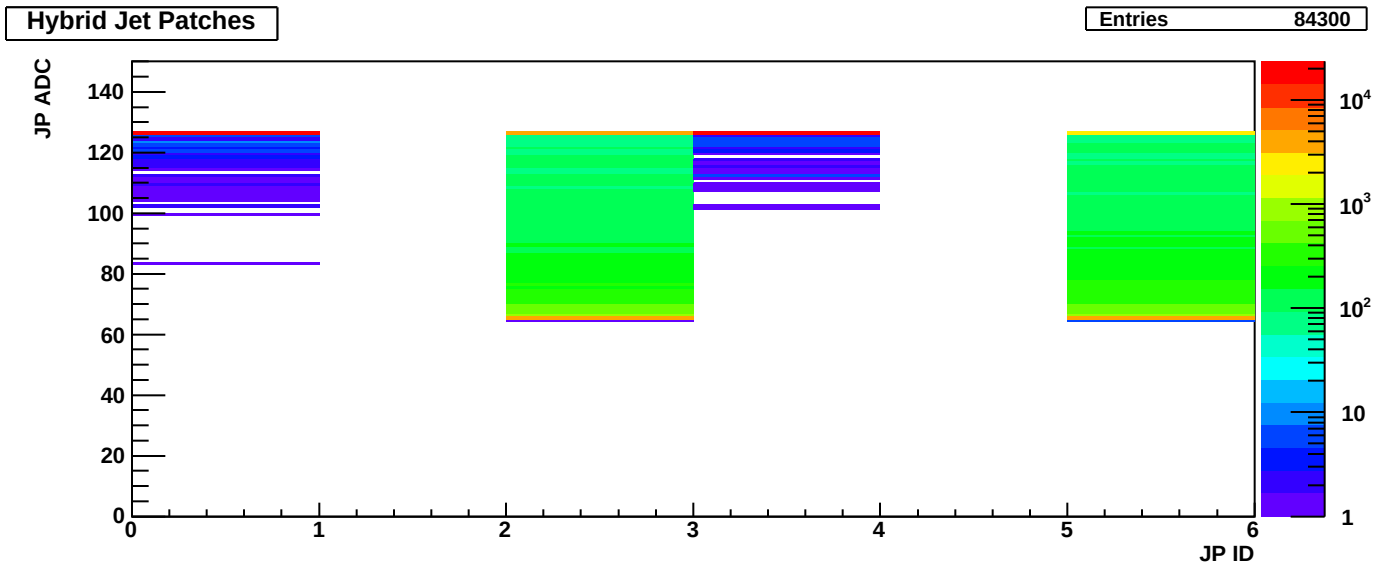

Barrel EMC L0 Input - Patch Sum
Entries 1.2645e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

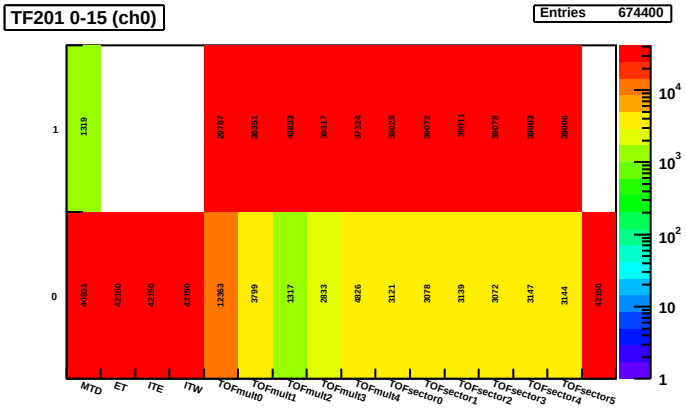

Endcap EMC L0 Input - High Tower
Entries 3793500
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

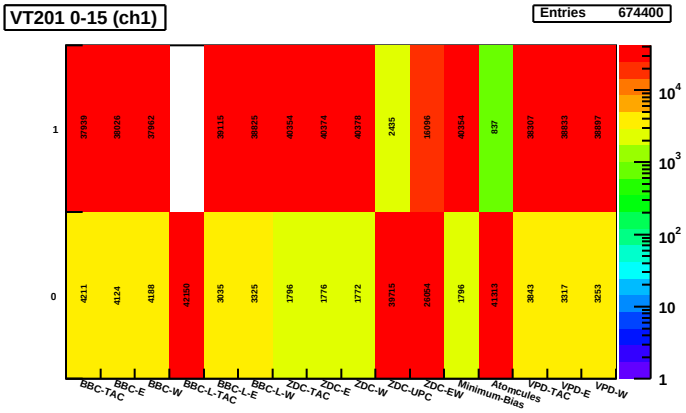

Endcap EMC L0 Input - Patch Sum
Entries 3793500
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

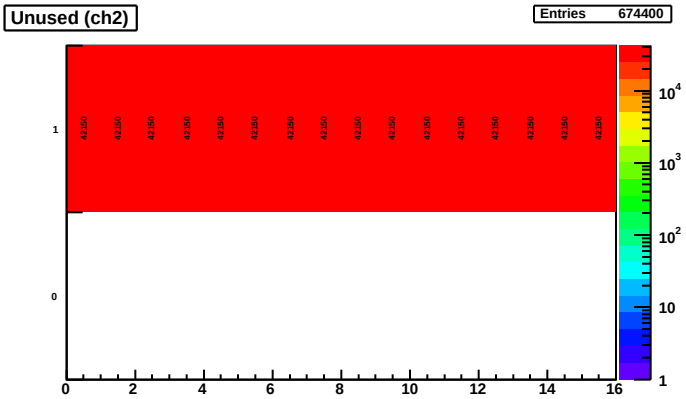

Barrel Jet Patches
Entries 758700
JP ADC
JP ID

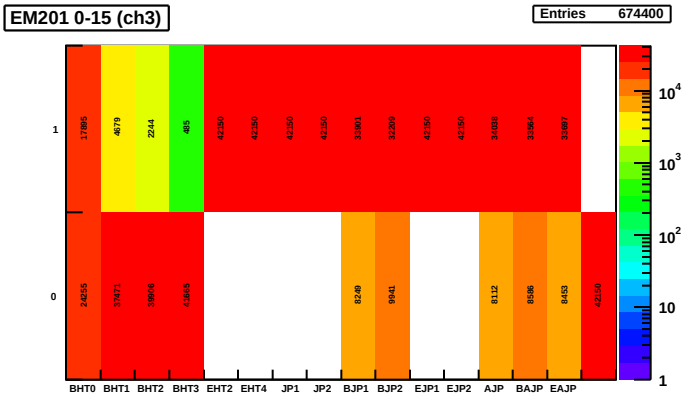

Endcap Jet Patches
Entries 252900
JP ADC
JP ID

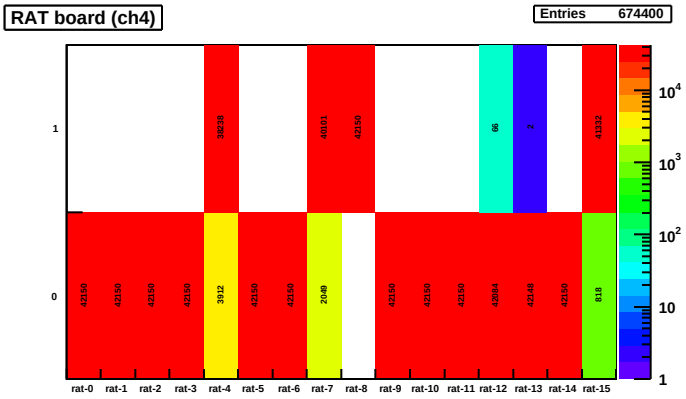

Hybrid Jet Patches
Entries 84300
JP ADC
JP ID

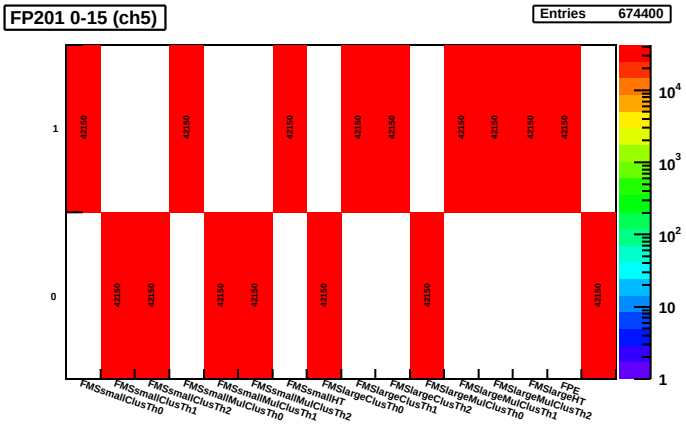

TF201 0-15 (ch0)
Entries 674400
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

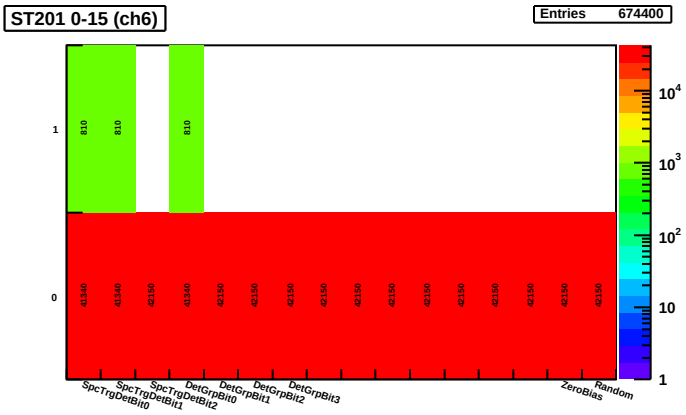

VT201 0-15 (ch1)
Entries 674400
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

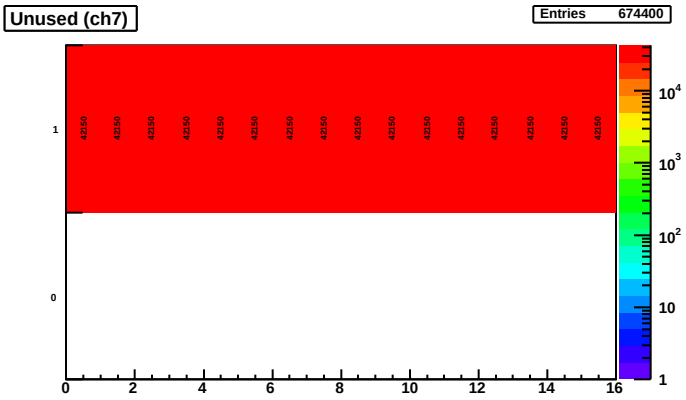

Unused (ch2)
Entries 674400

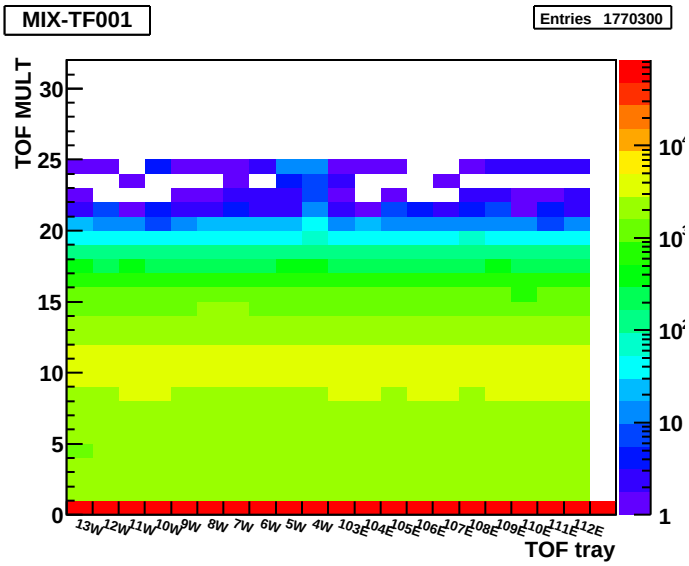

EM201 0-15 (ch3)
Entries 674400
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP

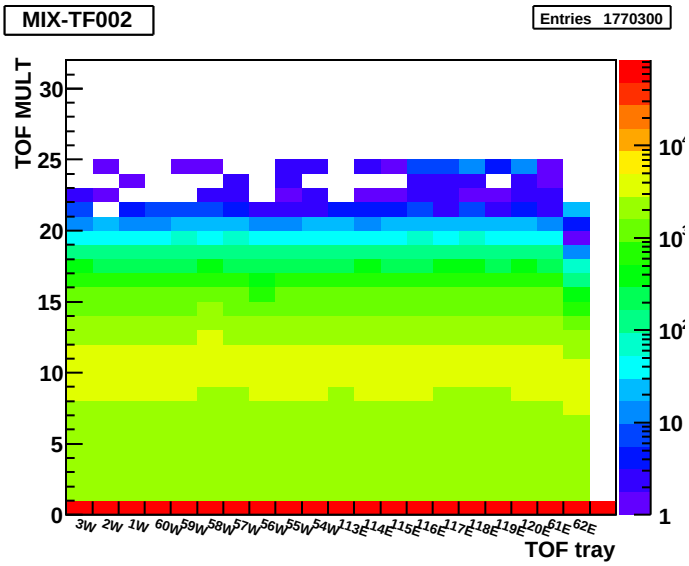

RAT board (ch4)
Entries 674400
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

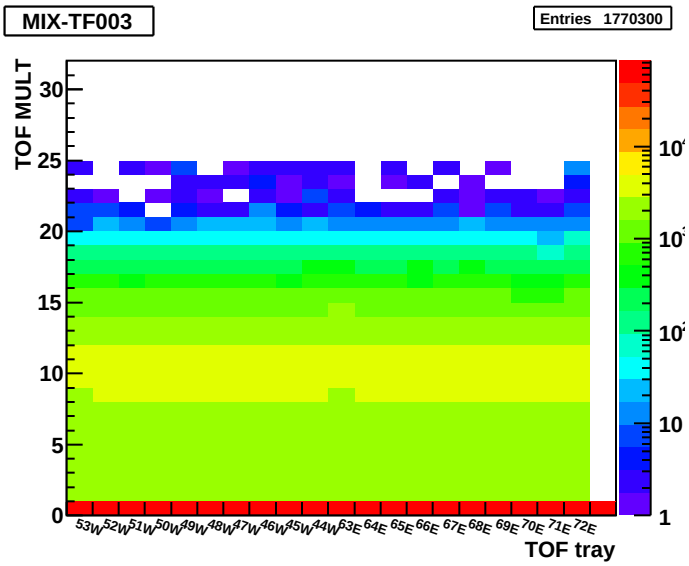

FP201 0-15 (ch5)
Entries 674400
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

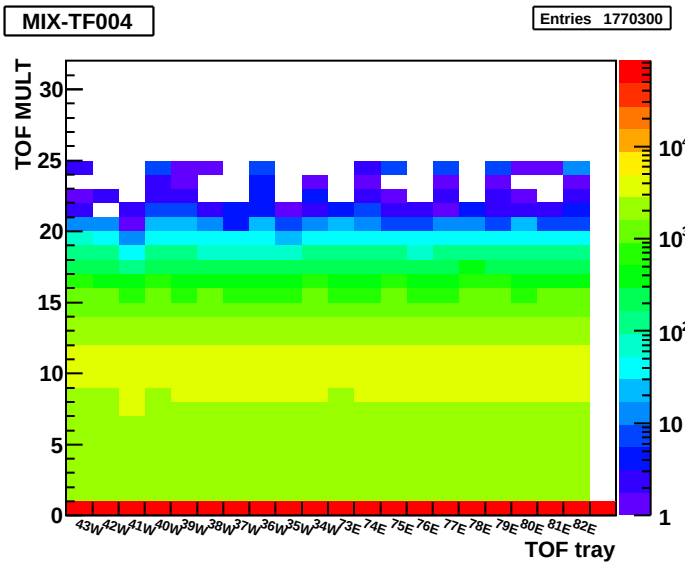

ST201 0-15 (ch6)
Entries 674400
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

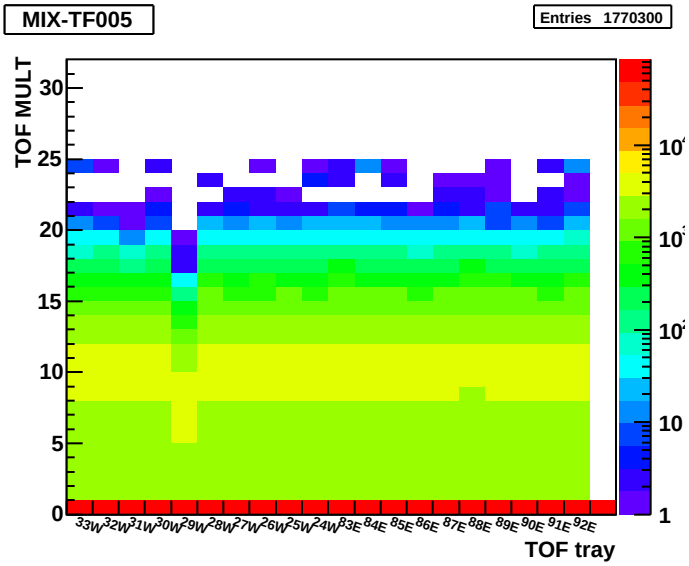

Unused (ch7)
Entries 674400

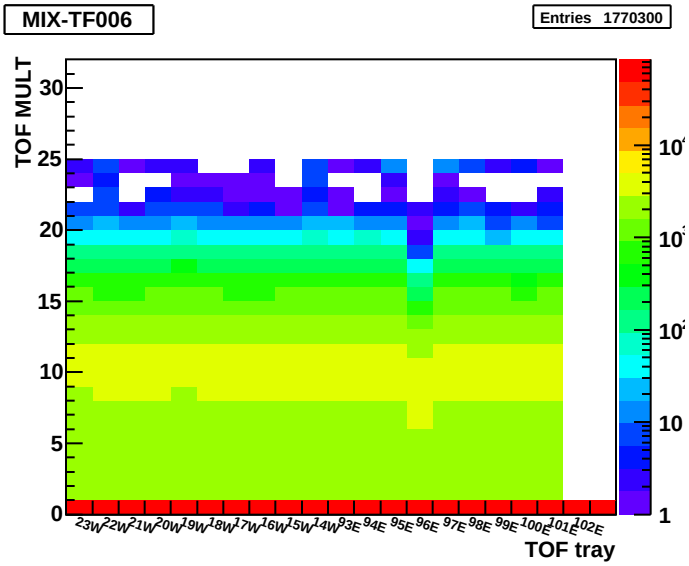

MIX-TF001
Entries 1770300
TOF MULT
TOF tray

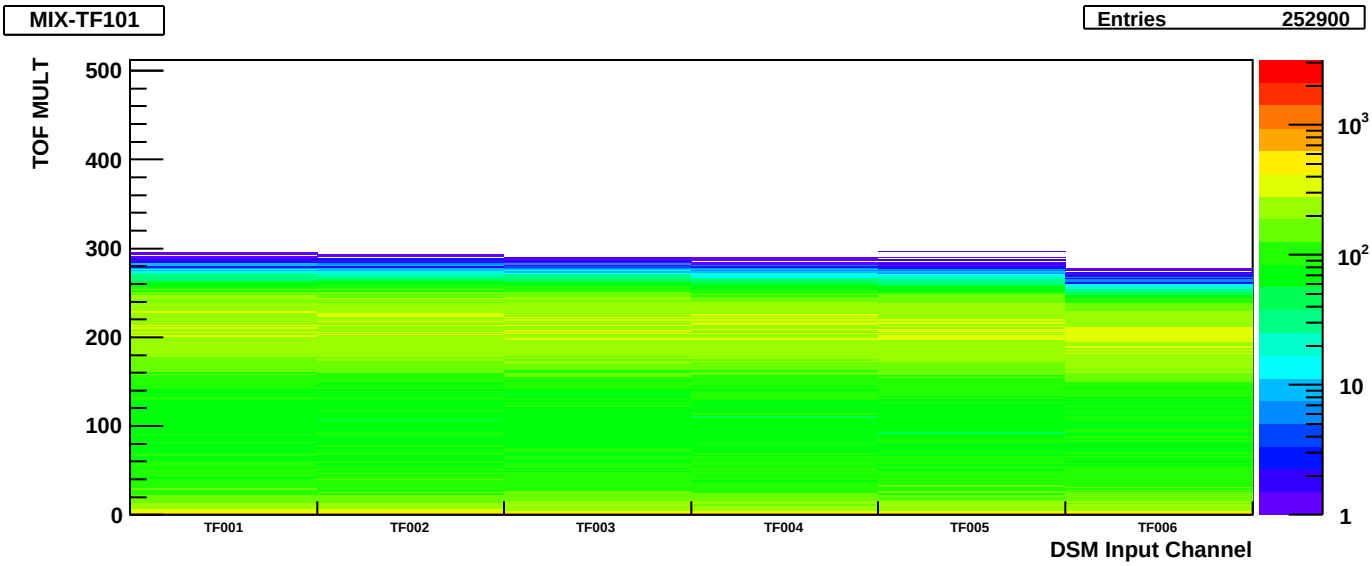

MIX-TF002
Entries 1770300
TOF MULT
TOF tray

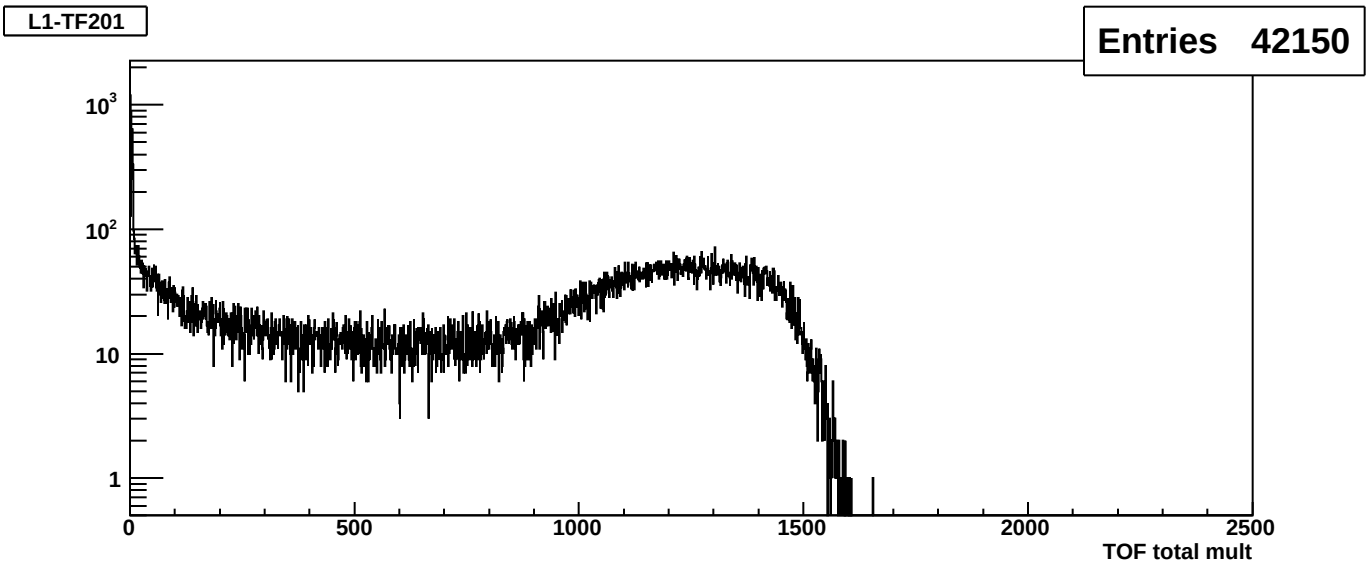

MIX-TF003
Entries 1770300
TOF MULT
TOF tray

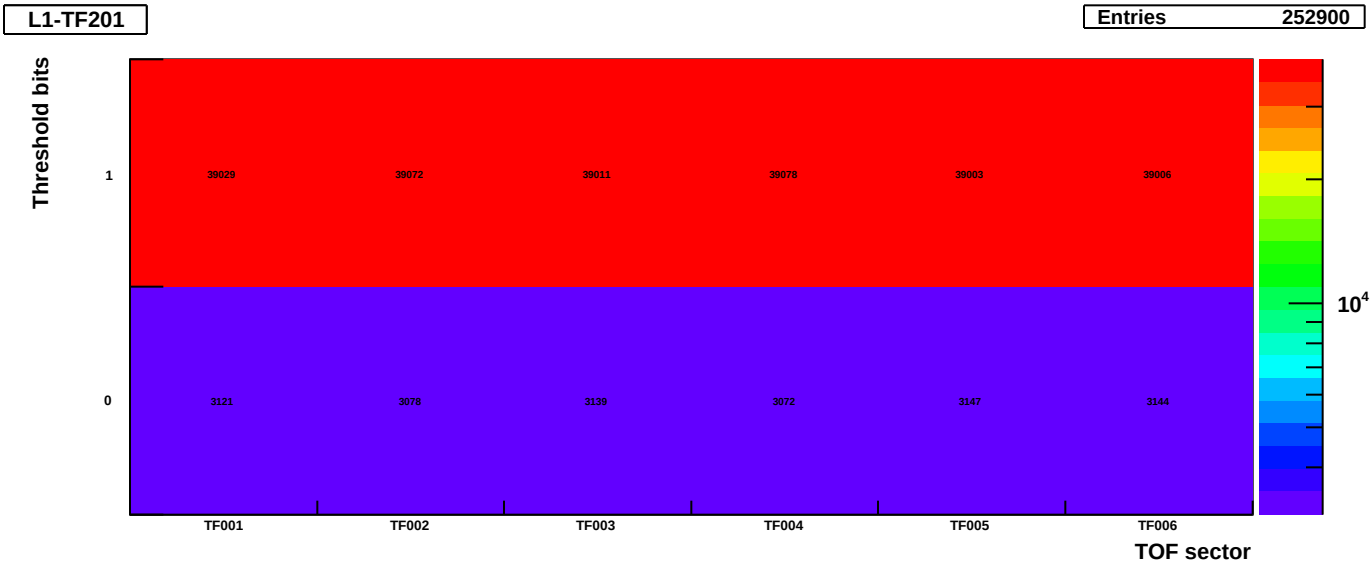

MIX-TF004
Entries 1770300
TOF MULT
TOF tray

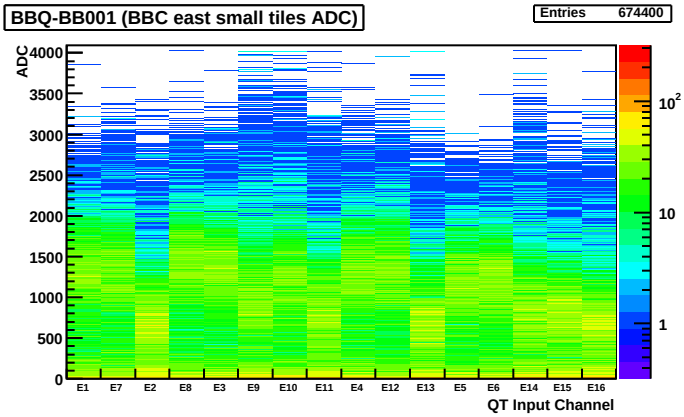

MIX-TF005
Entries 1770300
TOF MULT
TOF tray

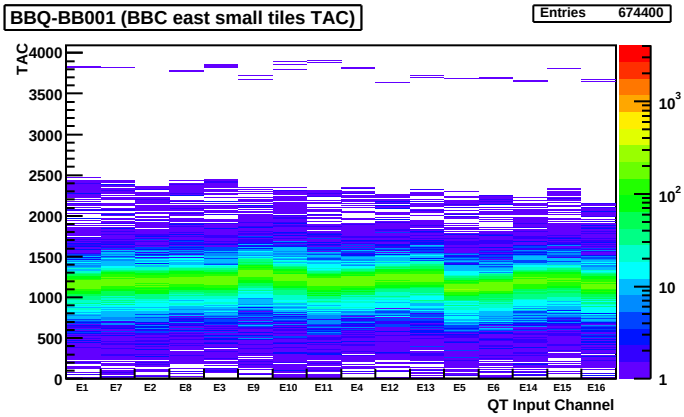

MIX-TF006
Entries 1770300
TOF MULT
TOF tray

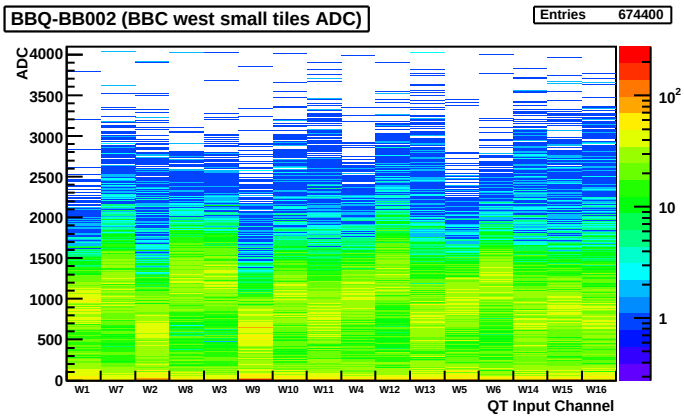

MIX-TF101
Entries 252900
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

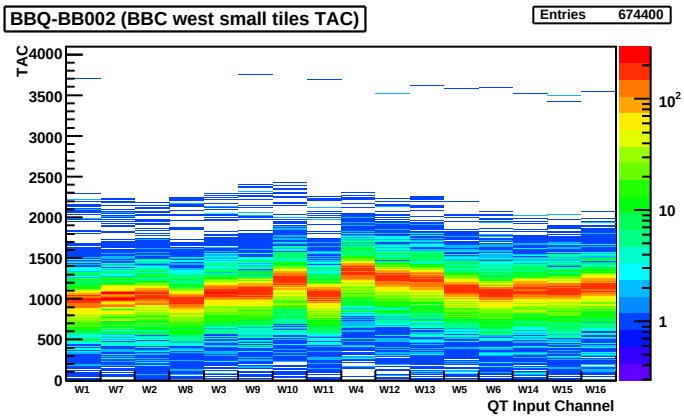

L1-TF201
Entries 42150
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

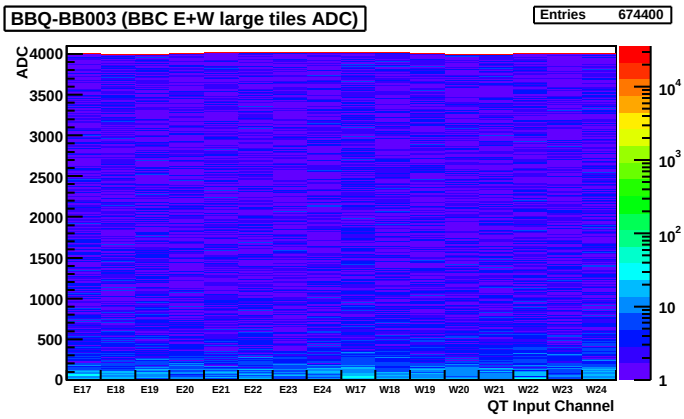

L1-TF201
Entries 252900
Threshold bits
1
0
39029
39072
39011
39078
39003
39006
3121
3078
3139
3072
3147
3144
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

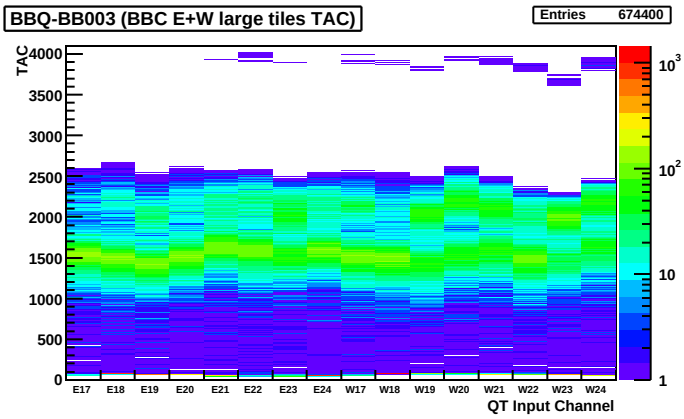

BBQ-BB001 (BBC east small tiles ADC)
Entries 674400
ADC
QT Input Channel

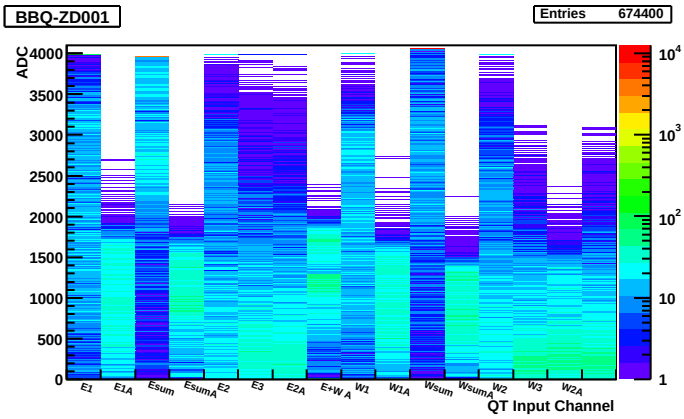

BBQ-BB001 (BBC east small tiles TAC)
Entries 674400
TAC
QT Input Channel

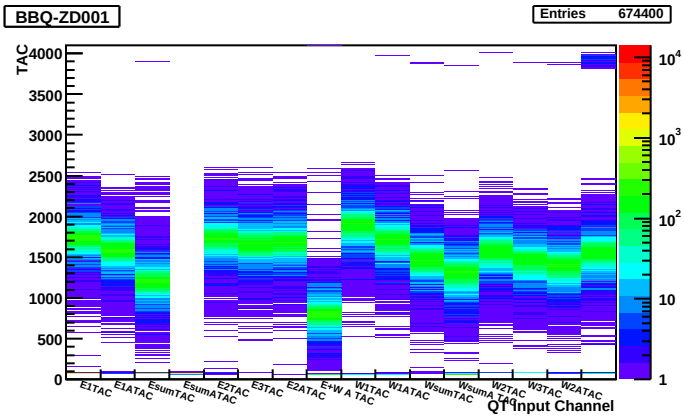

BBQ-BB002 (BBC west small tiles ADC)
Entries 674400
ADC
QT Input Channel

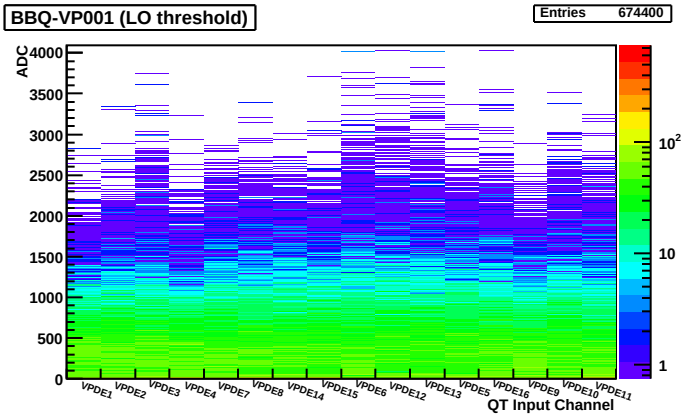

BBQ-BB002 (BBC west small tiles TAC)
Entries 674400
TAC
QT Input Channel

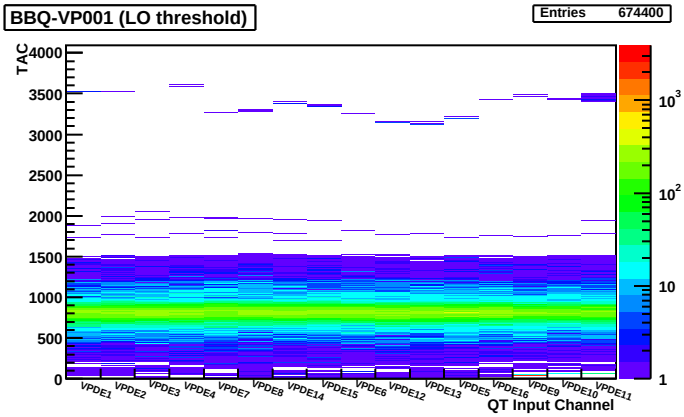

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 674400
ADC
QT Input Channel

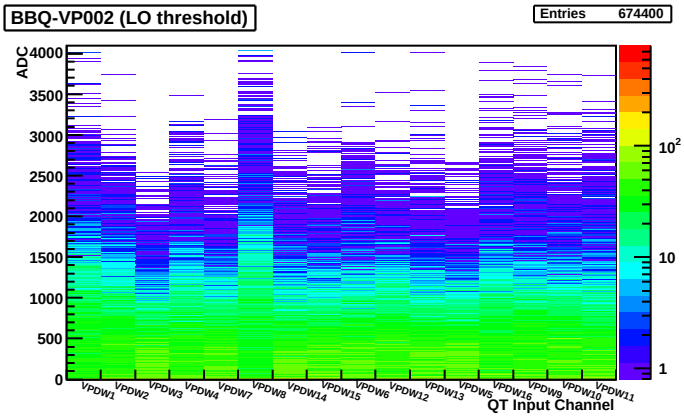

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 674400
TAC
QT Input Channel

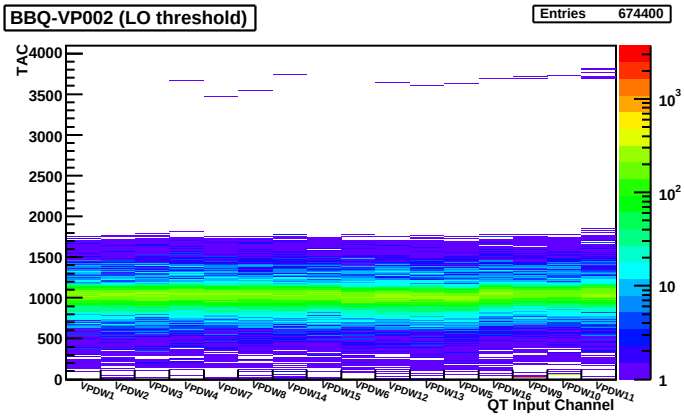

BBQ-ZD001
Entries 674400
ADC
QT Input Channel

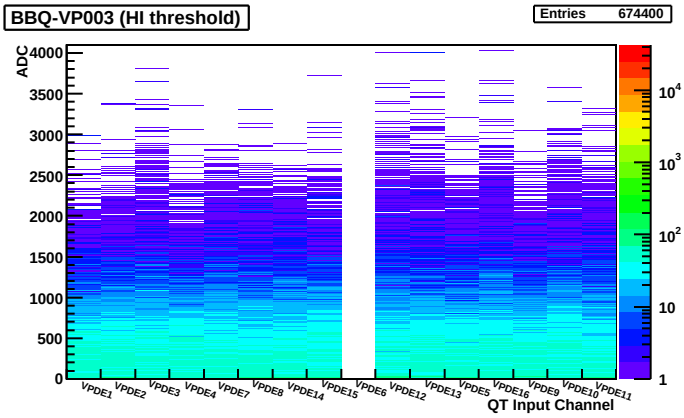

BBQ-ZD001
Entries 674400
TAC
QT Input Channel

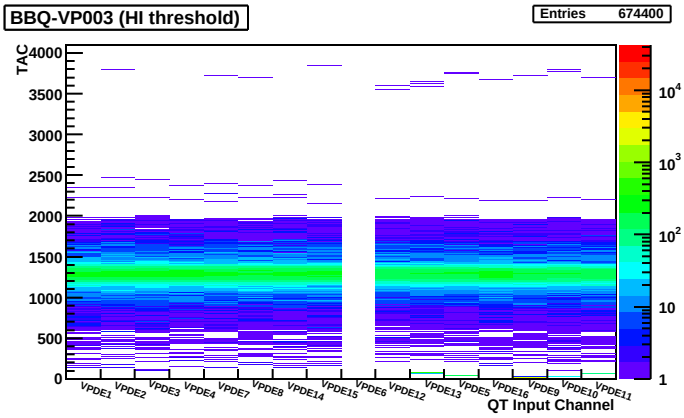

BBQ-VP001 (LO threshold)
Entries 674400
ADC
QT Input Channel

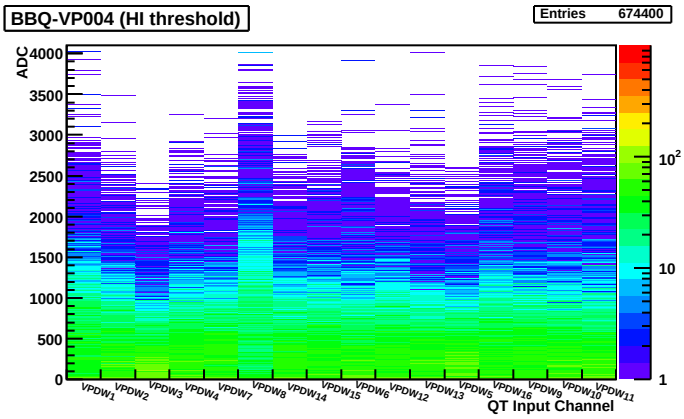

BBQ-VP001 (LO threshold)
Entries 674400
TAC
QT Input Channel

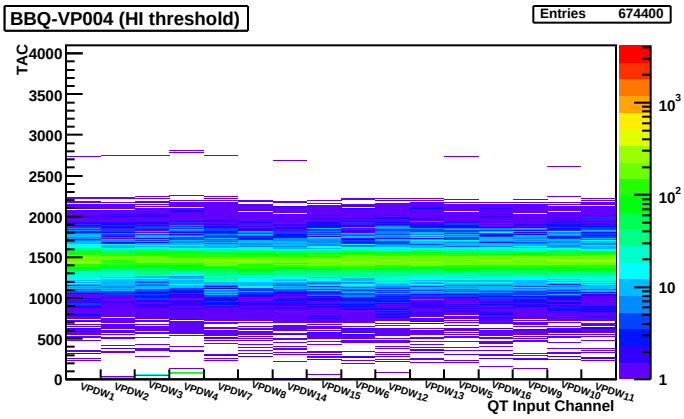

BBQ-VP002 (LO threshold)
Entries 674400
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 674400
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 674400
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 674400
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 674400
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 674400
TAC
QT Input Channel