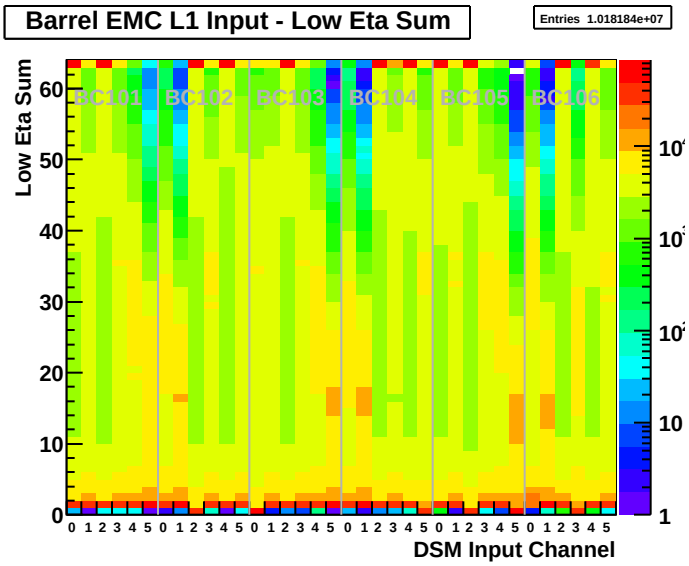
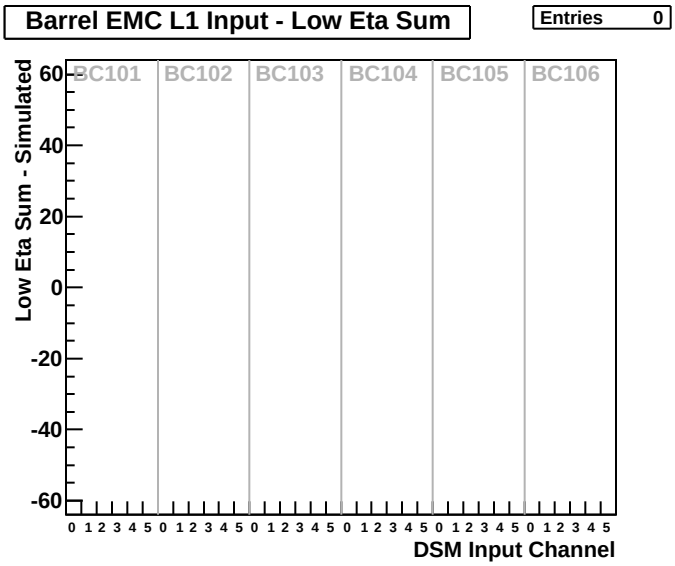
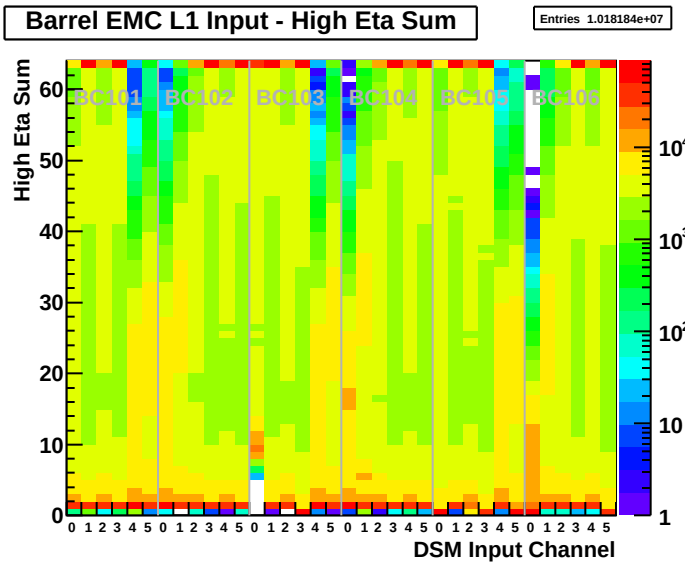
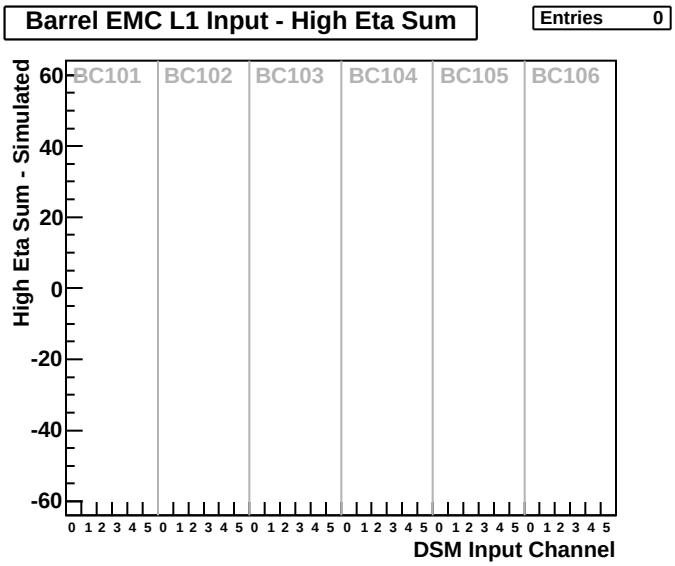
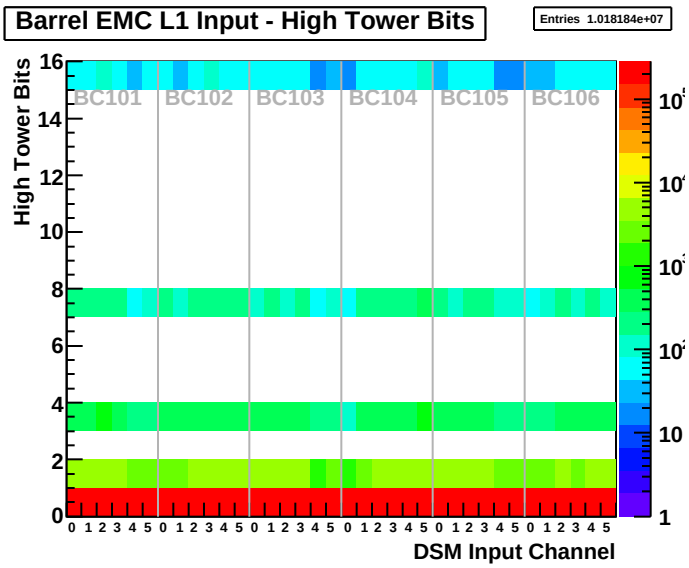
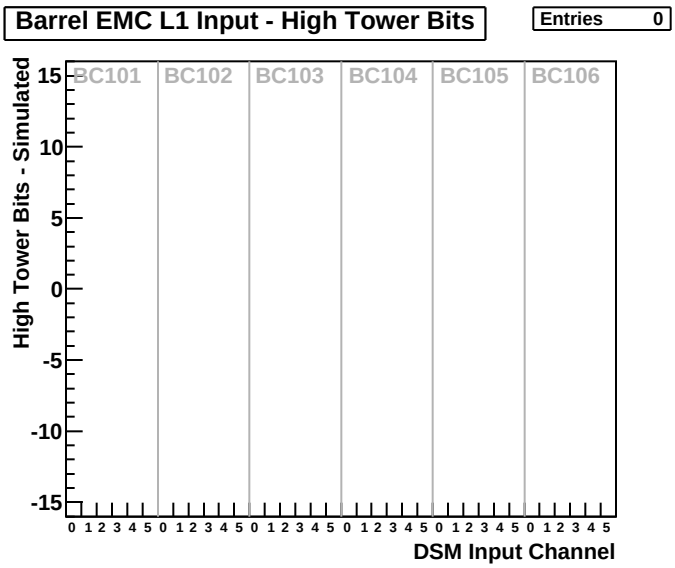

Barrel EMC L1 Input - Low Eta Sum
Entries 1.018184e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

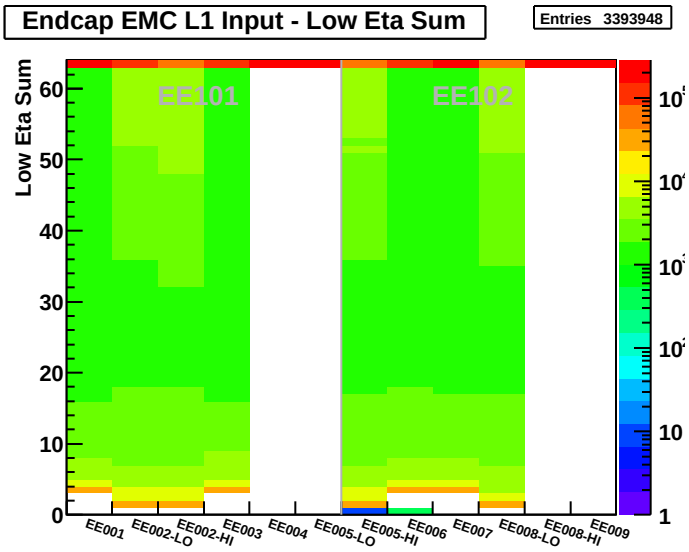

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

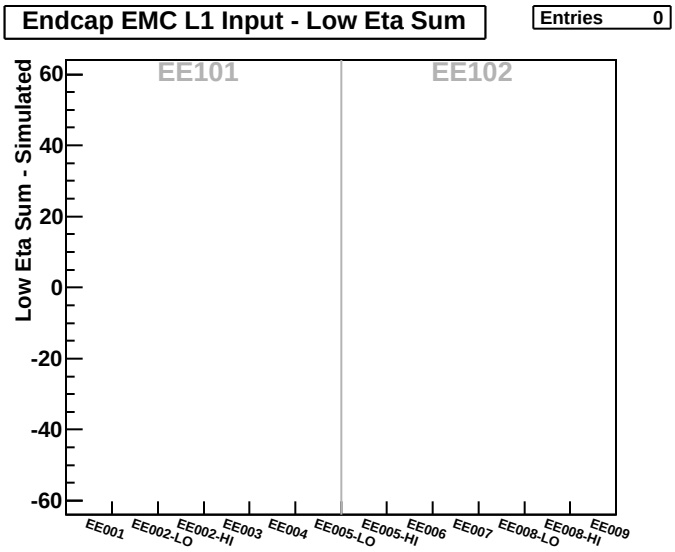

Barrel EMC L1 Input - High Eta Sum
Entries 1.018184e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

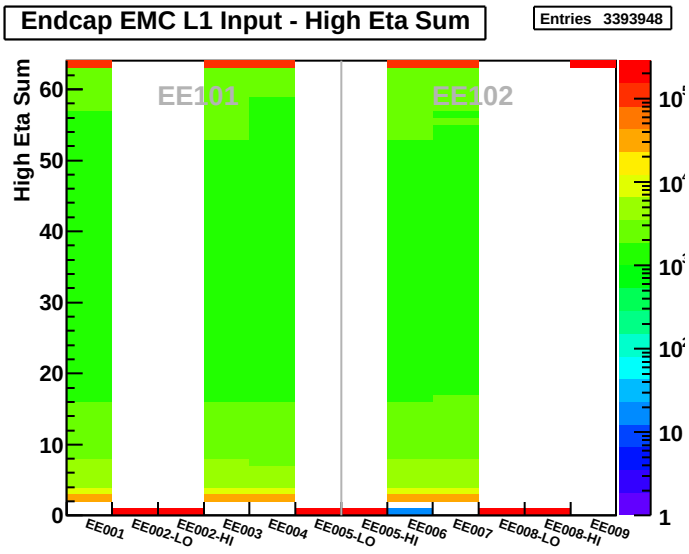

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

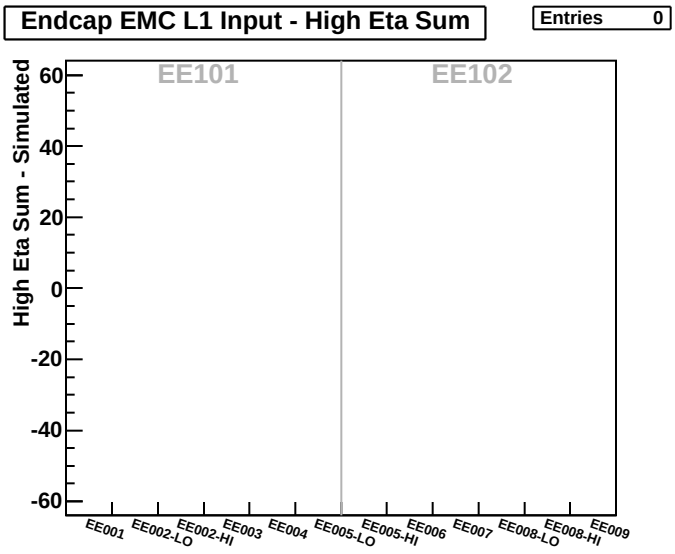

Barrel EMC L1 Input - High Tower Bits
Entries 1.018184e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

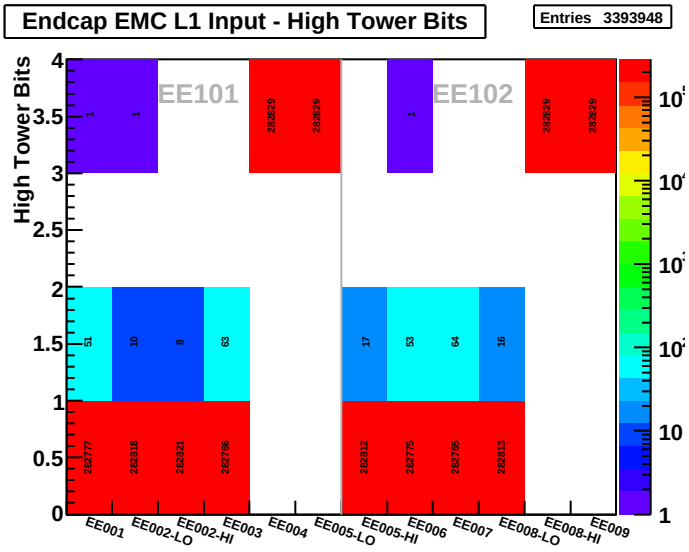

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

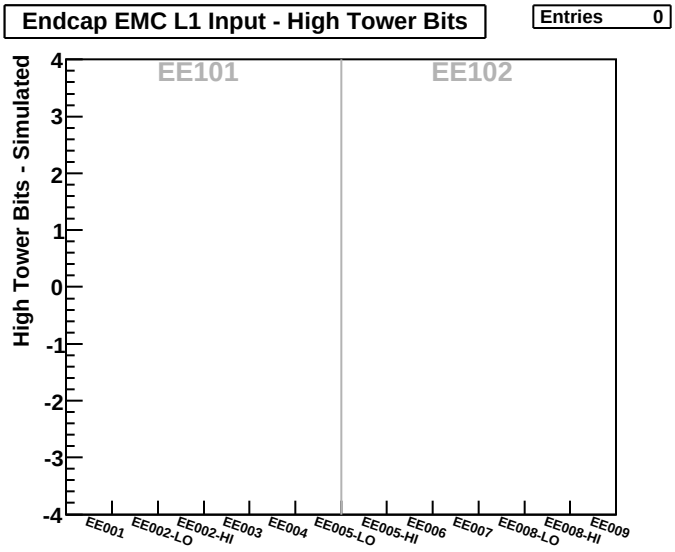

Endcap EMC L1 Input - Low Eta Sum
Entries 3393948
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

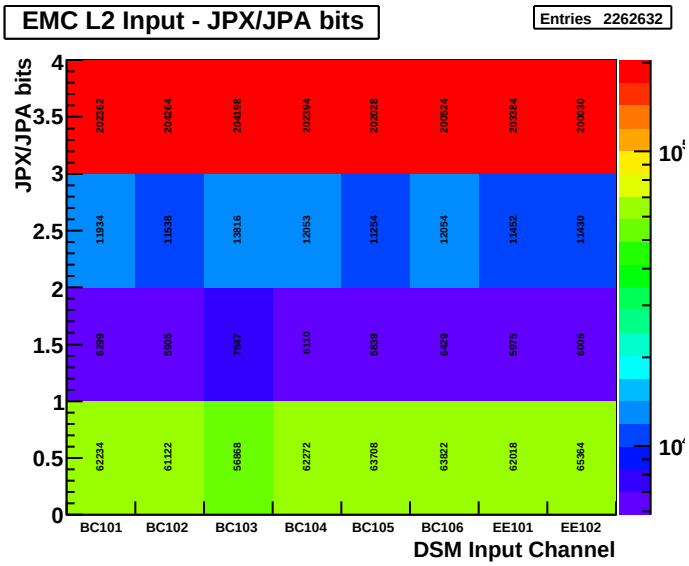

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

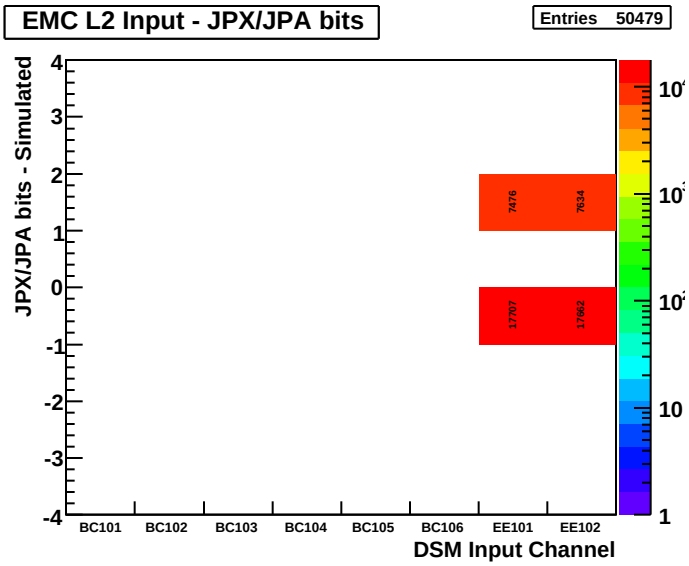

Endcap EMC L1 Input - High Eta Sum
Entries 3393948
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

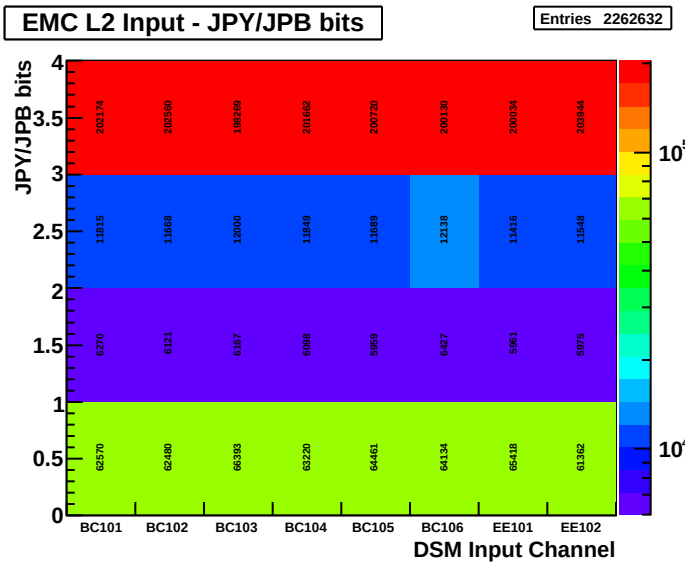

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

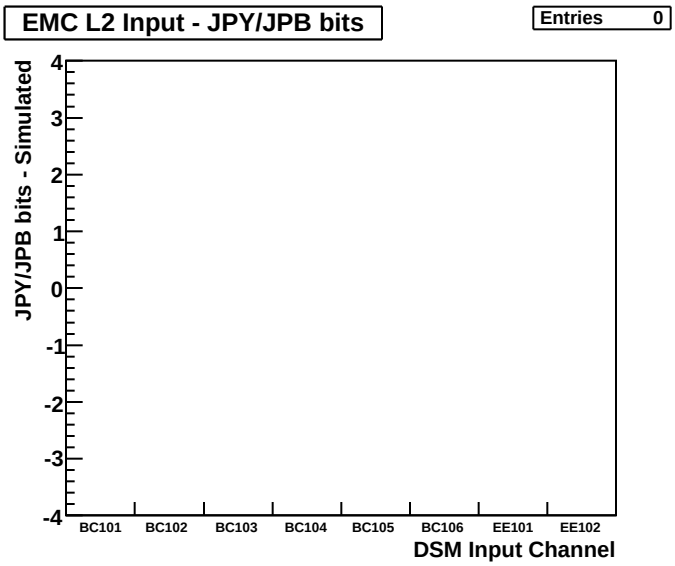

Endcap EMC L1 Input - High Tower Bits
Entries 3393948
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

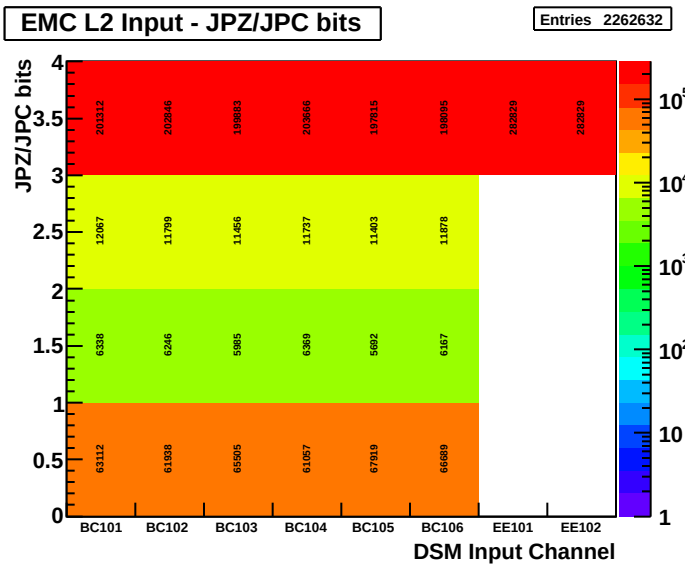

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

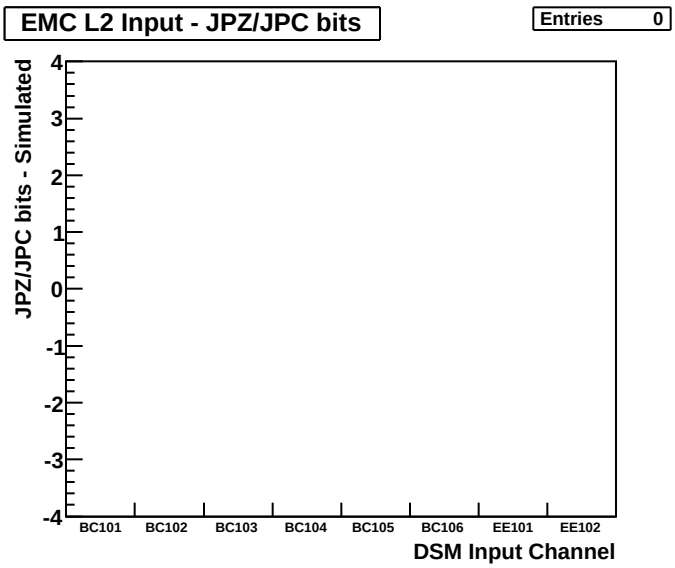

EMC L2 Input - JPX/JPA bits
Entries 2262632
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
202362 204264 204198 202394 202028 200524 203384 200030
11934 11538 13816 12053 11254 12054 11452 11430
6299 5905 7947 6110 5839 6429 5975 6005
62234 61122 56868 62272 63708 63822 62018 65364

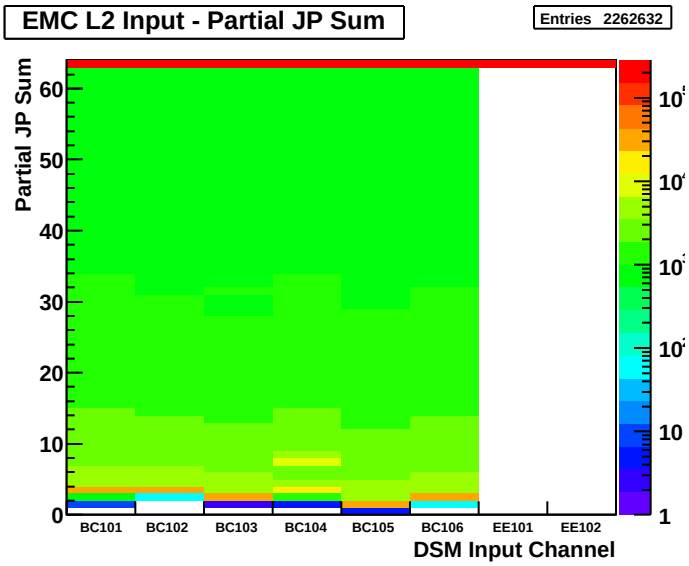

EMC L2 Input - JPX/JPA bits
Entries 50479
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7476 7634
17707 17662

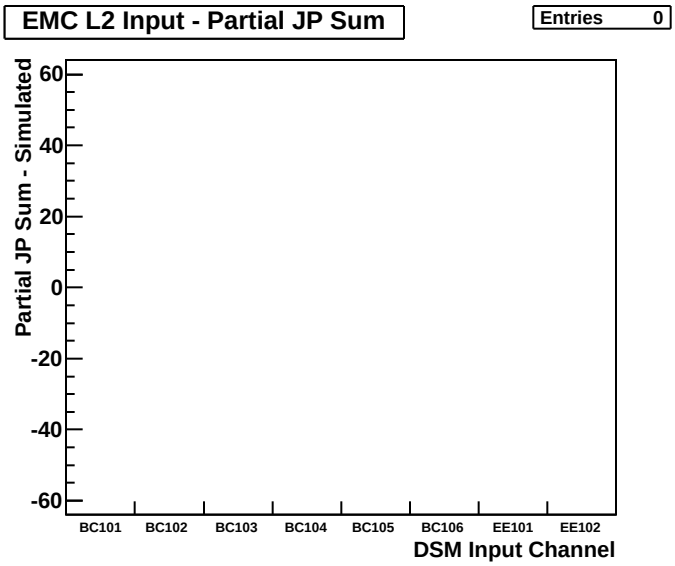

EMC L2 Input - JPY/JPB bits
Entries 2262632
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
202174 202580 198289 201682 200720 200130 200034 203944
11815 11568 12000 11849 11689 12138 11416 11548
6270 6221 6167 6098 5959 6427 5961 5975
62570 62480 66393 63220 64461 64134 65418 61362

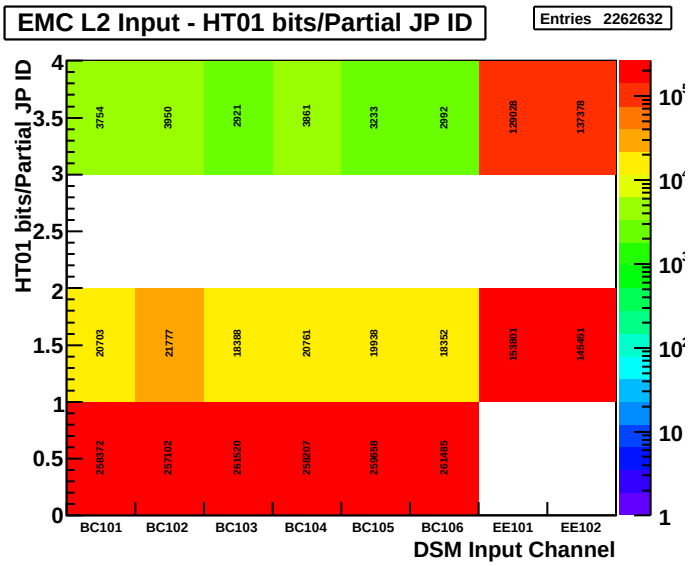

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

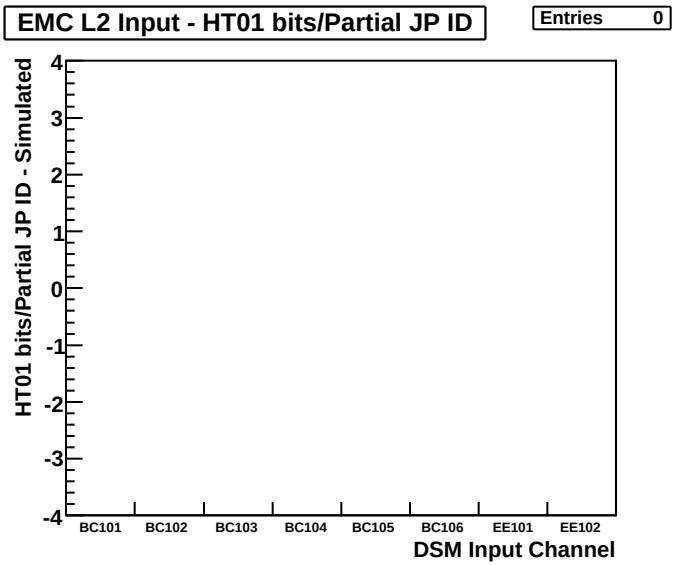

EMC L2 Input - JPZ/JPC bits
Entries 2262632
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
201312 202846 198883 203866 197815 198095 282829 282829
12067 11799 11456 11737 11403 11878
6338 6246 5985 6369 5692 6167
63112 61938 65505 61057 67919 66689

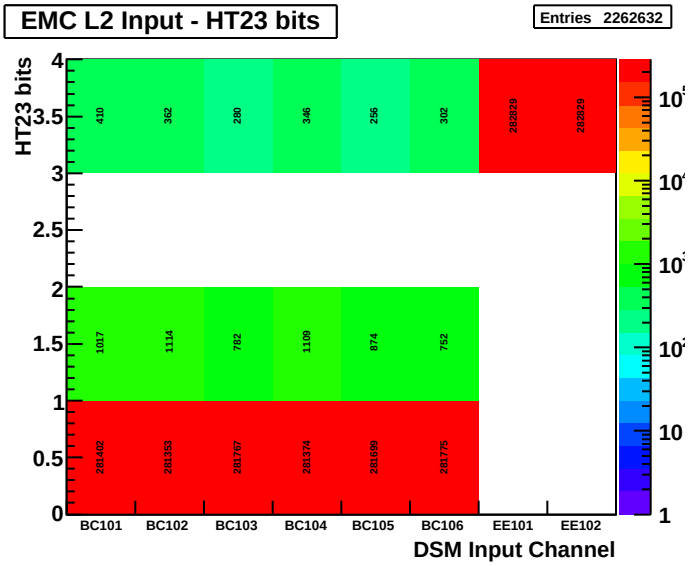

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

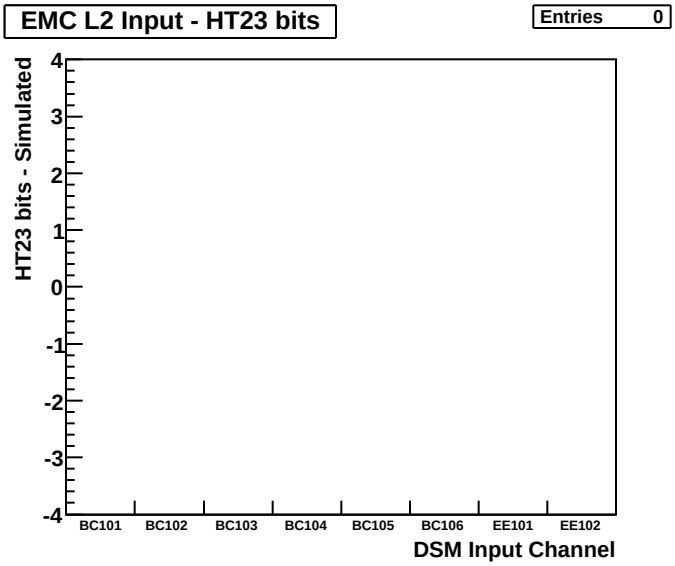

EMC L2 Input - Partial JP Sum
Entries 2262632
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

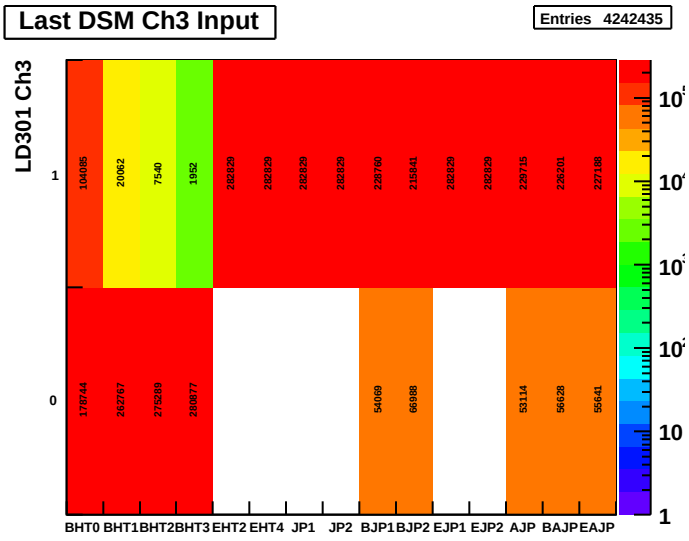

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

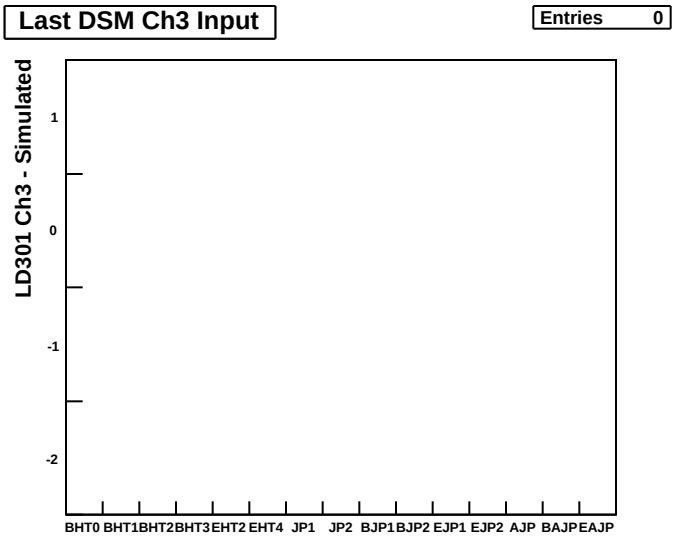

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2262632
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

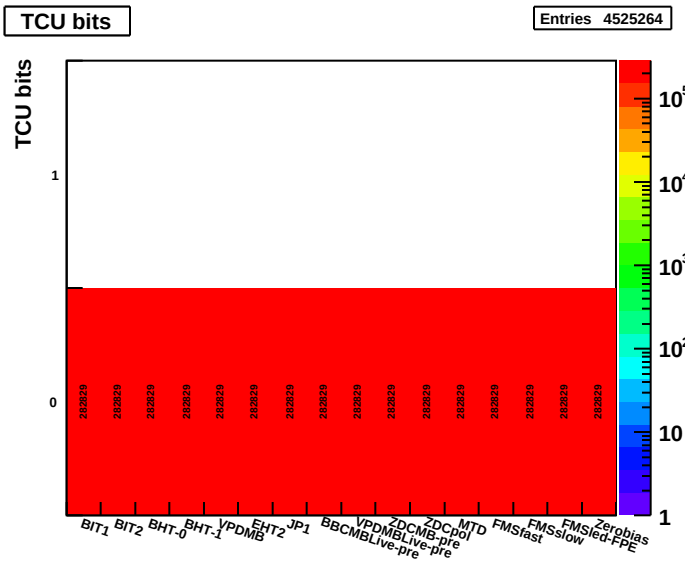

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

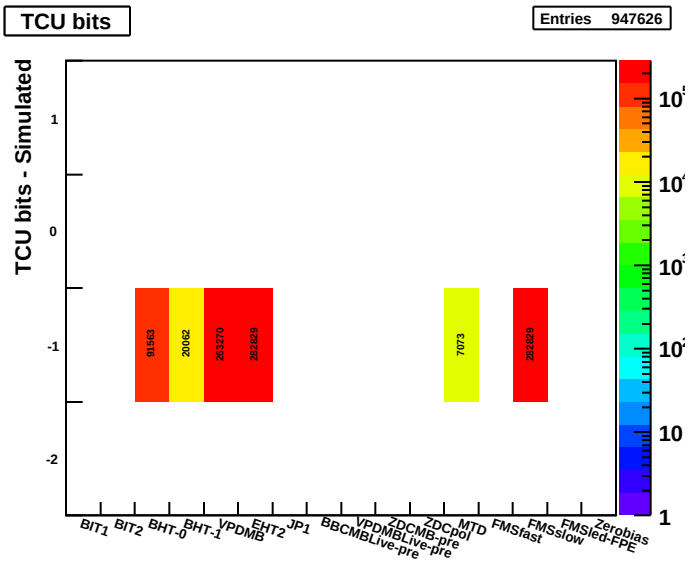

EMC L2 Input - HT23 bits
Entries 2262632
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

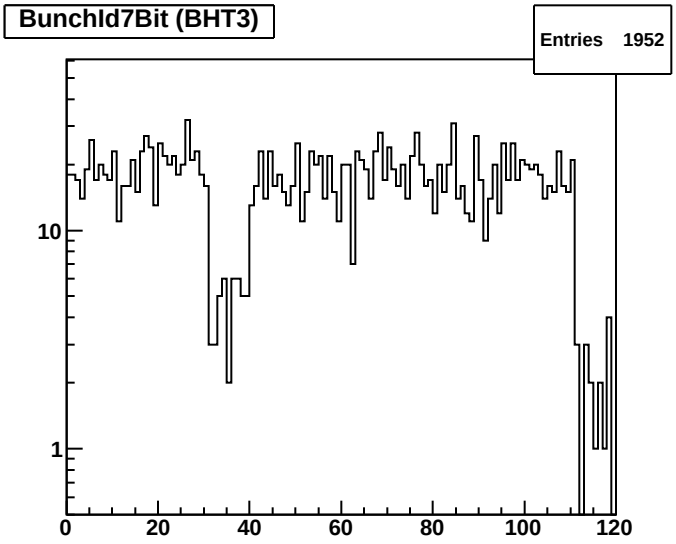

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

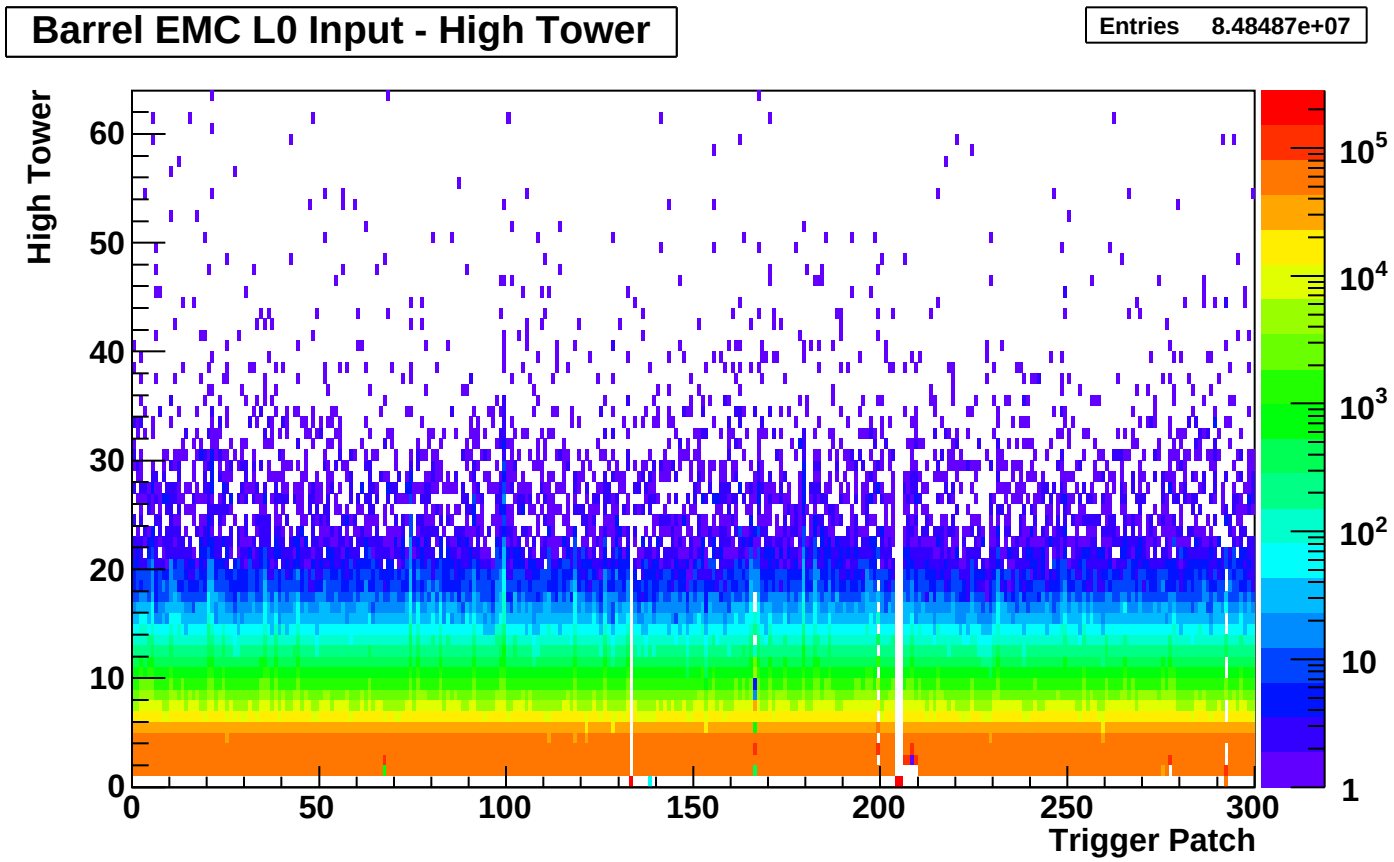

Last DSM Ch3 Input
Entries 4242435
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

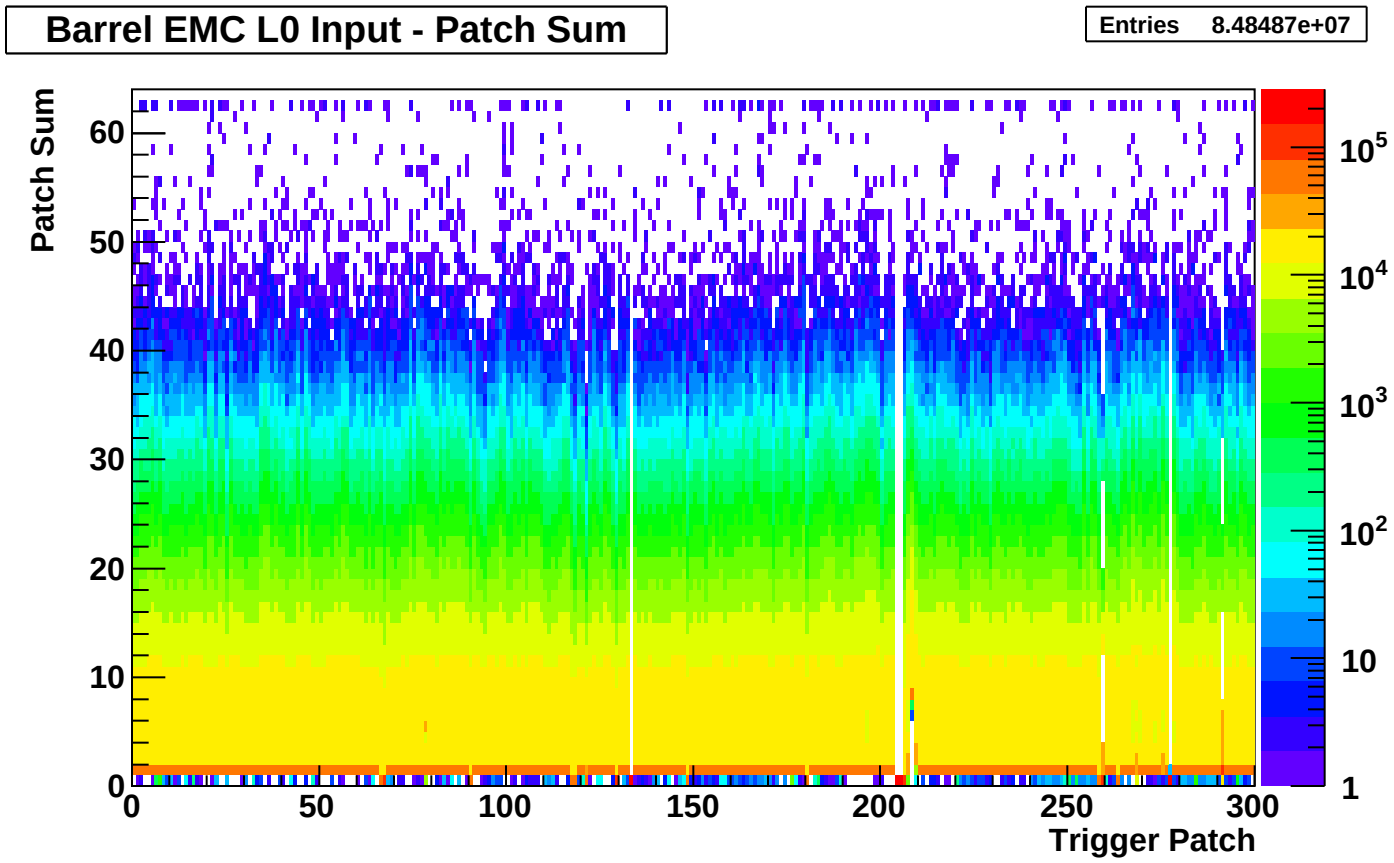

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

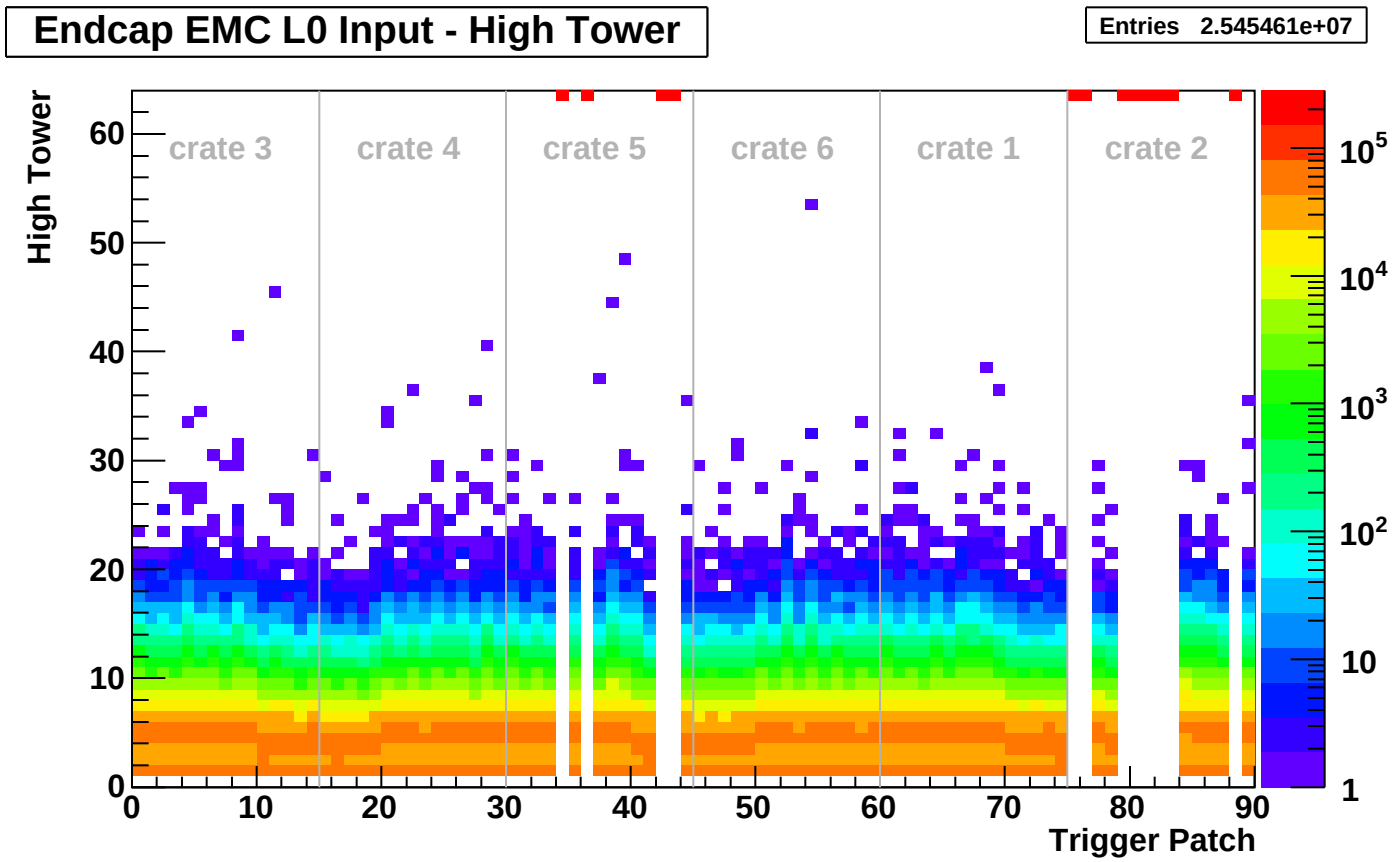

TCU bits
Entries 4525264
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

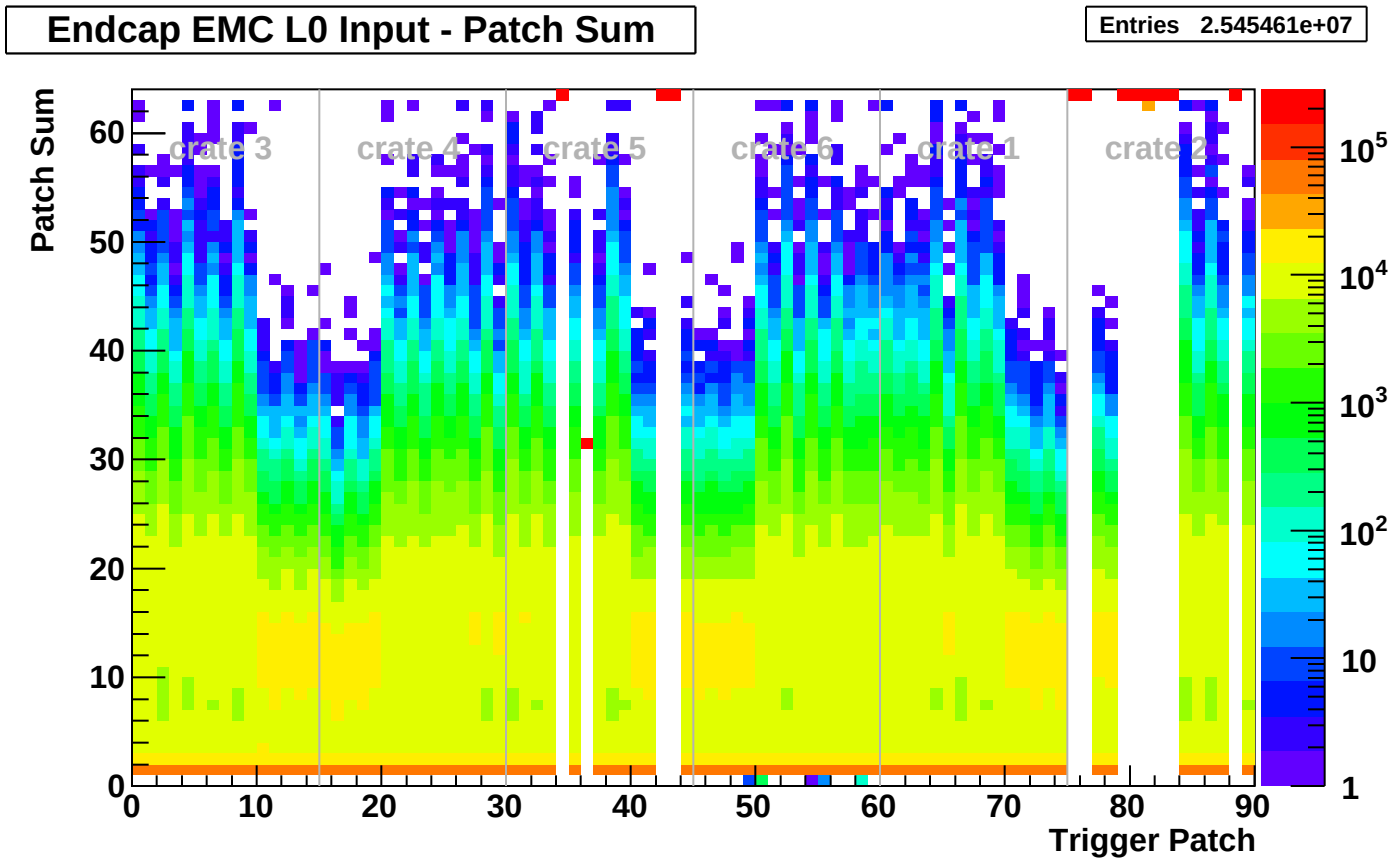

TCU bits
Entries 947626
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

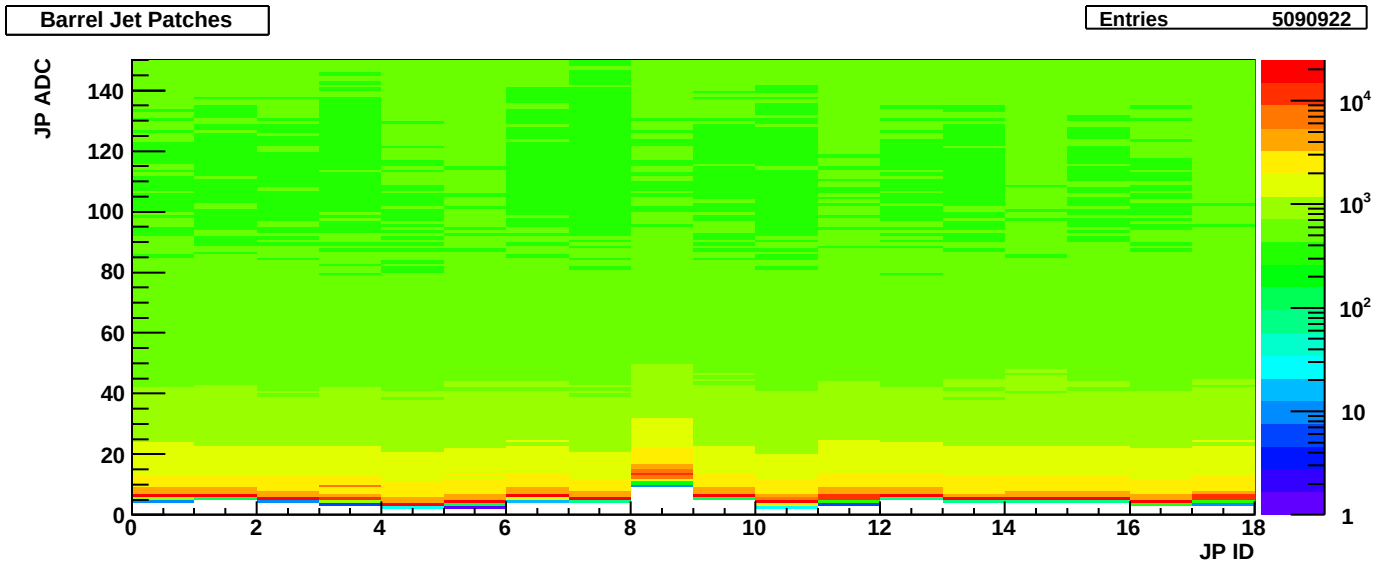

BunchId7Bit (BHT3)
Entries 1952

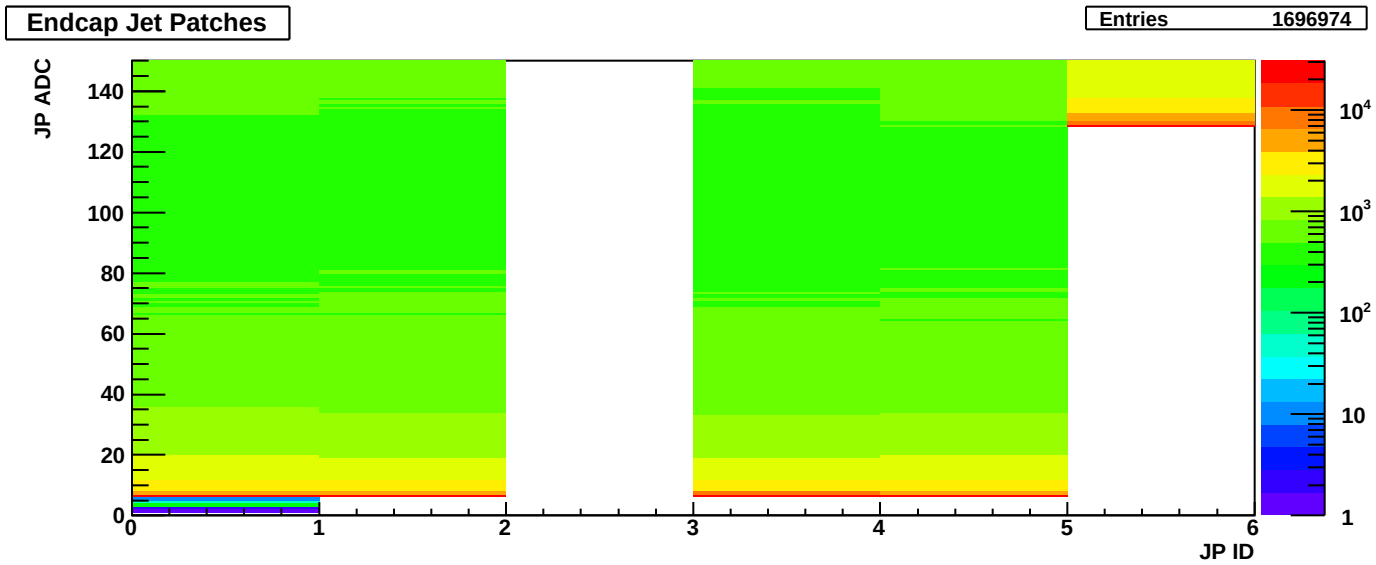

Barrel EMC L0 Input - High Tower
Entries 8.48487e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

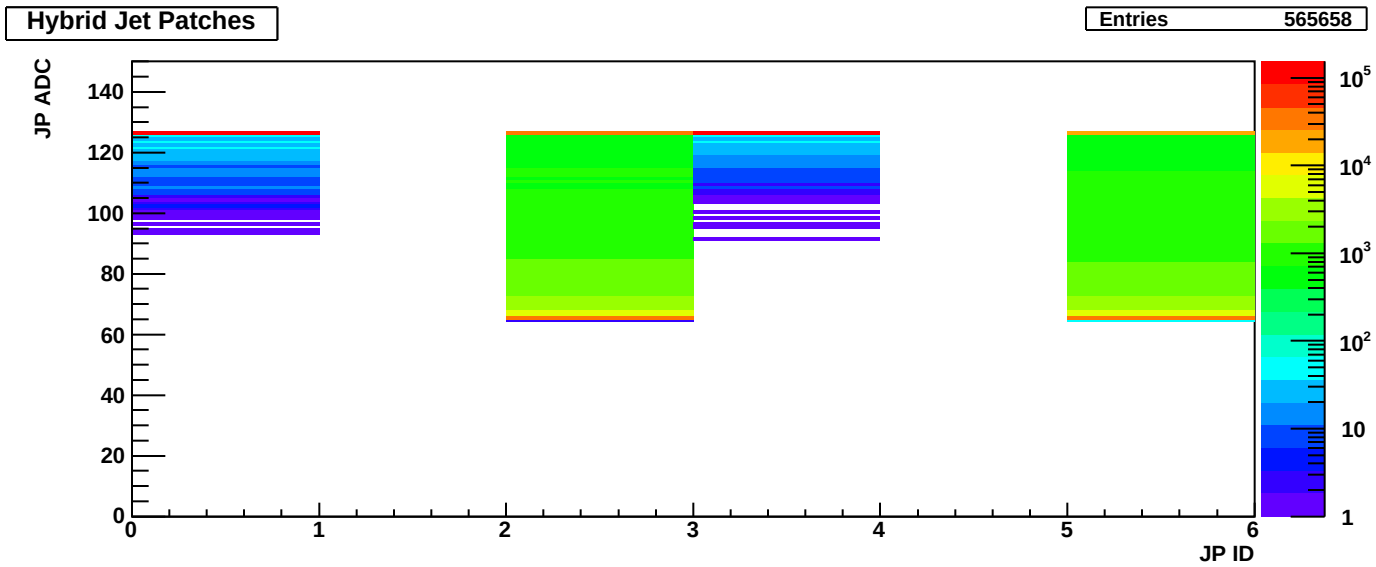

Barrel EMC L0 Input - Patch Sum
Entries 8.48487e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

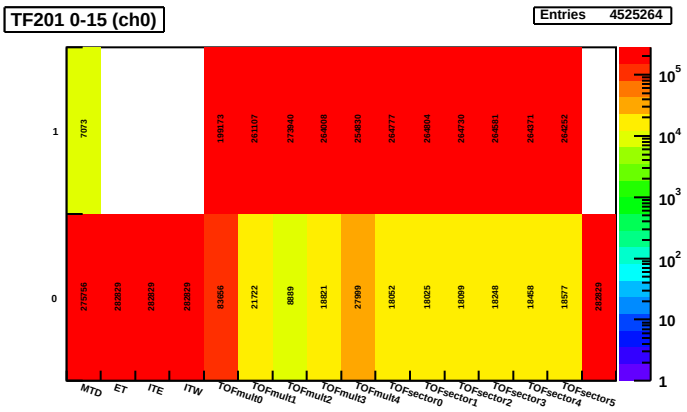

Endcap EMC L0 Input - High Tower
Entries 2.545461e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

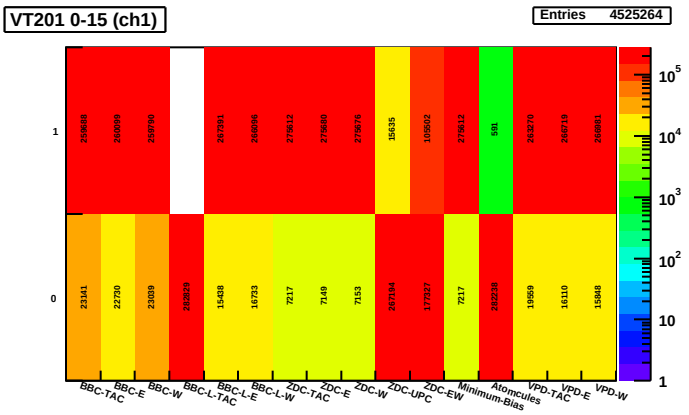

Endcap EMC L0 Input - Patch Sum
Entries 2.545461e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

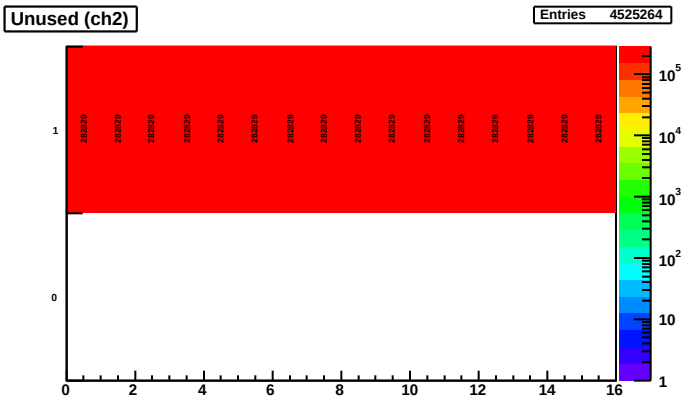

Barrel Jet Patches
Entries 5090922
JP ADC
JP ID

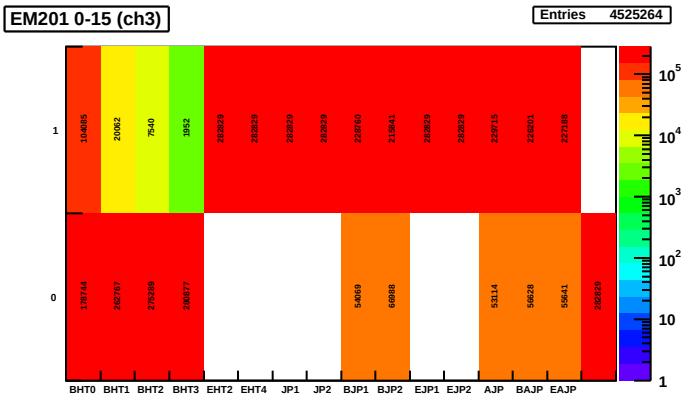

Endcap Jet Patches
Entries 1696974
JP ADC
JP ID

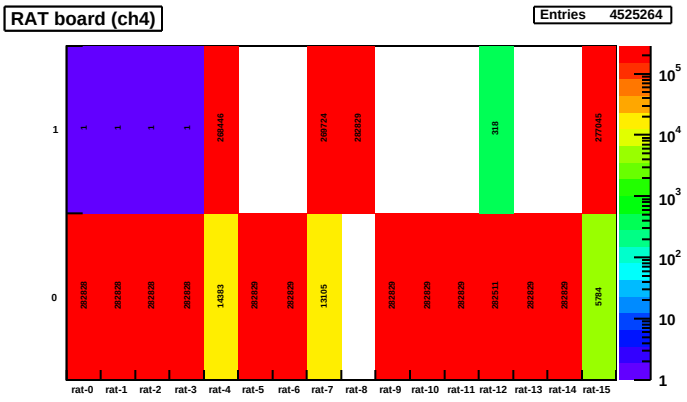

Hybrid Jet Patches
Entries 565658
JP ADC
JP ID

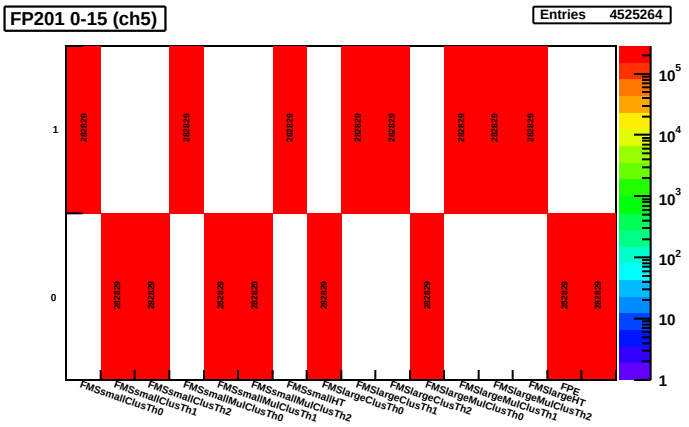

TF201 0-15 (ch0)
Entries 4525264
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

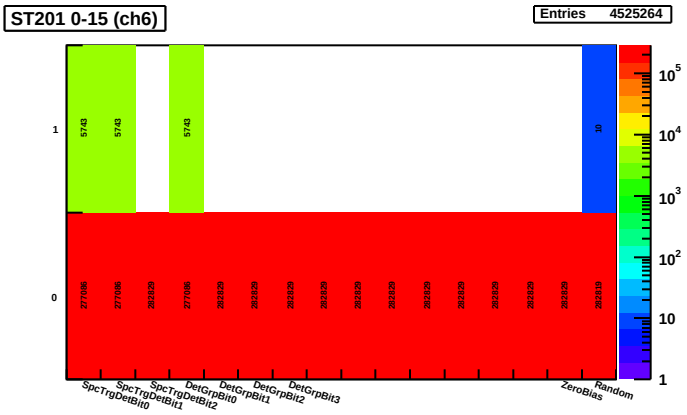

VT201 0-15 (ch1)
Entries 4525264
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

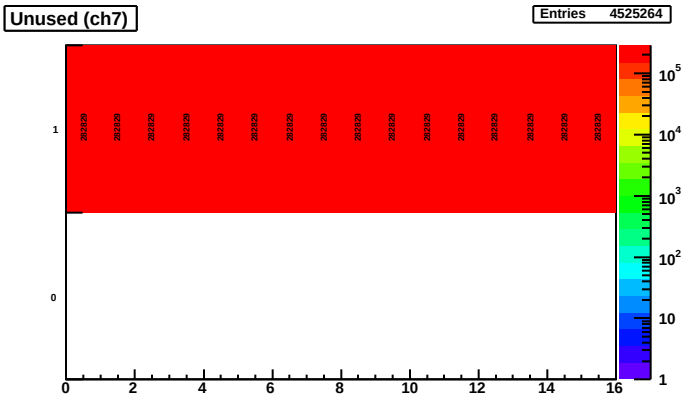

Unused (ch2)
Entries 4525264

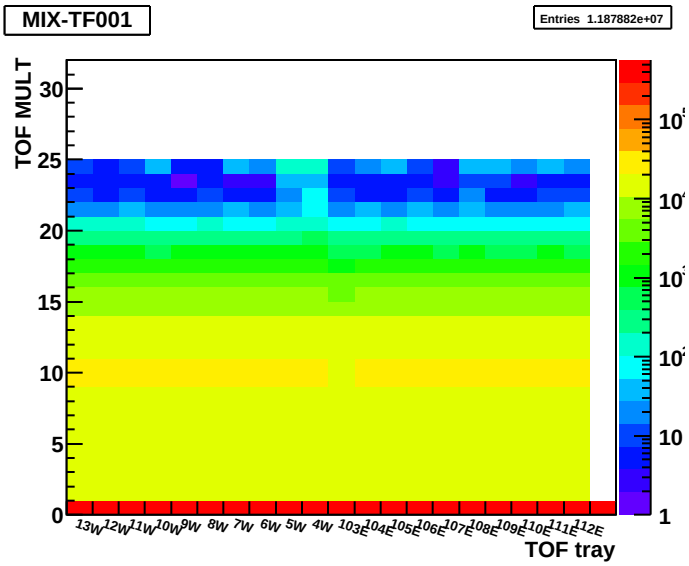

EM201 0-15 (ch3)
Entries 4525264
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP

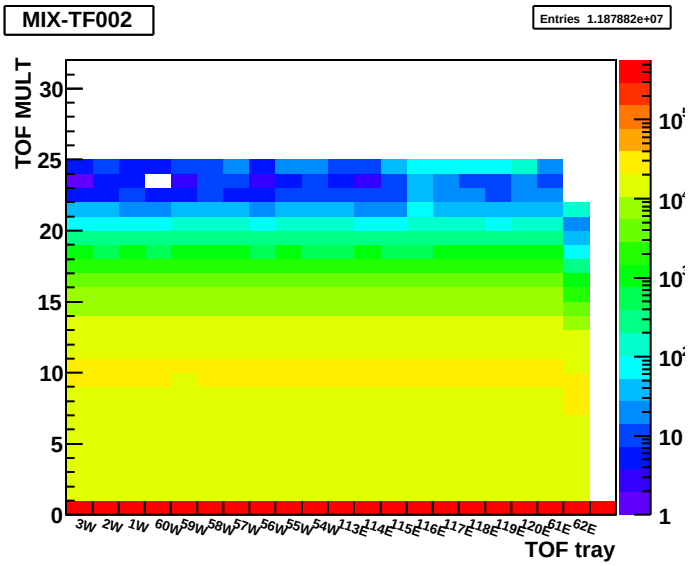

RAT board (ch4)
Entries 4525264
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

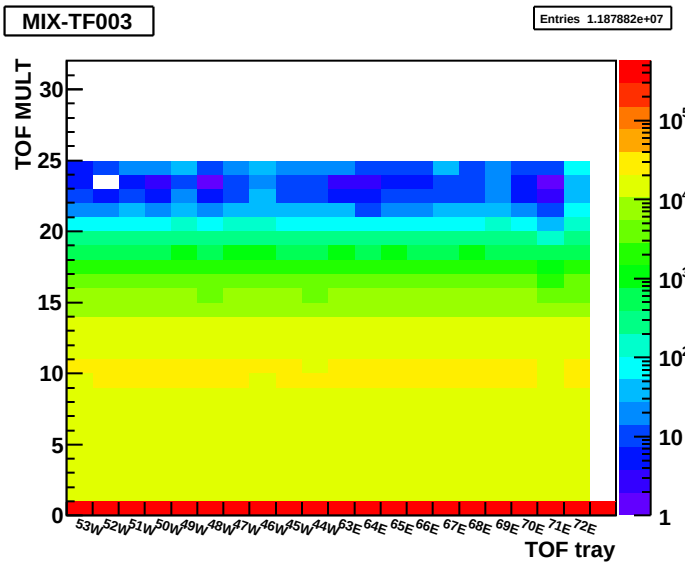

FP201 0-15 (ch5)
Entries 4525264
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

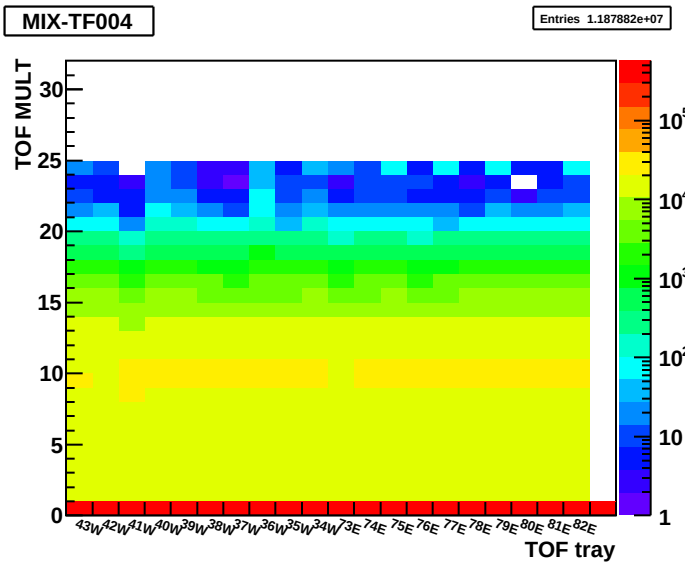

ST201 0-15 (ch6)
Entries 4525264
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

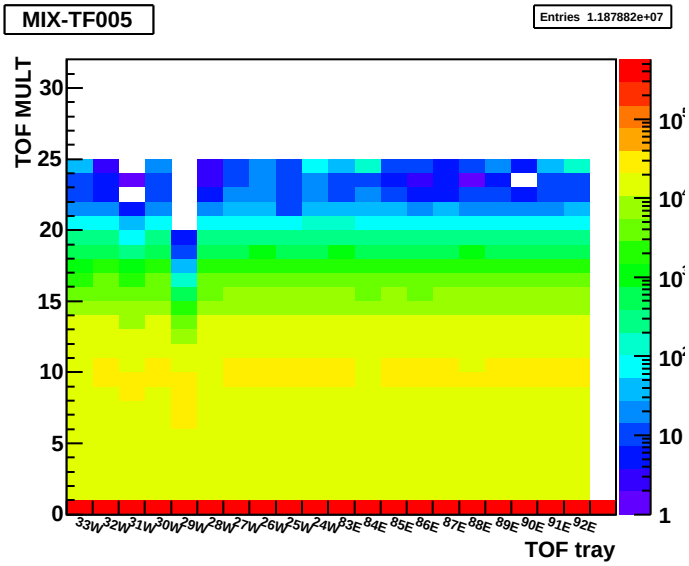

Unused (ch7)
Entries 4525264

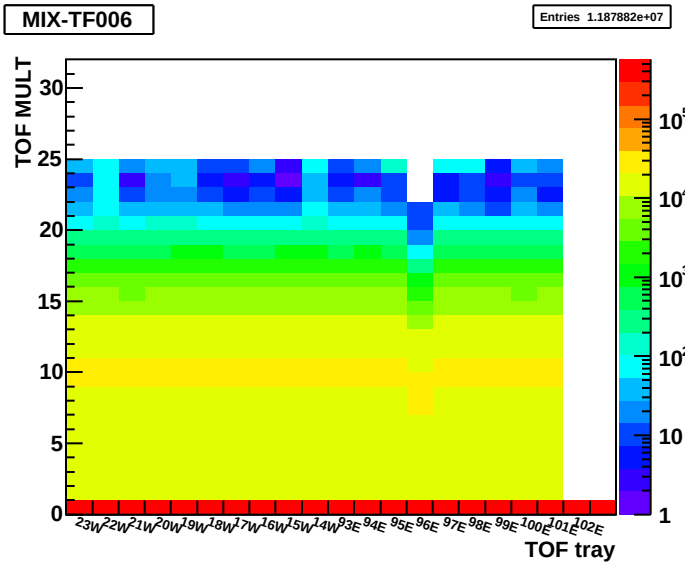

MIX-TF001
Entries 1.187882e+07
TOF MULT
TOF tray

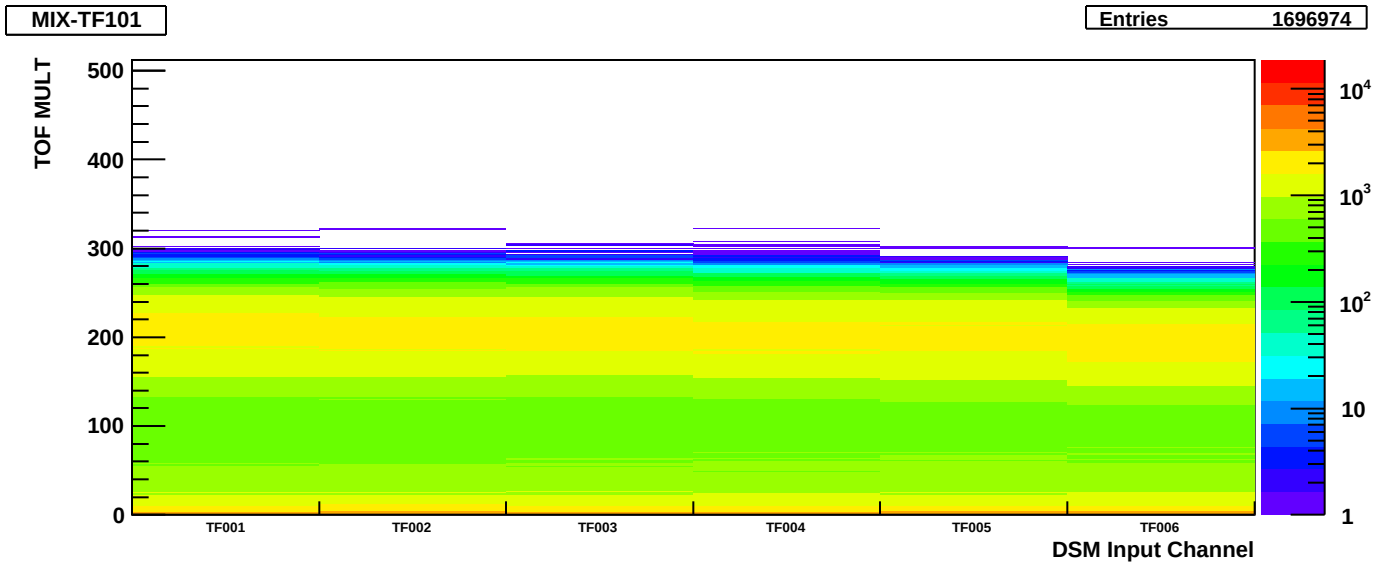

MIX-TF002
Entries 1.187882e+07
TOF MULT
TOF tray

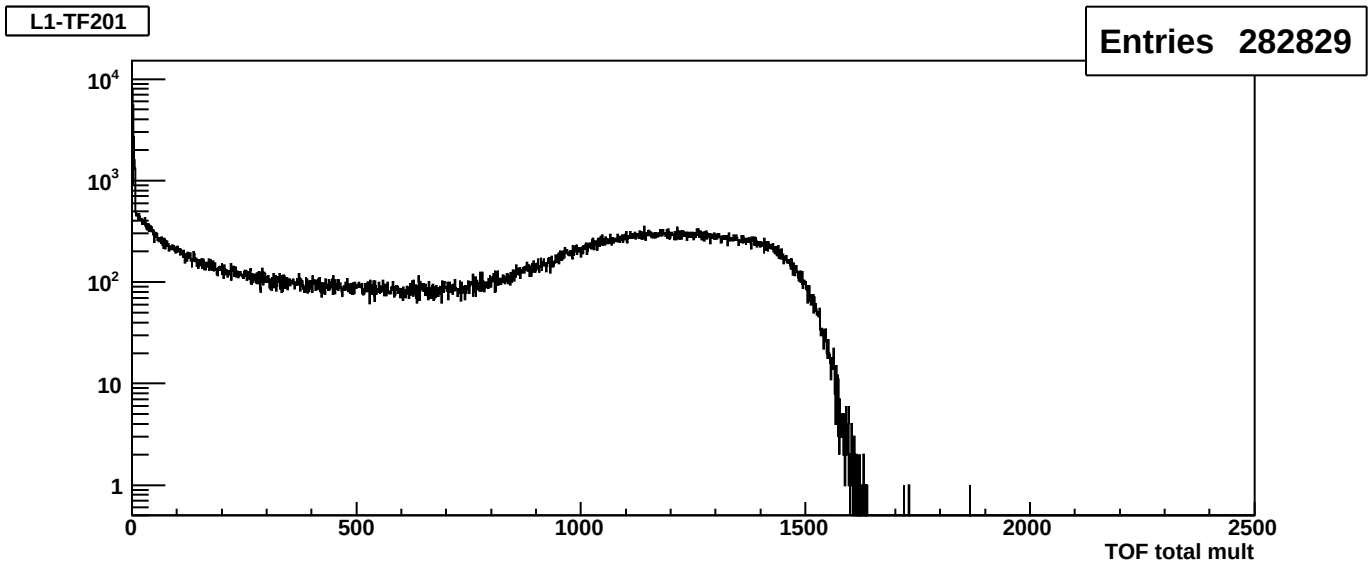

MIX-TF003
Entries 1.187882e+07
TOF MULT
TOF tray

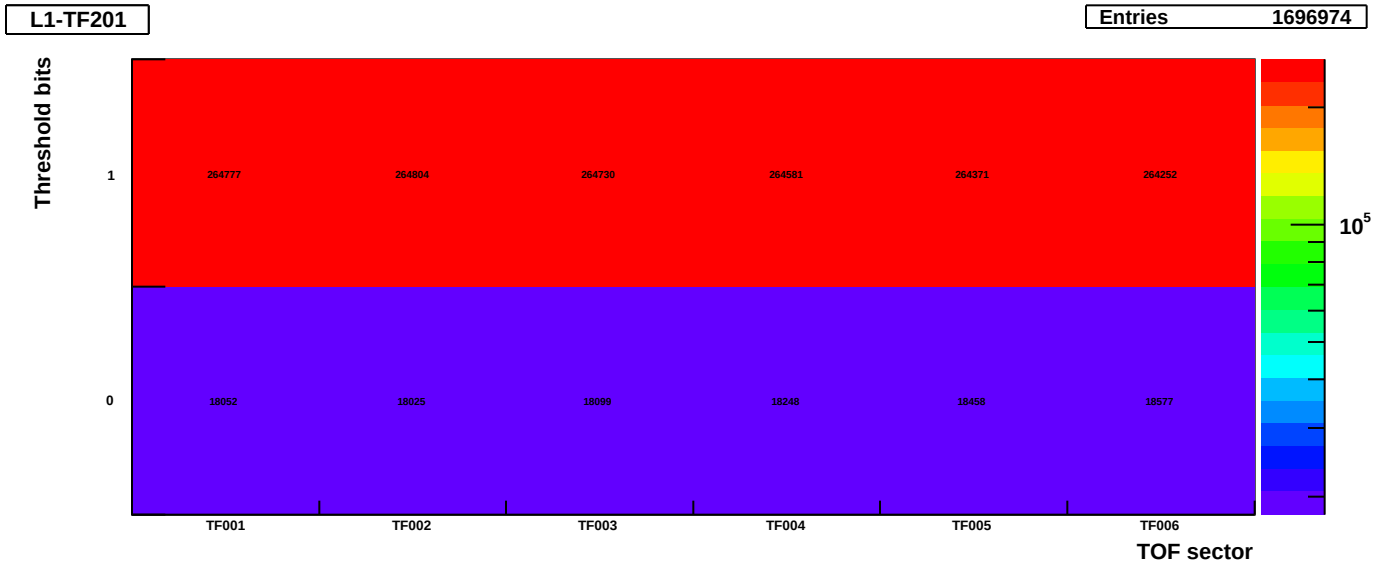

MIX-TF004
Entries 1.187882e+07
TOF MULT
TOF tray

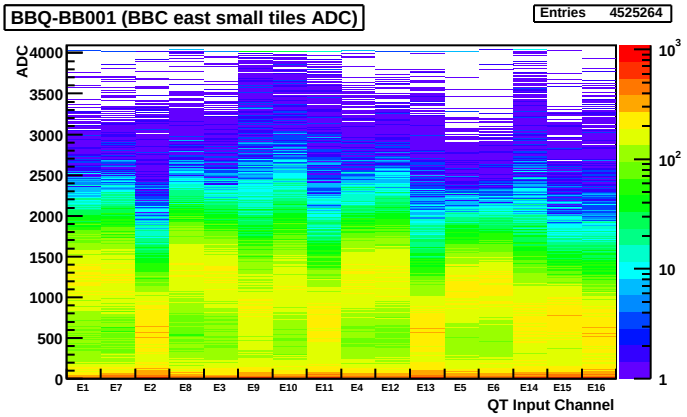

MIX-TF005
Entries 1.187882e+07
TOF MULT
TOF tray

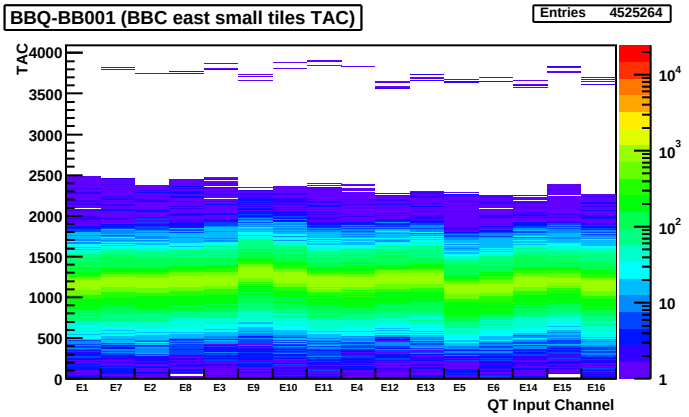

MIX-TF006
Entries 1.187882e+07
TOF MULT
TOF tray

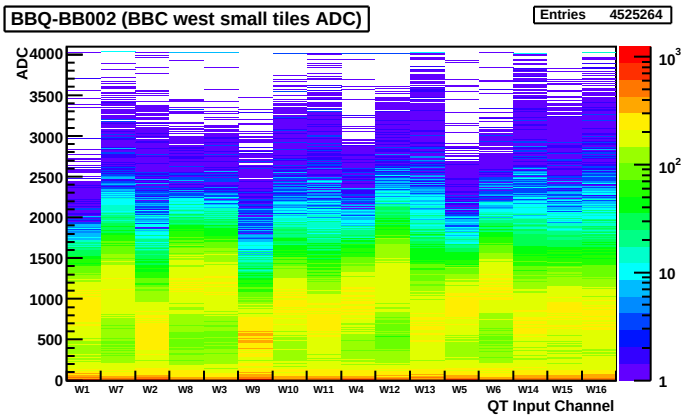

MIX-TF101
Entries 1696974
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

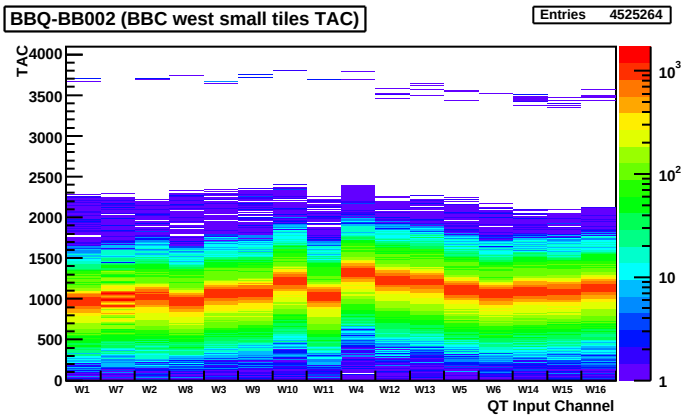

L1-TF201
Entries 282829
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

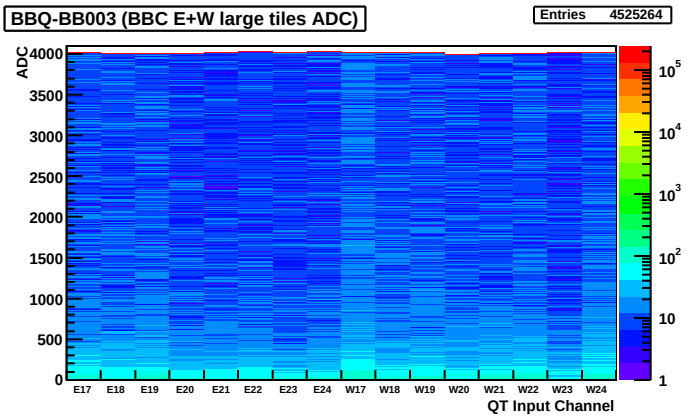

L1-TF201
Entries 1696974
Threshold bits
1
0
264777
264804
264730
264581
264371
264252
18052
18025
18099
18248
18458
18577
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

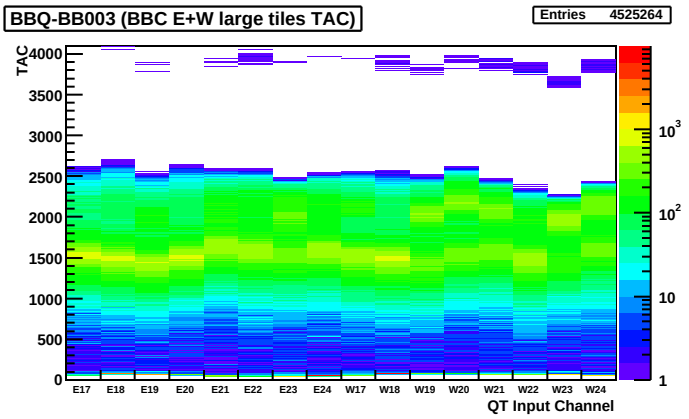

BBQ-BB001 (BBC east small tiles ADC)
Entries 4525264
ADC
QT Input Channel

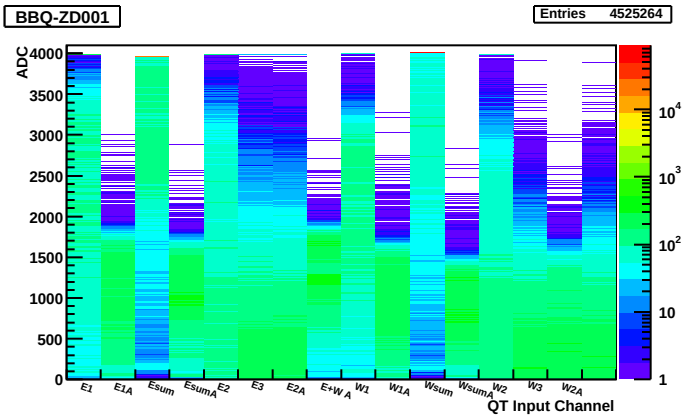

BBQ-BB001 (BBC east small tiles TAC)
Entries 4525264
TAC
QT Input Channel

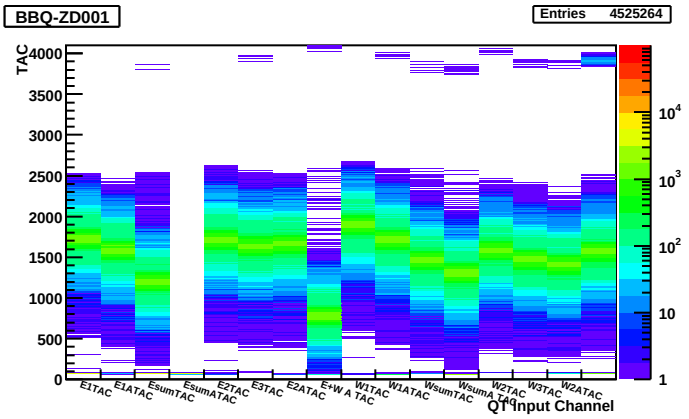

BBQ-BB002 (BBC west small tiles ADC)
Entries 4525264
ADC
QT Input Channel

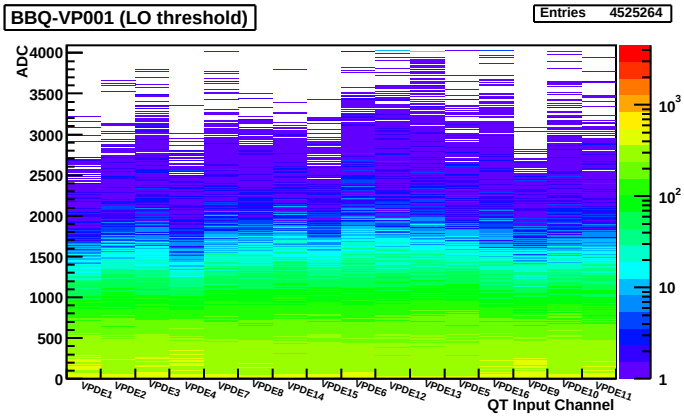

BBQ-BB002 (BBC west small tiles TAC)
Entries 4525264
TAC
QT Input Channel

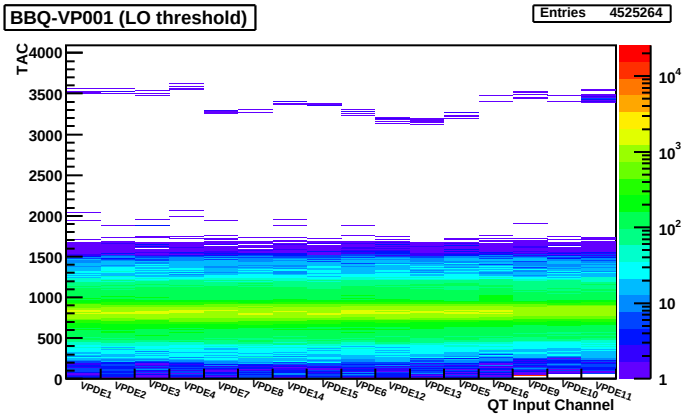

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4525264
ADC
QT Input Channel

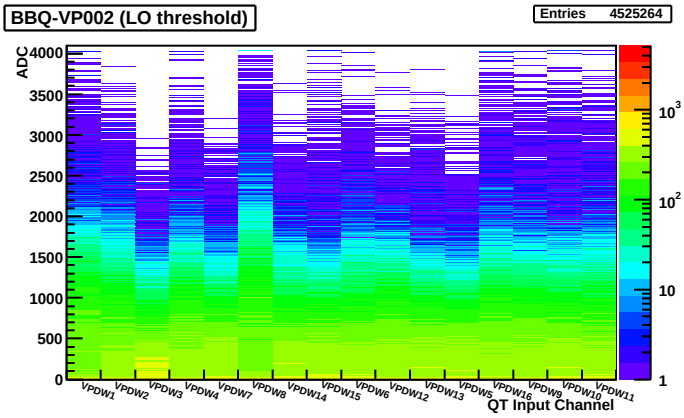

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4525264
TAC
QT Input Channel

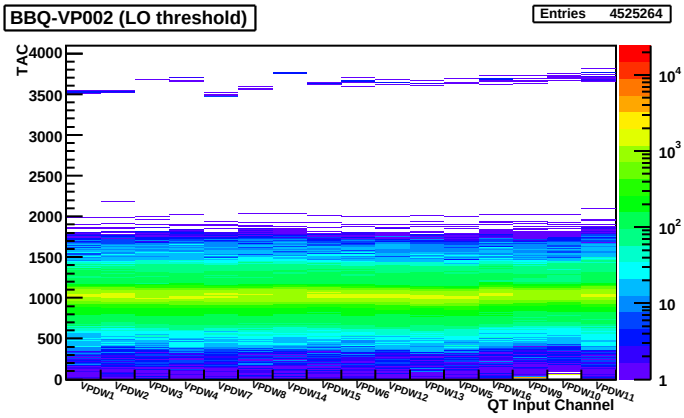

BBQ-ZD001
Entries 4525264
ADC
QT Input Channel

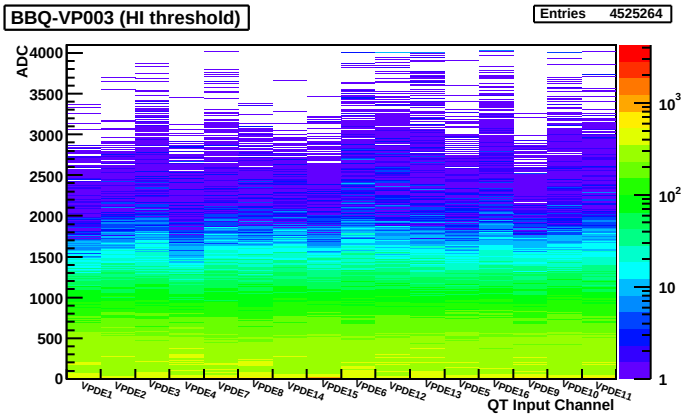

BBQ-ZD001
Entries 4525264
TAC
QT Input Channel

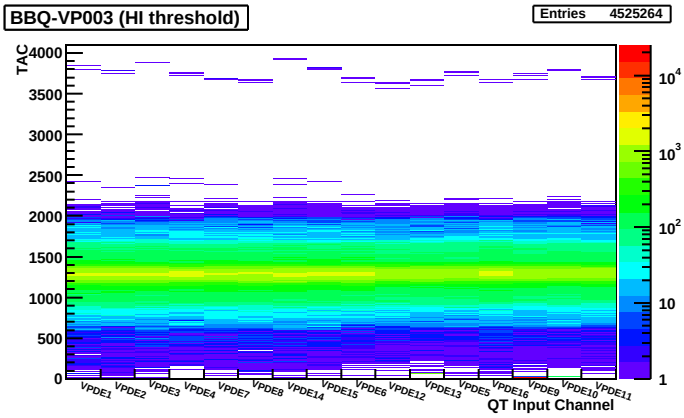

BBQ-VP001 (LO threshold)
Entries 4525264
ADC
QT Input Channel

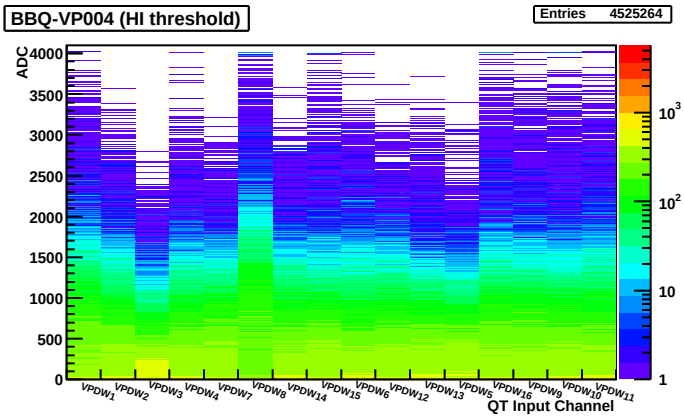

BBQ-VP001 (LO threshold)
Entries 4525264
TAC
QT Input Channel

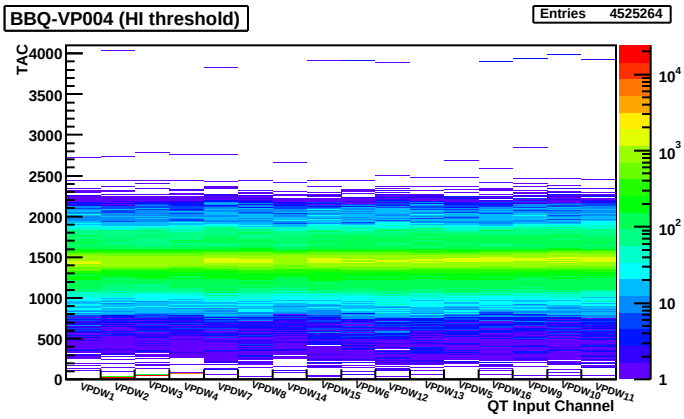

BBQ-VP002 (LO threshold)
Entries 4525264
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 4525264
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4525264
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4525264
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4525264
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4525264
TAC
QT Input Channel