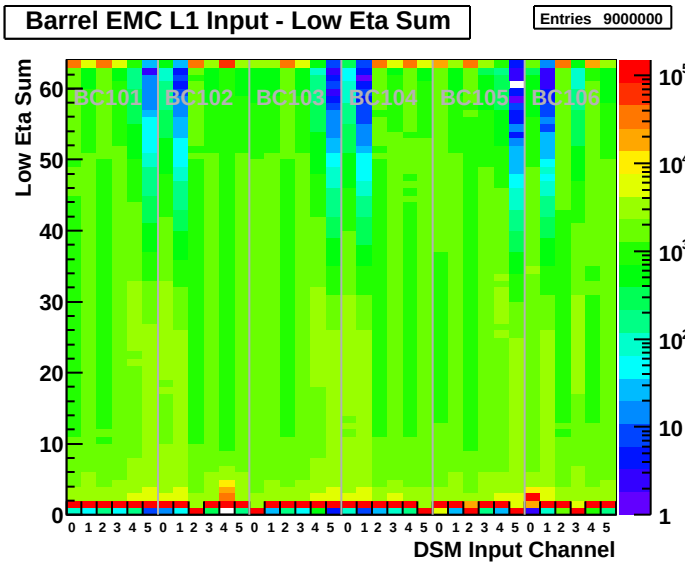
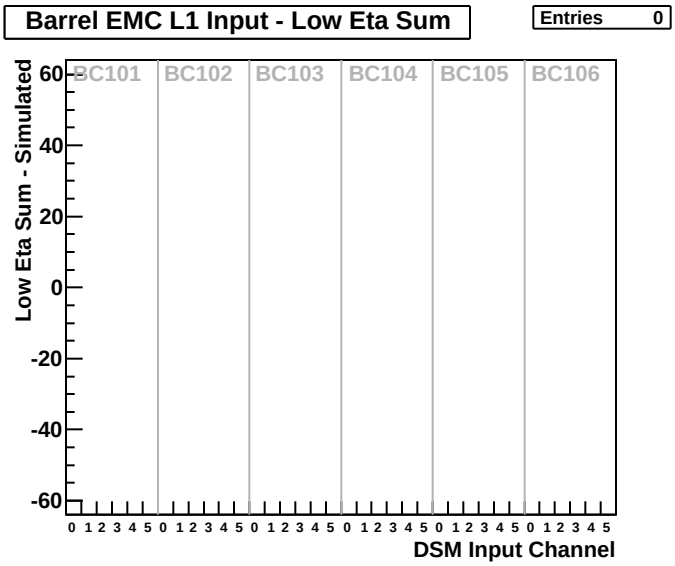
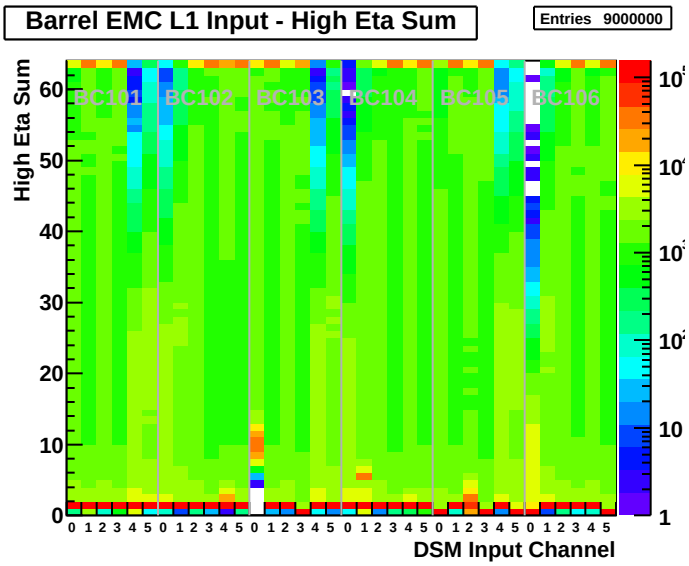
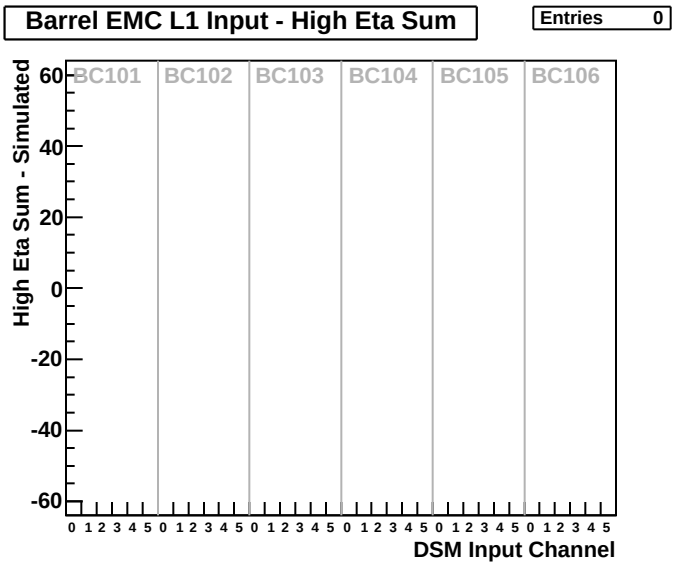
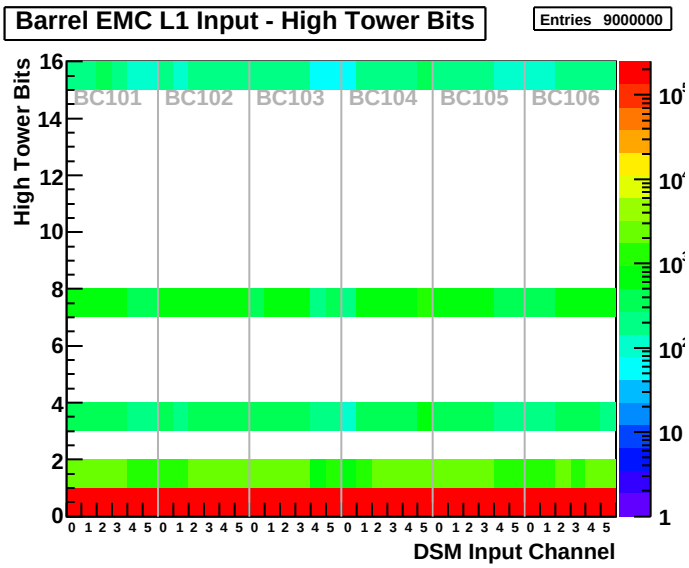
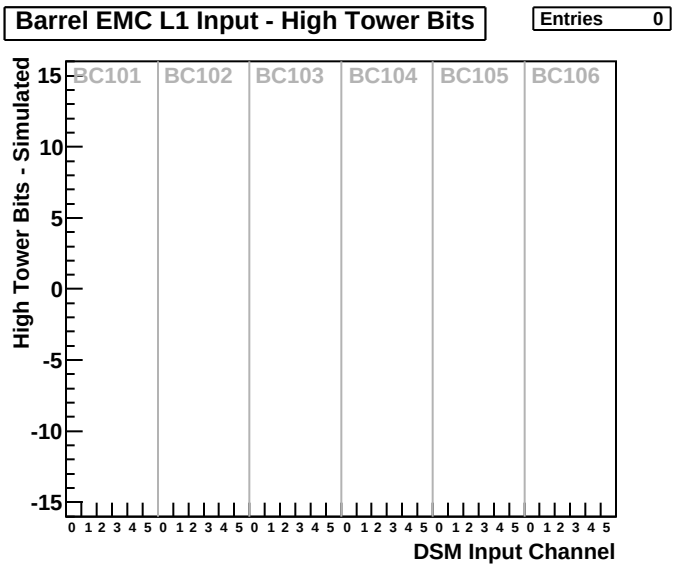

Barrel EMC L1 Input - Low Eta Sum
Entries 9000000
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

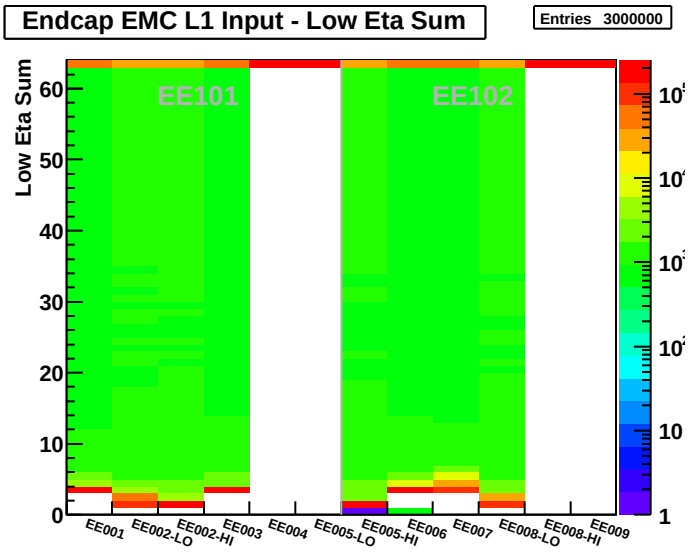

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

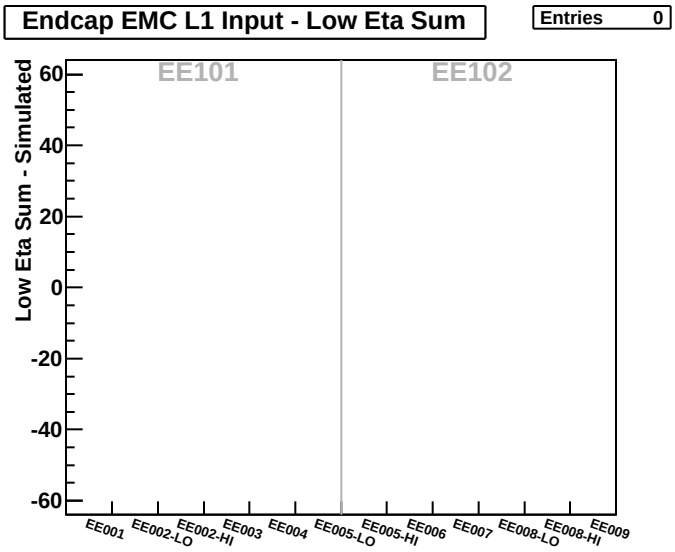

Barrel EMC L1 Input - High Eta Sum
Entries 9000000
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

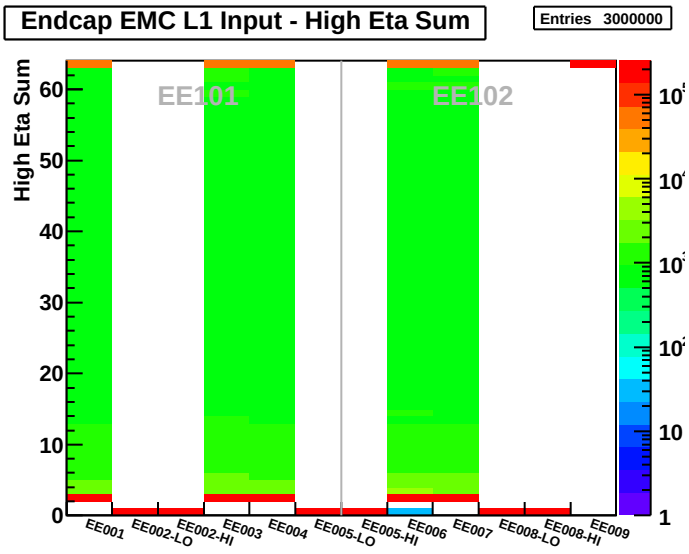

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

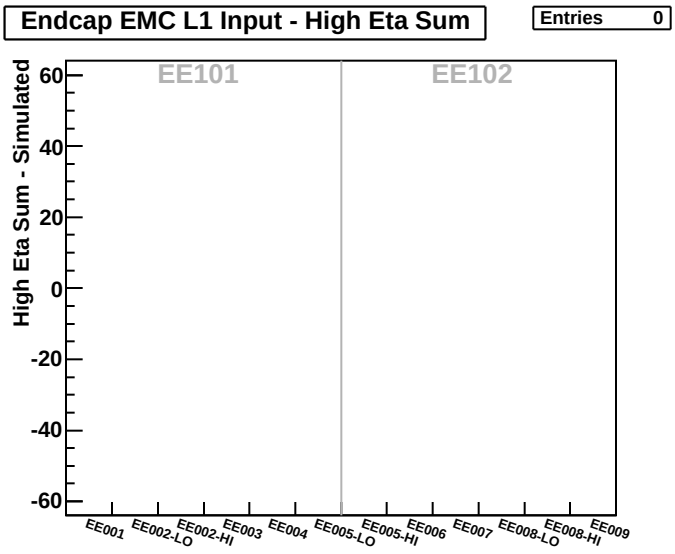

Barrel EMC L1 Input - High Tower Bits
Entries 9000000
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

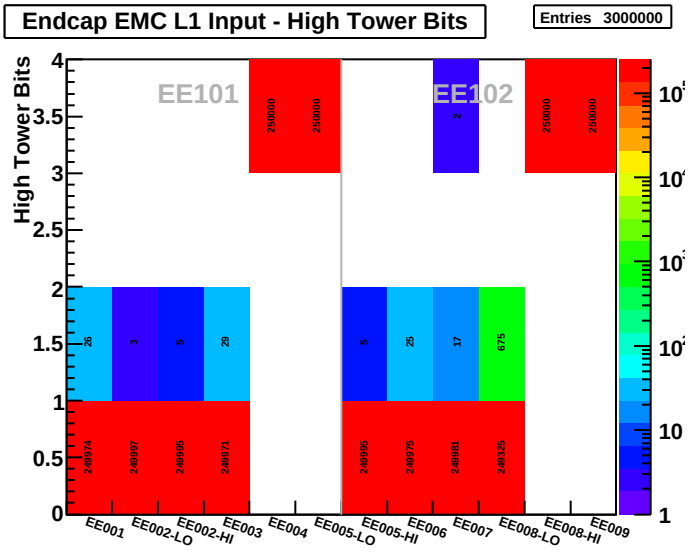

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

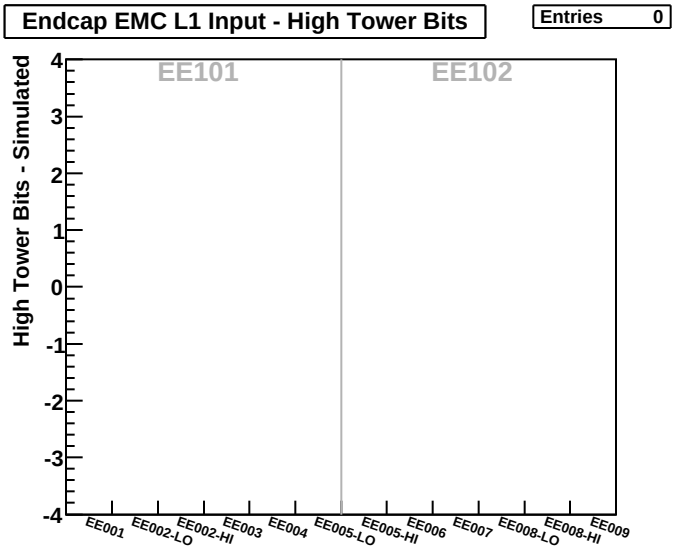

Endcap EMC L1 Input - Low Eta Sum
Entries 3000000
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

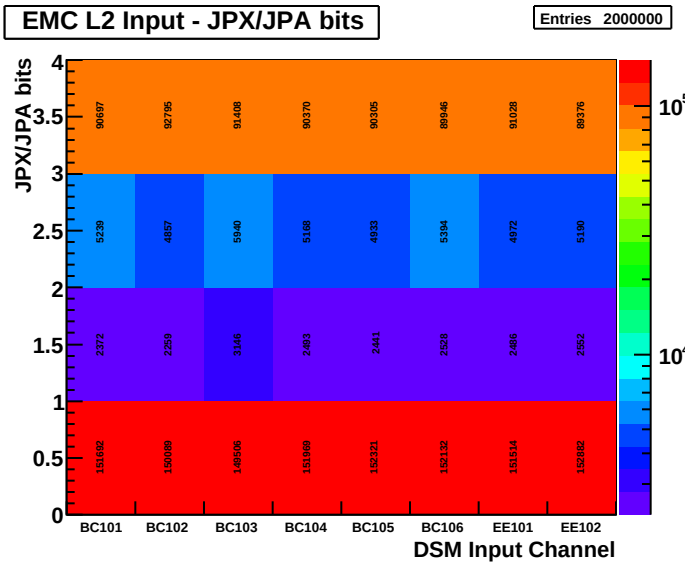

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

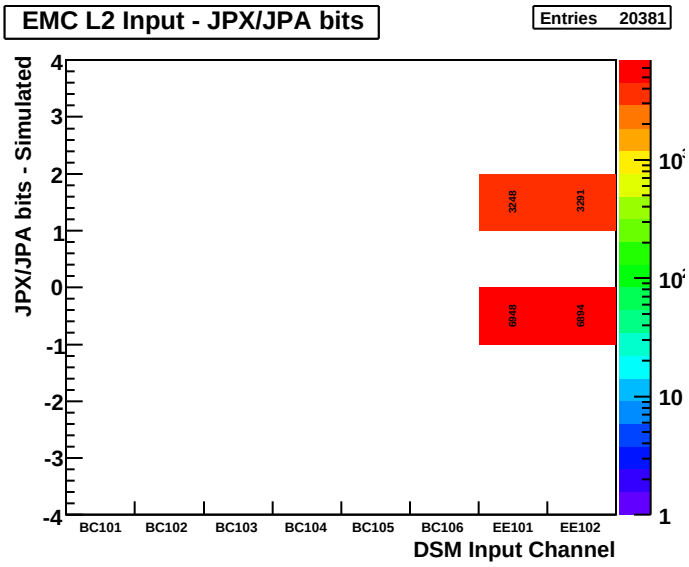

Endcap EMC L1 Input - High Eta Sum
Entries 3000000
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

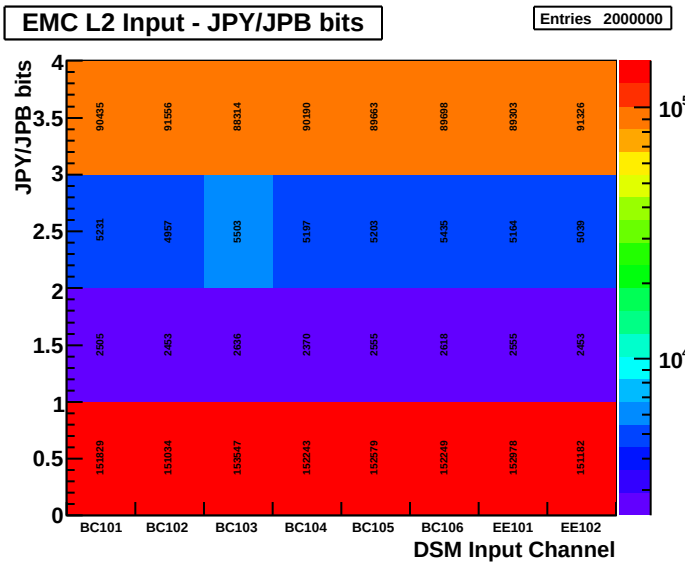

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

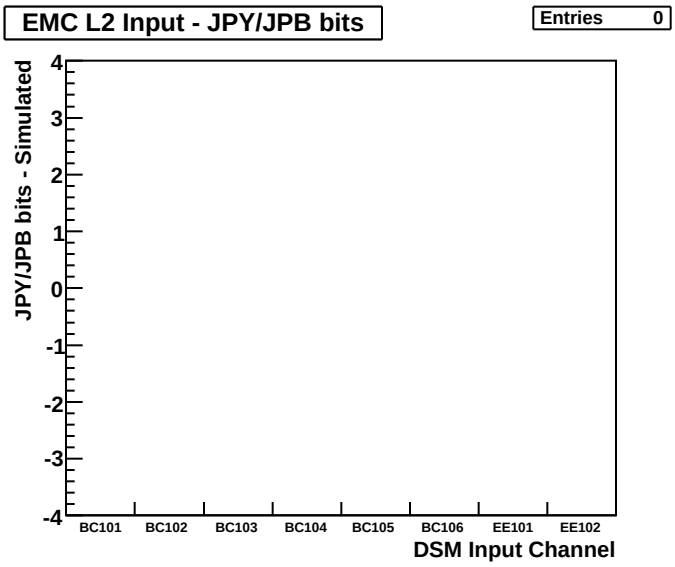

Endcap EMC L1 Input - High Tower Bits
Entries 3000000
High Tower Bits
EE101
EE102
250000
250000
2
250000
250000
26
3
5
29
5
25
17
675
249974
249997
249995
249971
249995
249975
249981
249325
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

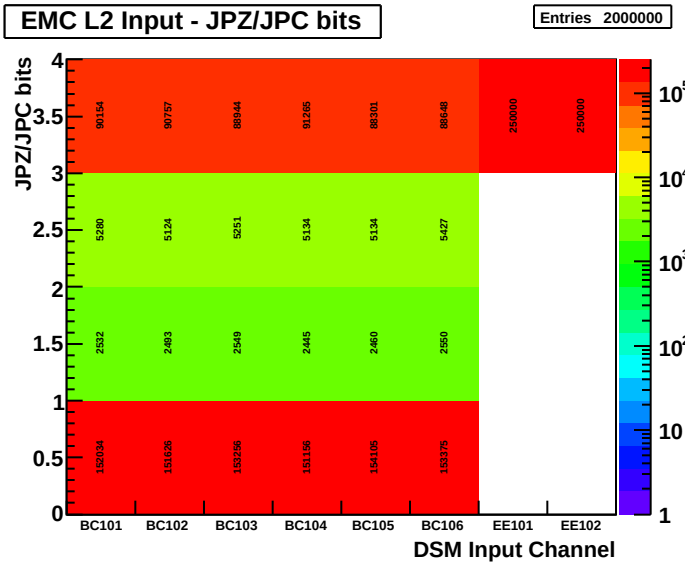

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

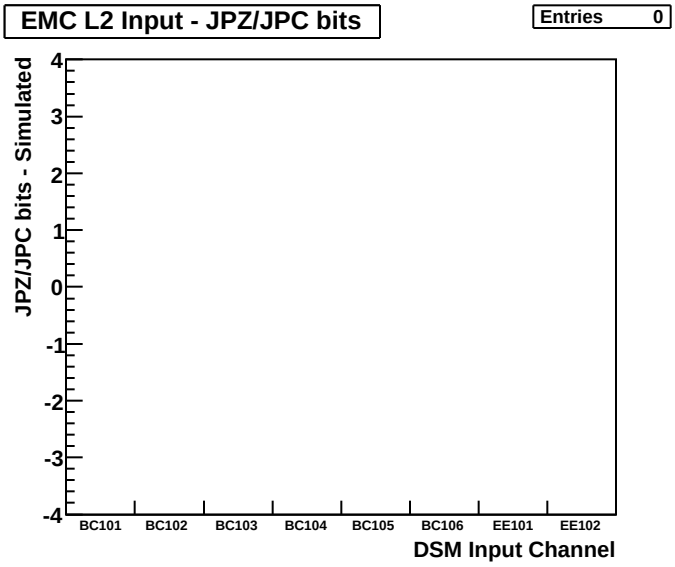

EMC L2 Input - JPX/JPA bits
Entries 2000000
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
90697 92795 91408 90370 90305 89946 91028 89376
5239 4957 5940 5168 4933 5394 4972 5190
2372 2259 3146 2493 2441 2528 2486 2552
151692 150089 149506 151969 152321 152132 151514 152882

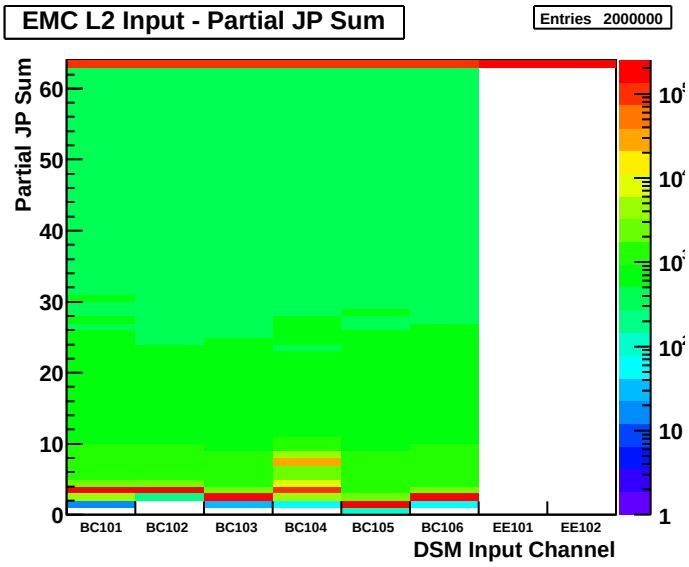

EMC L2 Input - JPX/JPA bits
Entries 20381
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3248 3291
6948 6894

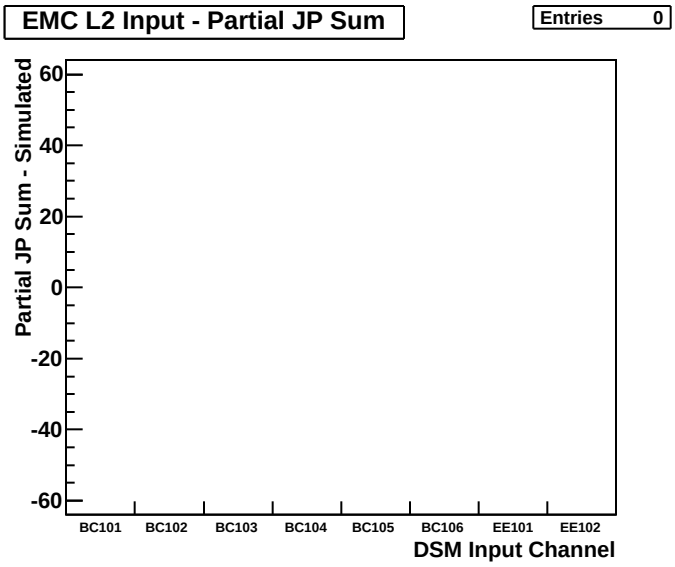

EMC L2 Input - JPY/JPB bits
Entries 2000000
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
90435 91556 88314 90190 89663 89698 89303 91326
5231 4957 5503 5197 5203 5435 5164 5039
2305 2453 2636 2370 2555 2618 2555 2453
151829 151034 153547 152243 152579 152249 152978 151182

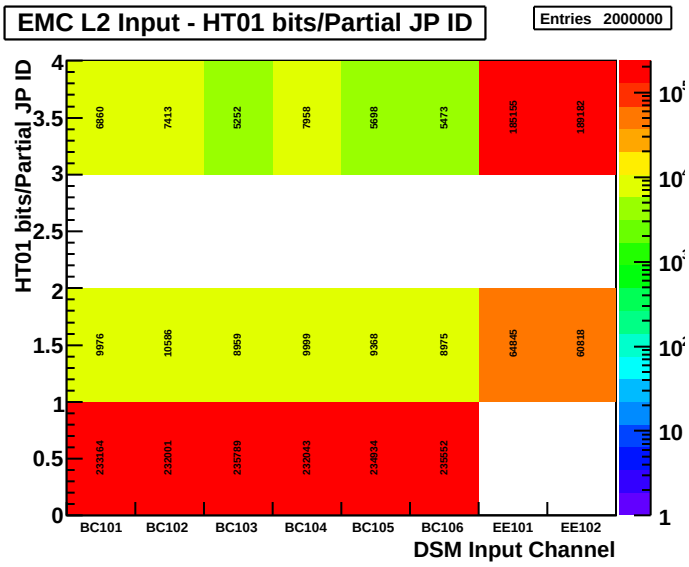

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

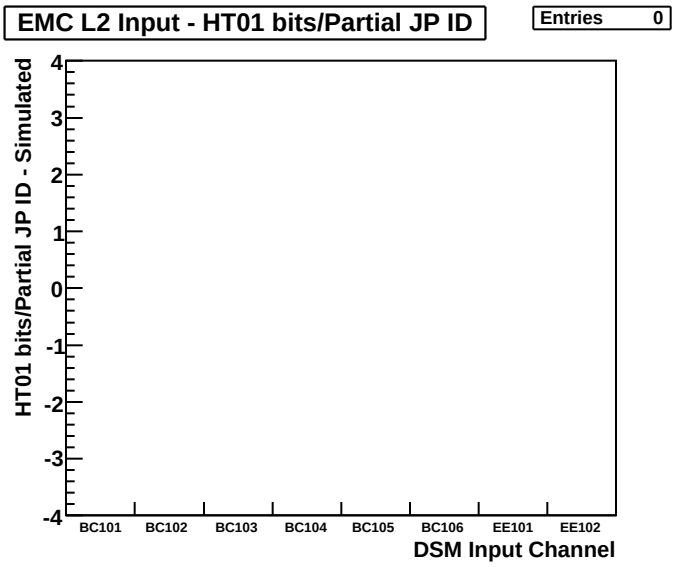

EMC L2 Input - JPZ/JPC bits
Entries 2000000
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
90154 90757 88944 91265 88301 88648 250000 250000
5280 5124 5251 5134 5134 5427
2532 2493 2549 2445 2460 2550
152034 151626 153256 151156 154105 153375

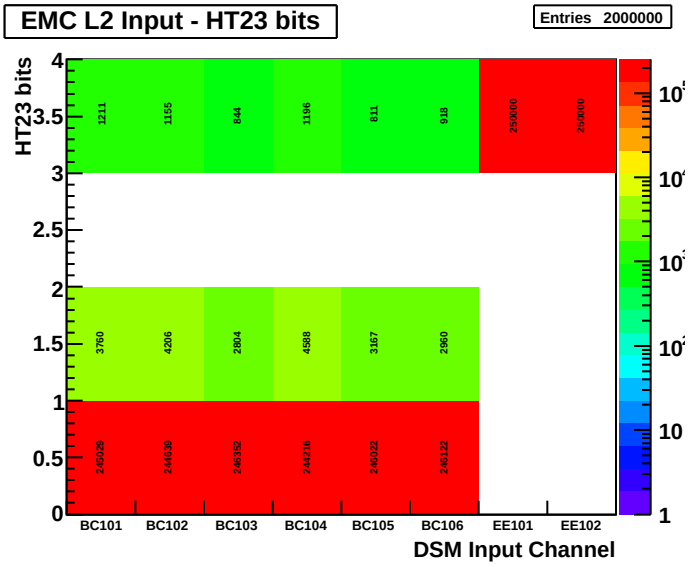

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

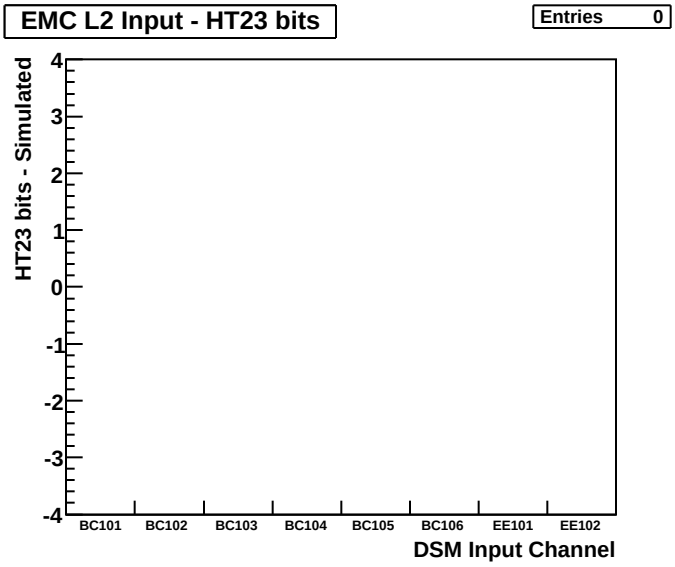

EMC L2 Input - Partial JP Sum
Entries 2000000
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

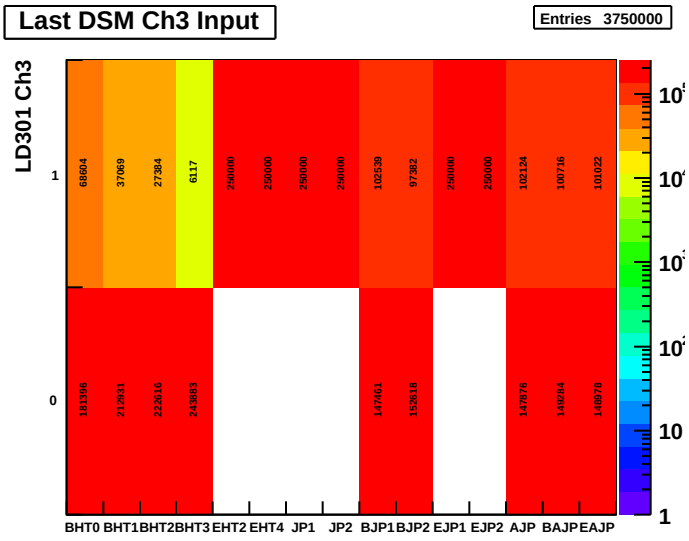

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

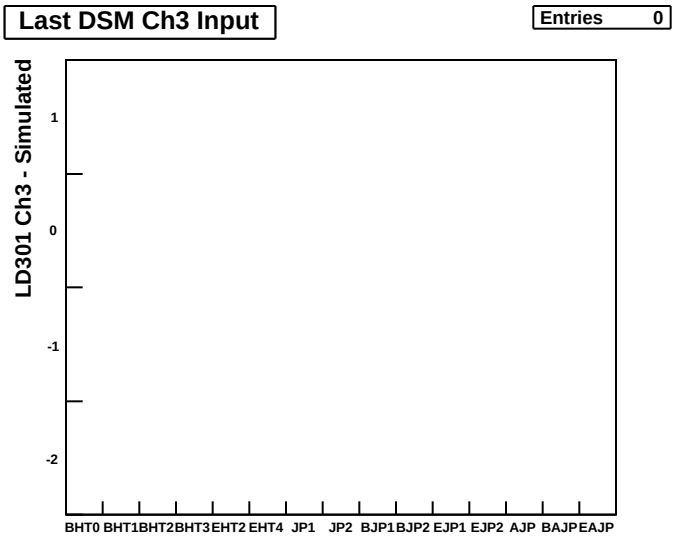

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2000000
HT01 bits/Partial JP ID
6860 7413 5252 7958 5698 5473 185155 189182
9976 10586 8859 9909 9368 8975 64845 60818
233164 232001 235789 233043 234934 235552
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

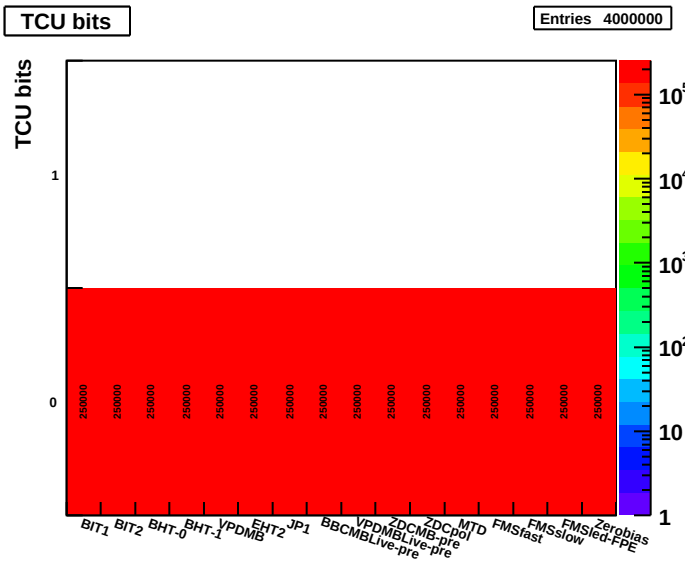

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

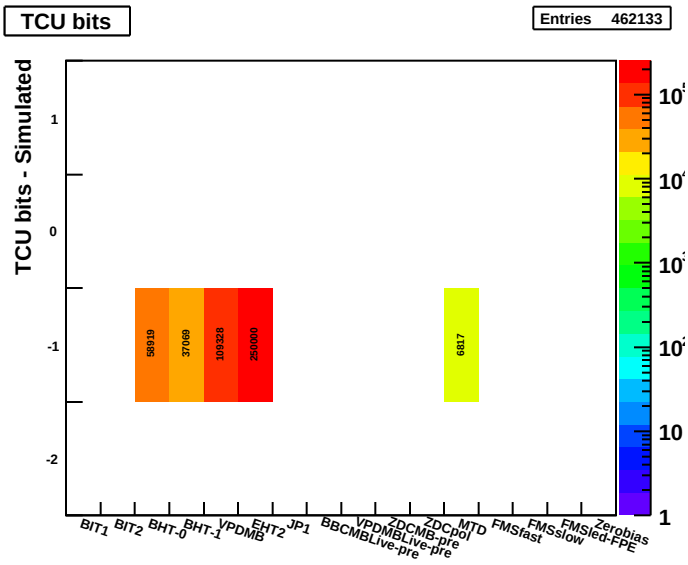

EMC L2 Input - HT23 bits
Entries 2000000
HT23 bits
1211 1155 844 1196 811 918 250000 250000
3760 4206 2804 4588 3167 2960
245029 244639 246352 244216 246022 246122
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

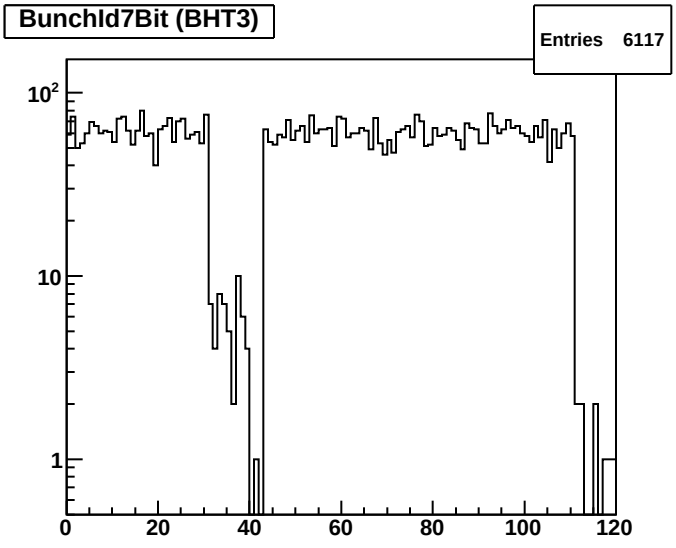

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

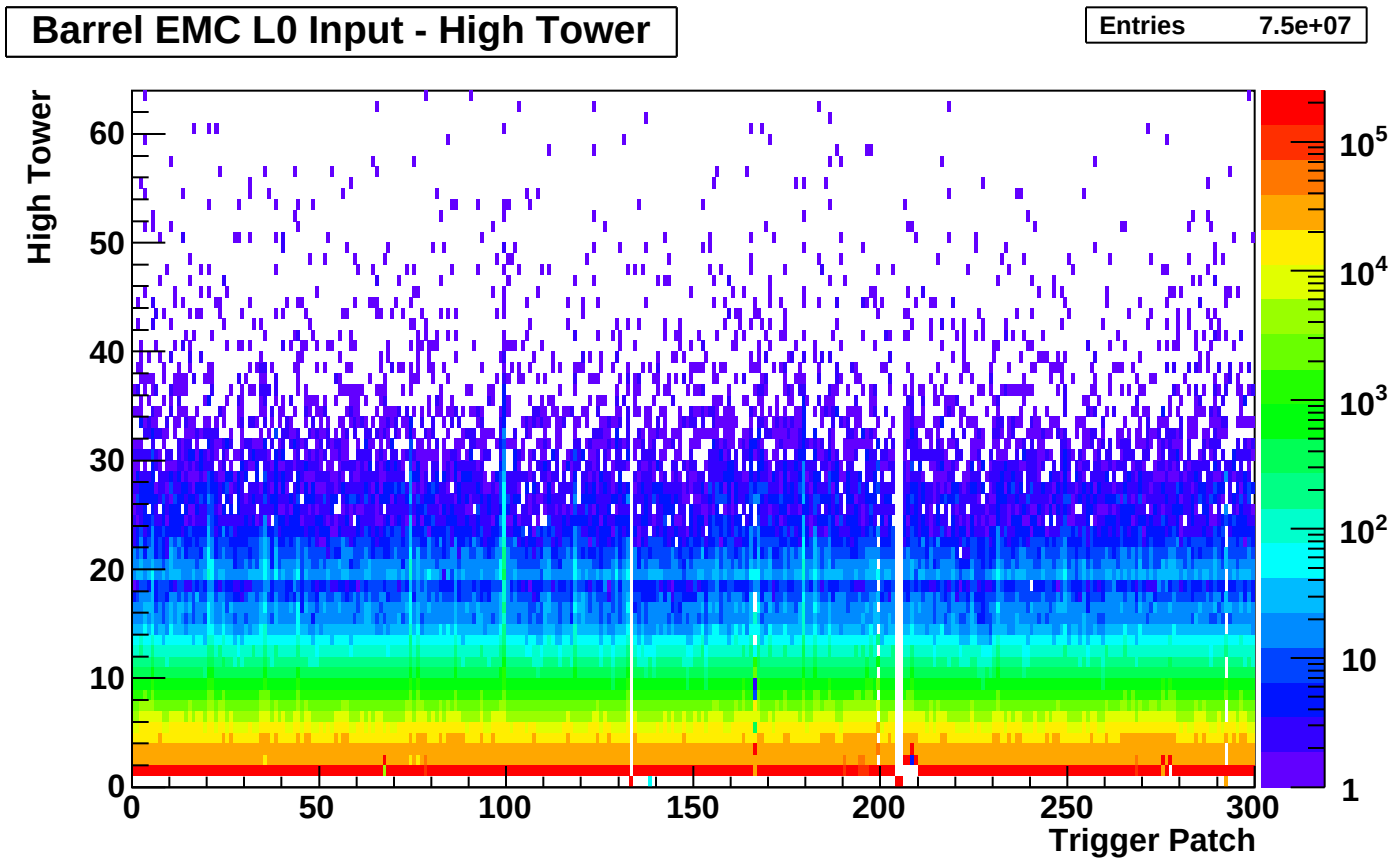

Last DSM Ch3 Input
Entries 3750000
LD301 Ch3
68604 37069 27384 6117 250000 250000 250000 250000 102539 97382 250000 250000 102124 100716 101022
181396 212931 222616 243883 147461 152618 147876 149284 149978
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

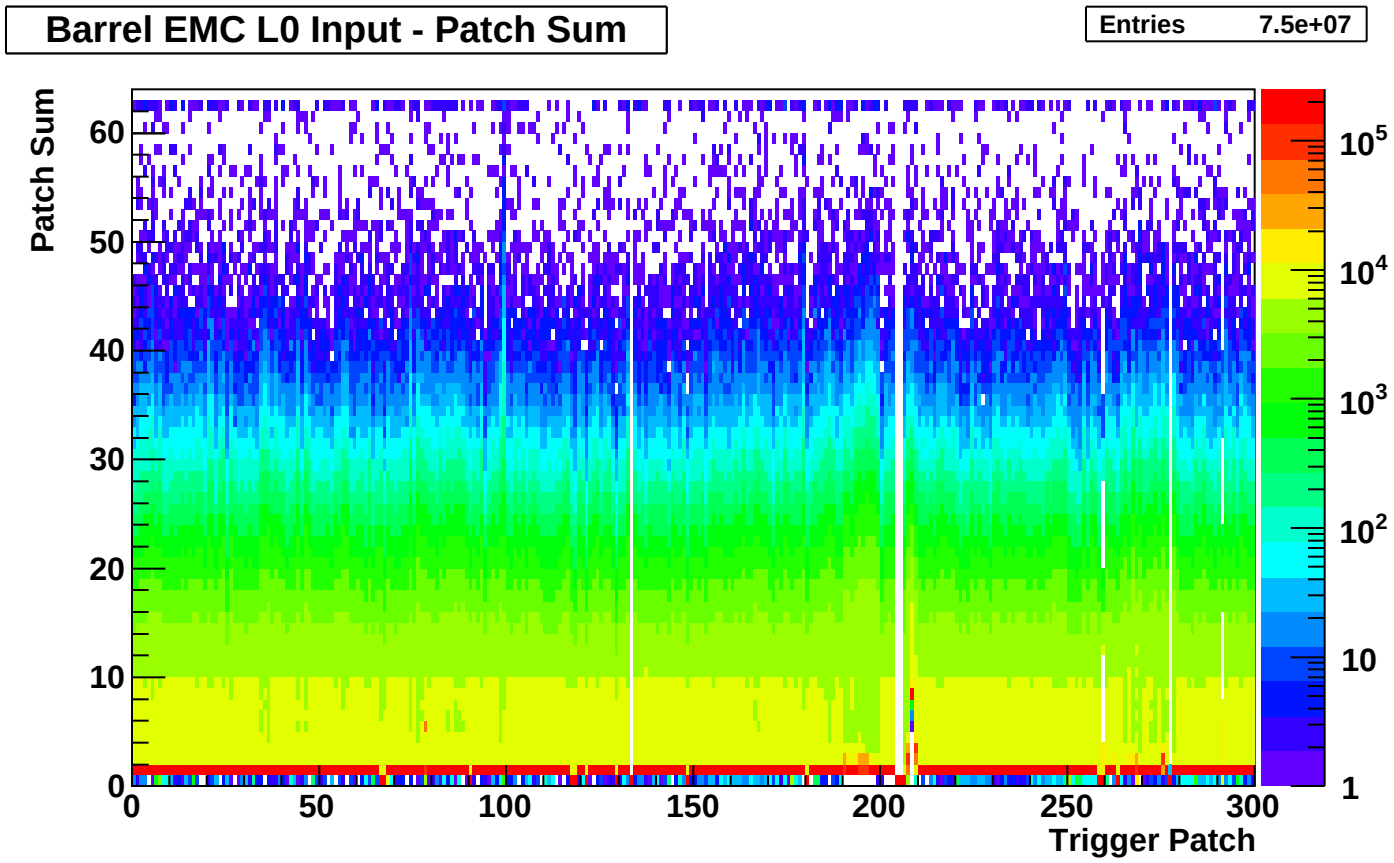

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

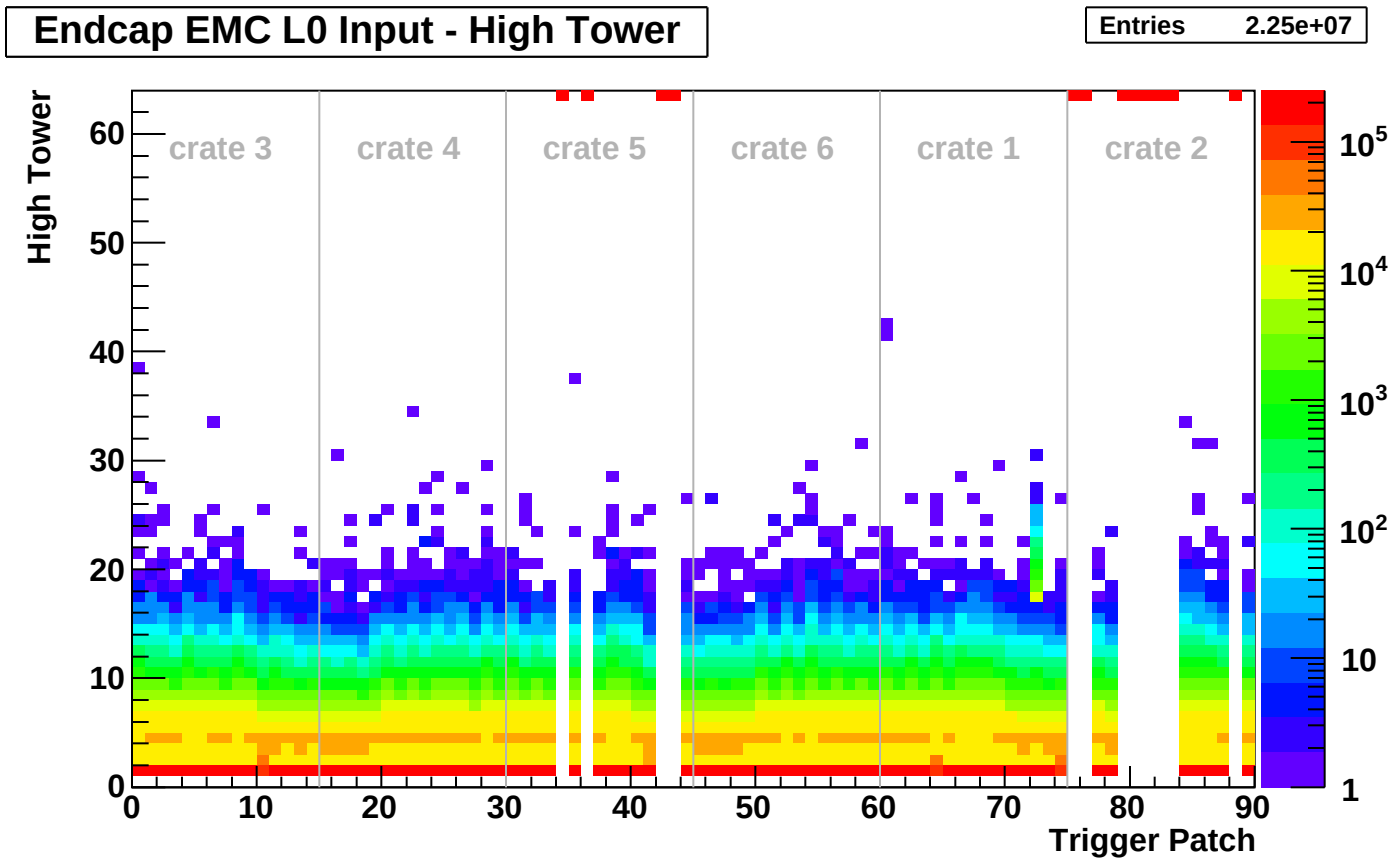

TCU bits
Entries 4000000
TCU bits
250000 250000 250000 250000 250000 250000 250000 250000 250000 250000 250000 250000 250000 250000 250000 250000
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

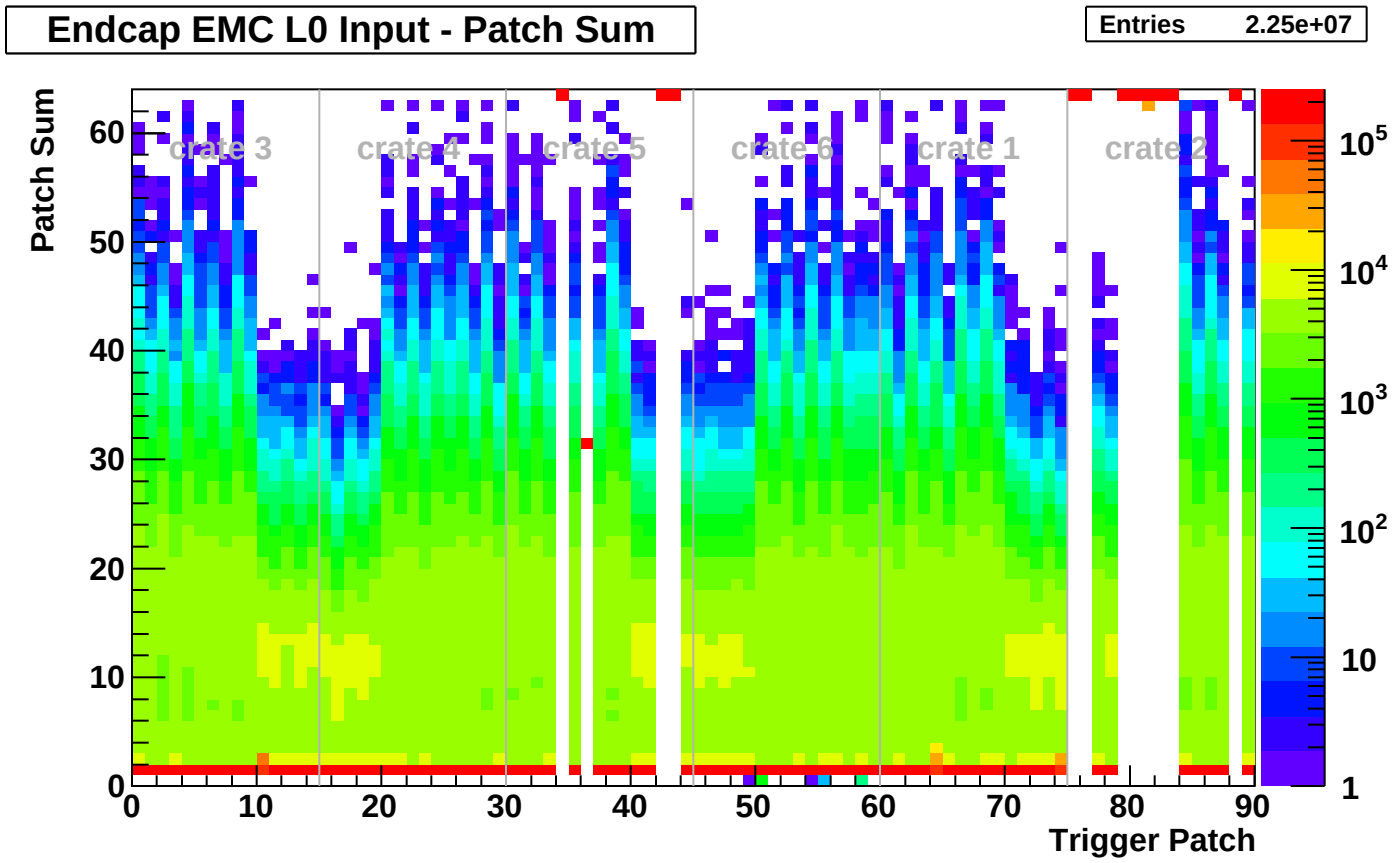

TCU bits
Entries 462133
TCU bits - Simulated
58919 37069 109328 250000 6817
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

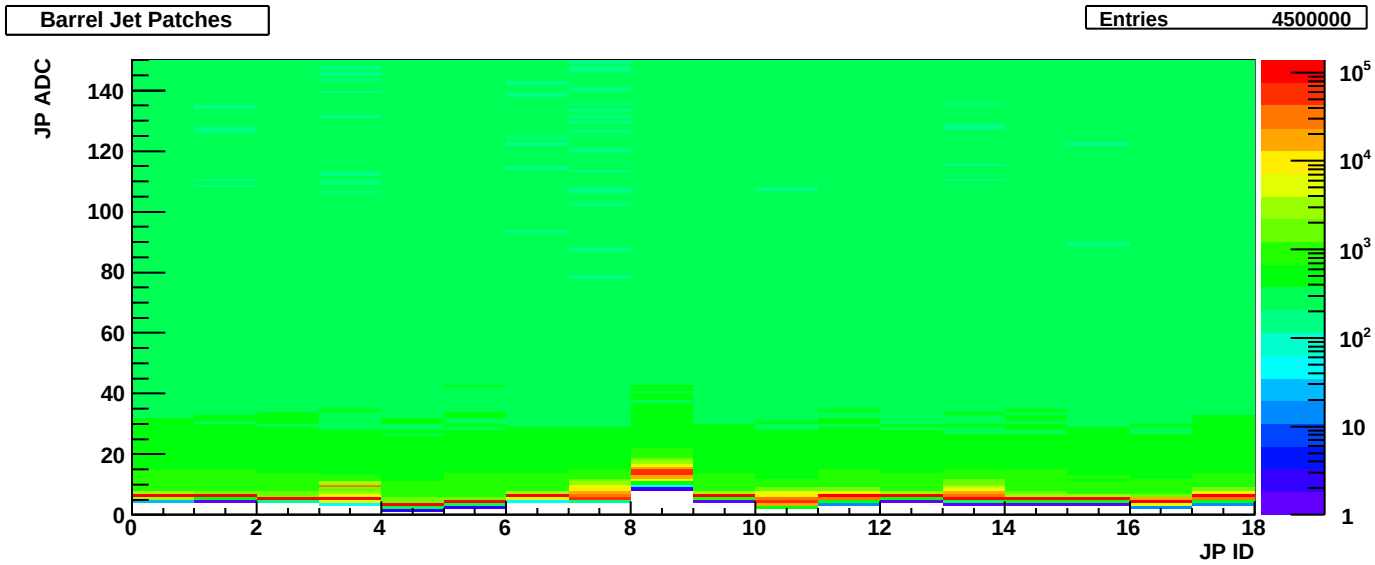

BunchId7Bit (BHT3)
Entries 6117

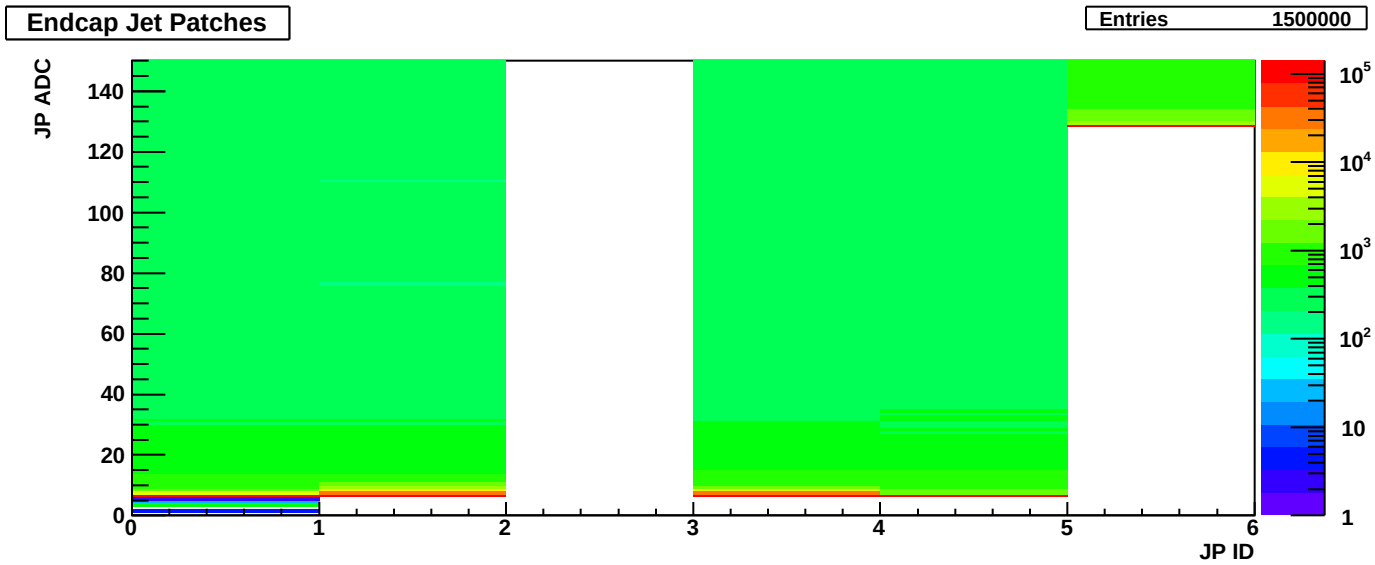

Barrel EMC L0 Input - High Tower
Entries 7.5e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

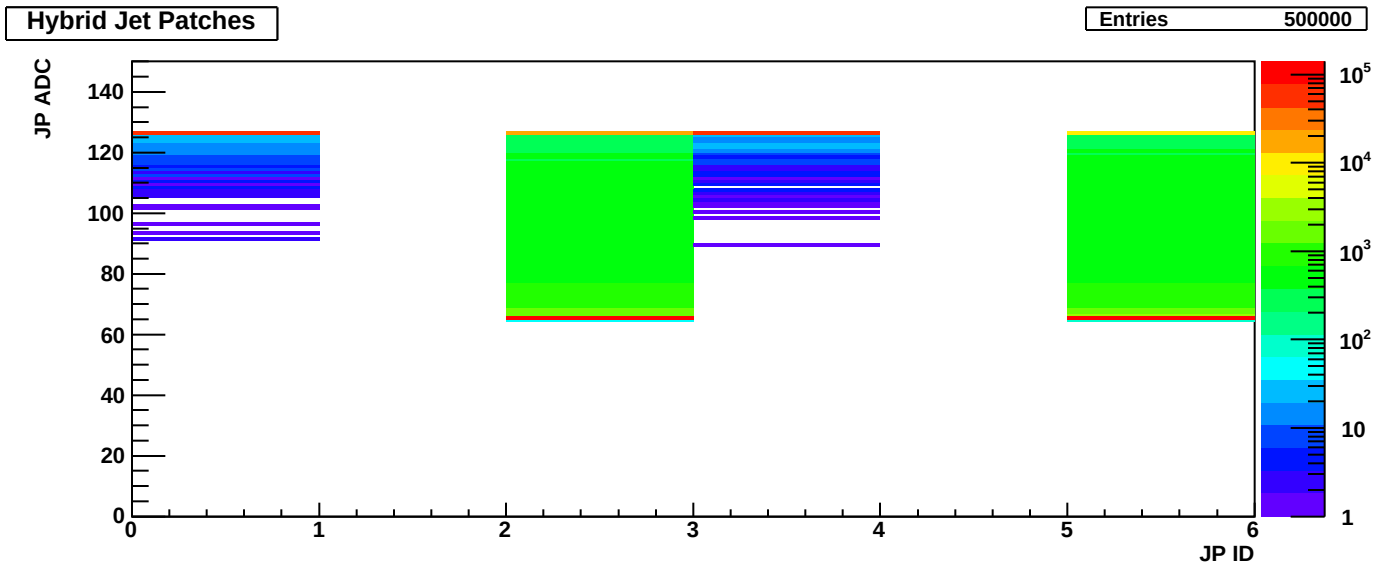

Barrel EMC L0 Input - Patch Sum
Entries 7.5e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

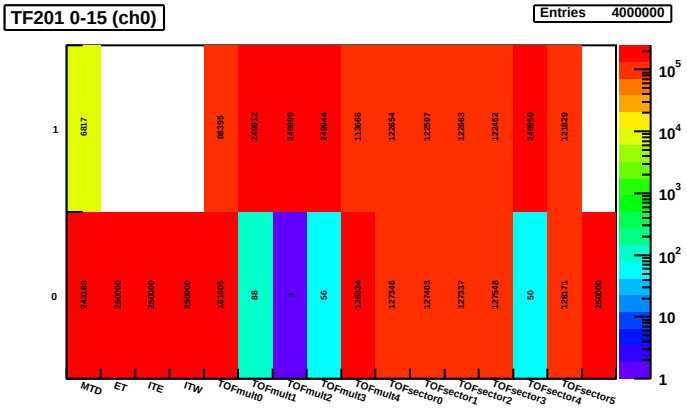

Endcap EMC L0 Input - High Tower
Entries 2.25e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

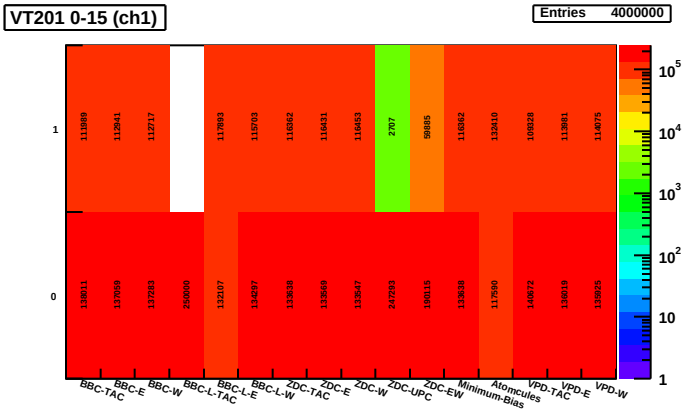

Endcap EMC L0 Input - Patch Sum
Entries 2.25e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

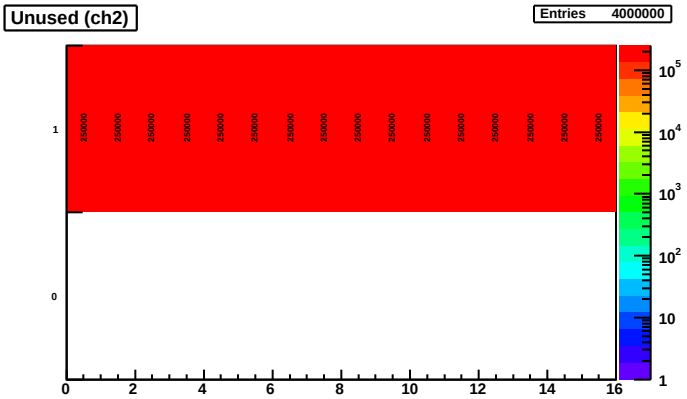

Barrel Jet Patches
Entries 4500000
JP ADC
JP ID

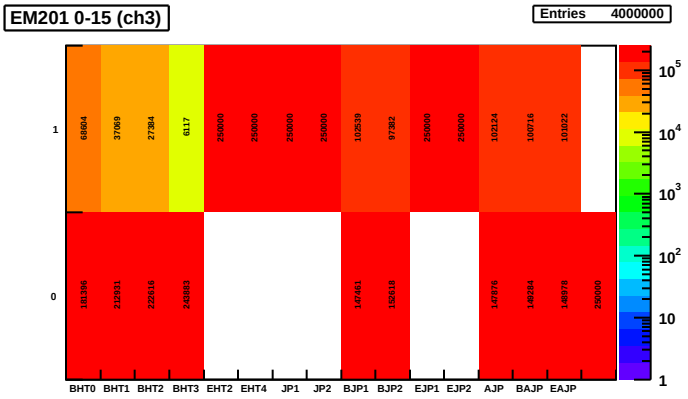

Endcap Jet Patches
Entries 1500000
JP ADC
JP ID

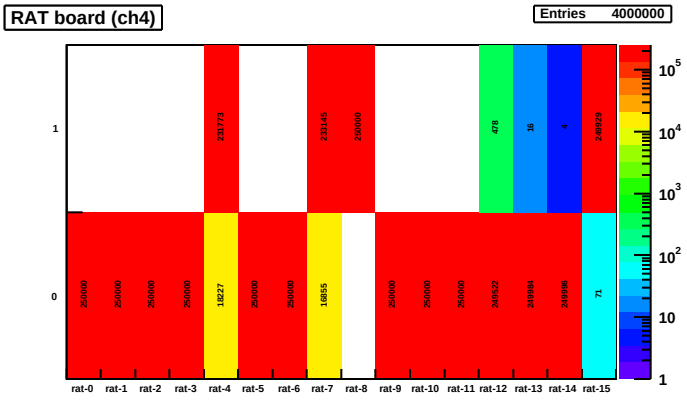

Hybrid Jet Patches
Entries 500000
JP ADC
JP ID

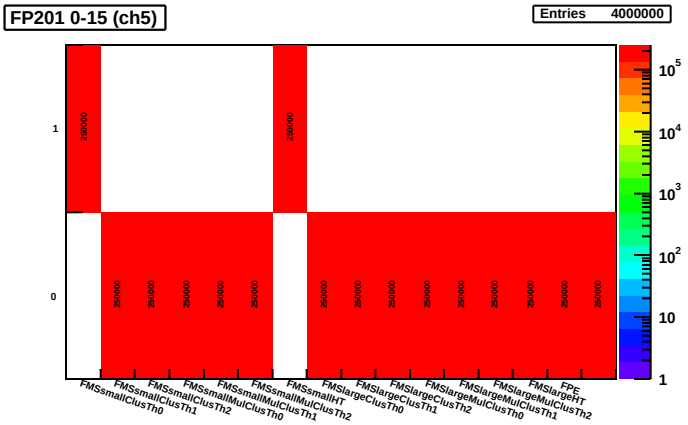

TF201 0-15 (ch0)
Entries 4000000
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

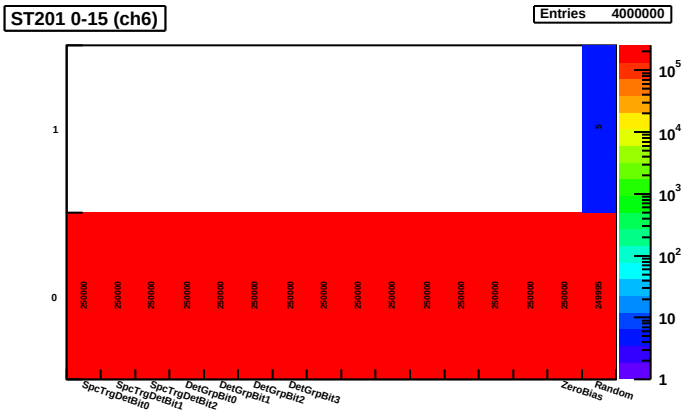

VT201 0-15 (ch1)
Entries 4000000
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

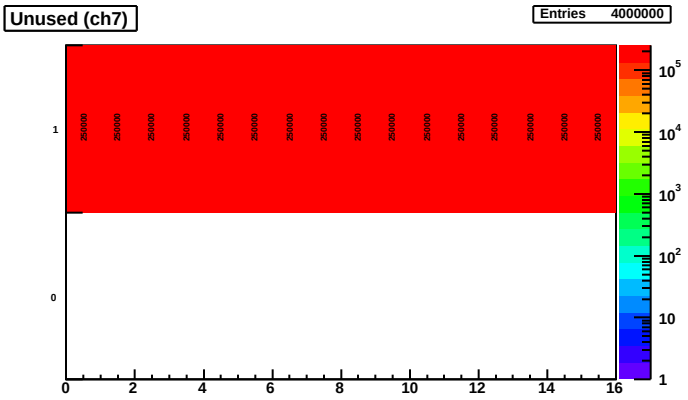

Unused (ch2)
Entries 4000000

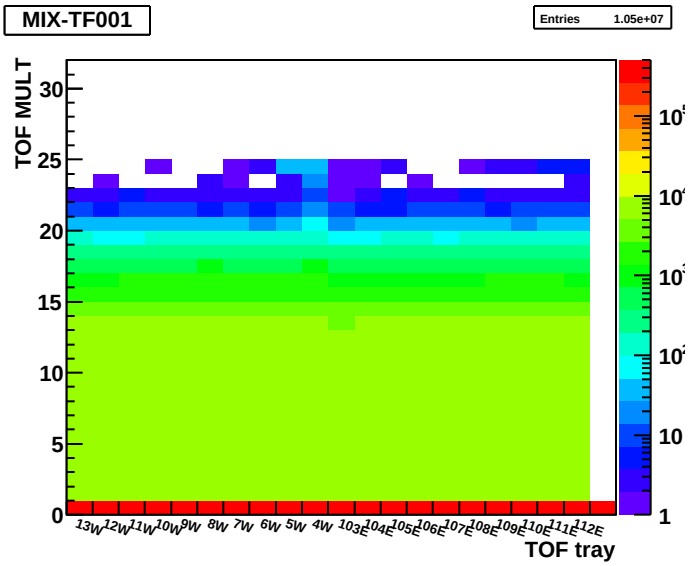

EM201 0-15 (ch3)
Entries 4000000
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP

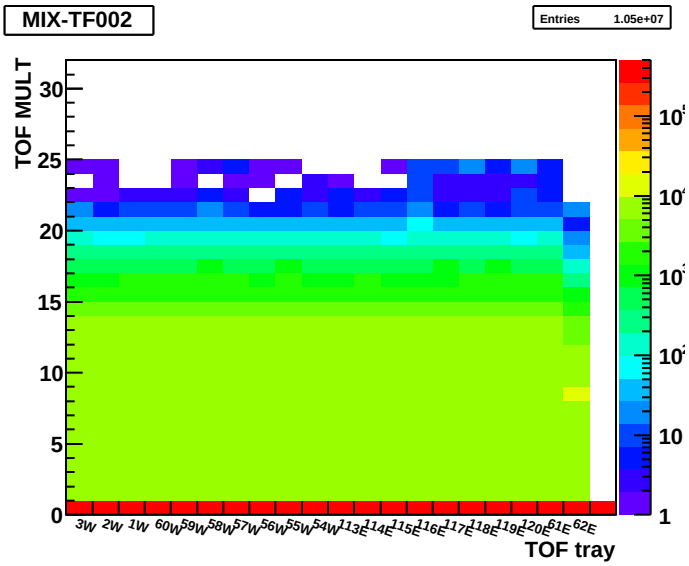

RAT board (ch4)
Entries 4000000
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

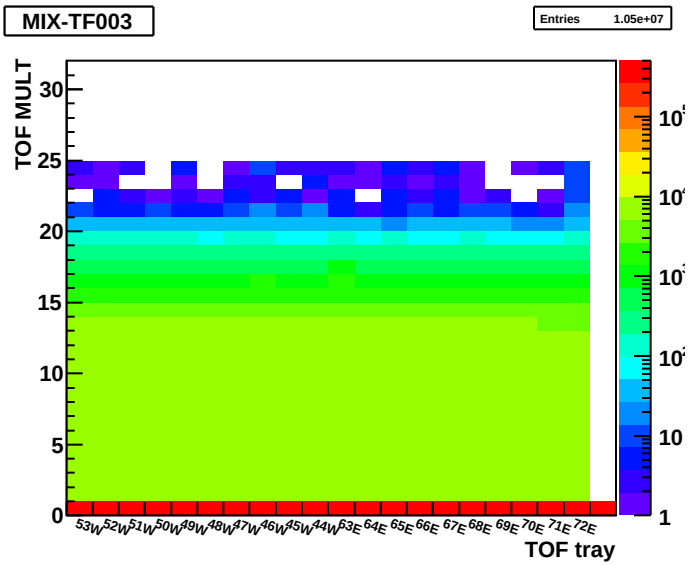

FP201 0-15 (ch5)
Entries 4000000
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

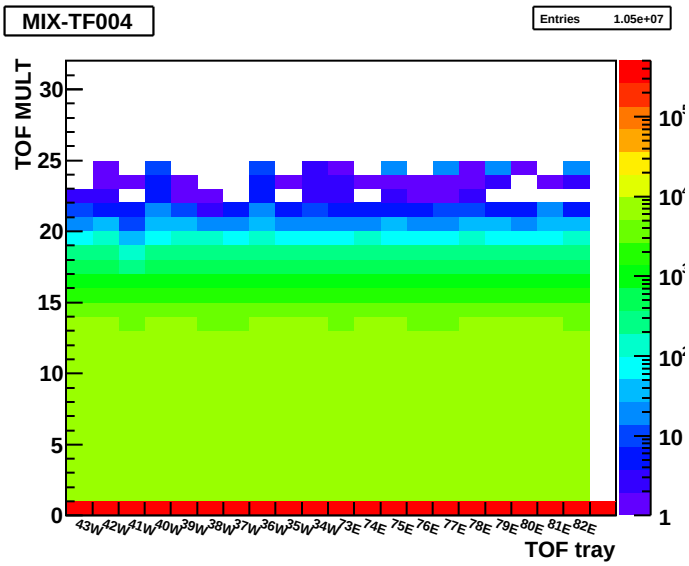

ST201 0-15 (ch6)
Entries 4000000
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

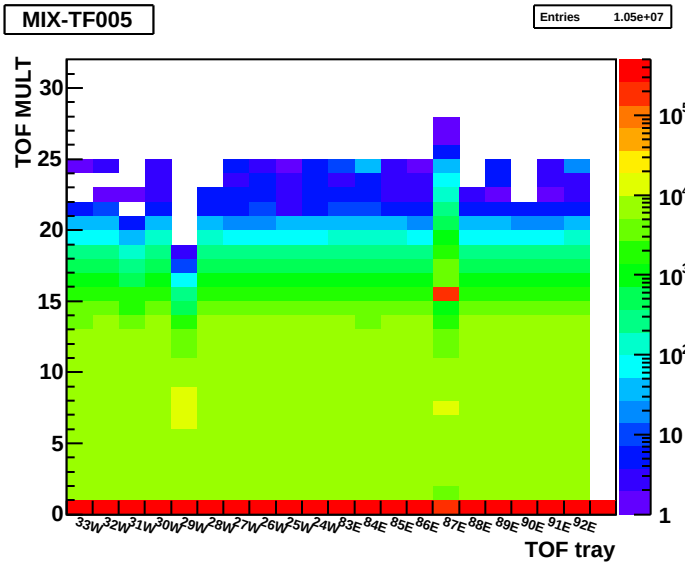

Unused (ch7)
Entries 4000000

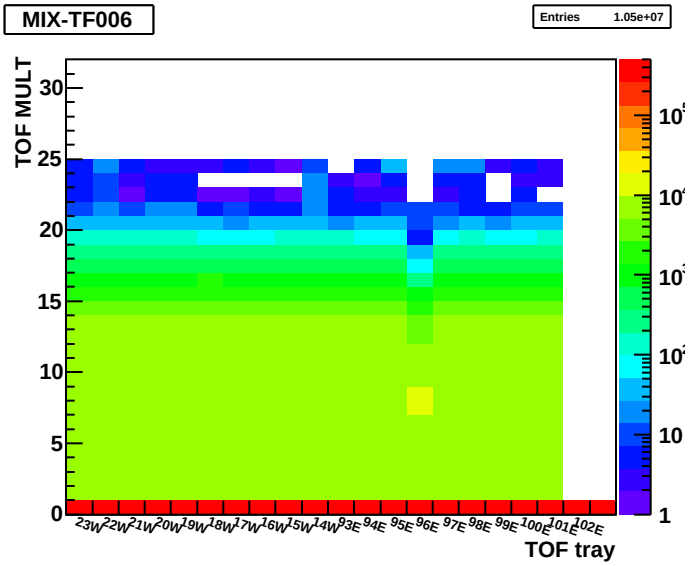

MIX-TF001
Entries 1.05e+07
TOF MULT
TOF tray

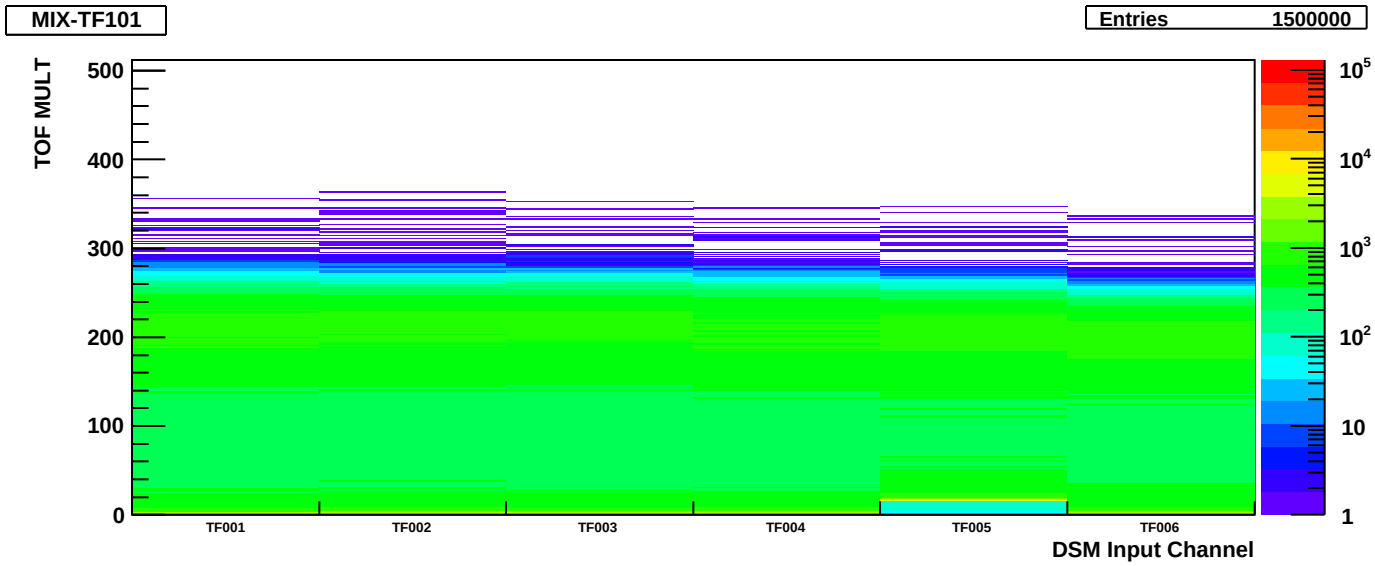

MIX-TF002
Entries 1.05e+07
TOF MULT
TOF tray

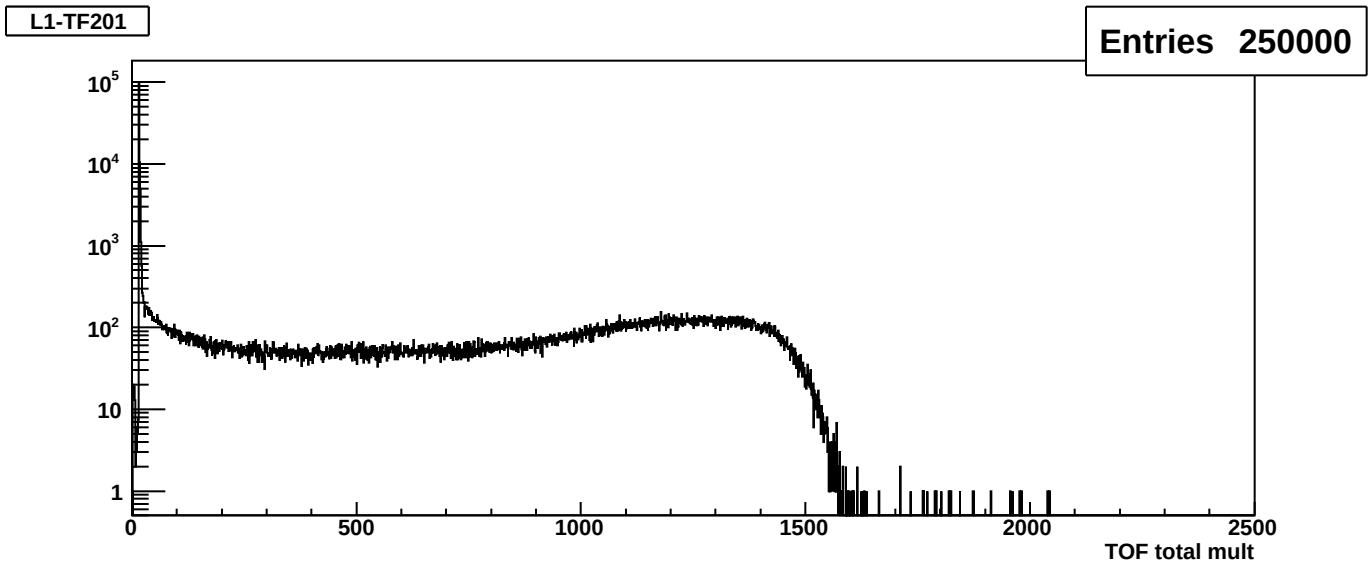

MIX-TF003
Entries 1.05e+07
TOF MULT
TOF tray

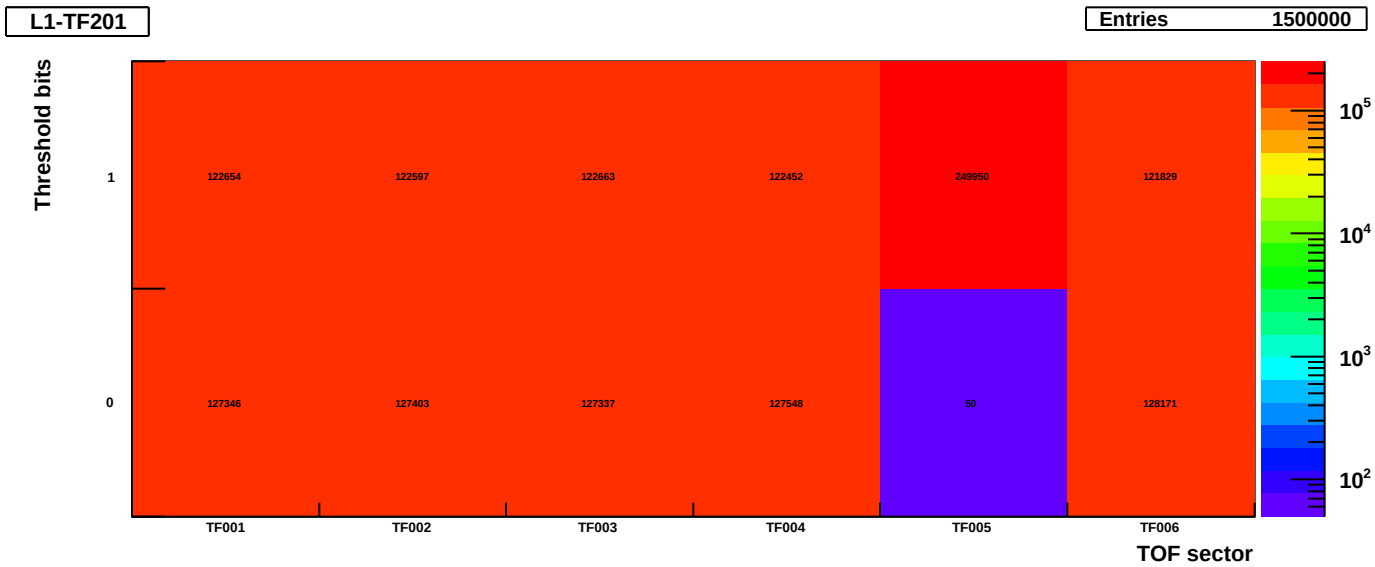

MIX-TF004
Entries 1.05e+07
TOF MULT
TOF tray

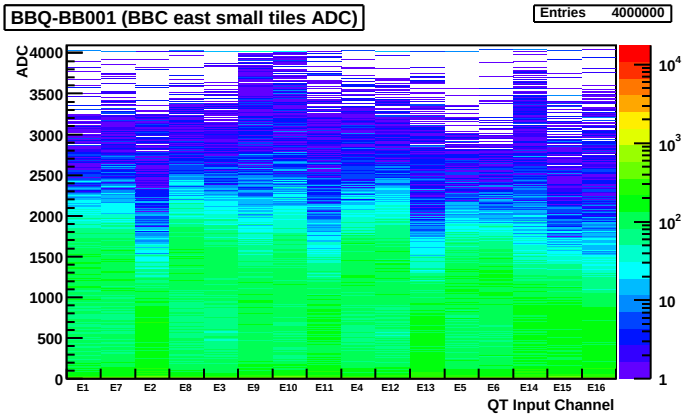

MIX-TF005
Entries 1.05e+07
TOF MULT
TOF tray

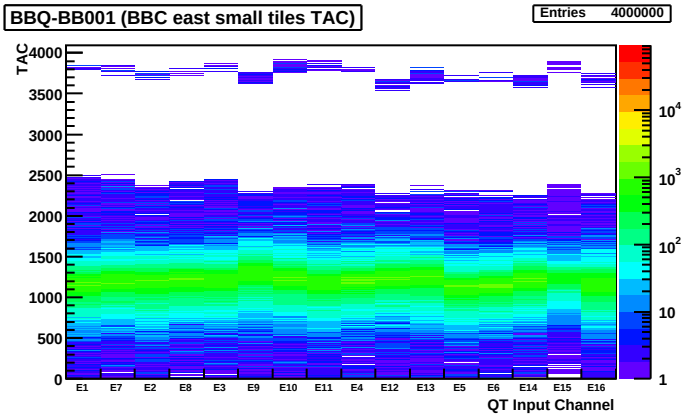

MIX-TF006
Entries 1.05e+07
TOF MULT
TOF tray

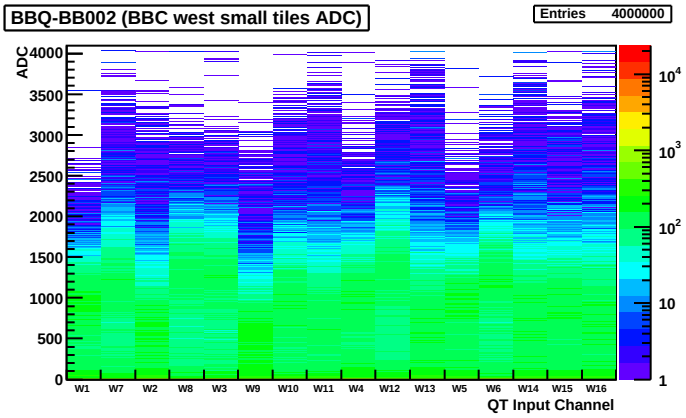

MIX-TF101
Entries 1500000
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

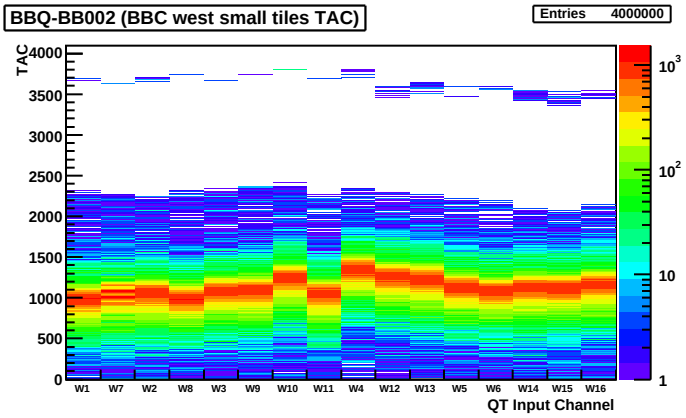

L1-TF201
Entries 250000
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

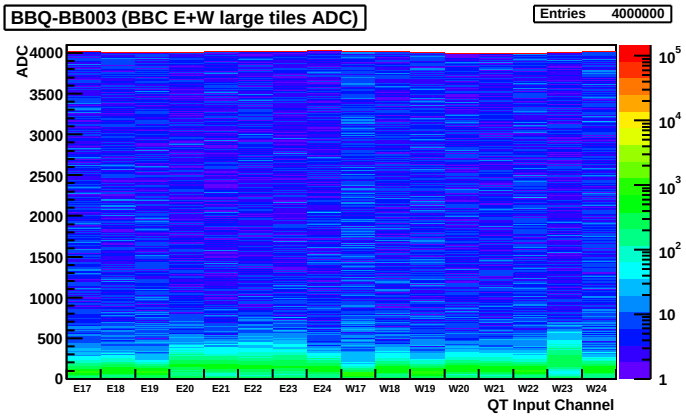

L1-TF201
Entries 1500000
Threshold bits
1
0
122654
122597
122663
122452
249950
121829
127346
127403
127337
127548
50
128171
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3
10^2

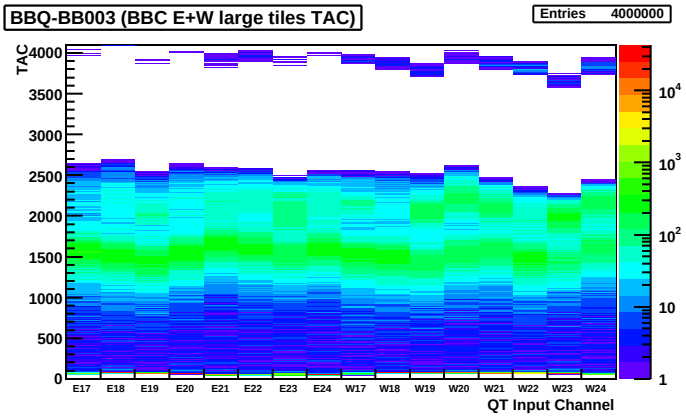

BBQ-BB001 (BBC east small tiles ADC)
Entries 4000000
ADC
QT Input Channel

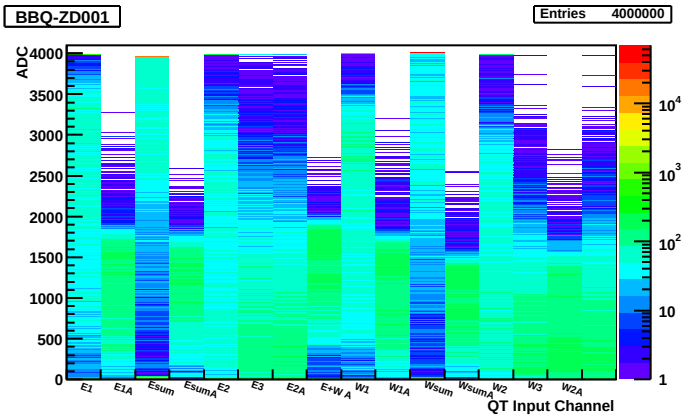

BBQ-BB001 (BBC east small tiles TAC)
Entries 4000000
TAC
QT Input Channel

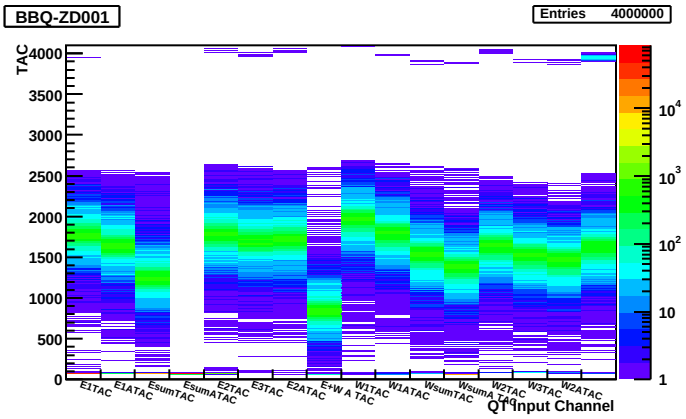

BBQ-BB002 (BBC west small tiles ADC)
Entries 4000000
ADC
QT Input Channel

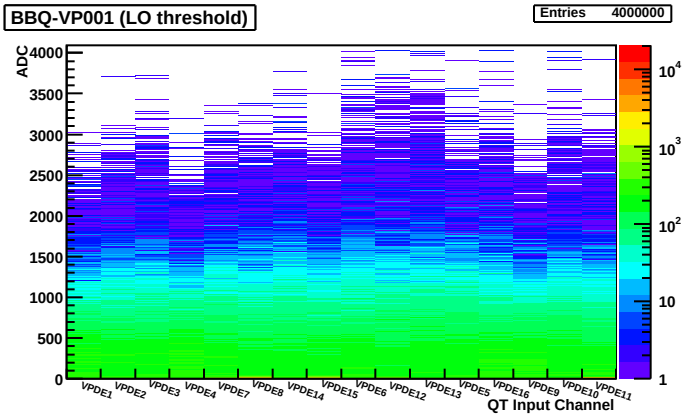

BBQ-BB002 (BBC west small tiles TAC)
Entries 4000000
TAC
QT Input Channel

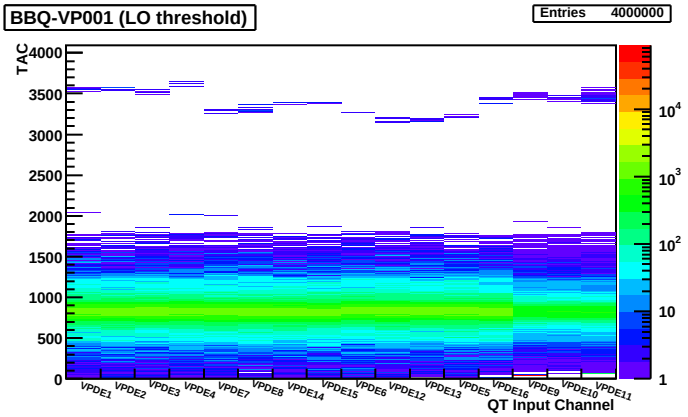

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4000000
ADC
QT Input Channel

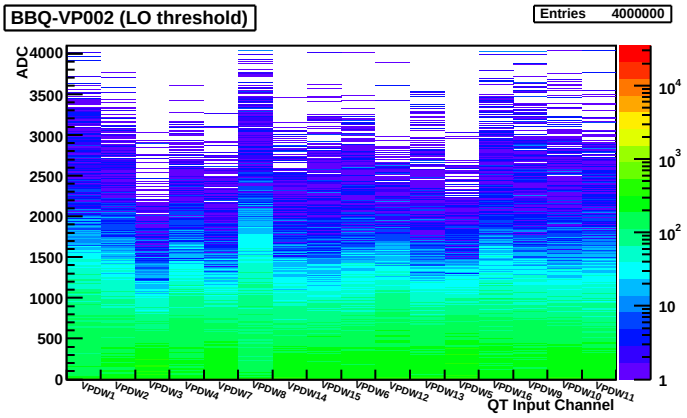

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4000000
TAC
QT Input Channel

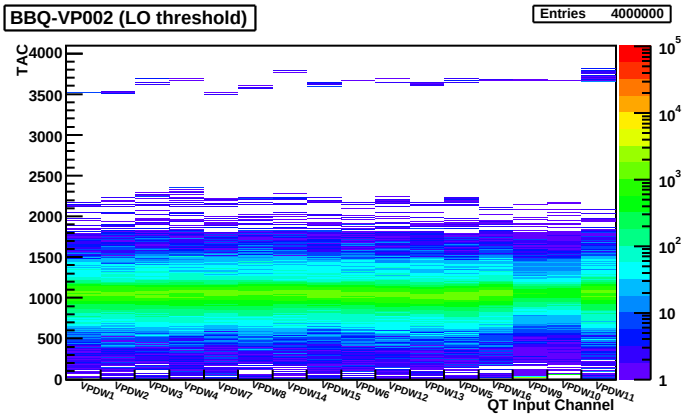

BBQ-ZD001
Entries 4000000
ADC
QT Input Channel

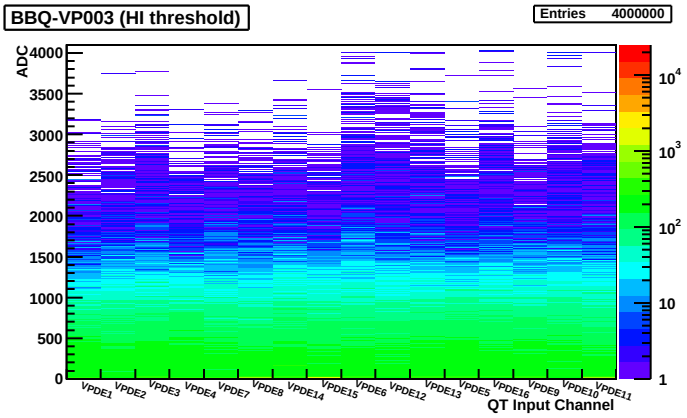

BBQ-ZD001
Entries 4000000
TAC
QT Input Channel

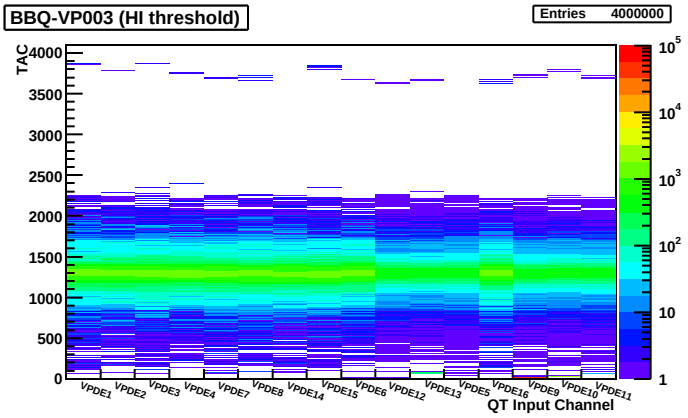

BBQ-VP001 (LO threshold)
Entries 4000000
ADC
QT Input Channel

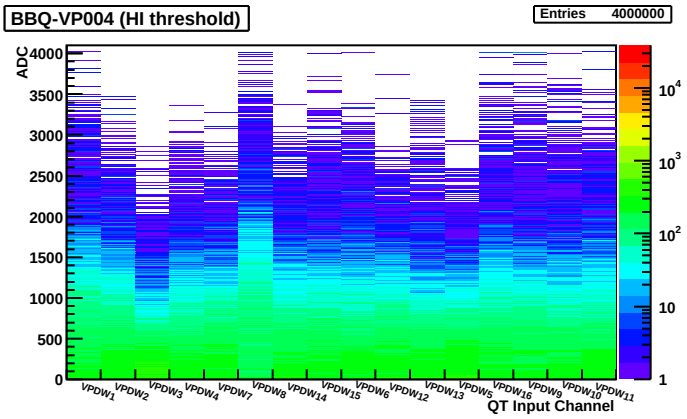

BBQ-VP001 (LO threshold)
Entries 4000000
TAC
QT Input Channel

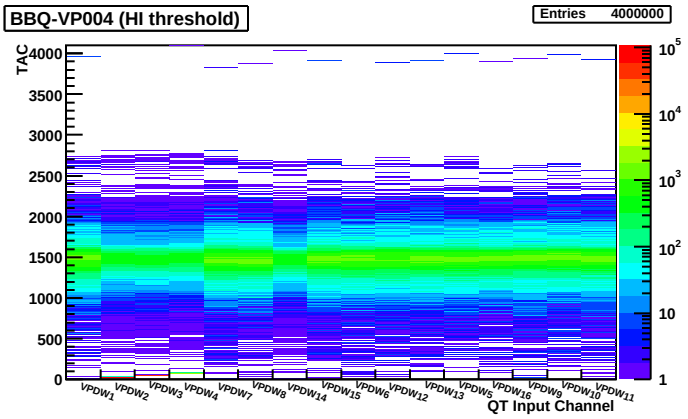

BBQ-VP002 (LO threshold)
Entries 4000000
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 4000000
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4000000
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4000000
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4000000
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4000000
TAC
QT Input Channel