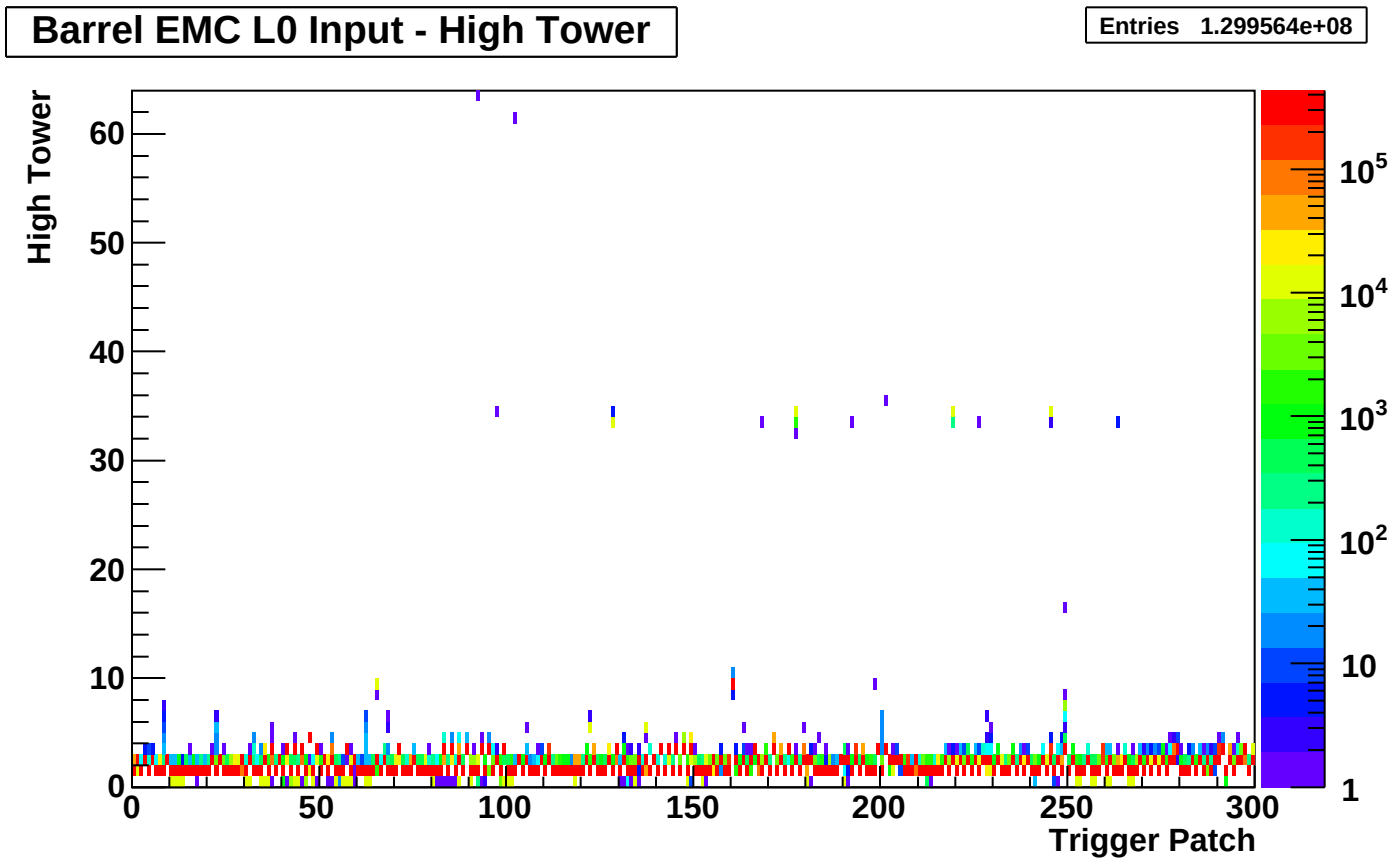
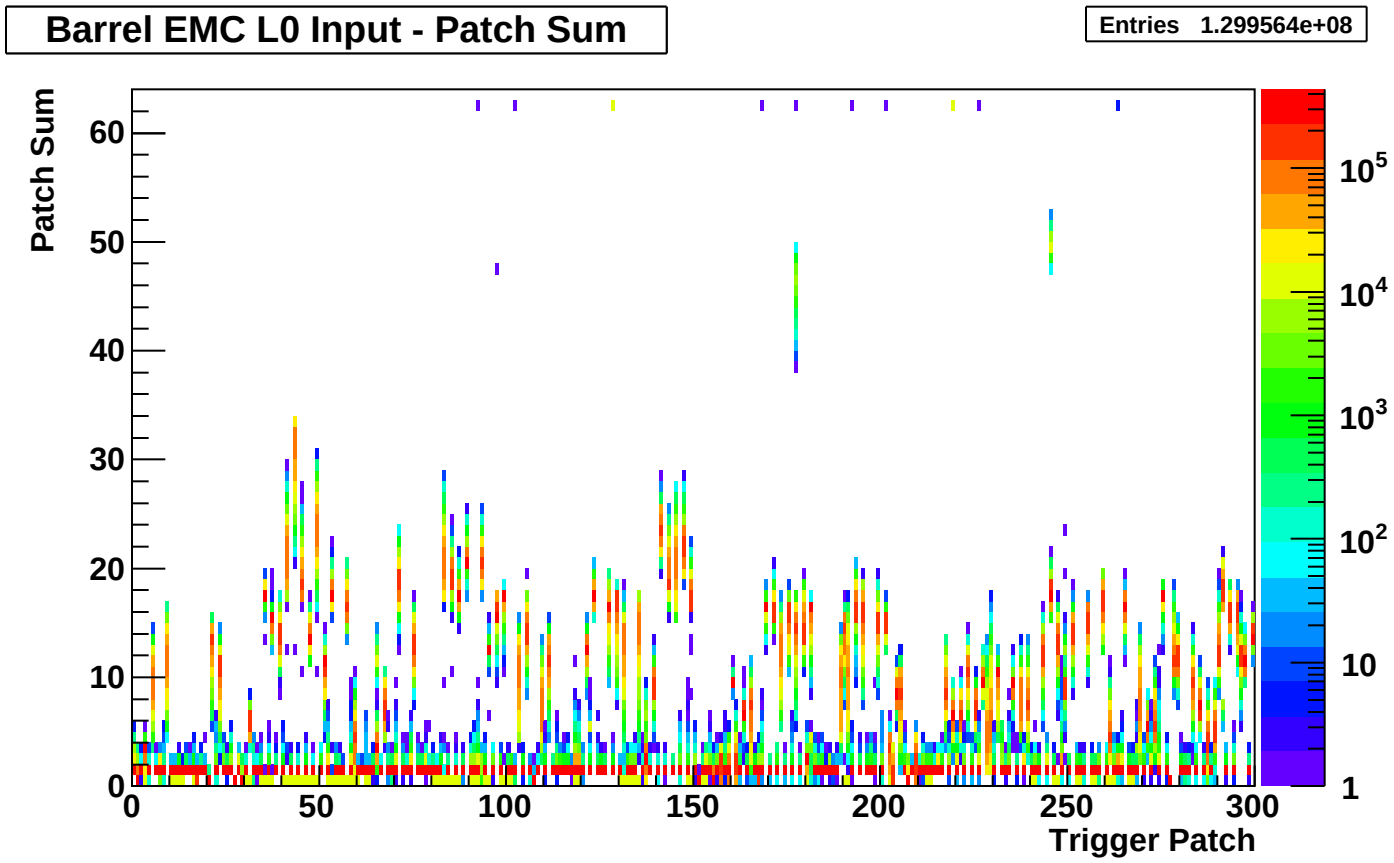
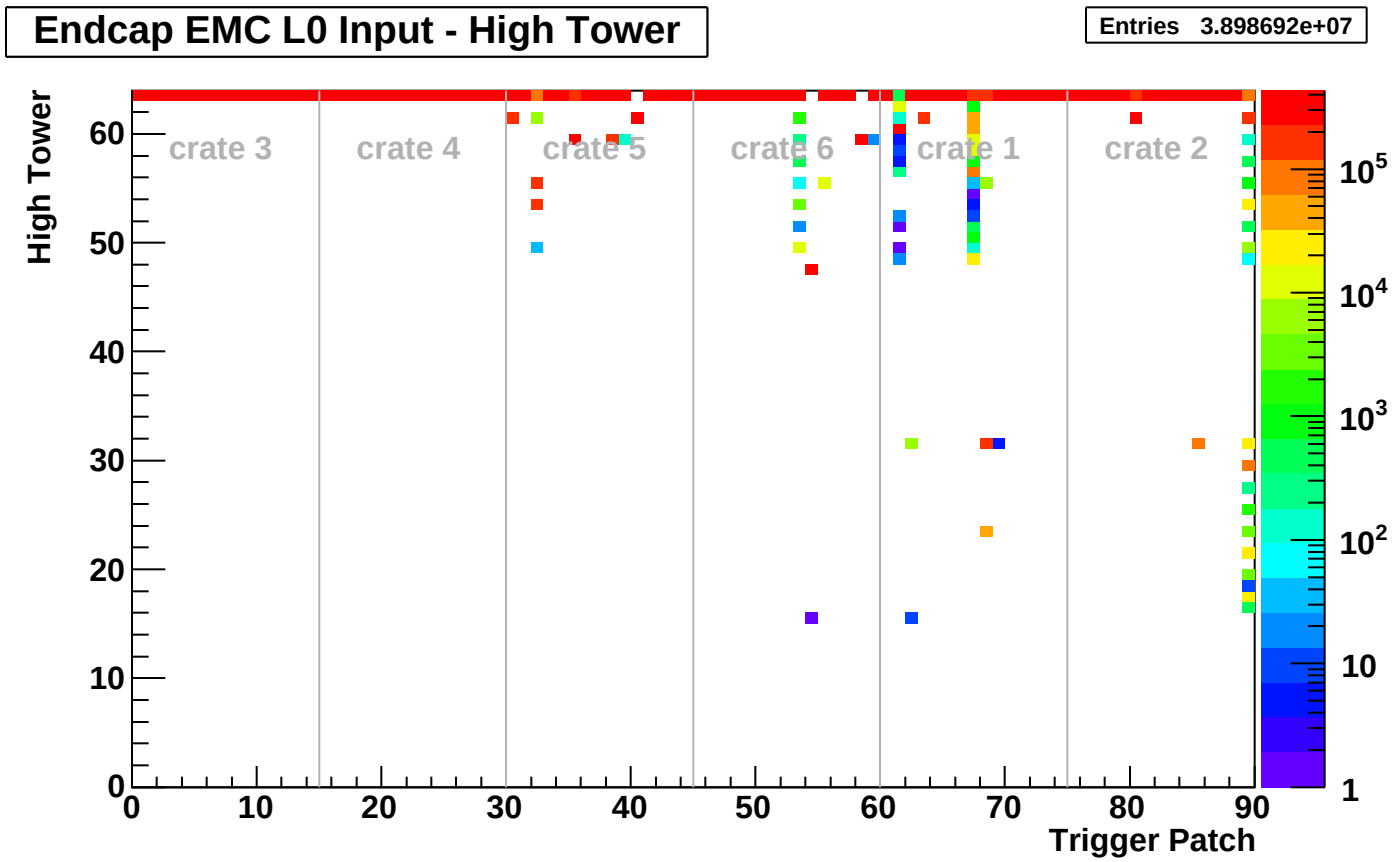
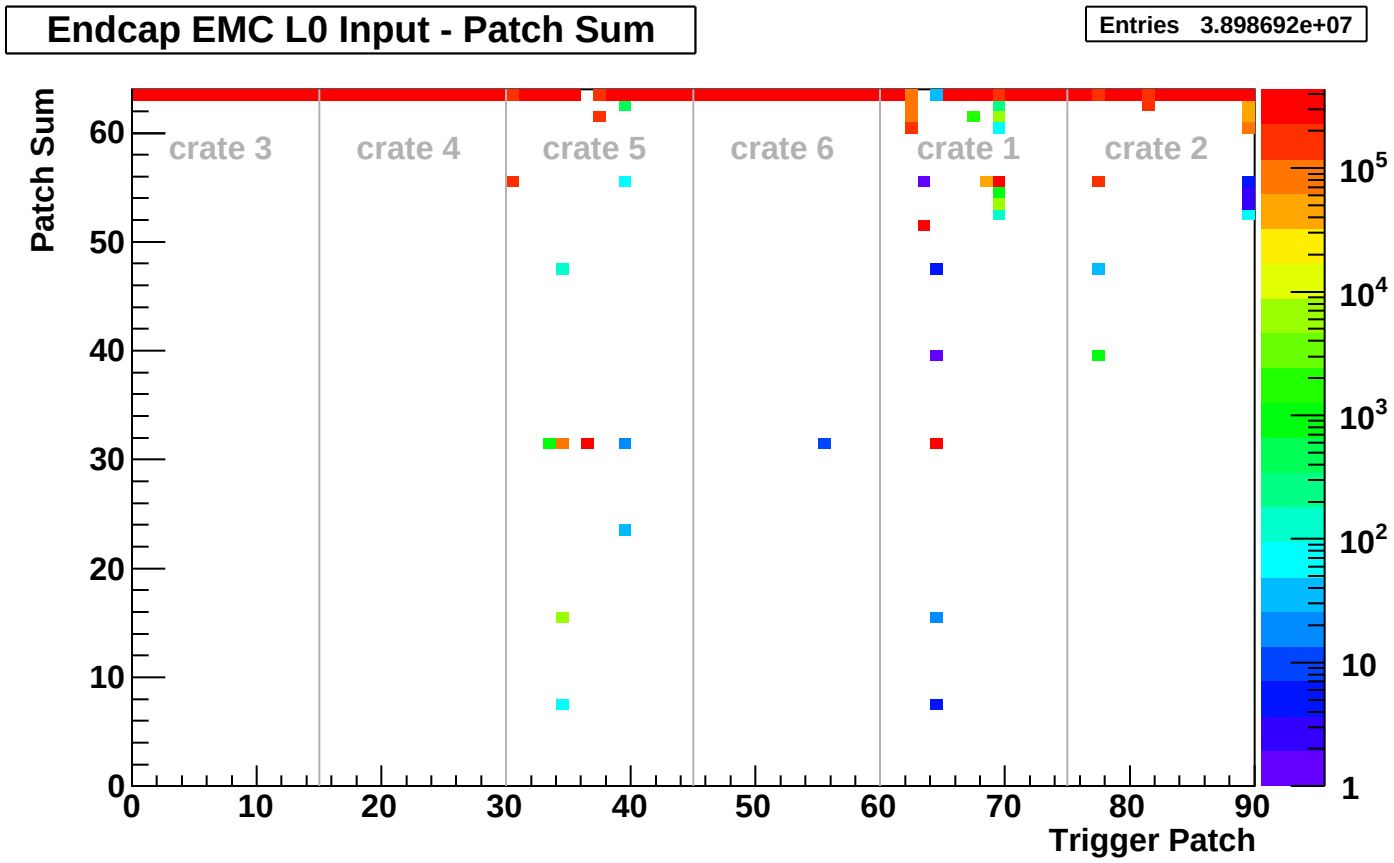
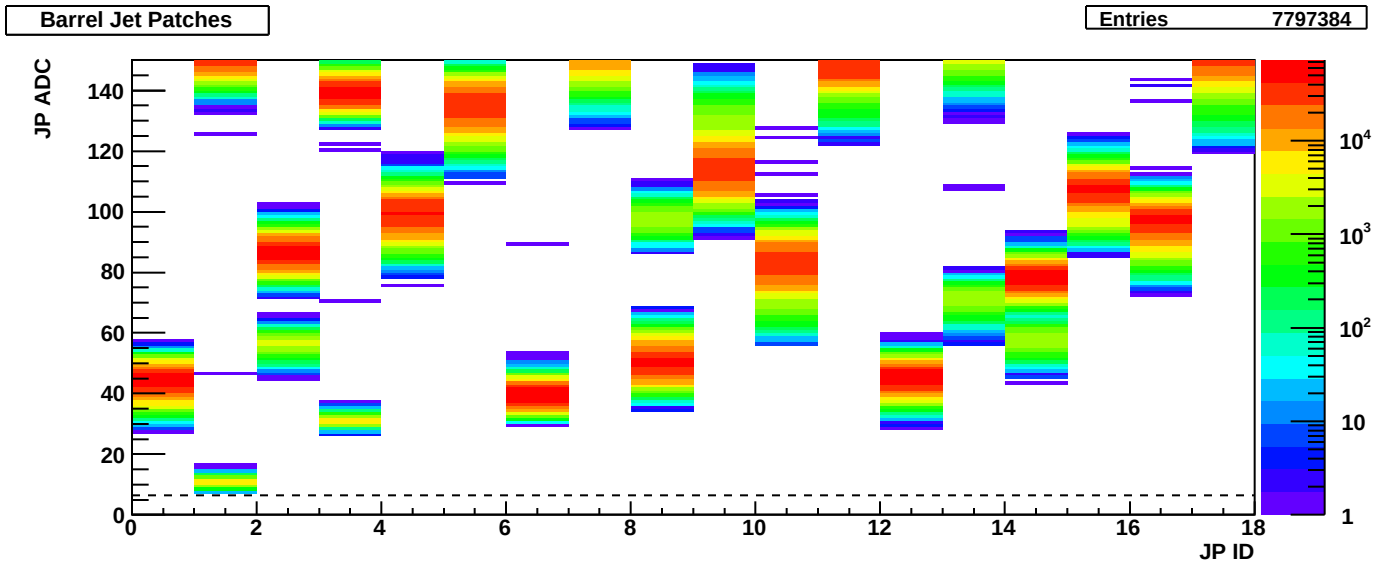
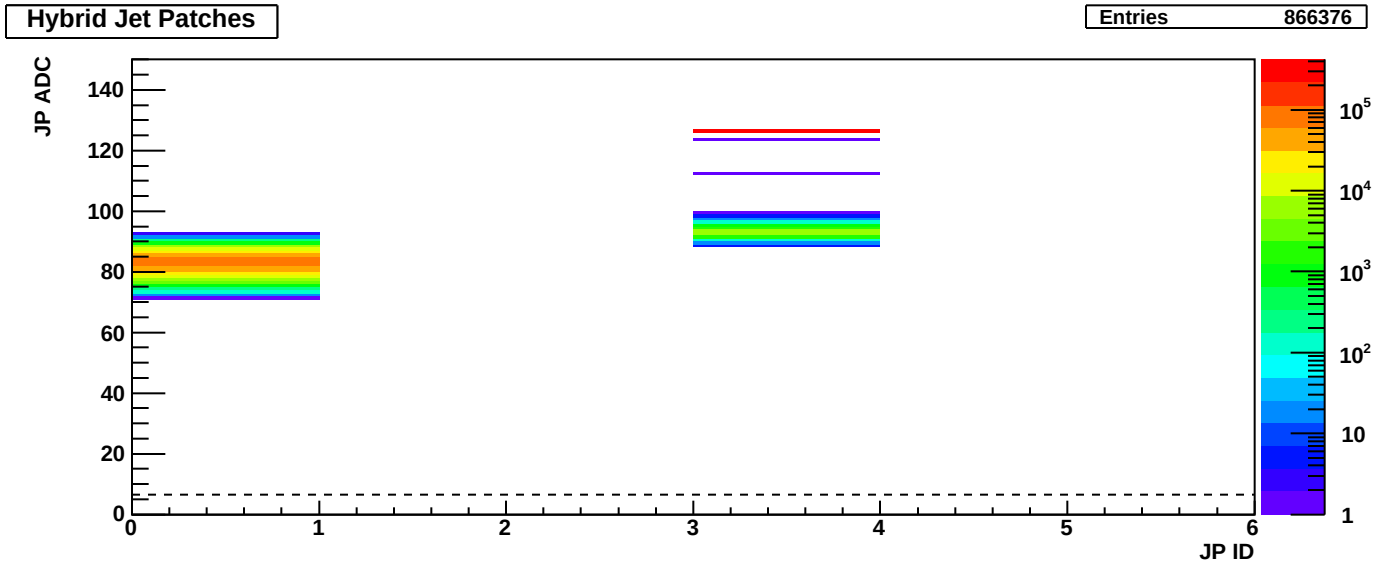

Barrel EMC L0 Input - High Tower
Entries 1.299564e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

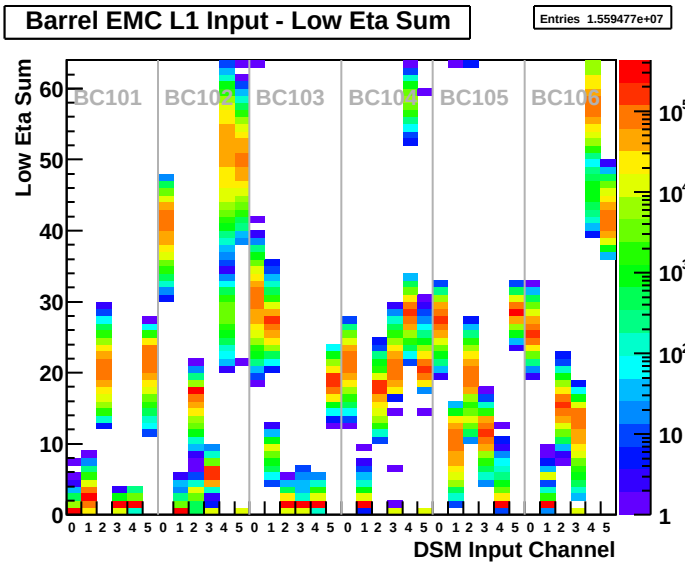

Barrel EMC L0 Input - Patch Sum
Entries 1.299564e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

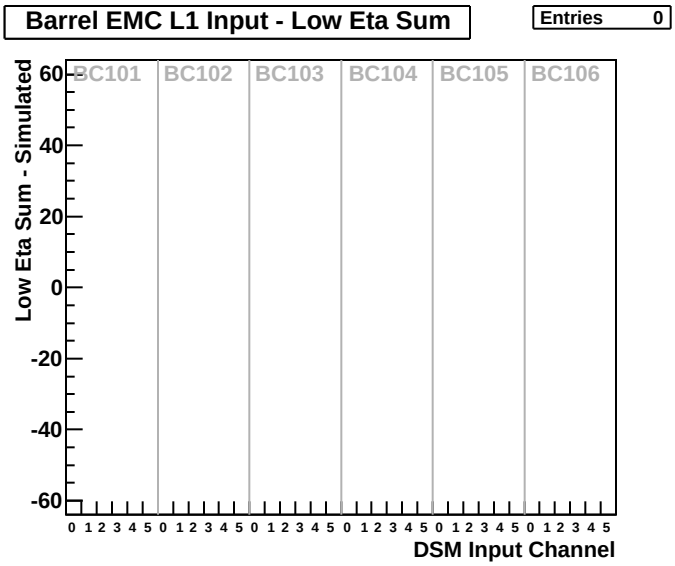

Endcap EMC L0 Input - High Tower
Entries 3.898692e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

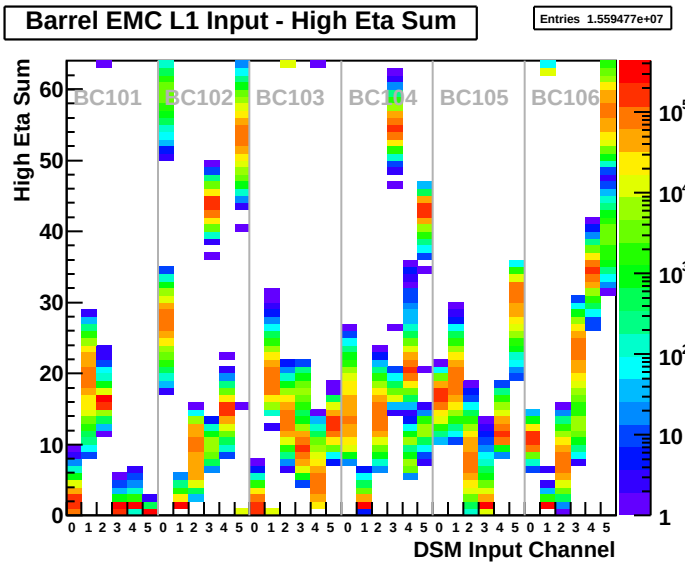

Endcap EMC L0 Input - Patch Sum
Entries 3.898692e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

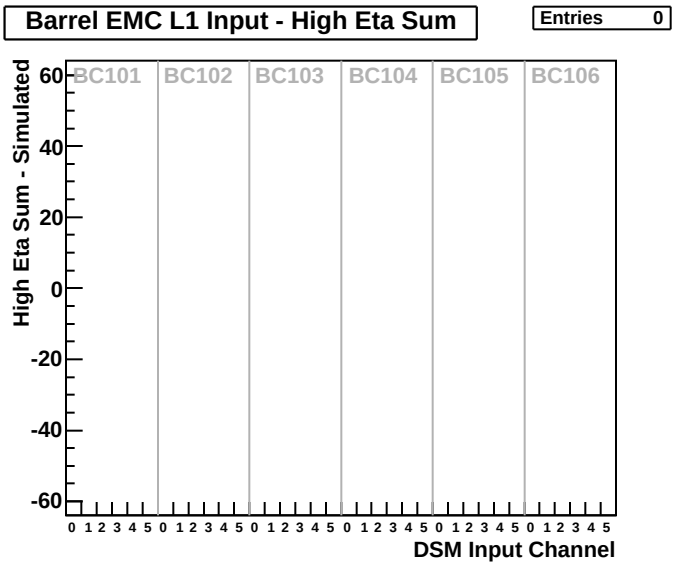

Barrel Jet Patches
Entries 7797384
JP ADC
JP ID

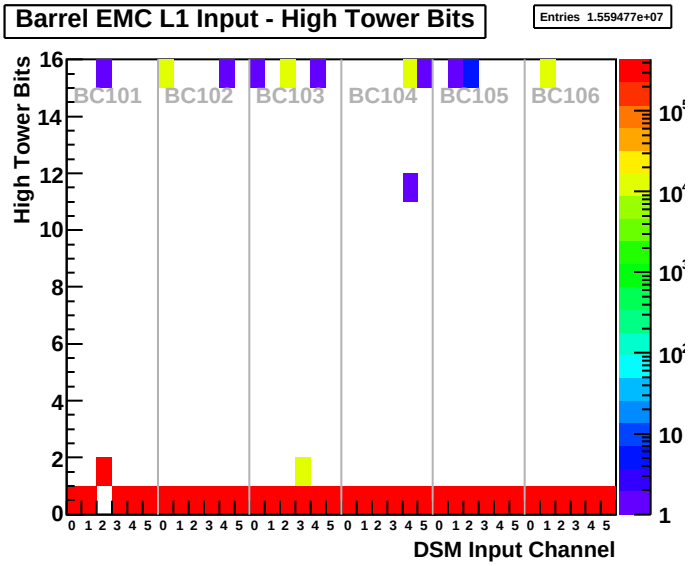

Hybrid Jet Patches
Entries 866376
JP ADC
JP ID

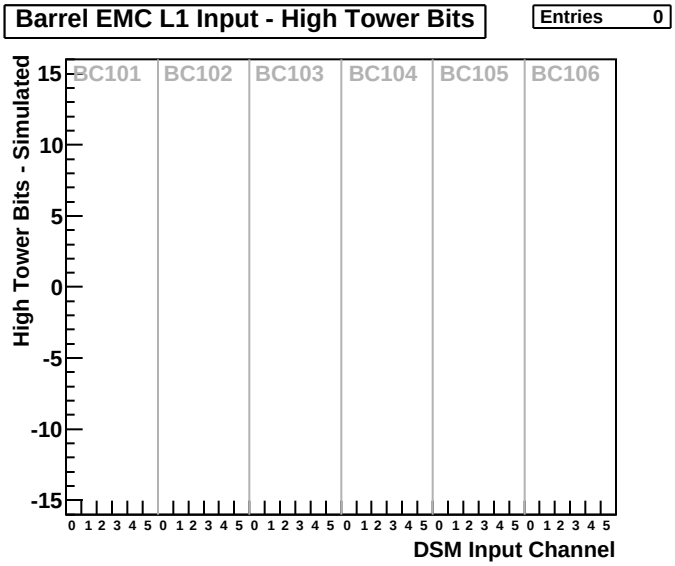

Barrel EMC L1 Input - Low Eta Sum
Entries 1.559477e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

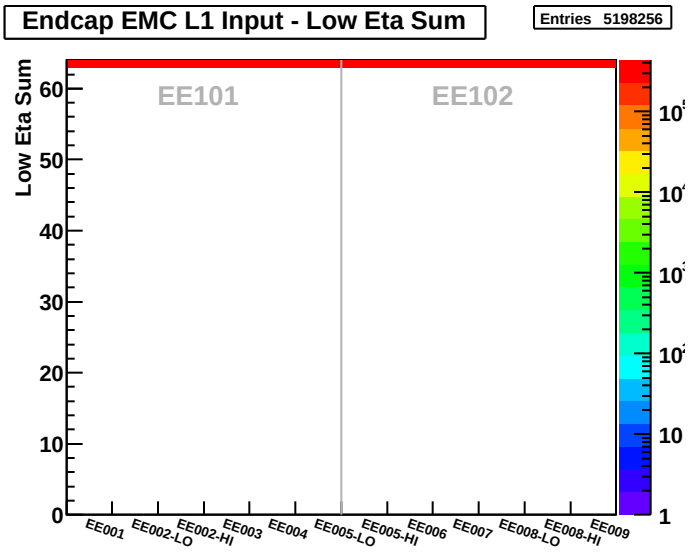

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

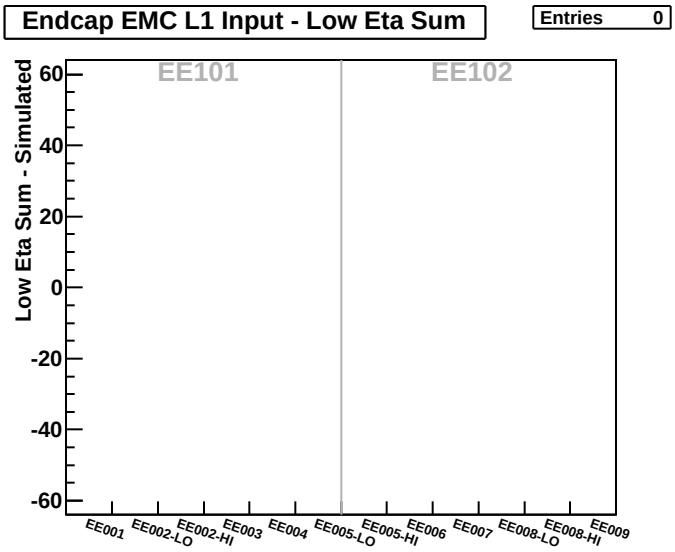

Barrel EMC L1 Input - High Eta Sum
Entries 1.559477e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

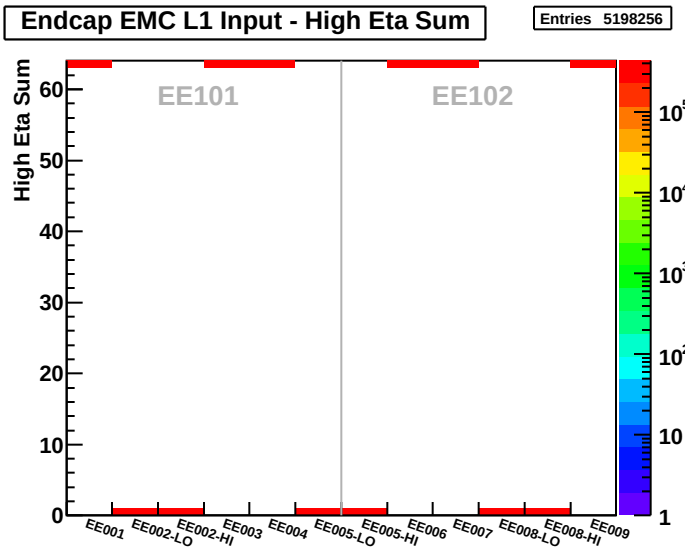

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

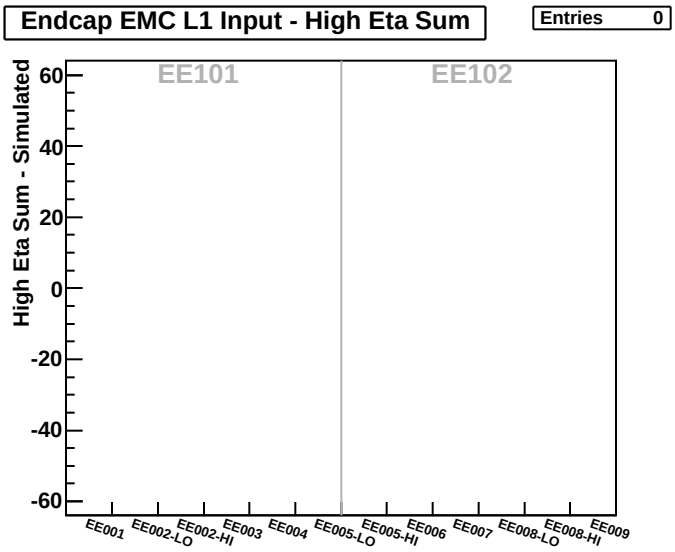

Barrel EMC L1 Input - High Tower Bits
Entries 1.559477e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

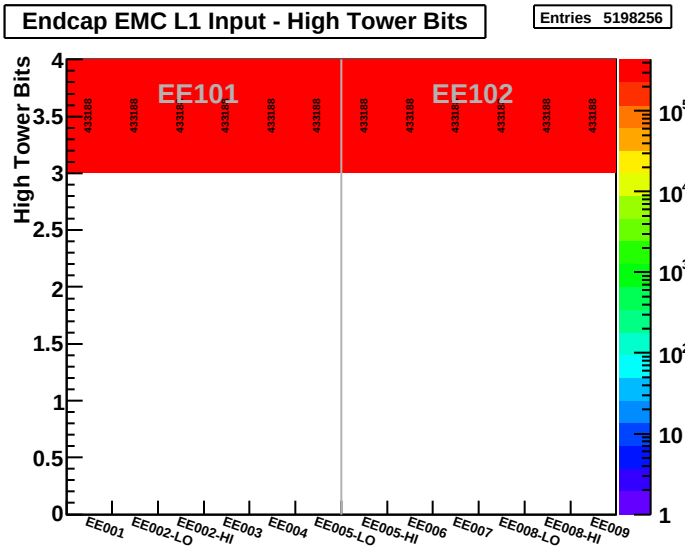

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

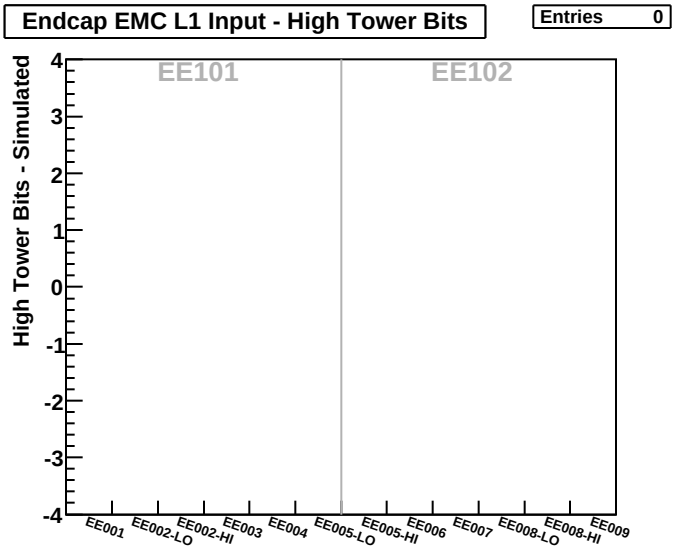

Endcap EMC L1 Input - Low Eta Sum
Entries 5198256
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

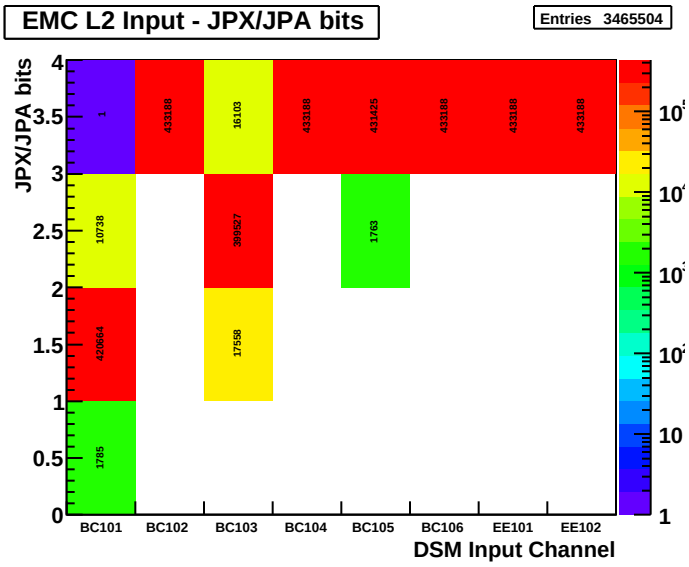

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

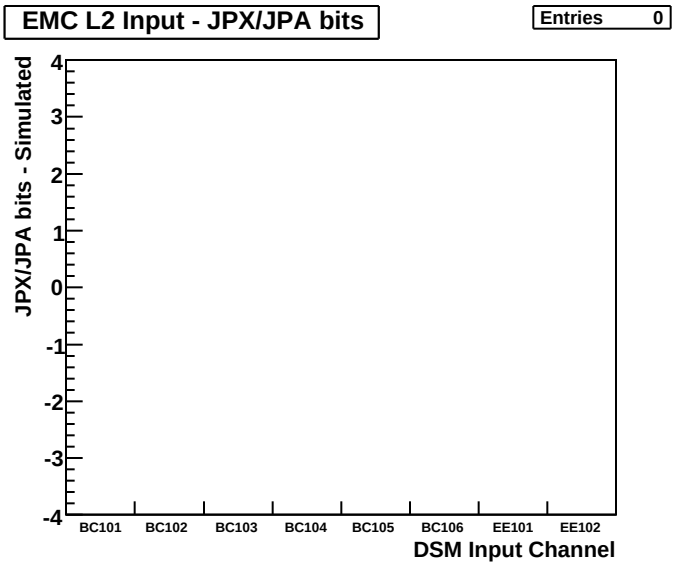

Endcap EMC L1 Input - High Eta Sum
Entries 5198256
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

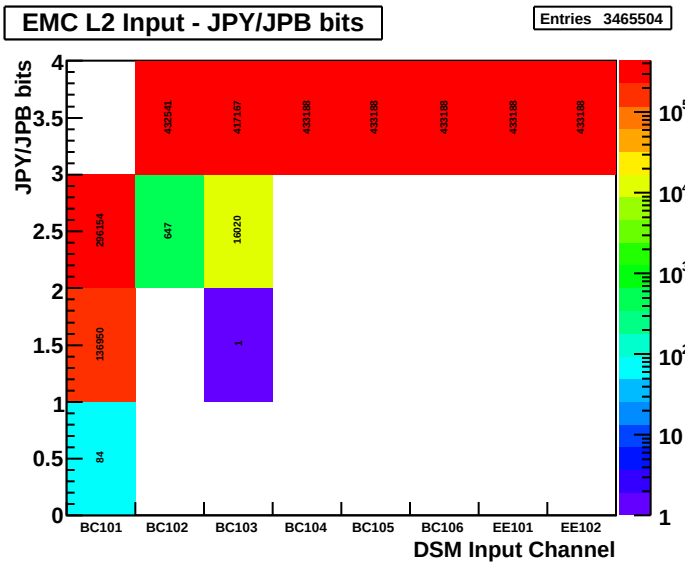

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

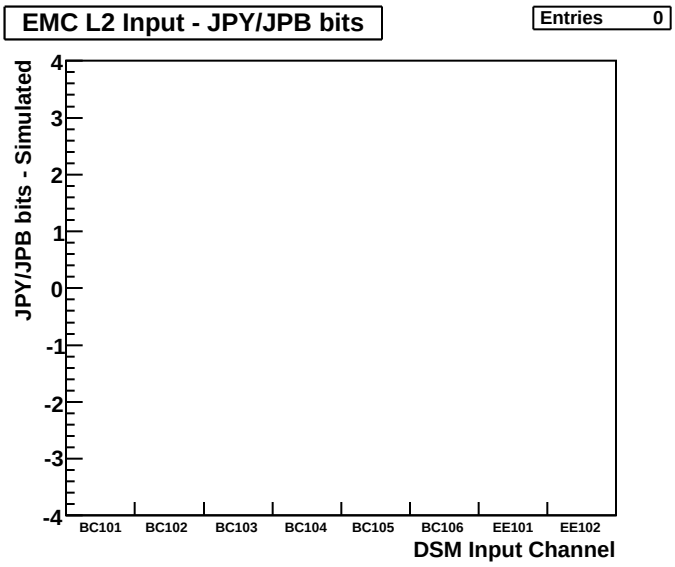

Endcap EMC L1 Input - High Tower Bits
Entries 5198256
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

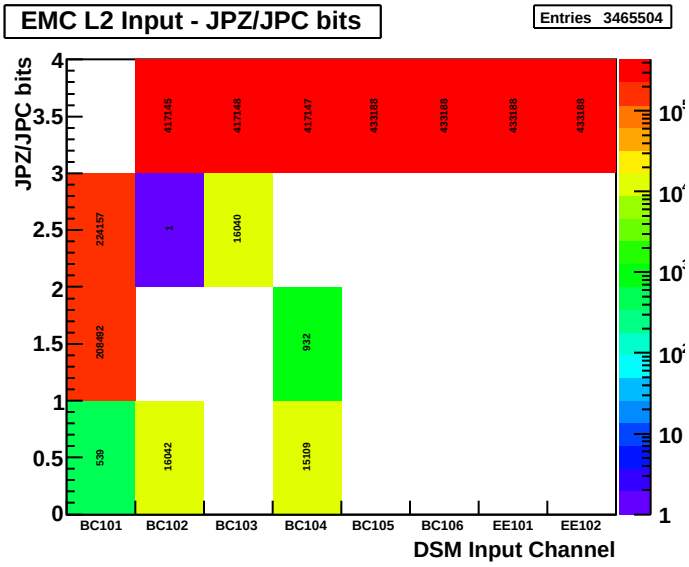

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

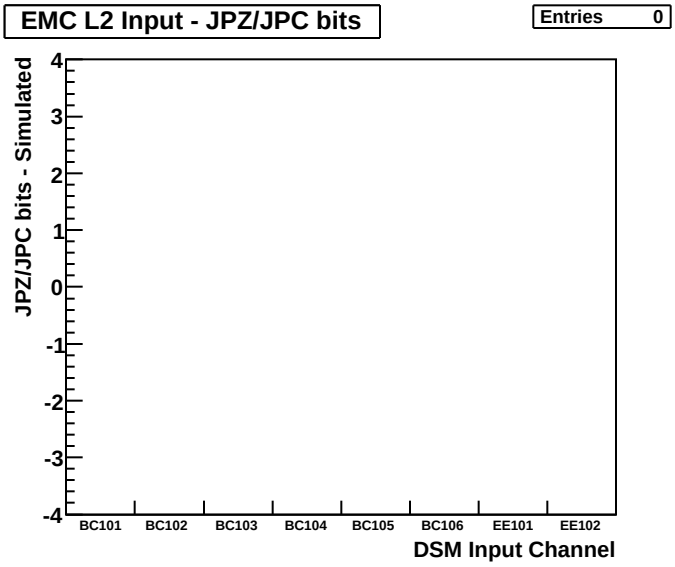

EMC L2 Input - JPX/JPA bits
Entries 3465504
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

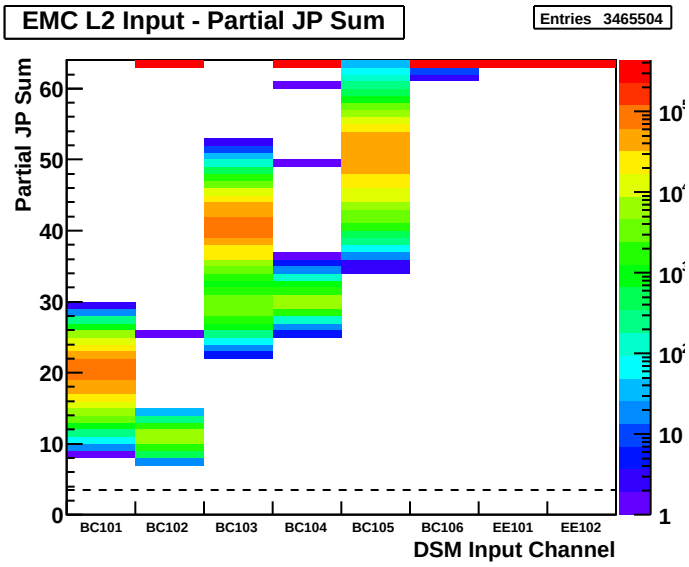

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

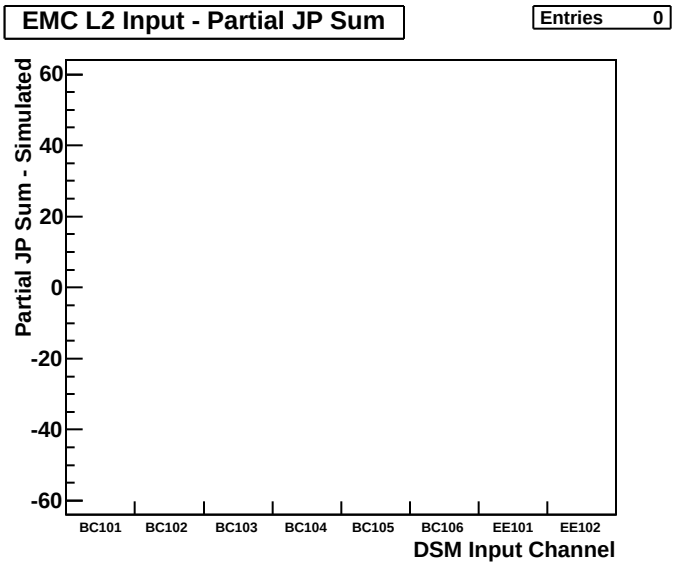

EMC L2 Input - JPY/JPB bits
Entries 3465504
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

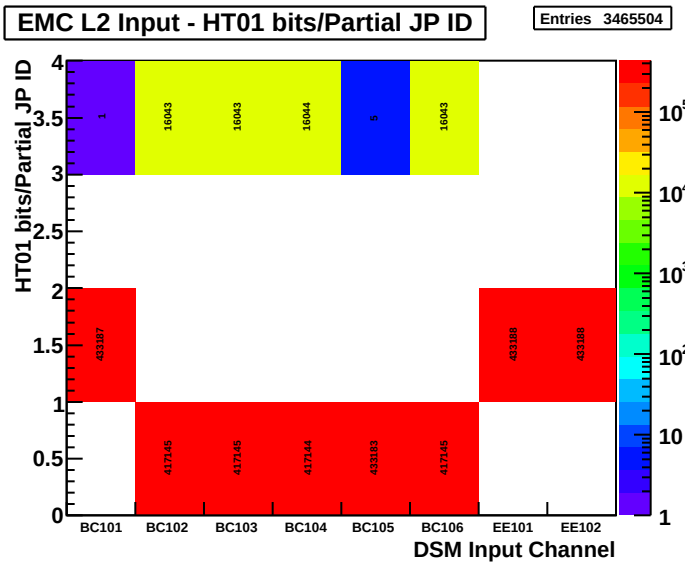

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

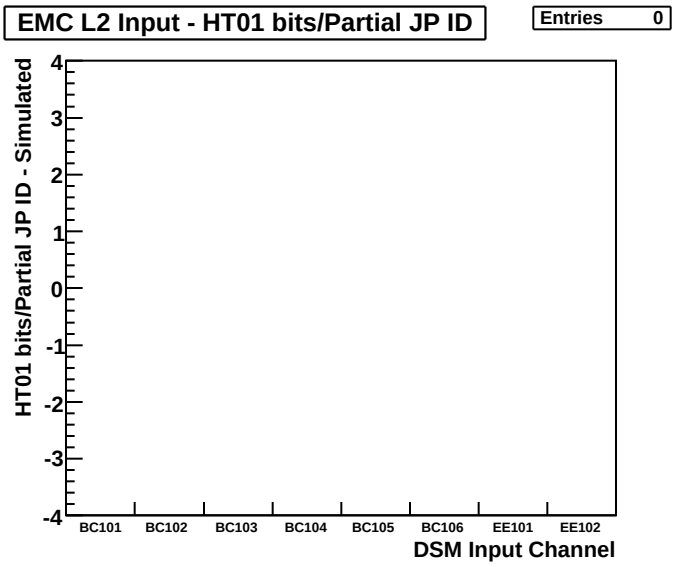

EMC L2 Input - JPZ/JPC bits
Entries 3465504
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

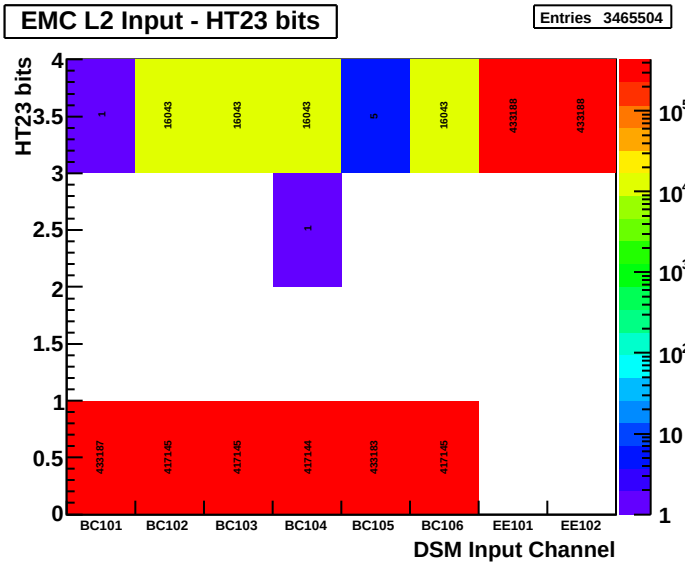

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

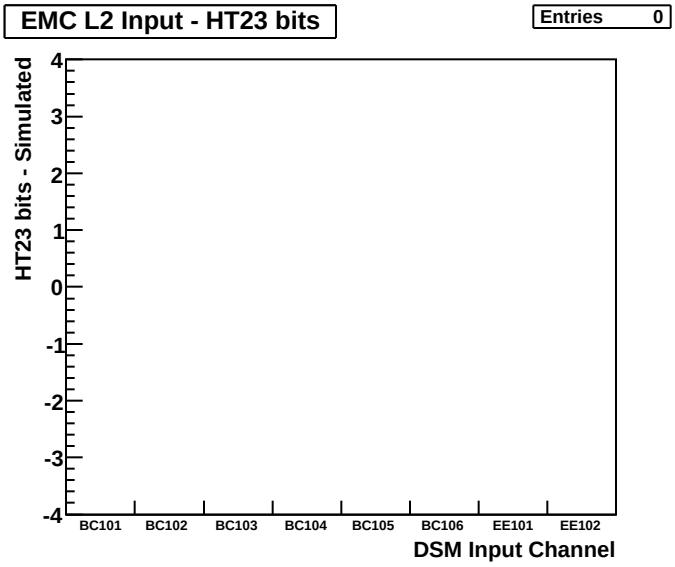

EMC L2 Input - Partial JP Sum
Entries 3465504
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

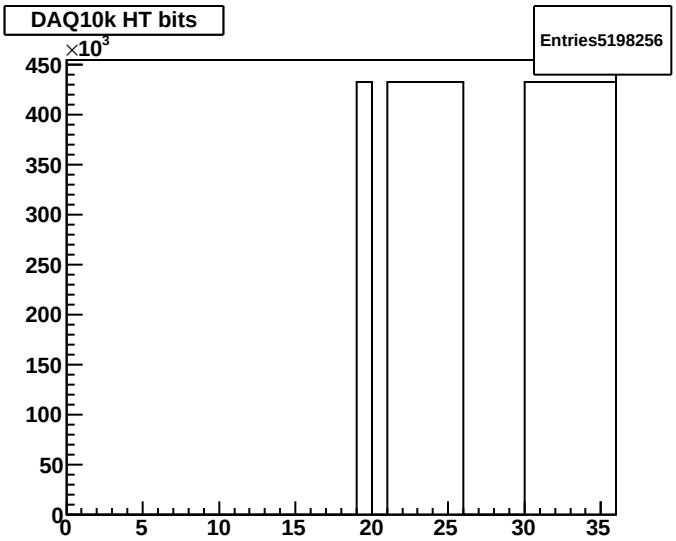

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

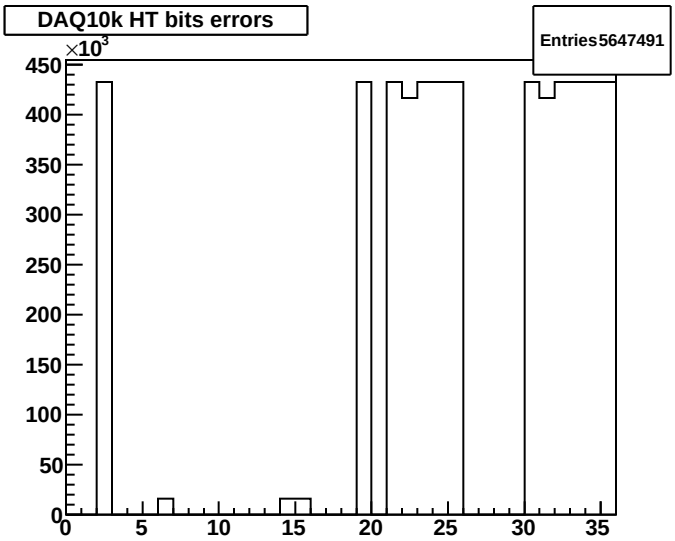

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3465504
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

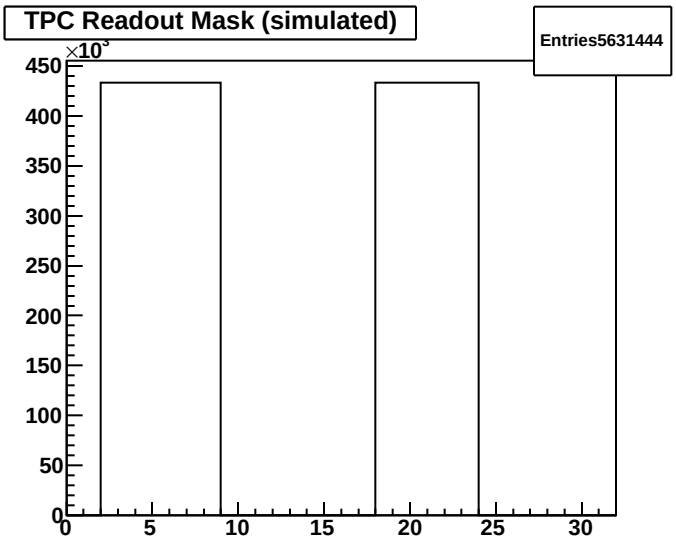

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

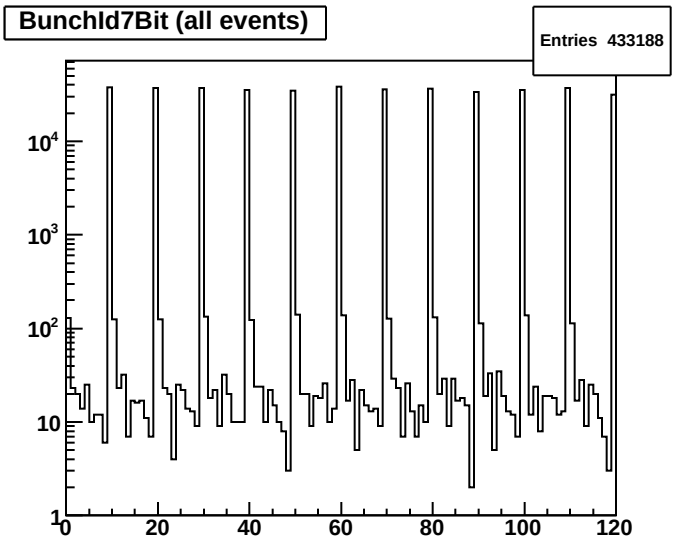

EMC L2 Input - HT23 bits
Entries 3465504
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

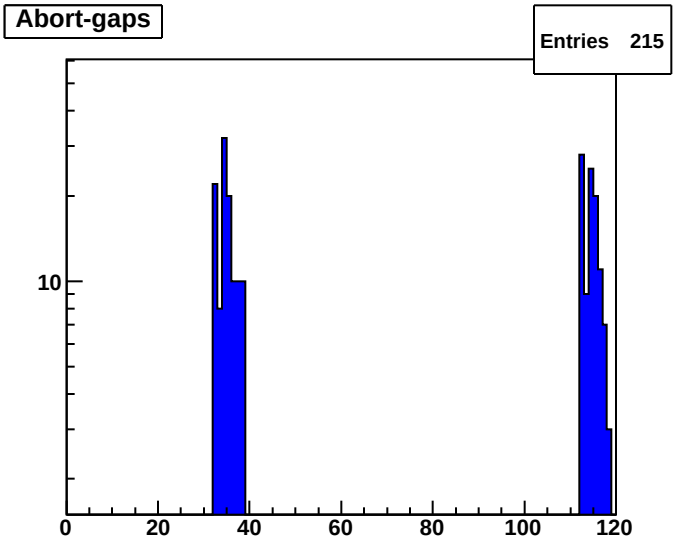

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

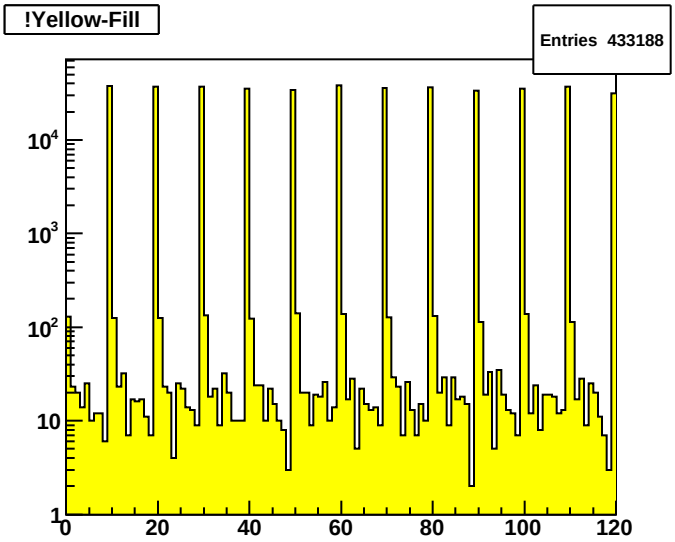

DAQ10k HT bits
Entries5198256
×10^3
450
400
350
300
250
200
150
100
50
0
0 5 10 15 20 25 30 35

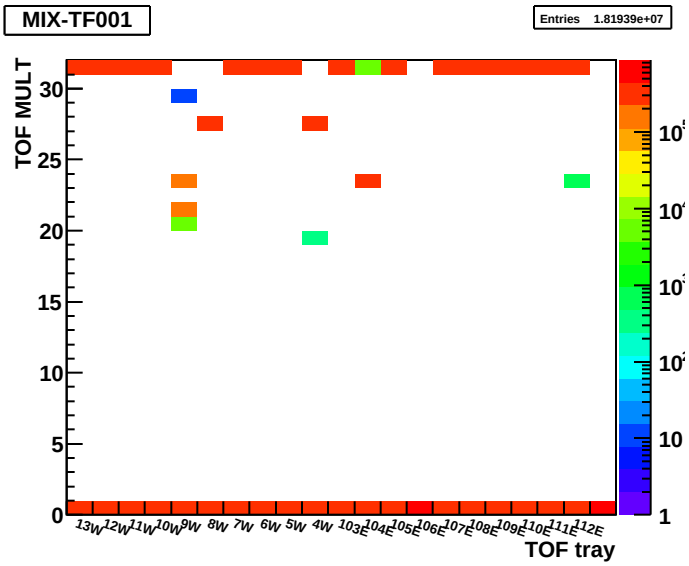

DAQ10k HT bits errors
Entries5647491
×10^3
450
400
350
300
250
200
150
100
50
0
0 5 10 15 20 25 30 35

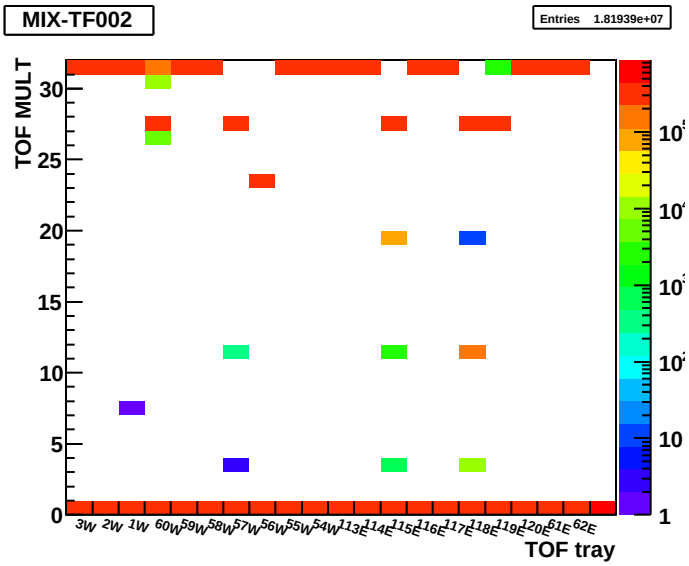

TPC Readout Mask (simulated)
Entries5631444
×10^3
450
400
350
300
250
200
150
100
50
0
0 5 10 15 20 25 30

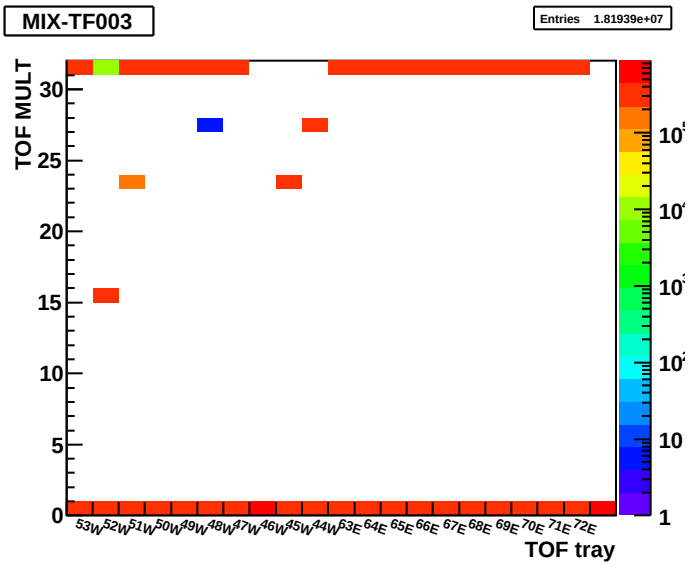

BunchId7Bit (all events)
Entries 433188
10^4
10^3
10^2
10
1
0 20 40 60 80 100 120

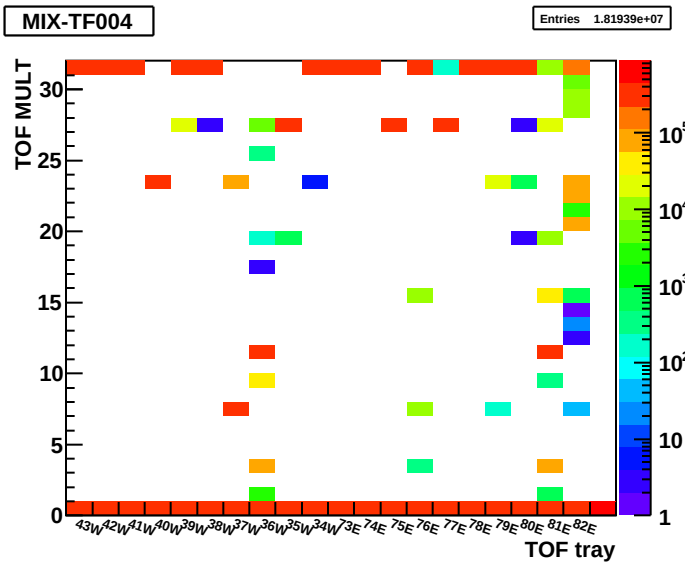

Abort-gaps
Entries 215
10
0 20 40 60 80 100 120

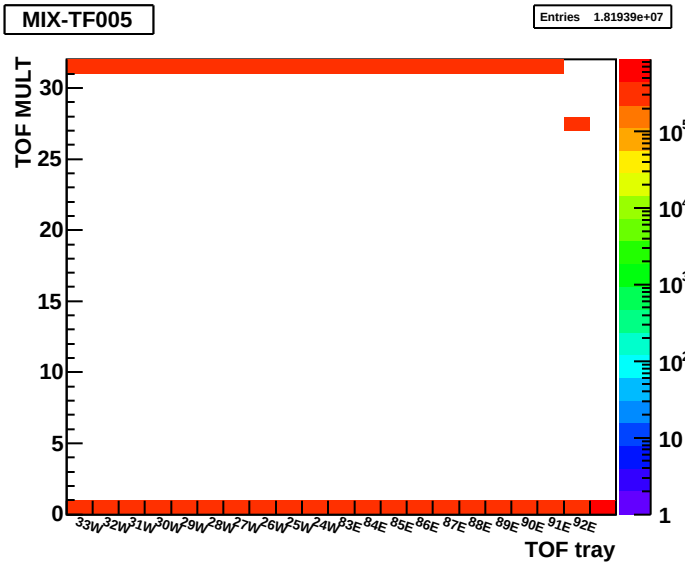

!Yellow-Fill
Entries 433188
10^4
10^3
10^2
10
1
0 20 40 60 80 100 120

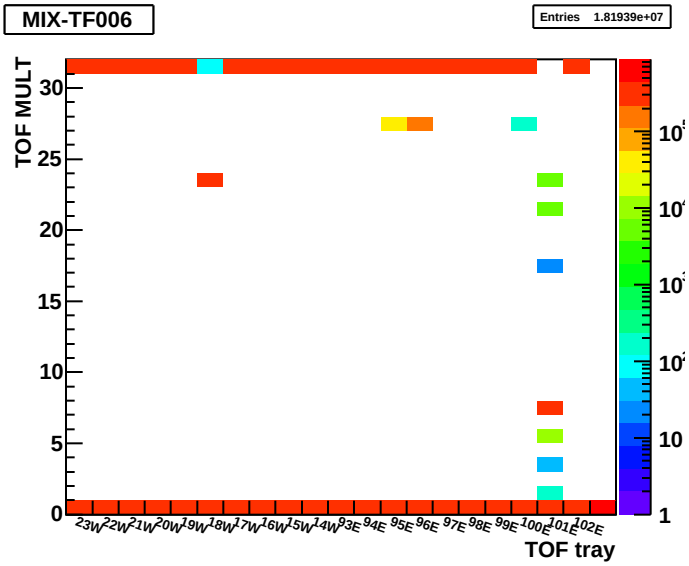

MIX-TF001
Entries 1.81939e+07
TOF MULT
TOF tray

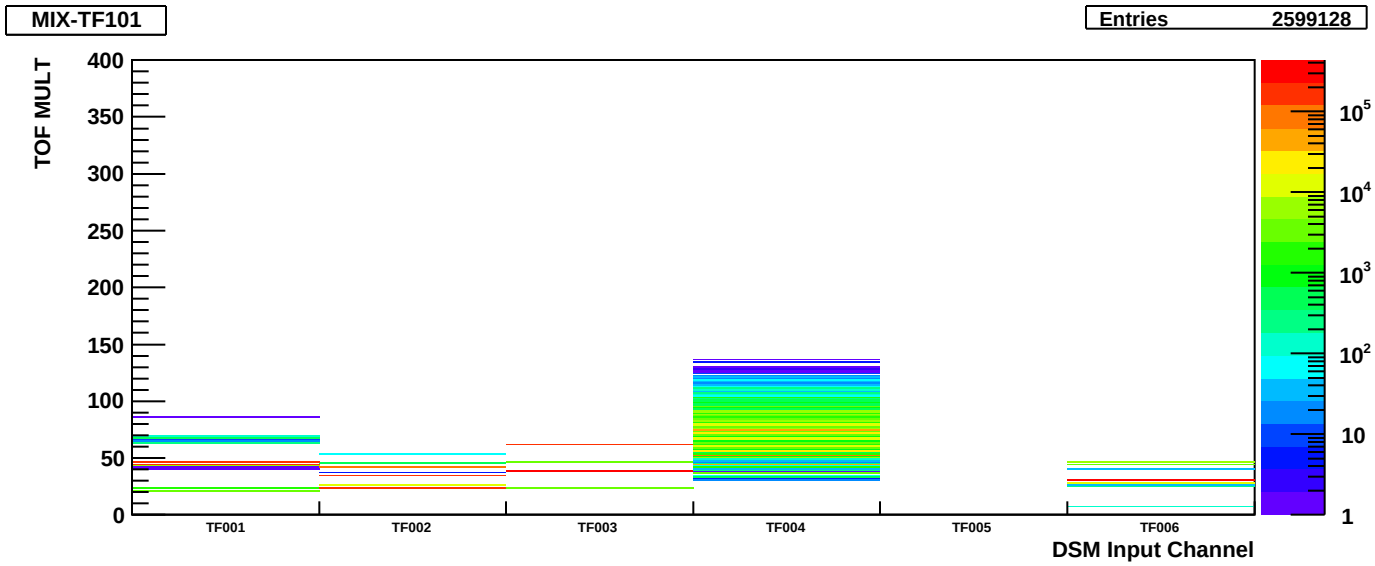

MIX-TF002
Entries 1.81939e+07
TOF MULT
TOF tray

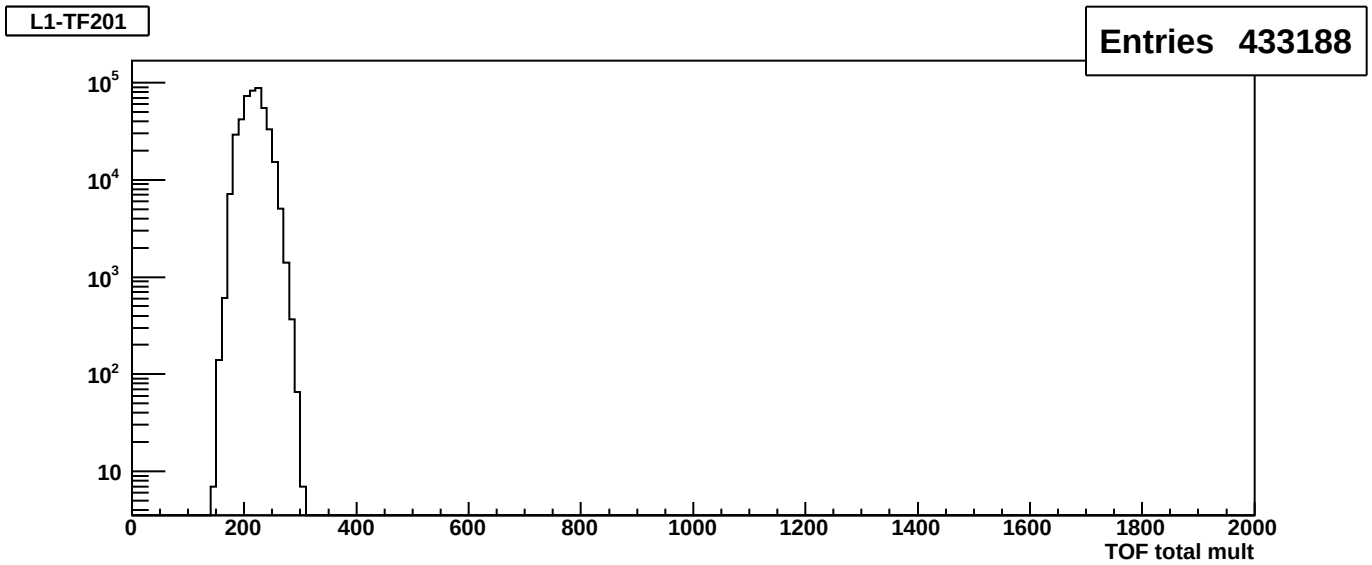

MIX-TF003
Entries 1.81939e+07
TOF MULT
TOF tray

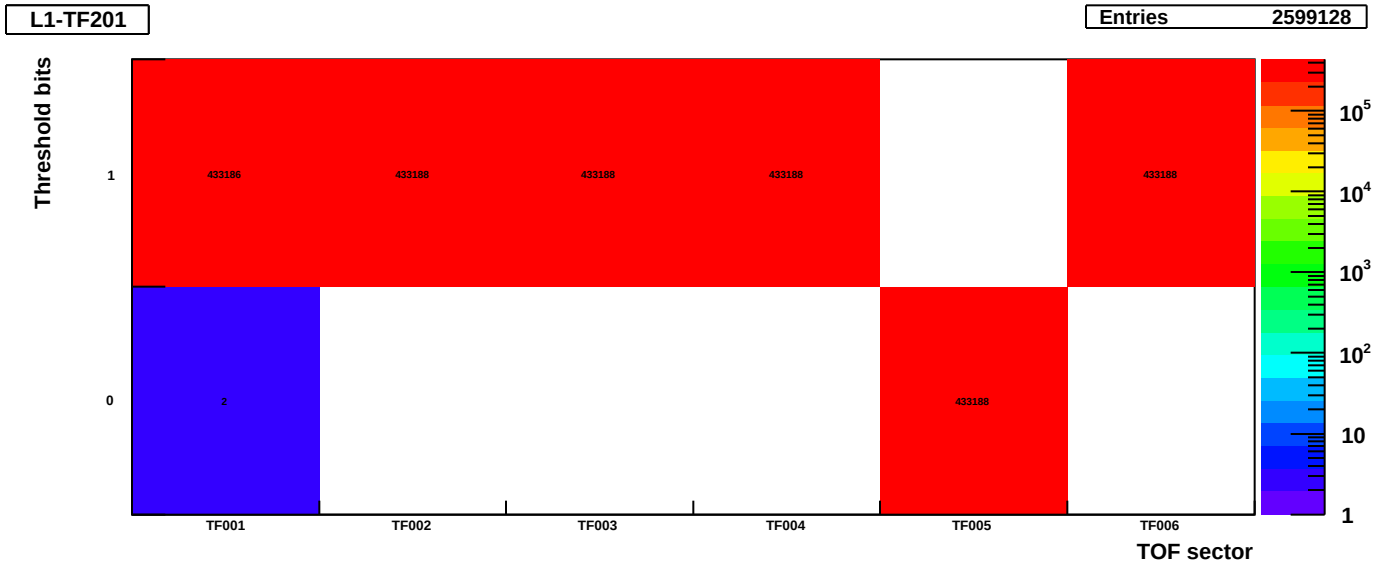

MIX-TF004
Entries 1.81939e+07
TOF MULT
TOF tray

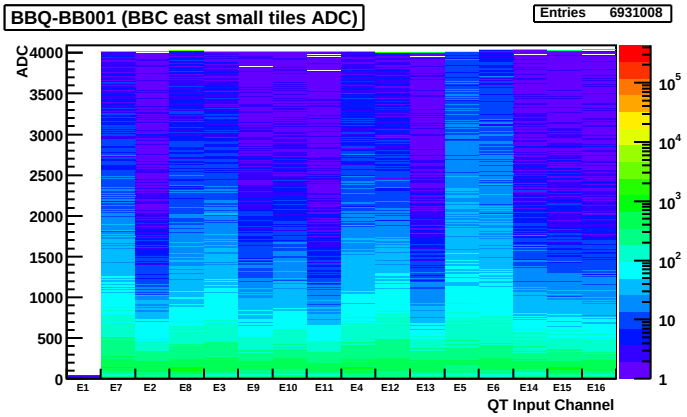

MIX-TF005
Entries 1.81939e+07
TOF MULT
TOF tray

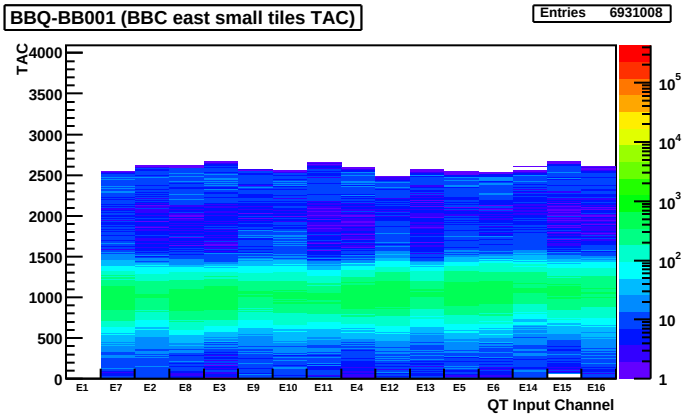

MIX-TF006
Entries 1.81939e+07
TOF MULT
TOF tray

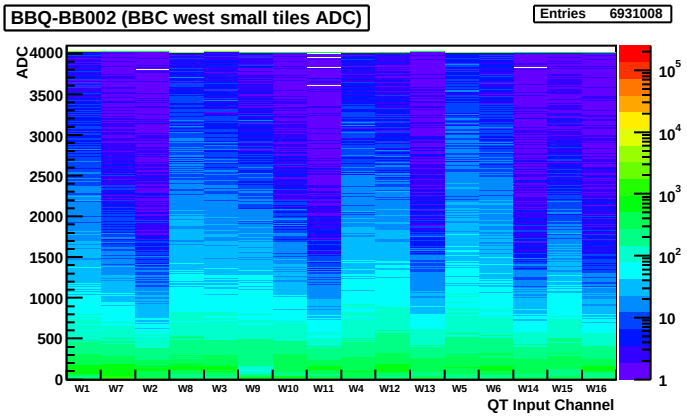

MIX-TF101
Entries 2599128
TOF MULT
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

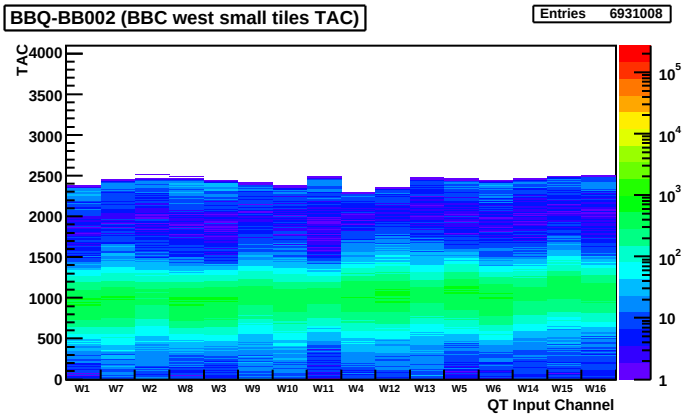

L1-TF201
Entries 433188
10^5
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000
TOF total mult

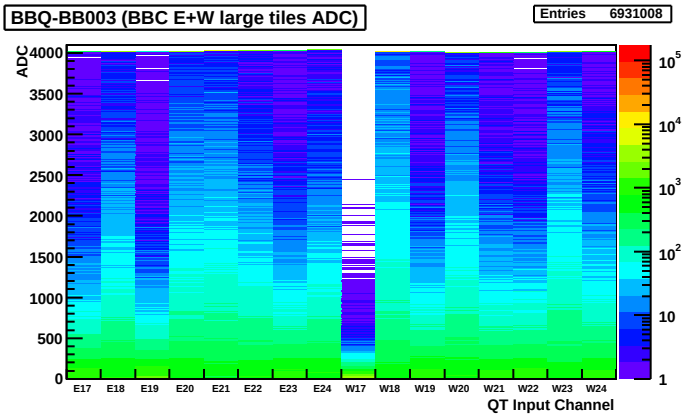

L1-TF201
Entries 2599128
Threshold bits
1
0
433186
433188
433188
433188
433188
2
433188
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3
10^2
10
1

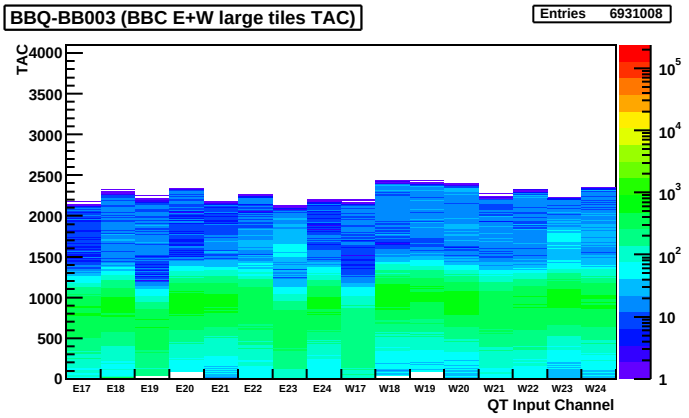

BBQ-BB001 (BBC east small tiles ADC)
Entries 6931008
ADC
QT Input Channel

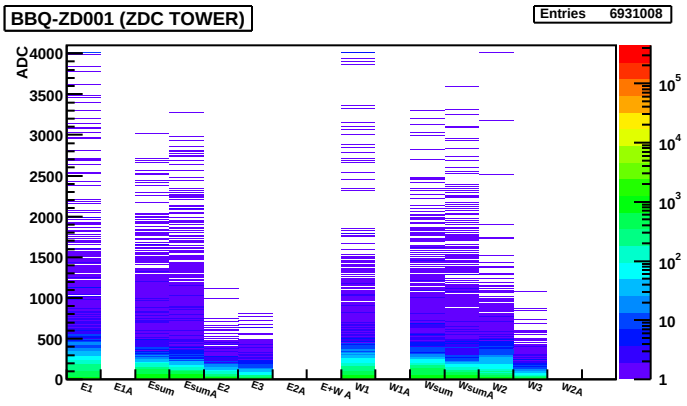

BBQ-BB001 (BBC east small tiles TAC)
Entries 6931008
TAC
QT Input Channel

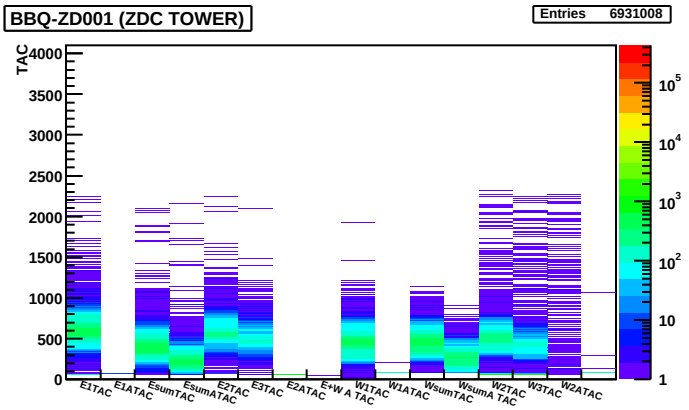

BBQ-BB002 (BBC west small tiles ADC)
Entries 6931008
ADC
QT Input Channel

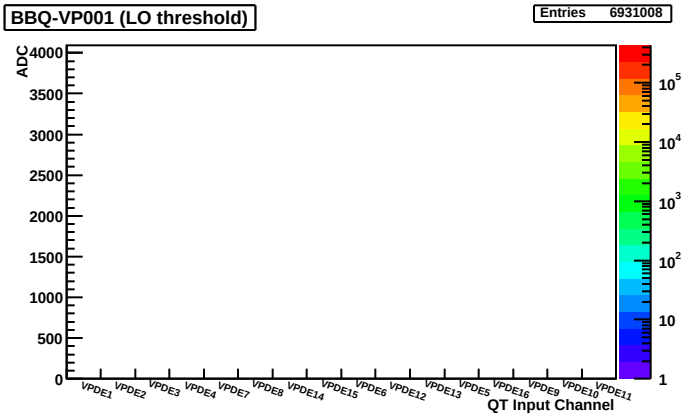

BBQ-BB002 (BBC west small tiles TAC)
Entries 6931008
TAC
QT Input Channel

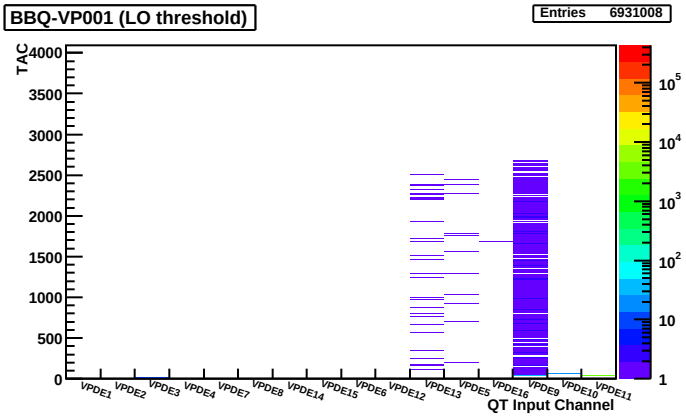

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6931008
ADC
QT Input Channel

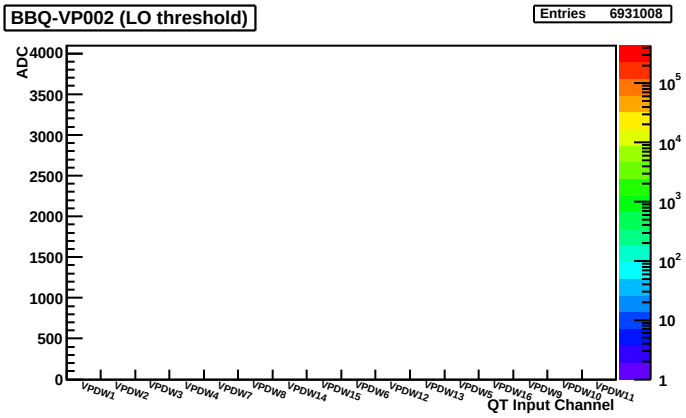

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6931008
TAC
QT Input Channel

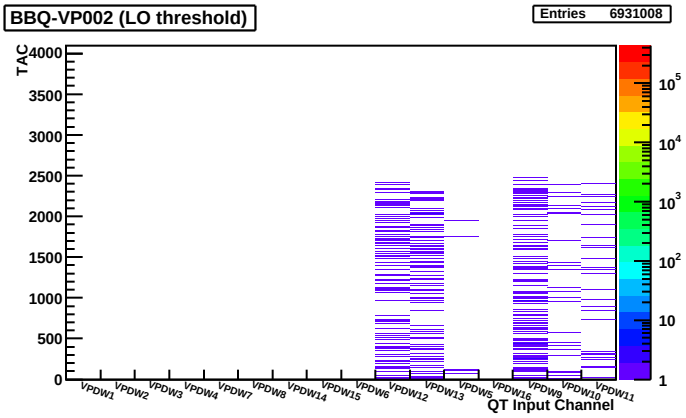

BBQ-ZD001 (ZDC TOWER)
Entries 6931008
ADC

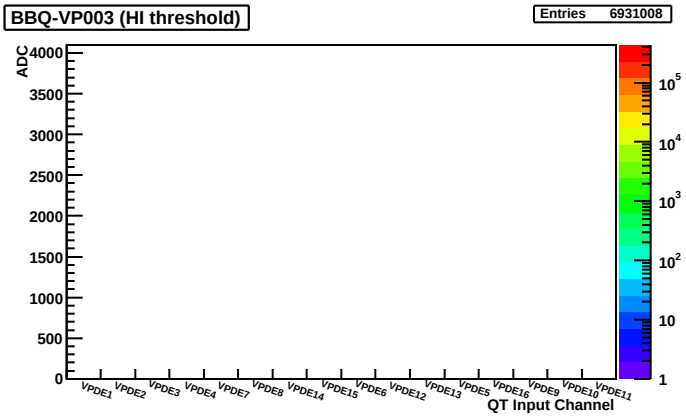

BBQ-ZD001 (ZDC TOWER)
Entries 6931008
TAC

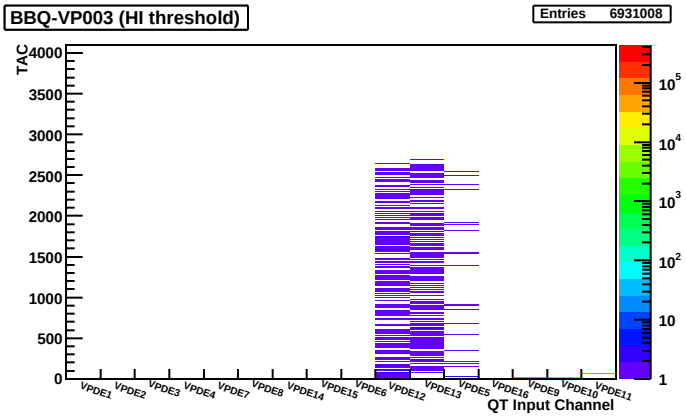

BBQ-VP001 (LO threshold)
Entries 6931008
ADC
QT Input Channel

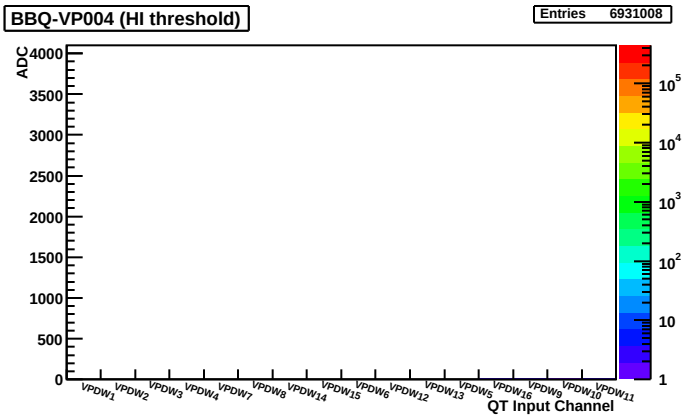

BBQ-VP001 (LO threshold)
Entries 6931008
TAC
QT Input Channel

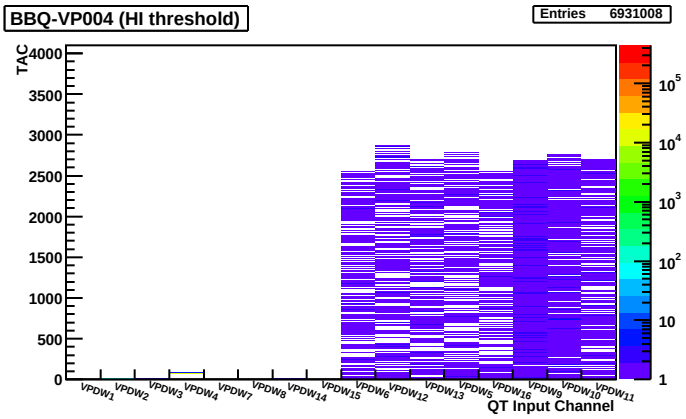

BBQ-VP002 (LO threshold)
Entries 6931008
ADC
QT Input Channel

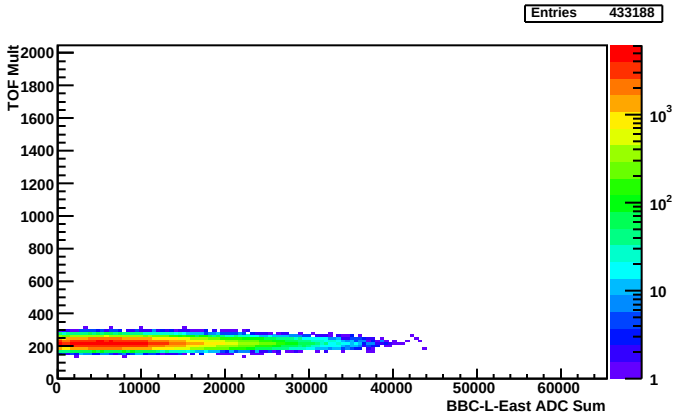

BBQ-VP002 (LO threshold)
Entries 6931008
TAC
QT Input Channel

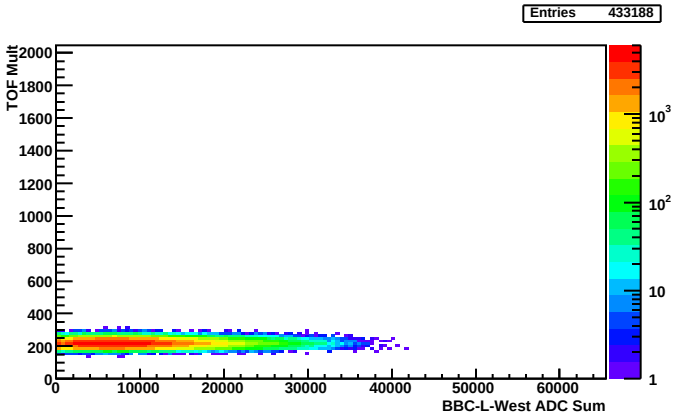

BBQ-VP003 (HI threshold)
Entries 6931008
ADC
QT Input Channel

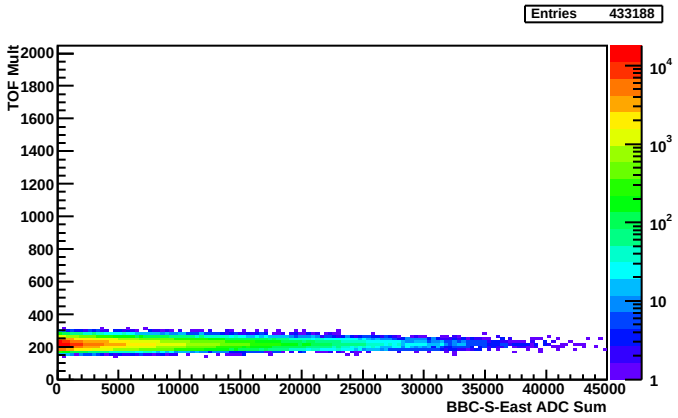

BBQ-VP003 (HI threshold)
Entries 6931008
TAC
QT Input Channel

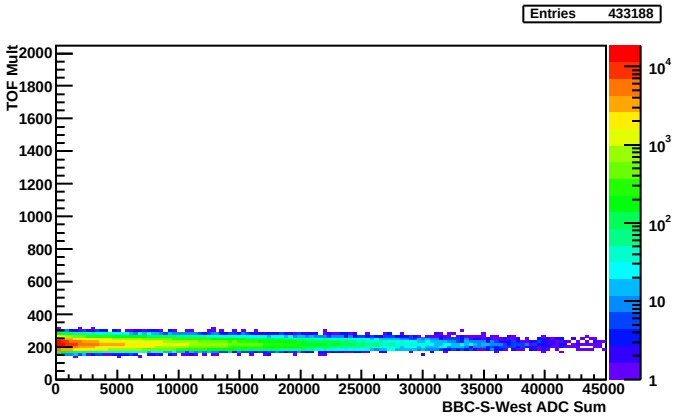

BBQ-VP004 (HI threshold)
Entries 6931008
ADC
QT Input Channel

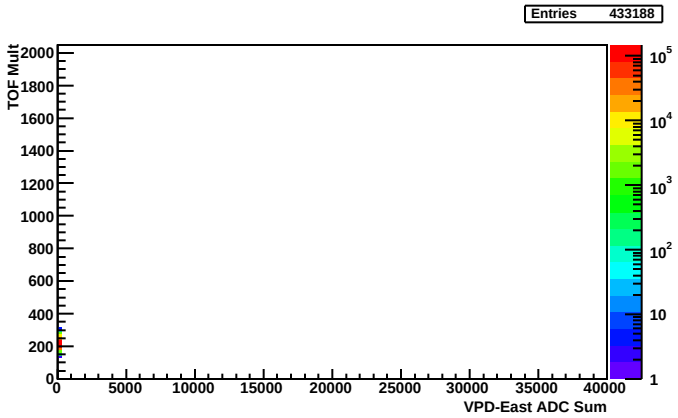

BBQ-VP004 (HI threshold)
Entries 6931008
TAC
QT Input Channel

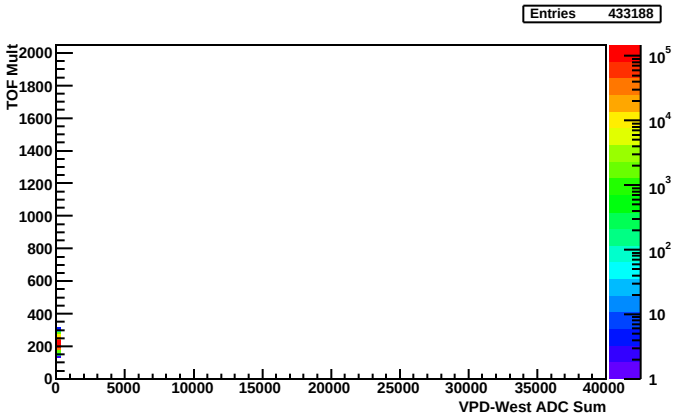

Entries 433188
TOF Mult
BBC-L-East ADC Sum

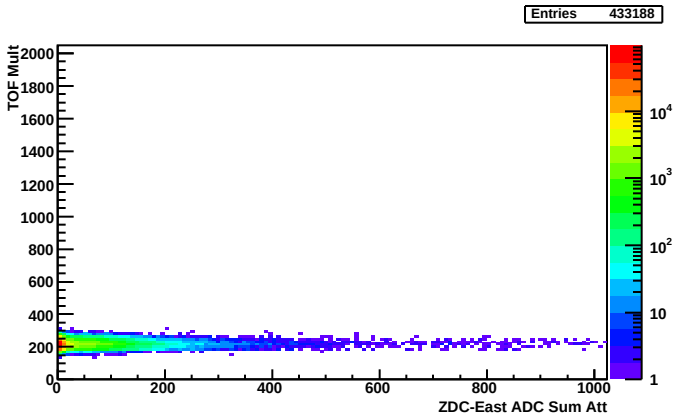

Entries 433188
TOF Mult
BBC-L-West ADC Sum

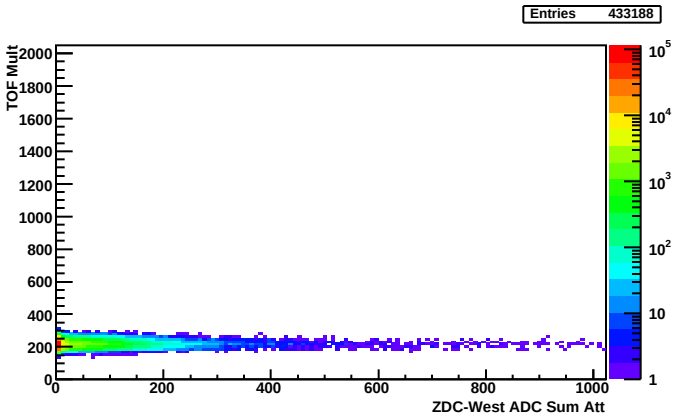

Entries 433188
TOF Mult
BBC-S-East ADC Sum

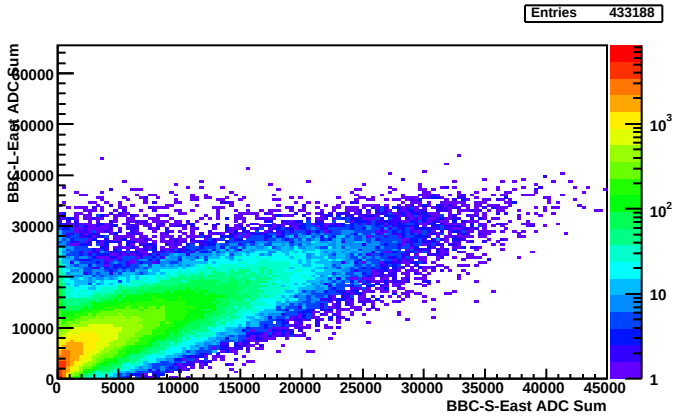

Entries 433188
TOF Mult
BBC-S-West ADC Sum

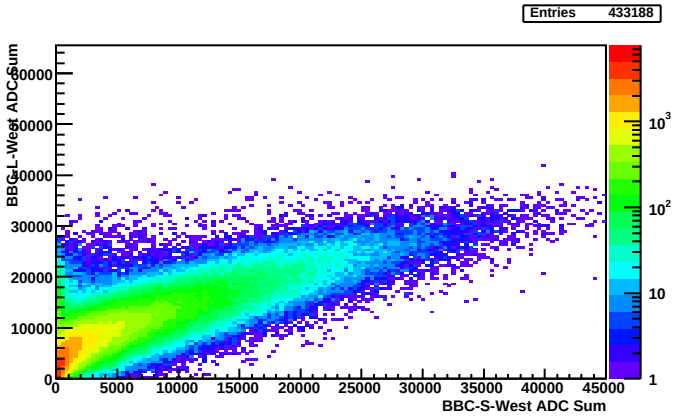

Entries 433188
TOF Mult
VPD-East ADC Sum

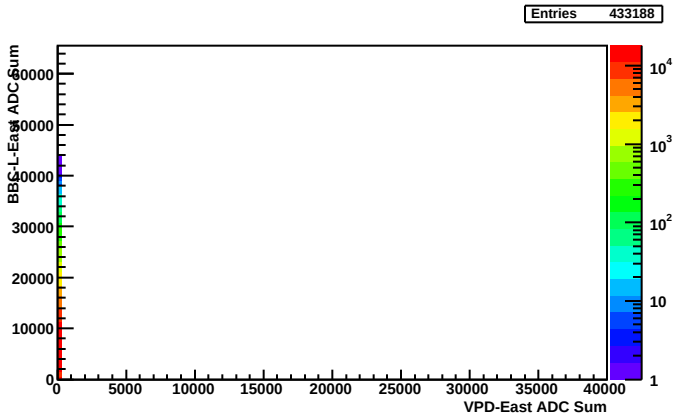

Entries 433188
TOF Mult
VPD-West ADC Sum

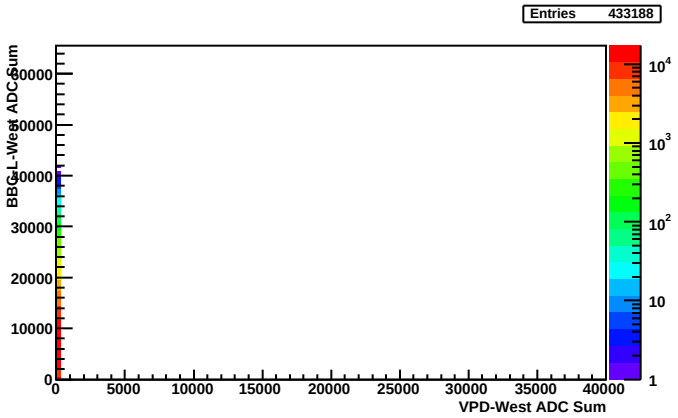

Entries 433188
TOF Mult
ZDC-East ADC Sum Att

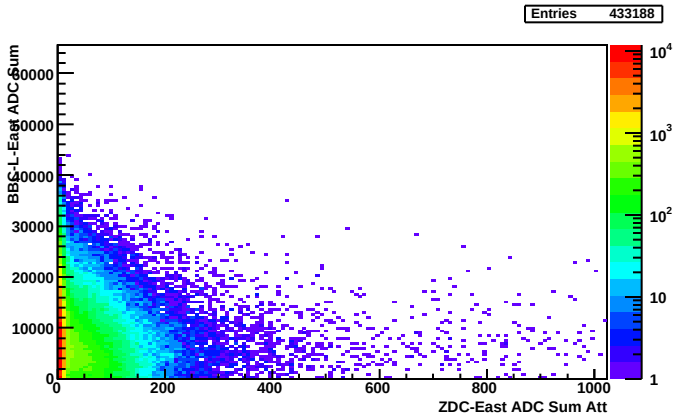

Entries 433188
TOF Mult
ZDC-West ADC Sum Att

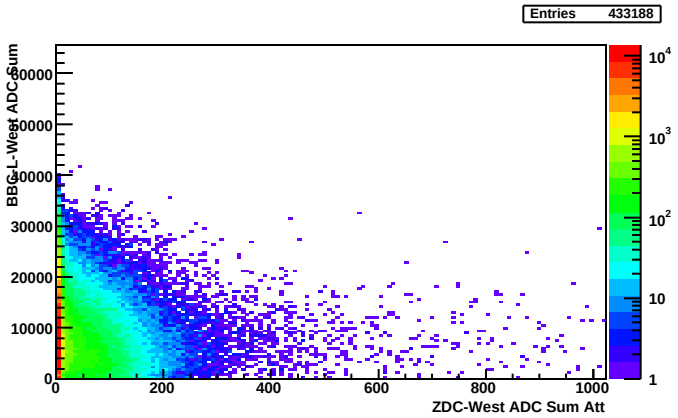

Entries 433188
BBC-L-East ADC Sum
BBC-S-East ADC Sum

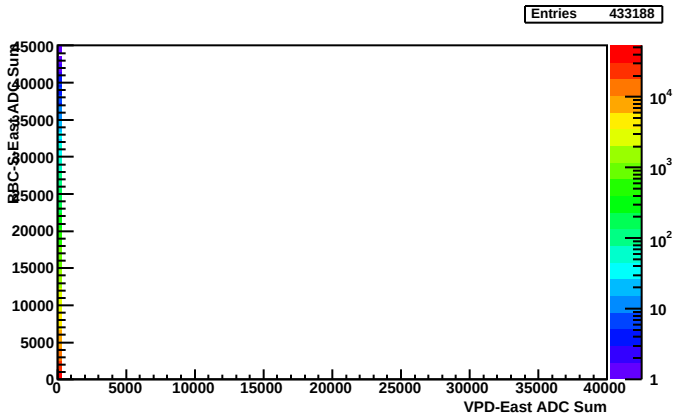

Entries 433188
BBC-L-West ADC Sum
BBC-S-West ADC Sum

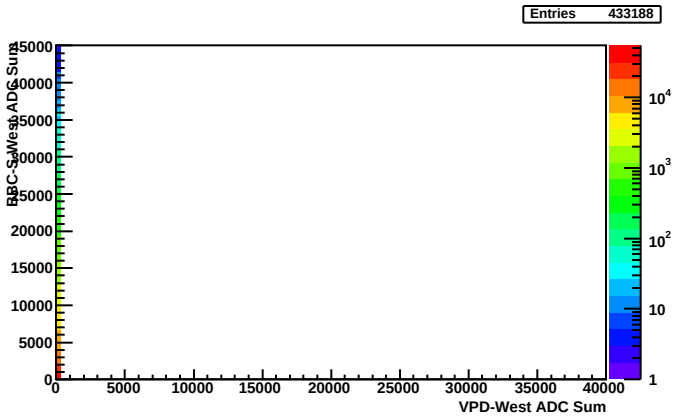

Entries 433188
BBC-L-East ADC Sum
VPD-East ADC Sum

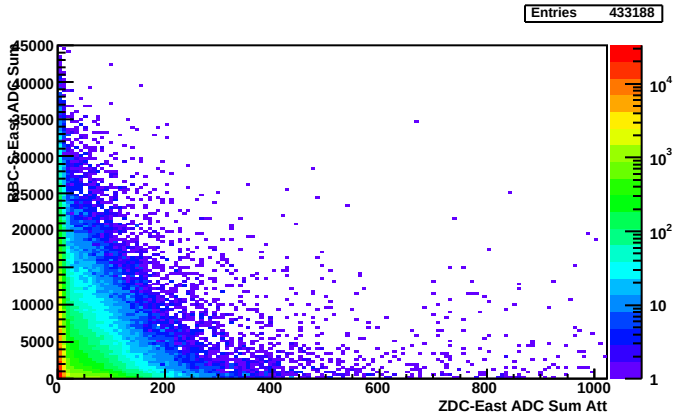

Entries 433188
BBC-L-West ADC Sum
VPD-West ADC Sum

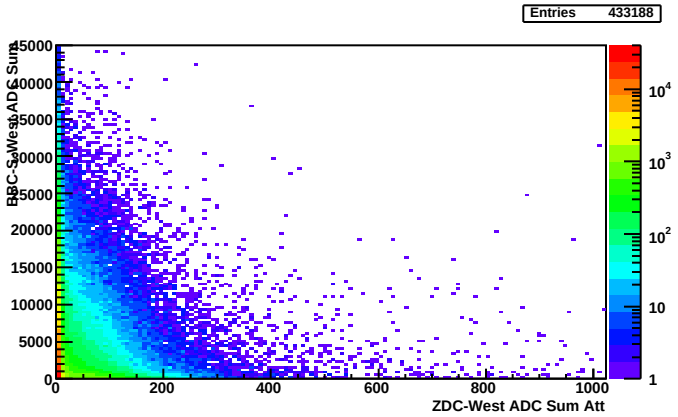

Entries 433188
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

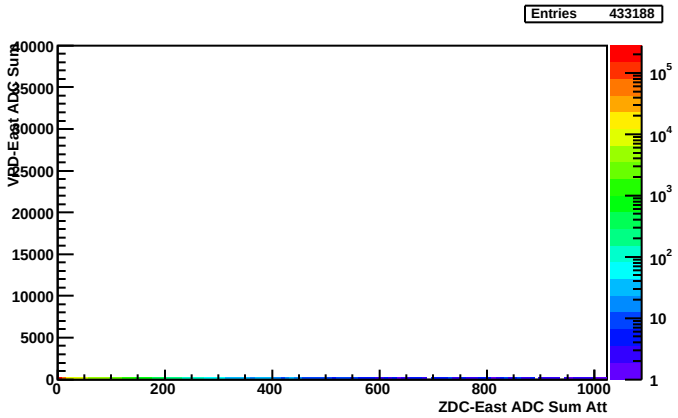

Entries 433188
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

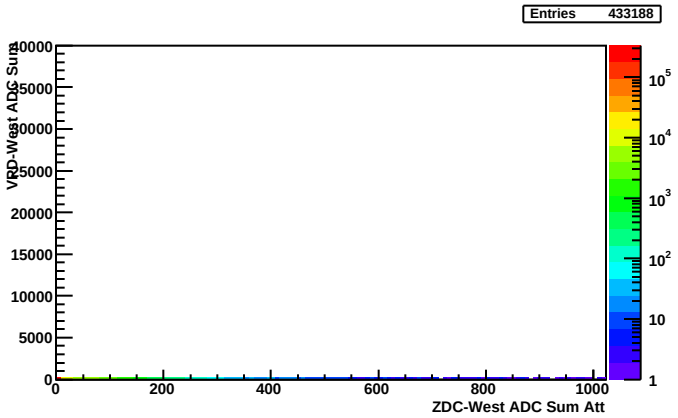

Entries 433188
BBC-S-East ADC Sum
VPD-East ADC Sum

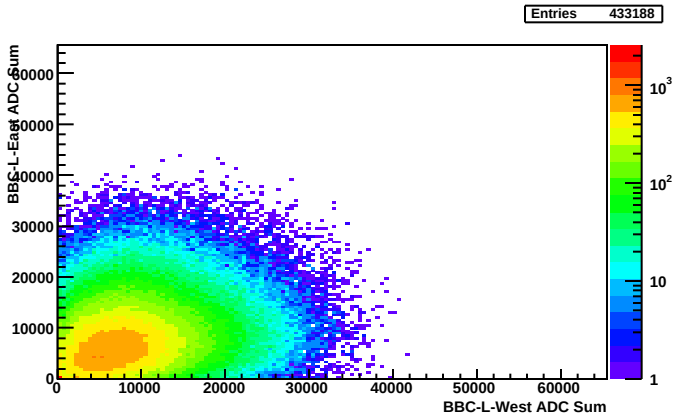

Entries 433188
BBC-S-West ADC Sum
VPD-West ADC Sum

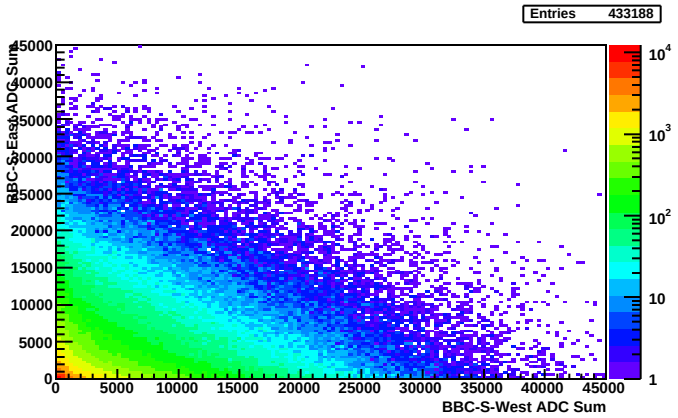

Entries 433188
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

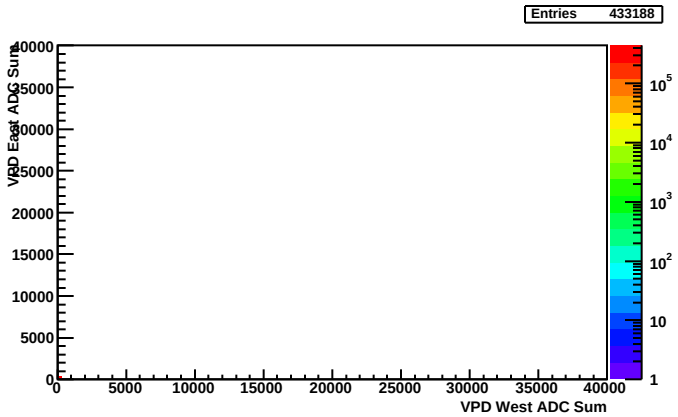

Entries 433188
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

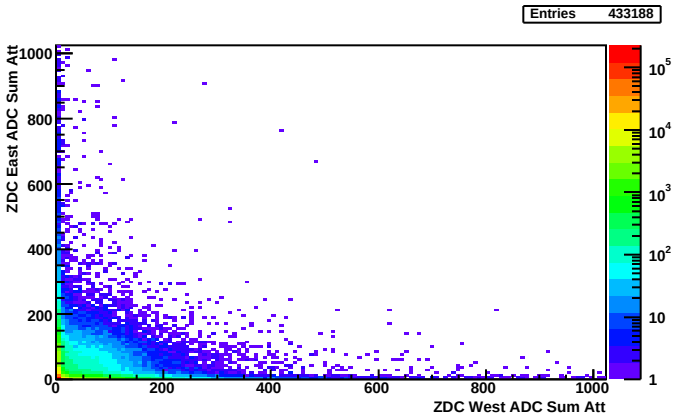

Entries 433188
VPD-East ADC Sum
ZDC-East ADC Sum Att

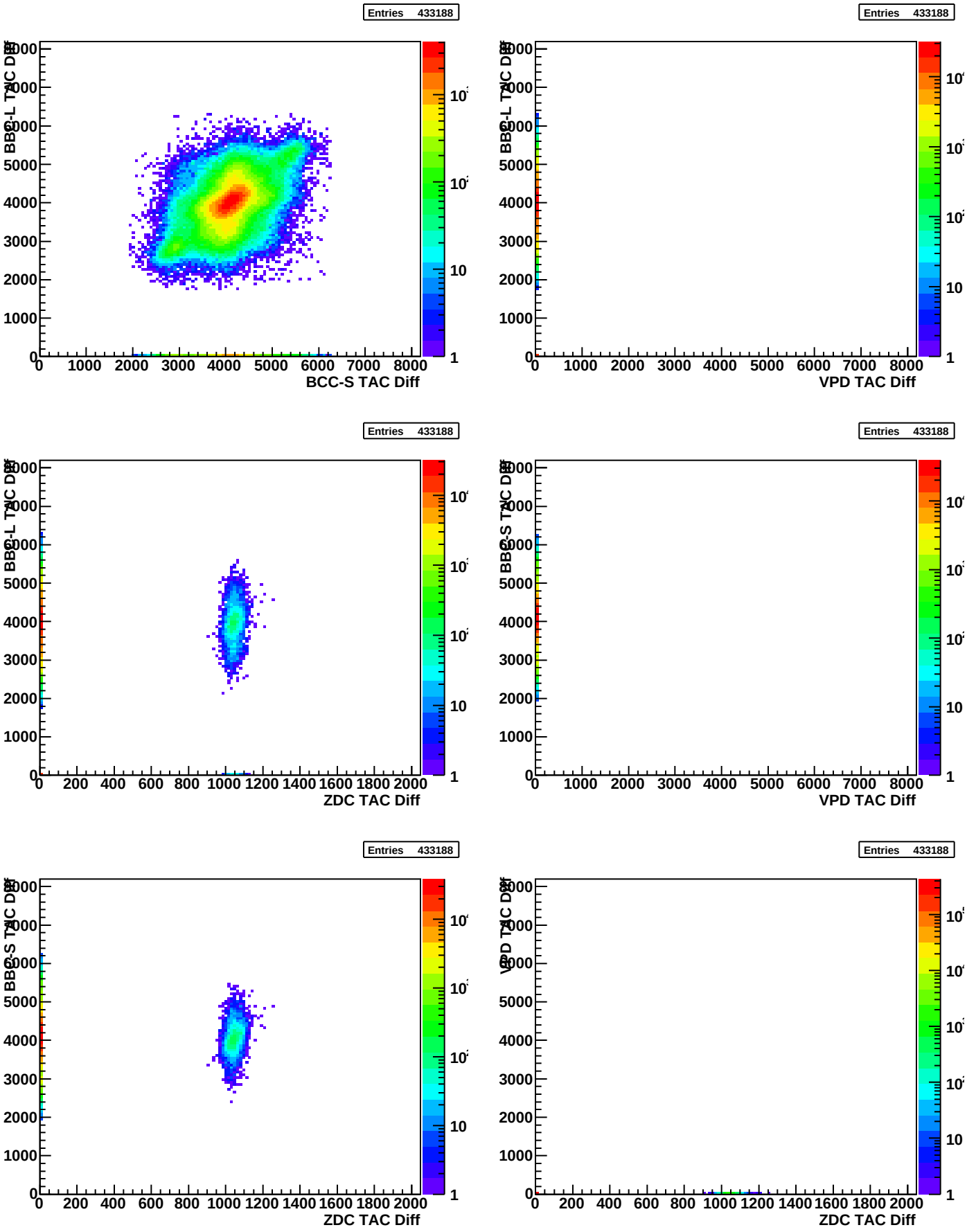

Entries 433188
VPD-West ADC Sum
ZDC-West ADC Sum Att

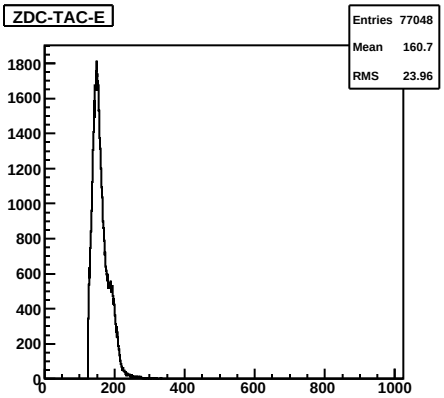

Entries 433188
BBC-L-East ADC Sum
BBC-L-West ADC Sum

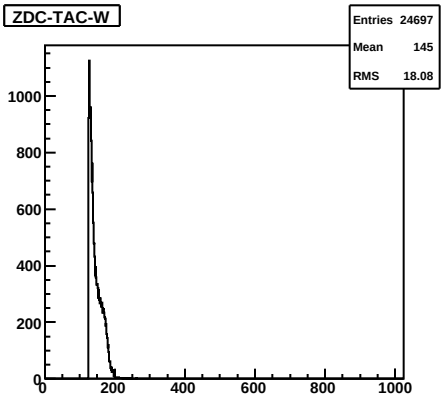

Entries 433188
BBC-S-East ADC Sum
BBC-S-West ADC Sum

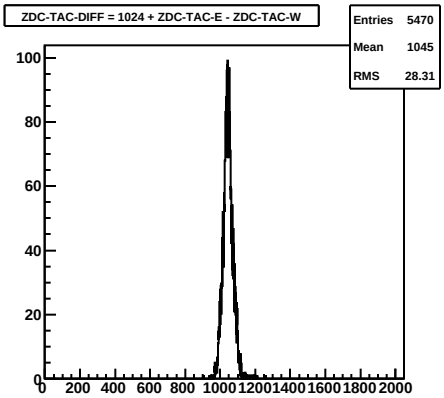

Entries 433188
VPD East ADC Sum
VPD West ADC Sum

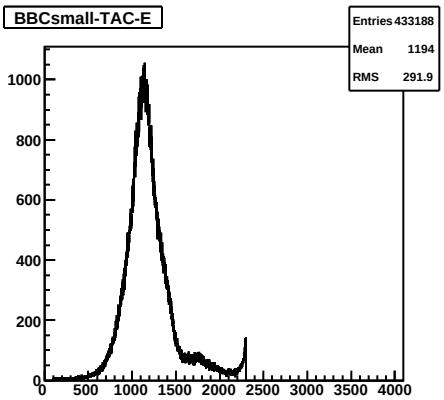

Entries 433188
ZDC East ADC Sum Att
ZDC West ADC Sum Att

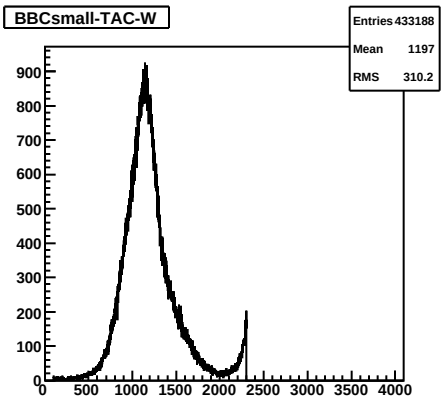

Entries 433188
BBC-L TAC Diff
BCC-S TAC Diff
Entries 433188
BBC-L TAC Diff
VPD TAC Diff
Entries 433188
BBC-L TAC Diff
ZDC TAC Diff
Entries 433188
BBC-S TAC Diff
VPD TAC Diff
Entries 433188
BBC-S TAC Diff
ZDC TAC Diff
Entries 433188
VPD TAC Diff
ZDC TAC Diff

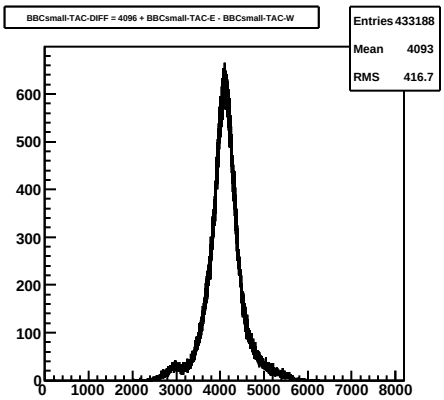

ZDC-TAC-E
Entries 77048
Mean 160.7
RMS 23.96

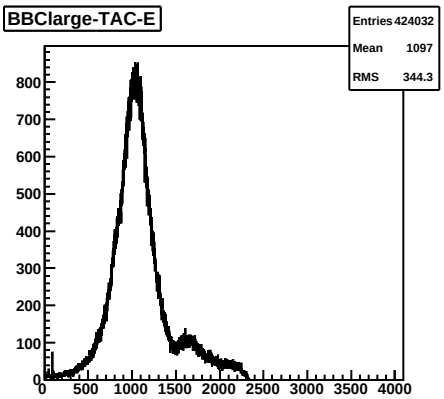

ZDC-TAC-W
Entries 24697
Mean 145
RMS 18.08

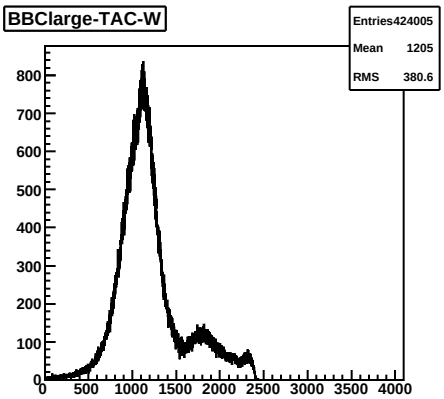

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5470
Mean 1045
RMS 28.31

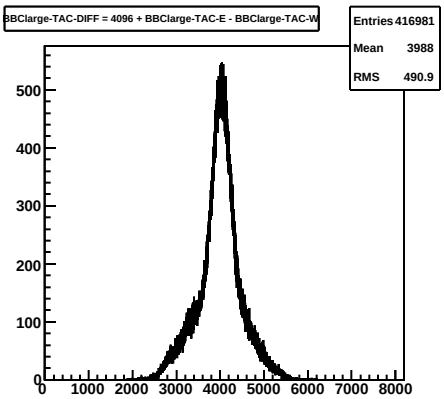

BBCsmall-TAC-E
Entries 433188
Mean 1194
RMS 291.9

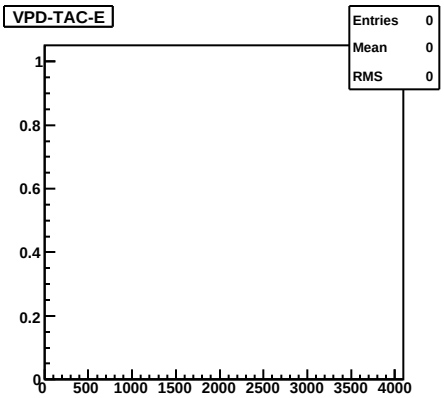

BBCsmall-TAC-W
Entries 433188
Mean 1197
RMS 310.2

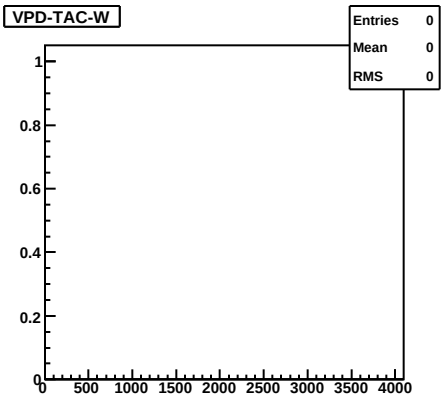

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 433188
Mean 4093
RMS 416.7

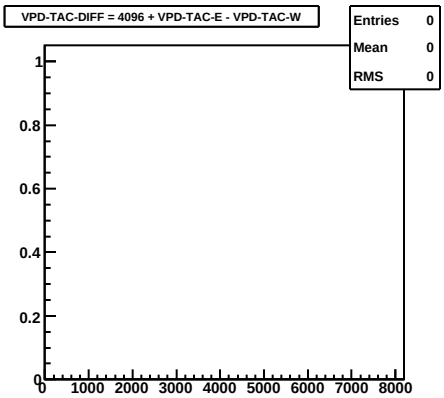

BBClarge-TAC-E
Entries 424032
Mean 1097
RMS 344.3

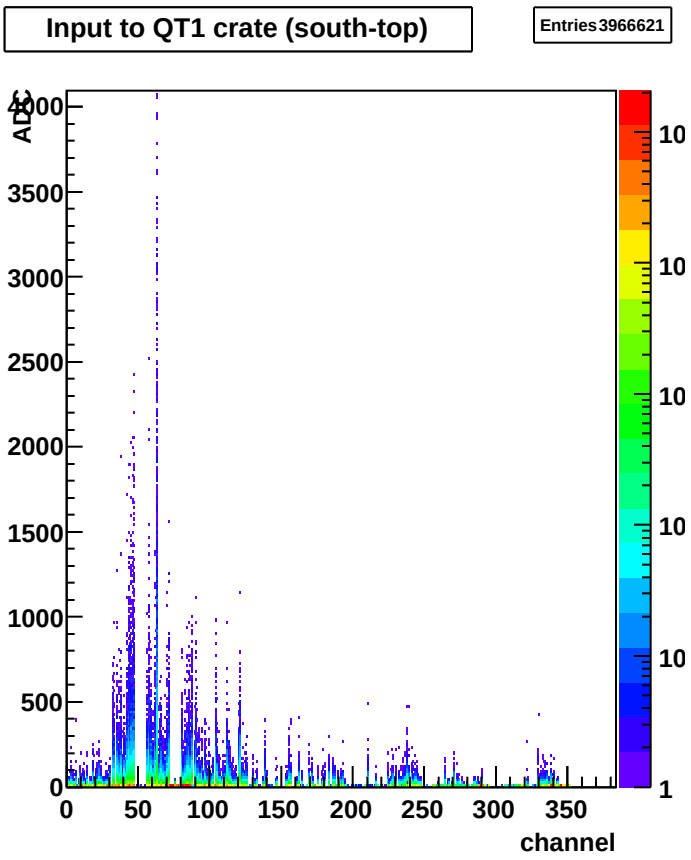

BBClarge-TAC-W
Entries 424005
Mean 1205
RMS 380.6

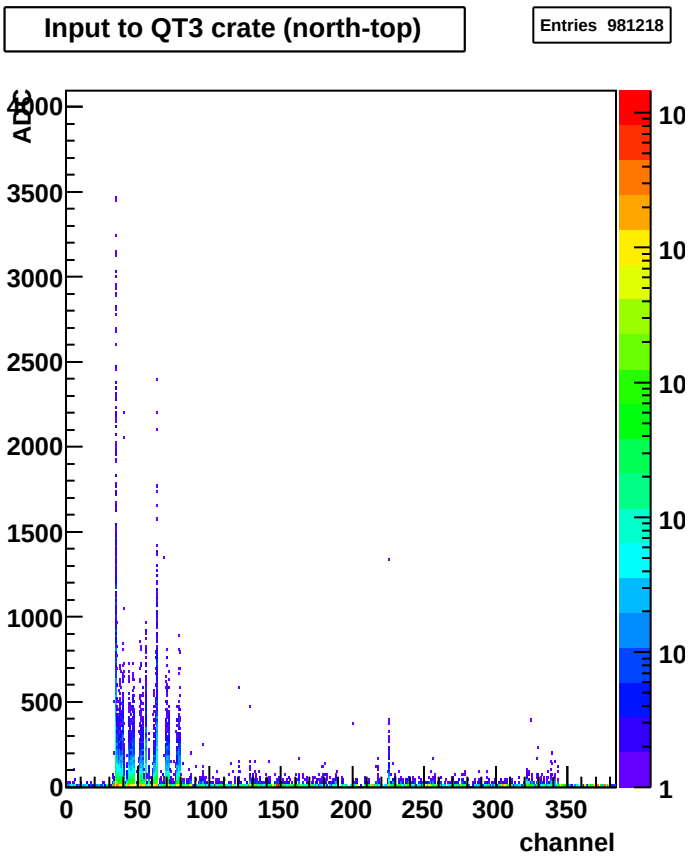

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 416981
Mean 3988
RMS 490.9

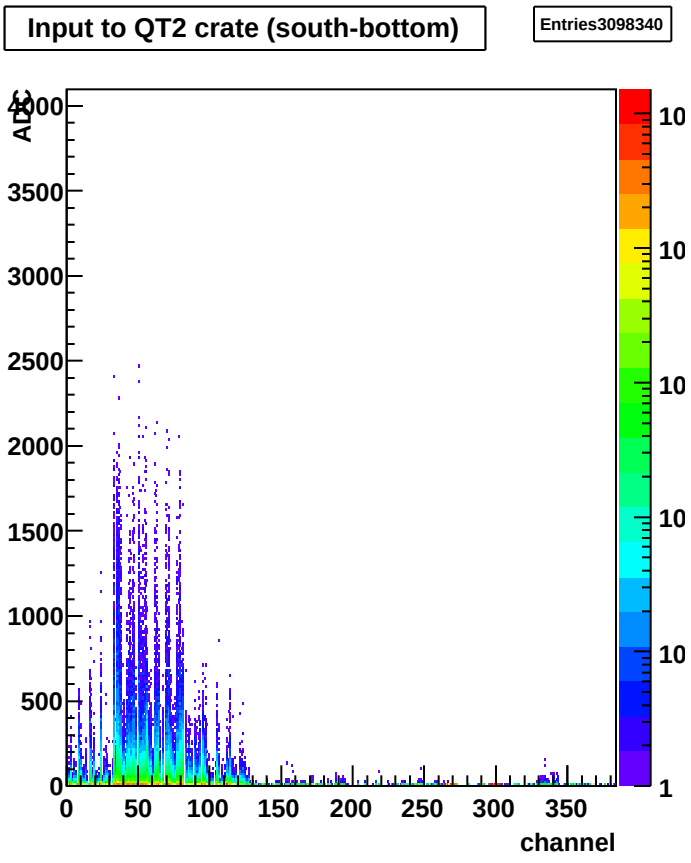

VPD-TAC-E
Entries 0
Mean 0
RMS 0

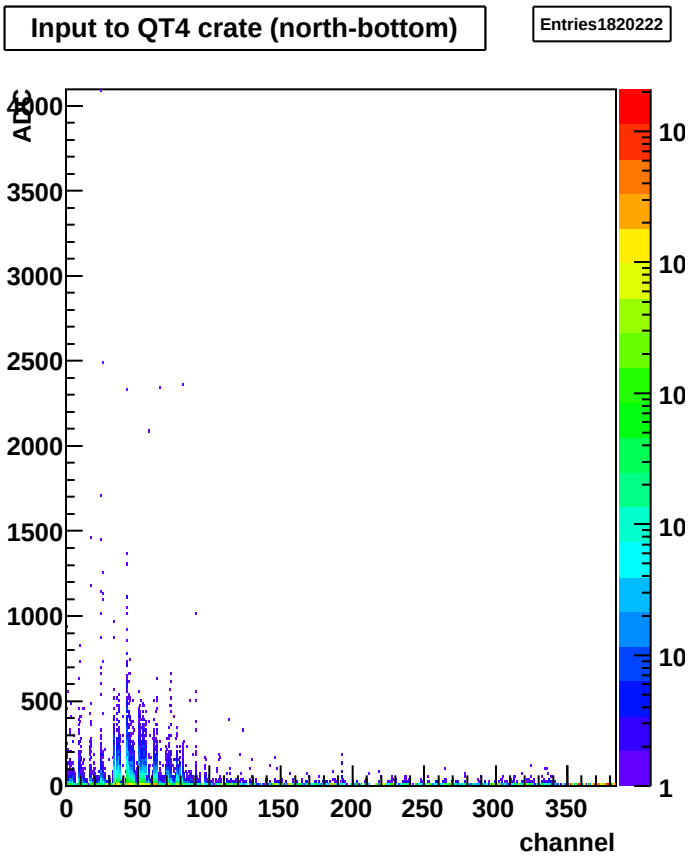

VPD-TAC-W
Entries 0
Mean 0
RMS 0

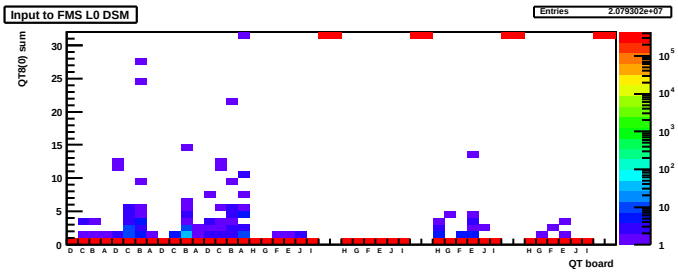

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

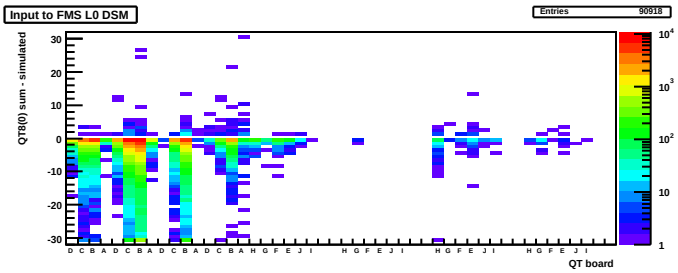

Input to QT1 crate (south-top)
Entries 3966621
ADC
channel

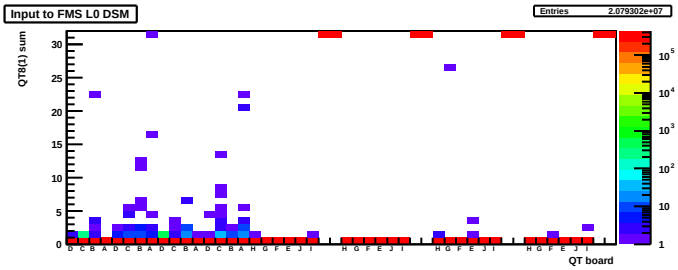

Input to QT3 crate (north-top)
Entries 981218
ADC
channel

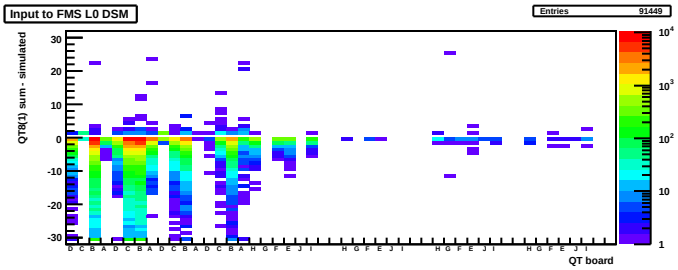

Input to QT2 crate (south-bottom)
Entries 3098340
ADC
channel

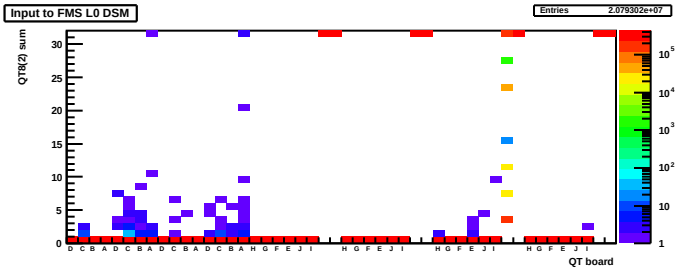

Input to QT4 crate (north-bottom)
Entries 1820222
ADC
channel

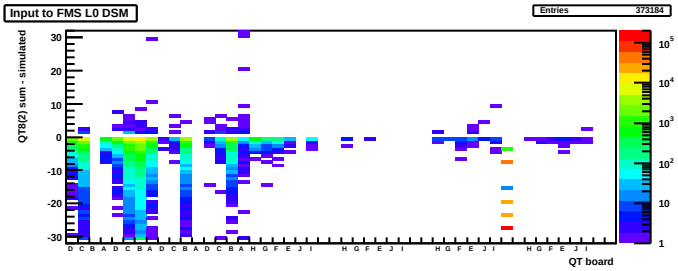

Input to FMS L0 DSM
Entries 2.079302e+07
QT8(0) sum
QT board

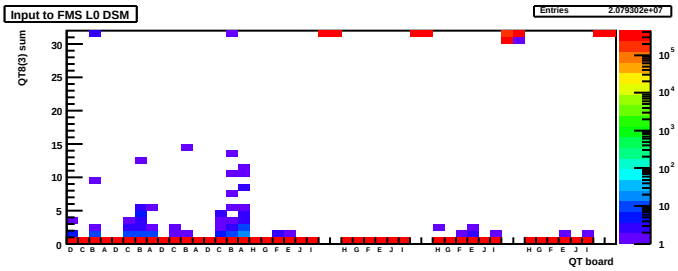

Input to FMS L0 DSM
Entries 90918
QT8(0) sum - simulated
QT board

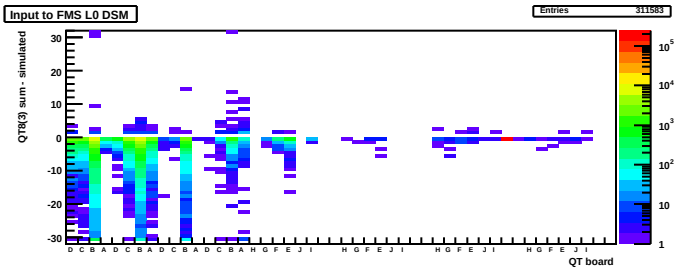

Input to FMS L0 DSM
Entries 2.079302e+07
QT8(1) sum
QT board

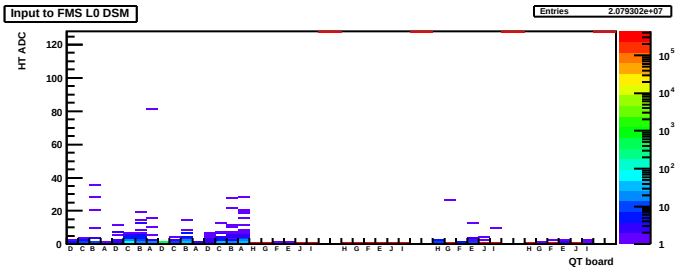

Input to FMS L0 DSM
Entries 91449
QT8(1) sum - simulated
QT board

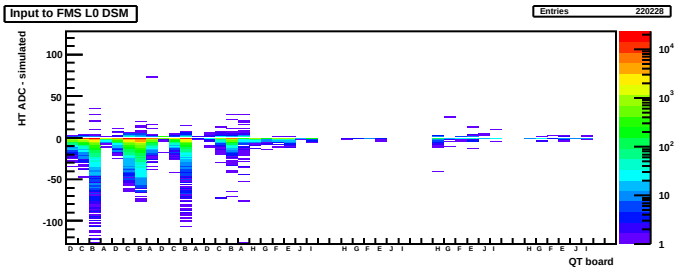

Input to FMS L0 DSM
Entries 2.079302e+07
QT8(2) sum
QT board

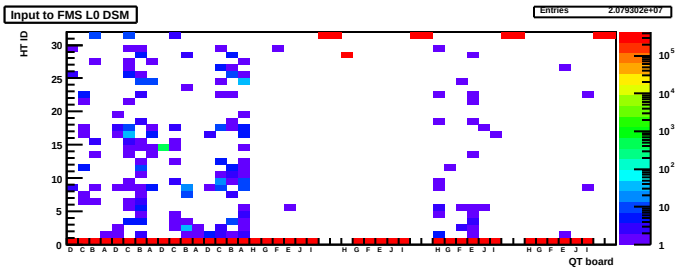

Input to FMS L0 DSM
Entries 373184
QT8(2) sum - simulated
QT board

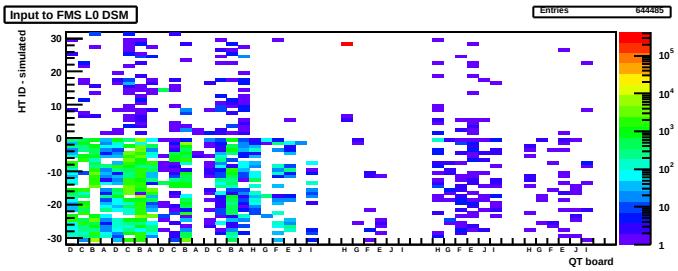

Input to FMS L0 DSM
Entries 2.079302e+07
QT8(3) sum
QT board

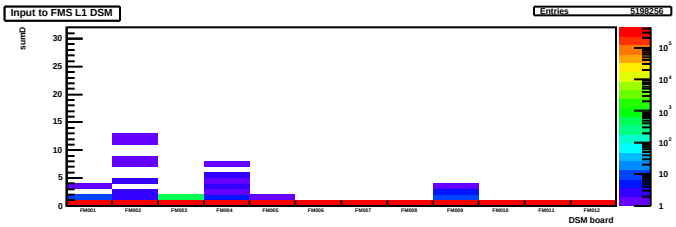

Input to FMS L0 DSM
Entries 311583
QT8(3) sum - simulated
QT board

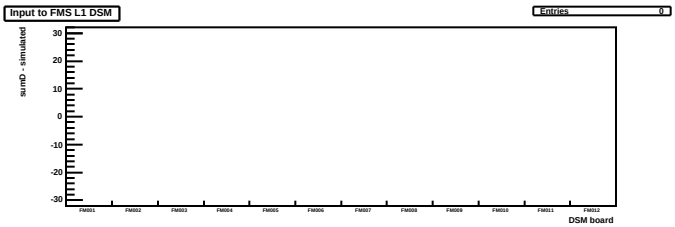

Input to FMS L0 DSM
Entries 2.079302e+07
HT ADC
QT board

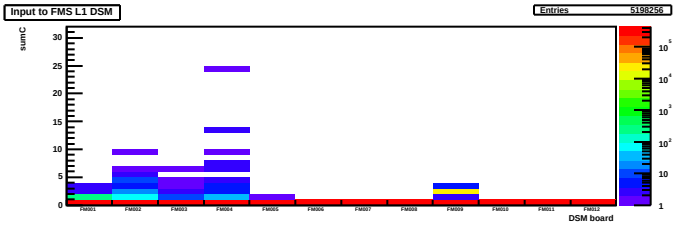

Input to FMS L0 DSM
Entries 220228
HT ADC - simulated
QT board

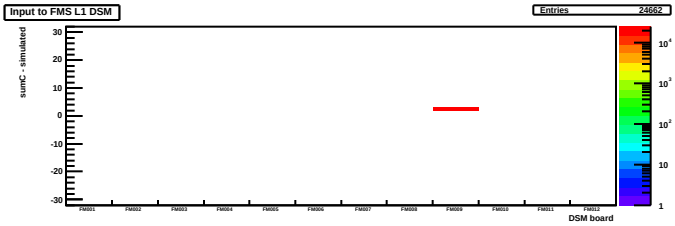

Input to FMS L0 DSM
Entries 2.079302e+07
HT ID
QT board

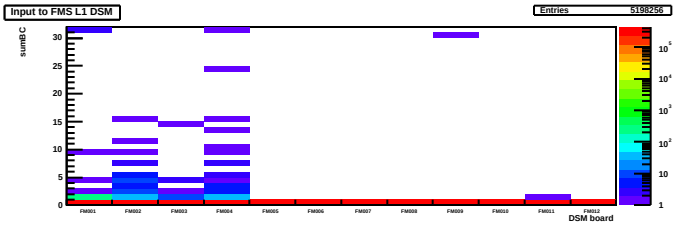

Input to FMS L0 DSM
Entries 644485
HT ID - simulated
QT board

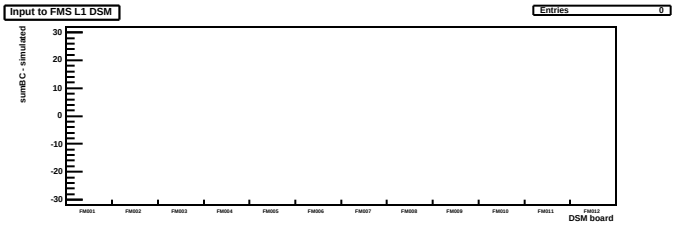

Input to FMS L1 DSM
Entries 5198256
sumD
DSM board

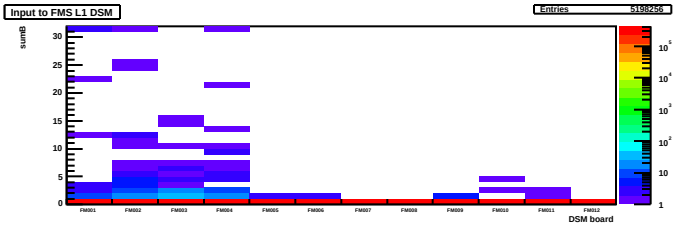

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

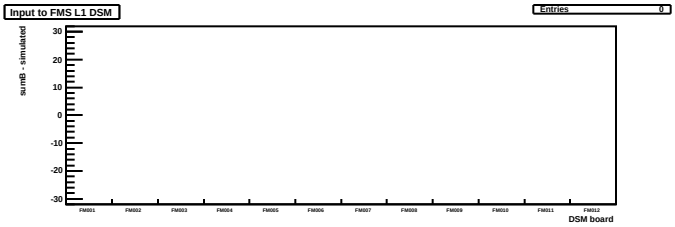

Input to FMS L1 DSM
Entries 5198256
sumC
DSM board

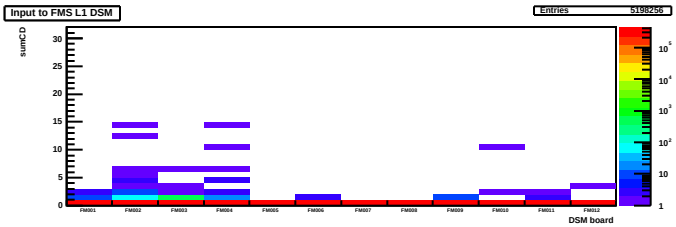

Input to FMS L1 DSM
Entries 24662
sumC - simulated
DSM board

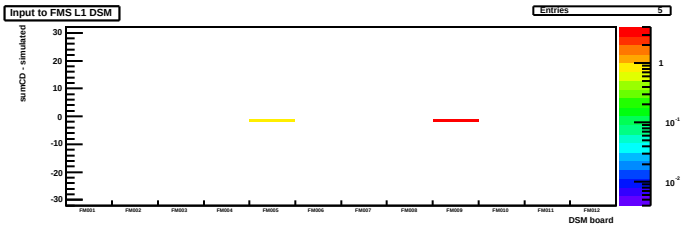

Input to FMS L1 DSM
Entries 5198256
sumBC
DSM board

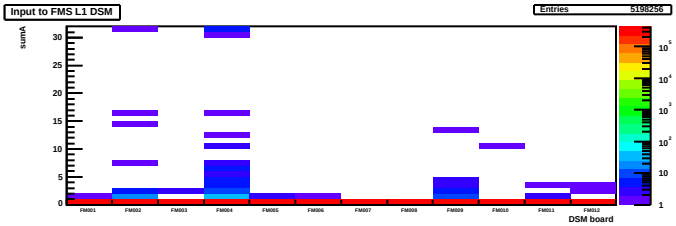

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

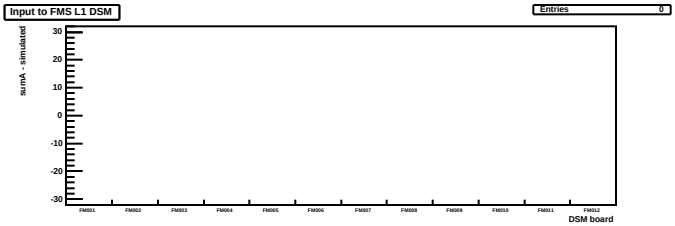

Input to FMS L1 DSM
Entries 5198256
sumB
DSM board

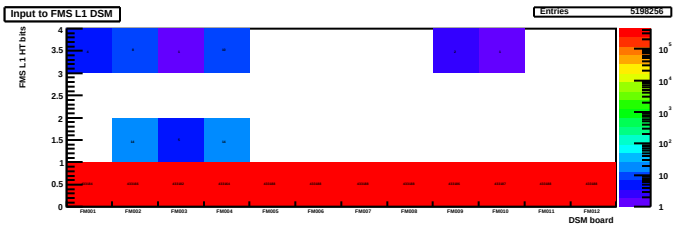

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

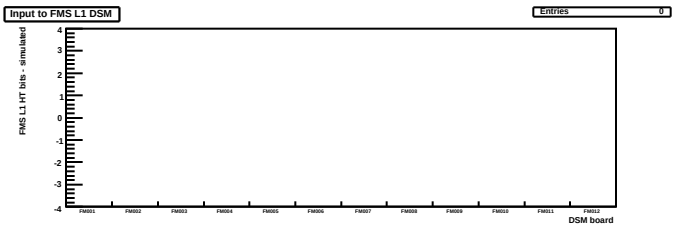

Input to FMS L1 DSM
Entries 5198256
sumCD
DSM board

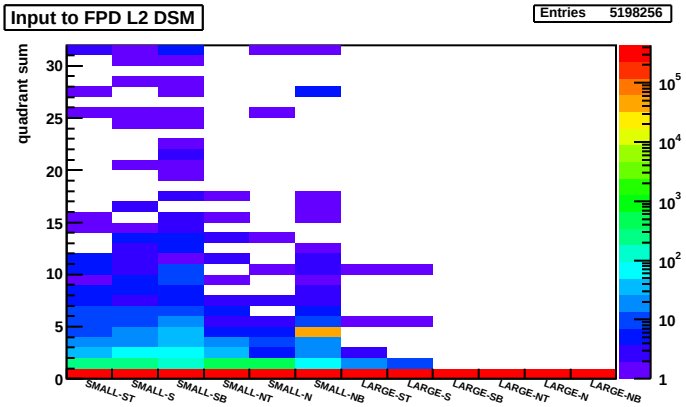

Input to FMS L1 DSM
Entries 5
sumCD - simulated
DSM board

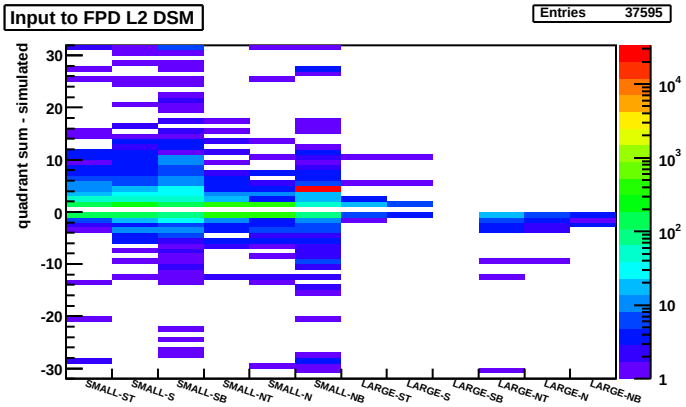

Input to FMS L1 DSM
Entries 5198256
sumA
DSM board

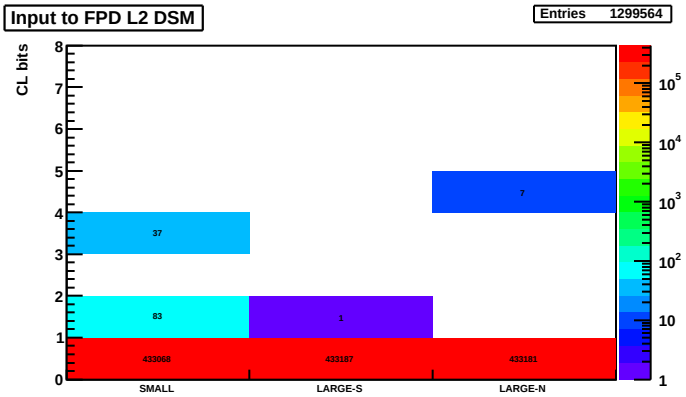

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

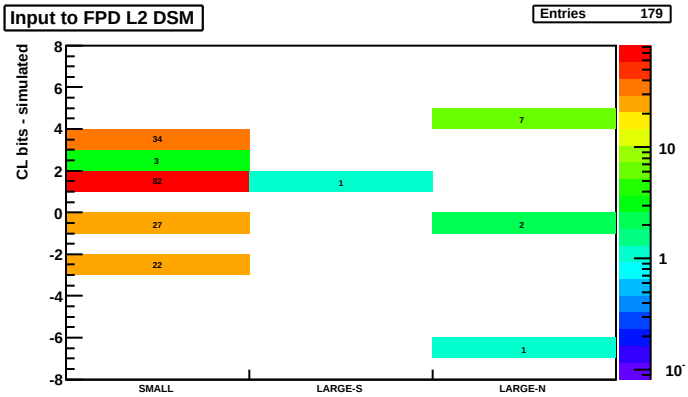

Input to FMS L1 DSM
Entries 5198256
FMS L1 HT bits
DSM board

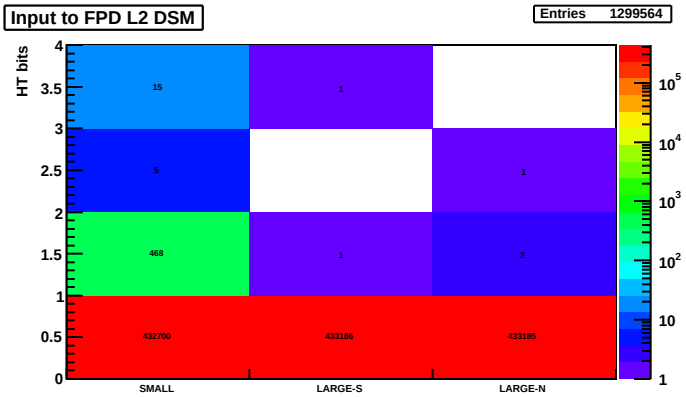

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

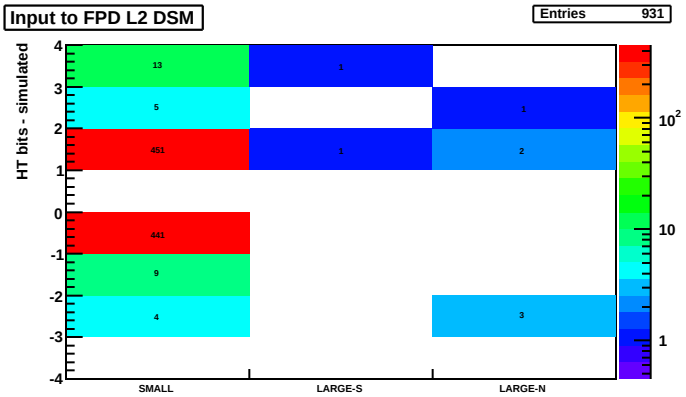

Input to FPD L2 DSM
Entries 5198256
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

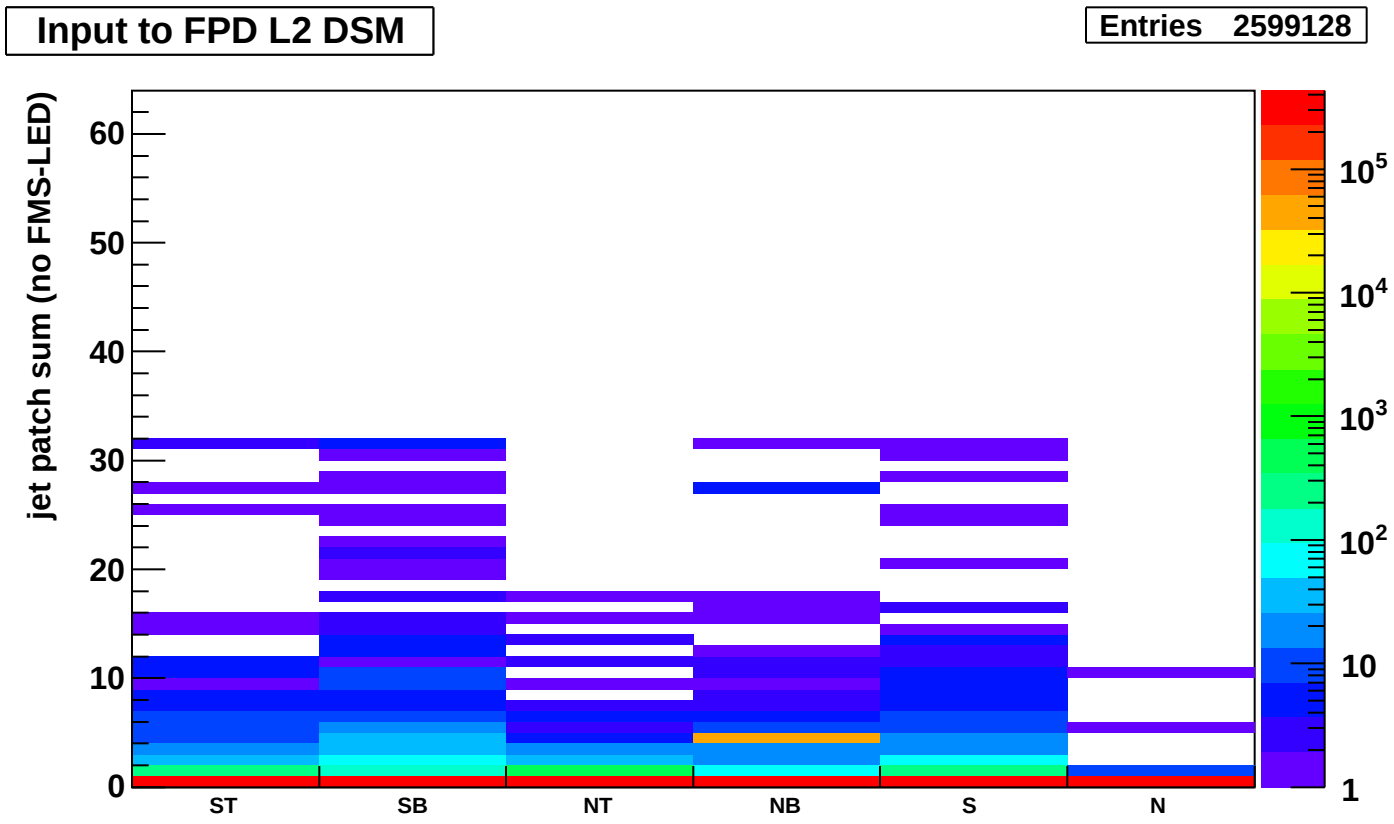

Input to FPD L2 DSM
Entries 37595
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

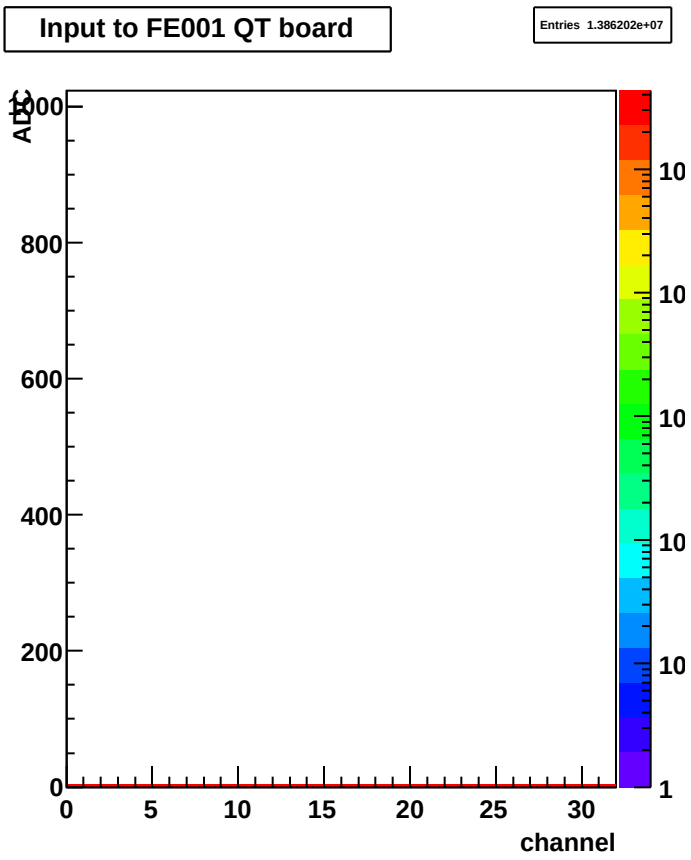

Input to FPD L2 DSM
Entries 1299564
CL bits
37
7
83
1
433068
433187
433181
SMALL
LARGE-S
LARGE-N

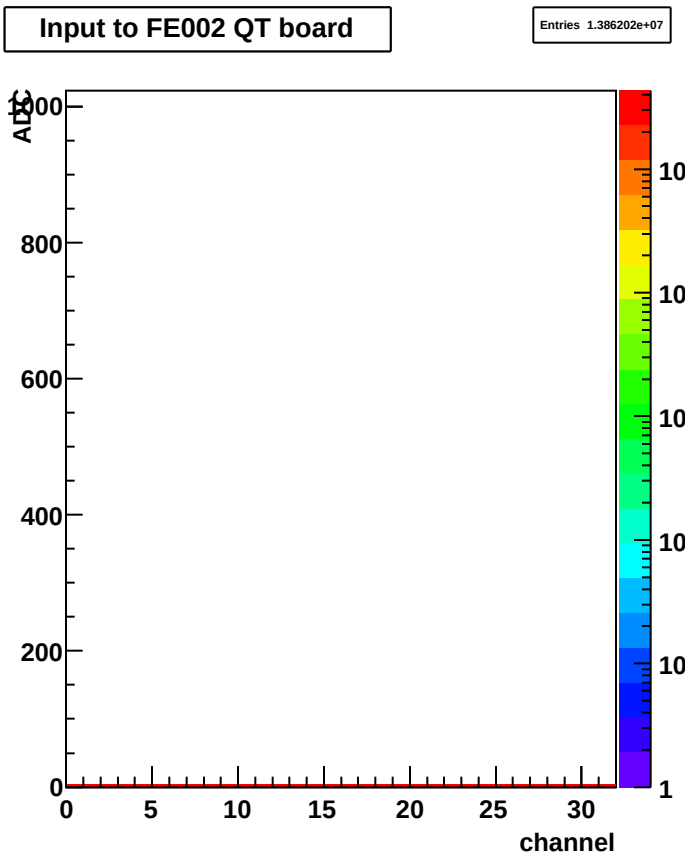

Input to FPD L2 DSM
Entries 179
CL bits - simulated
34
3
82
1
27
22
7
2
1
SMALL
LARGE-S
LARGE-N

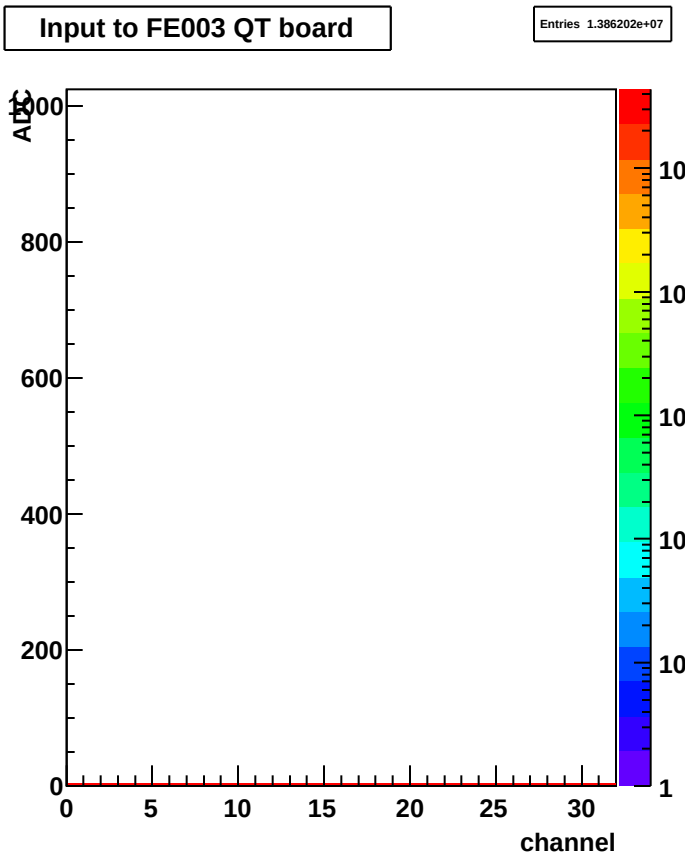

Input to FPD L2 DSM
Entries 1299564
HT bits
15
1
5
1
468
1
2
432700
433186
433185
SMALL
LARGE-S
LARGE-N

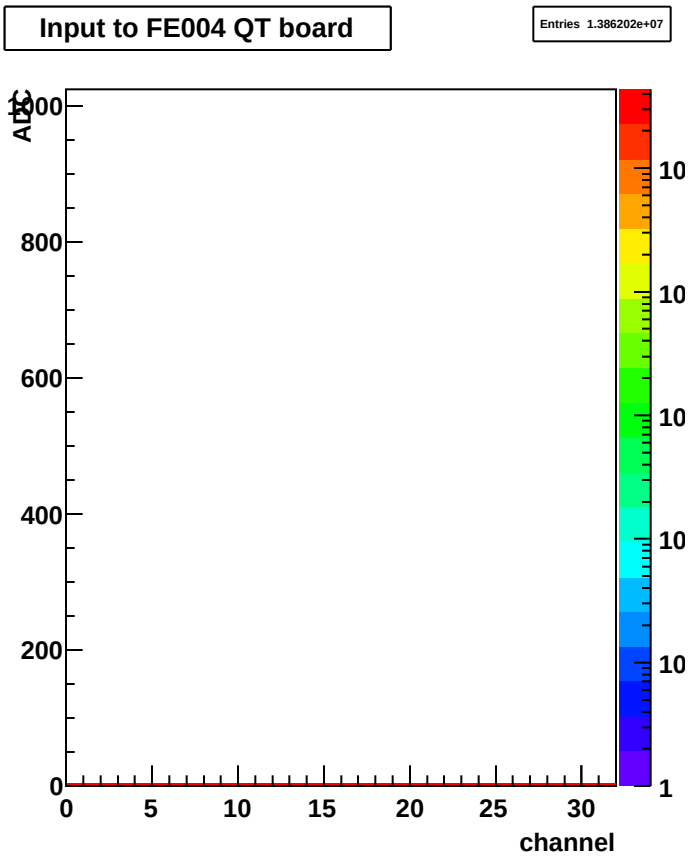

Input to FPD L2 DSM
Entries 931
HT bits - simulated
13
1
5
1
451
1
2
441
9
4
3
SMALL
LARGE-S
LARGE-N

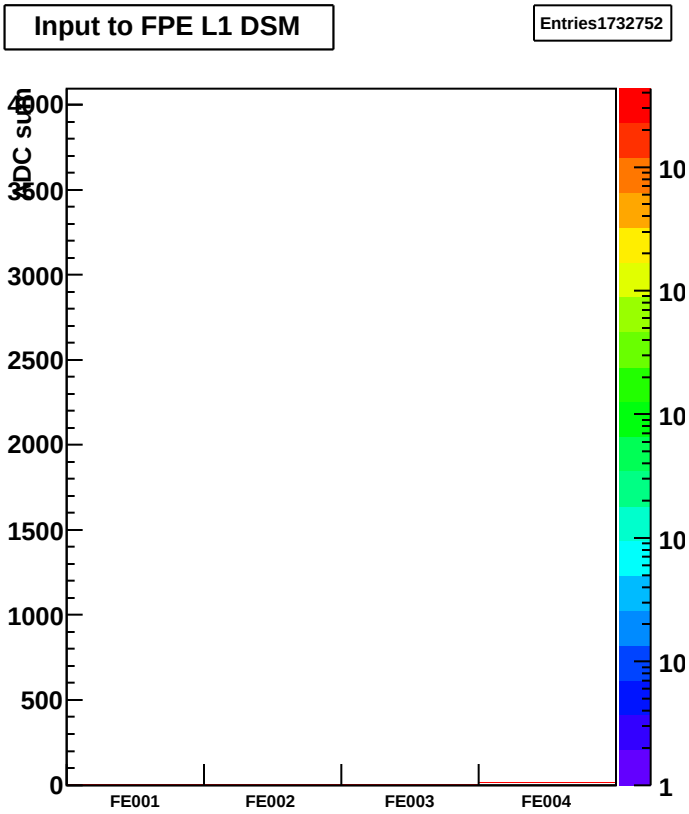

Input to FPD L2 DSM
Entries 2599128
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

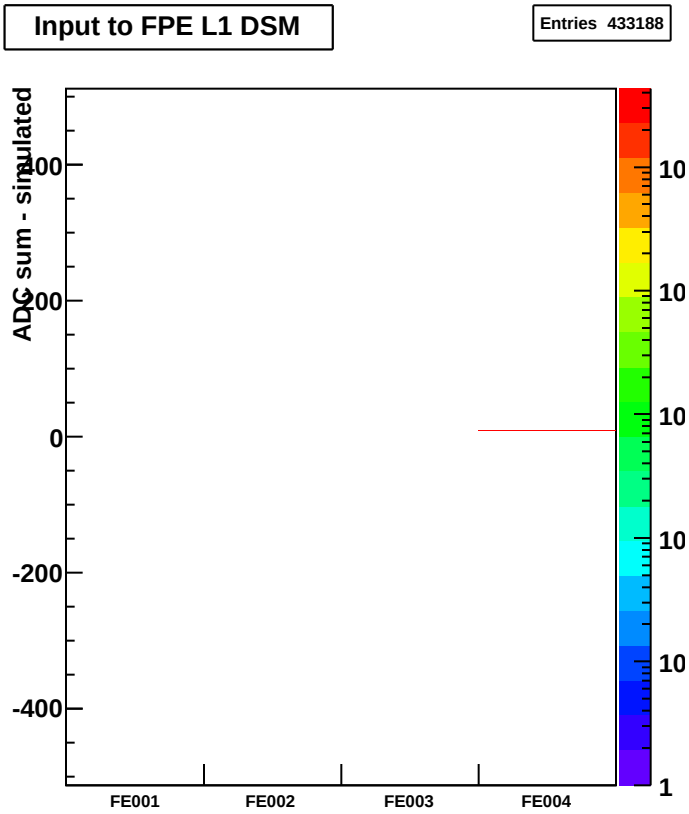

Input to FE001 QT board
Entries 1.386202e+07
ADC
channel

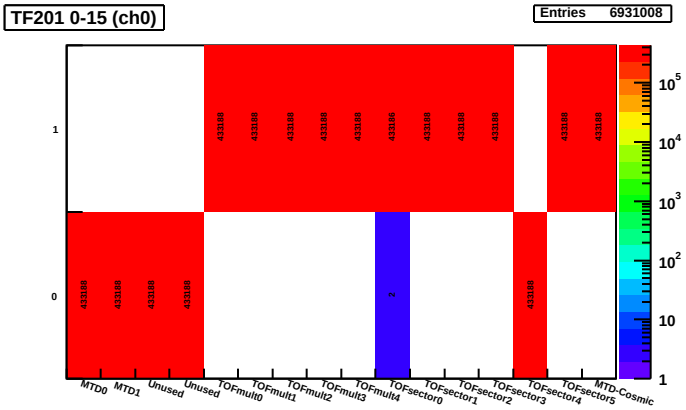

Input to FE002 QT board
Entries 1.386202e+07
ADC
channel

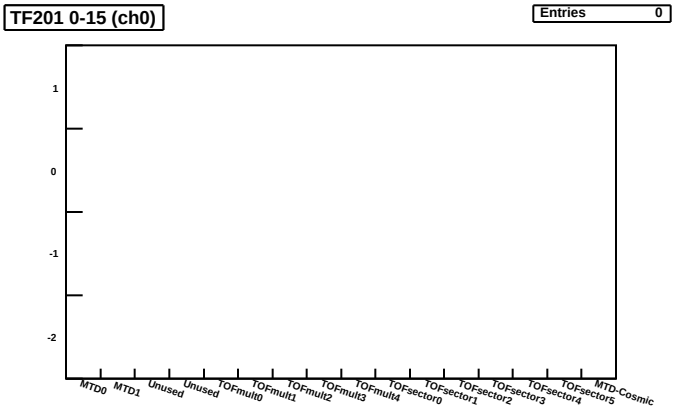

Input to FE003 QT board
Entries 1.386202e+07
ADC
channel

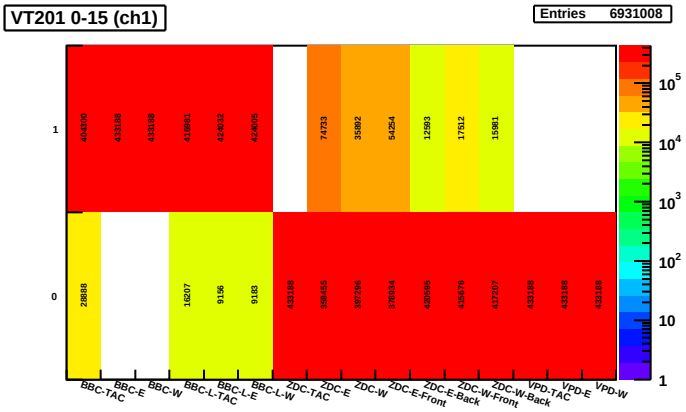

Input to FE004 QT board
Entries 1.386202e+07
ADC
channel

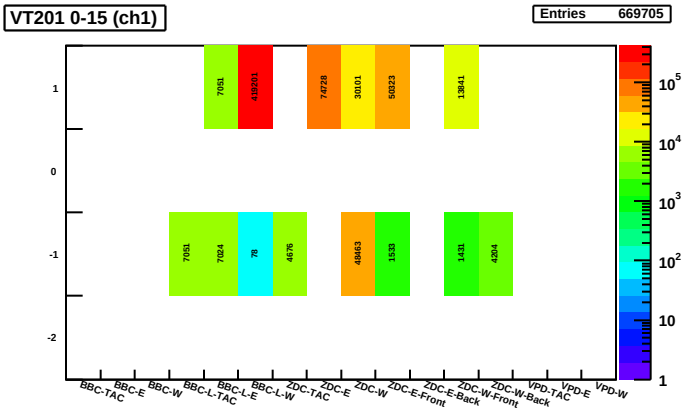

Input to FPE L1 DSM
Entries1732752
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 433188
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 6931008
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 6931008
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
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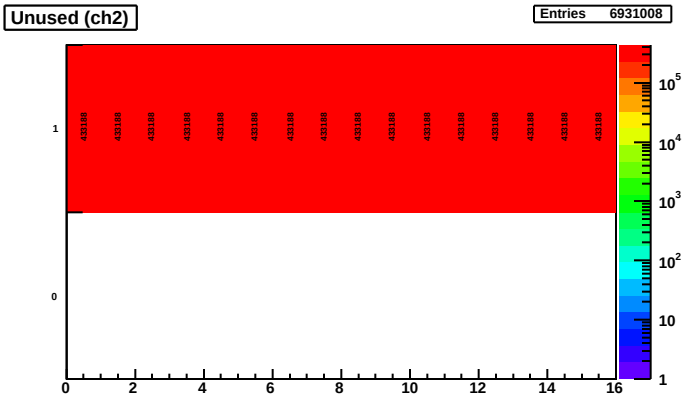
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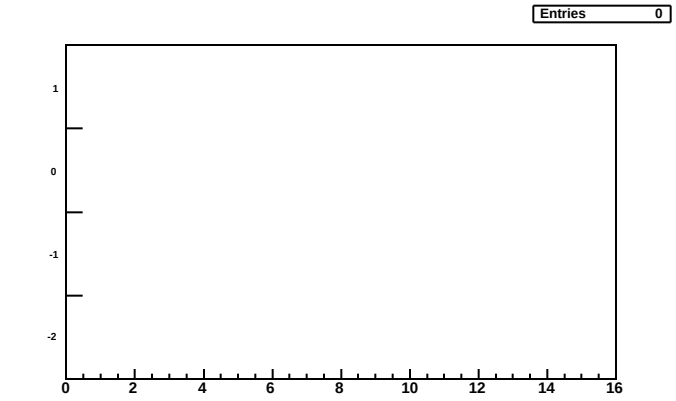
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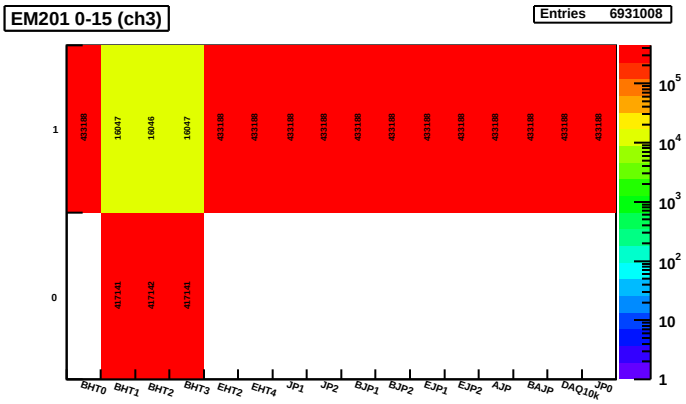
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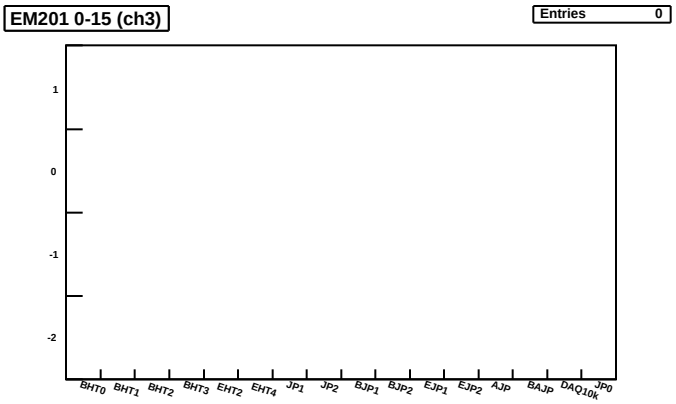
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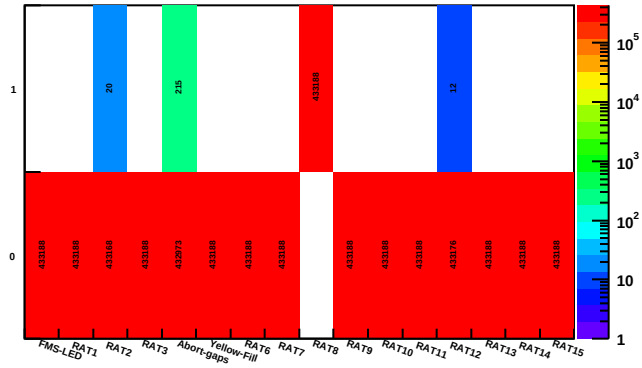
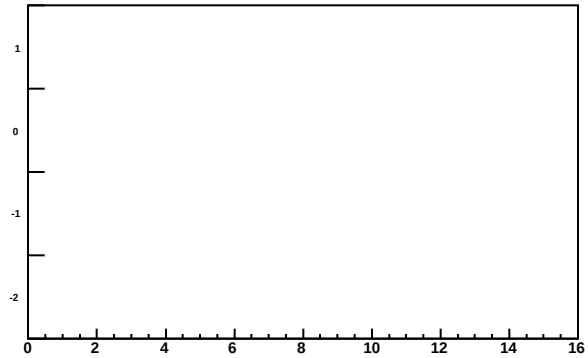
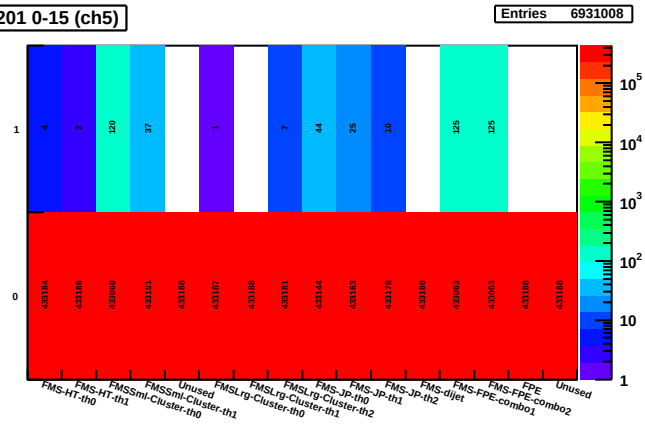
RAT board (ch4)

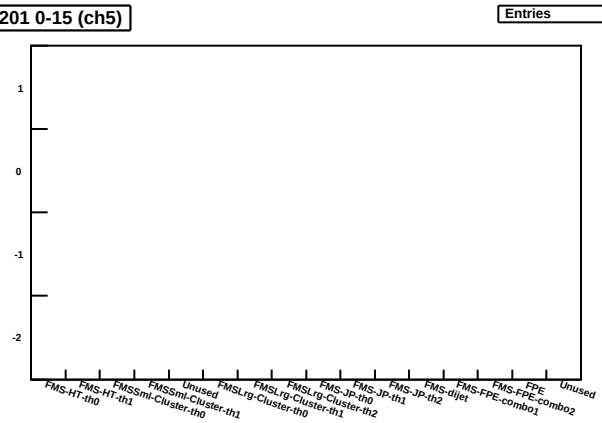




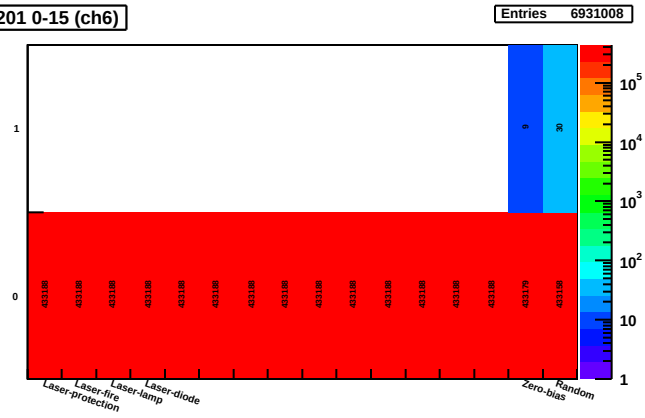
FP201 0-15 (ch5)

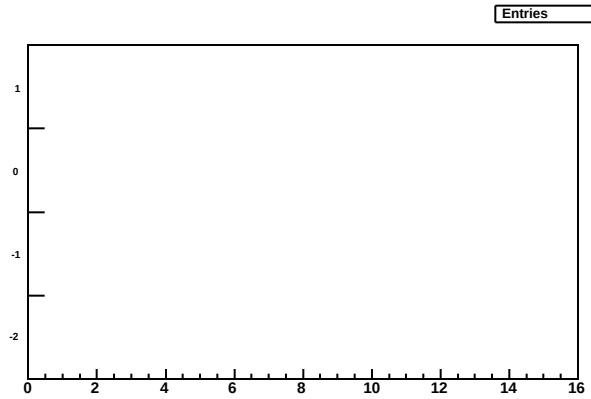
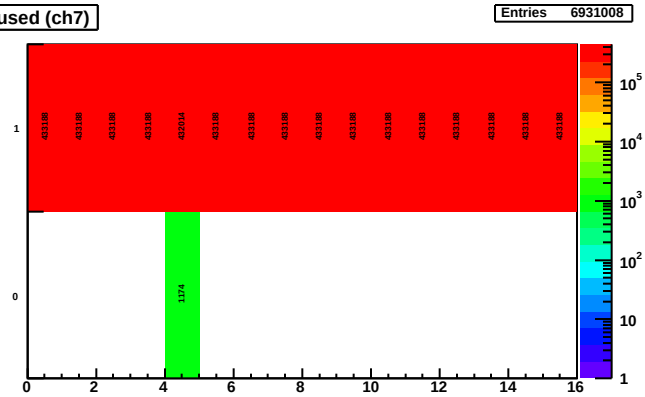
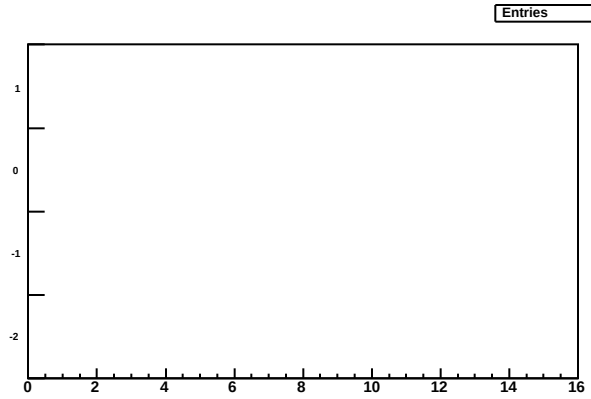


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)