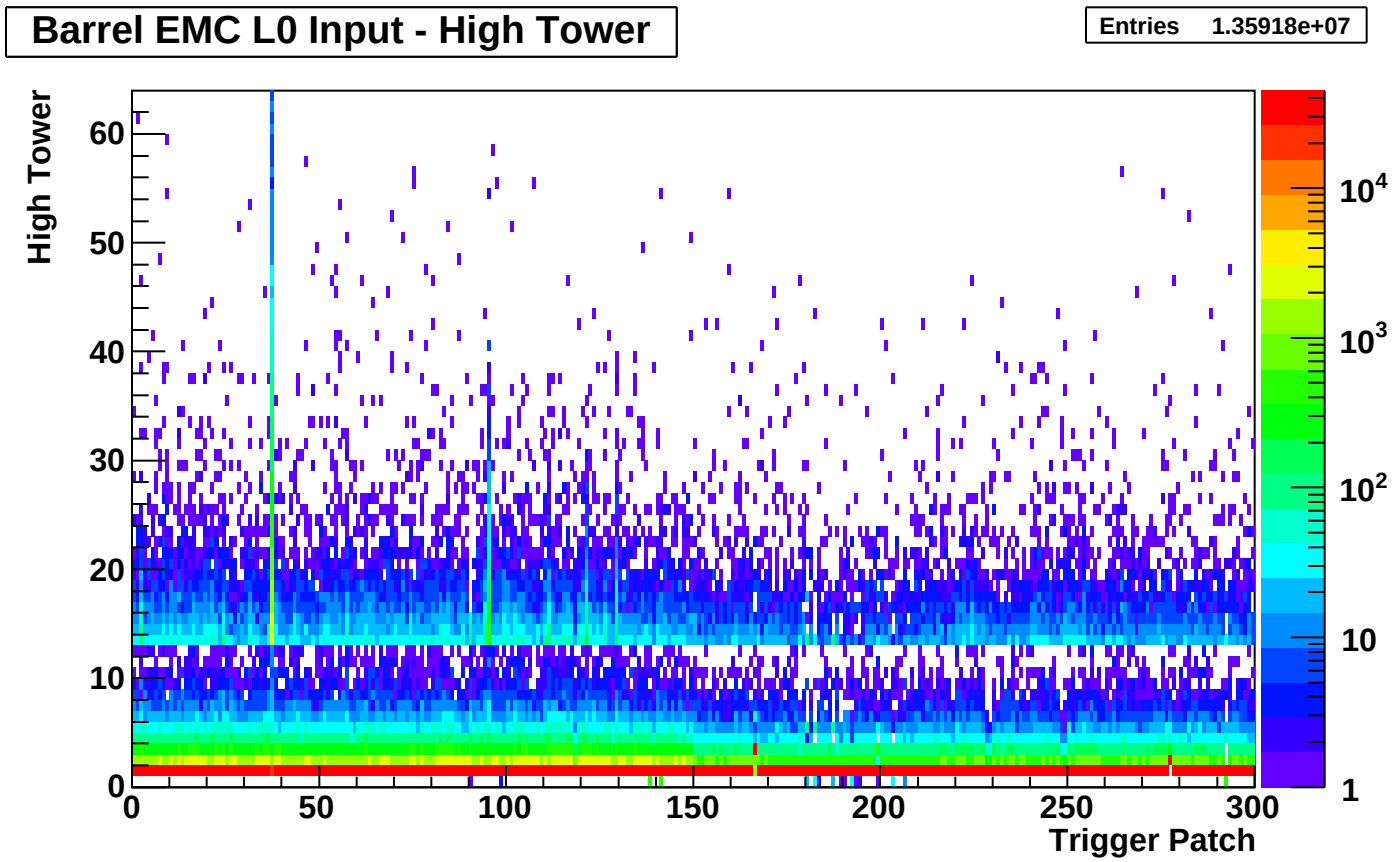
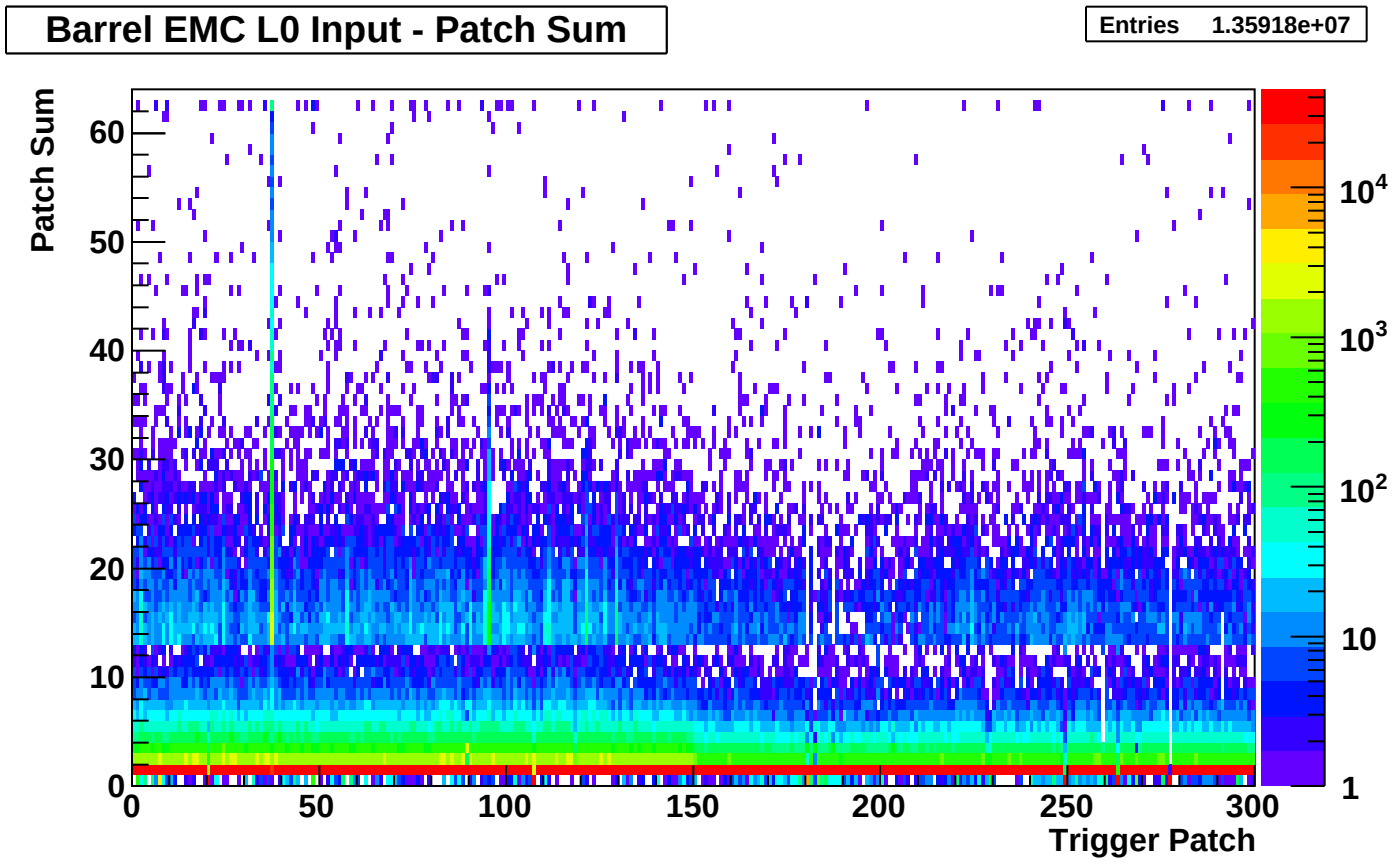
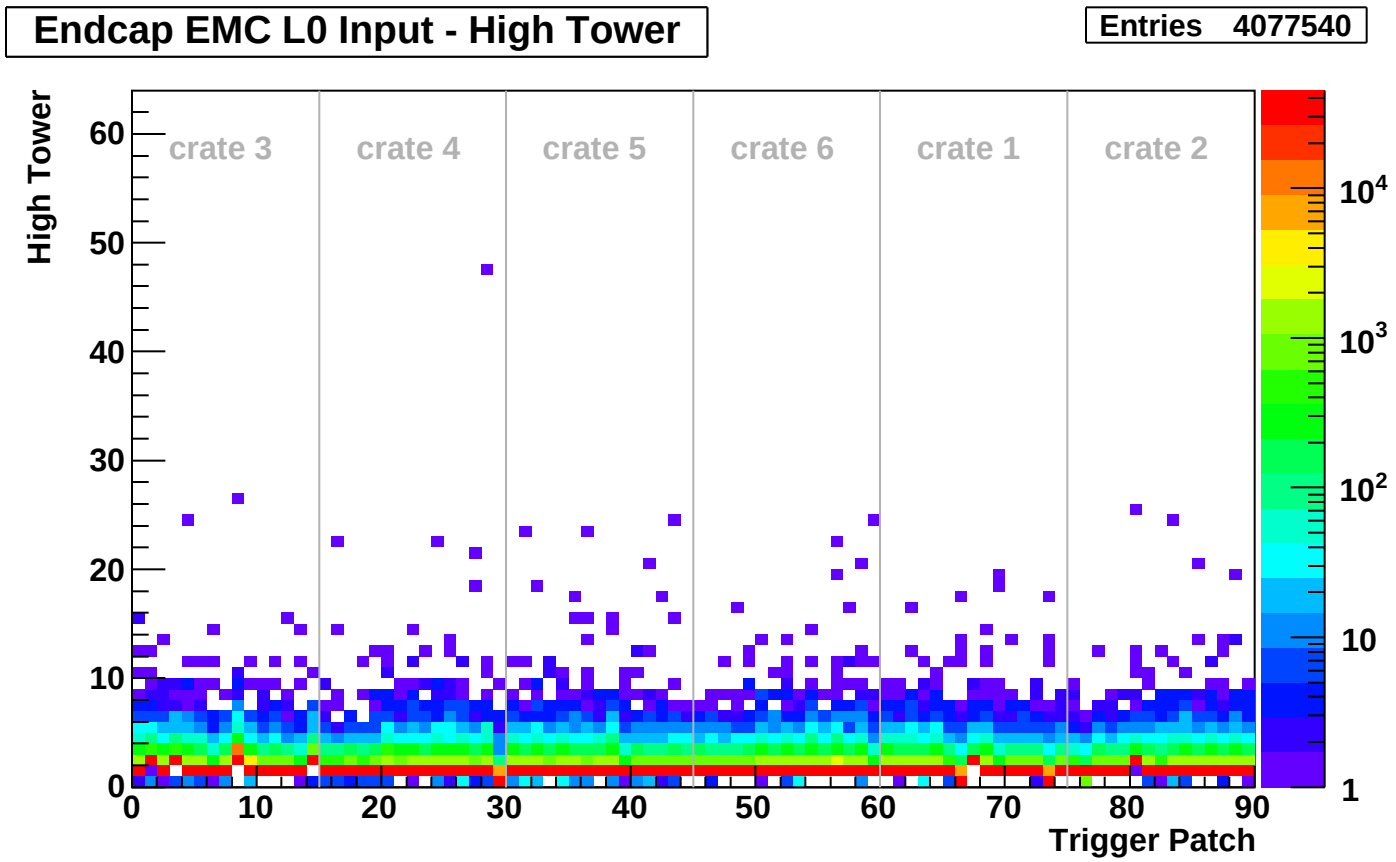
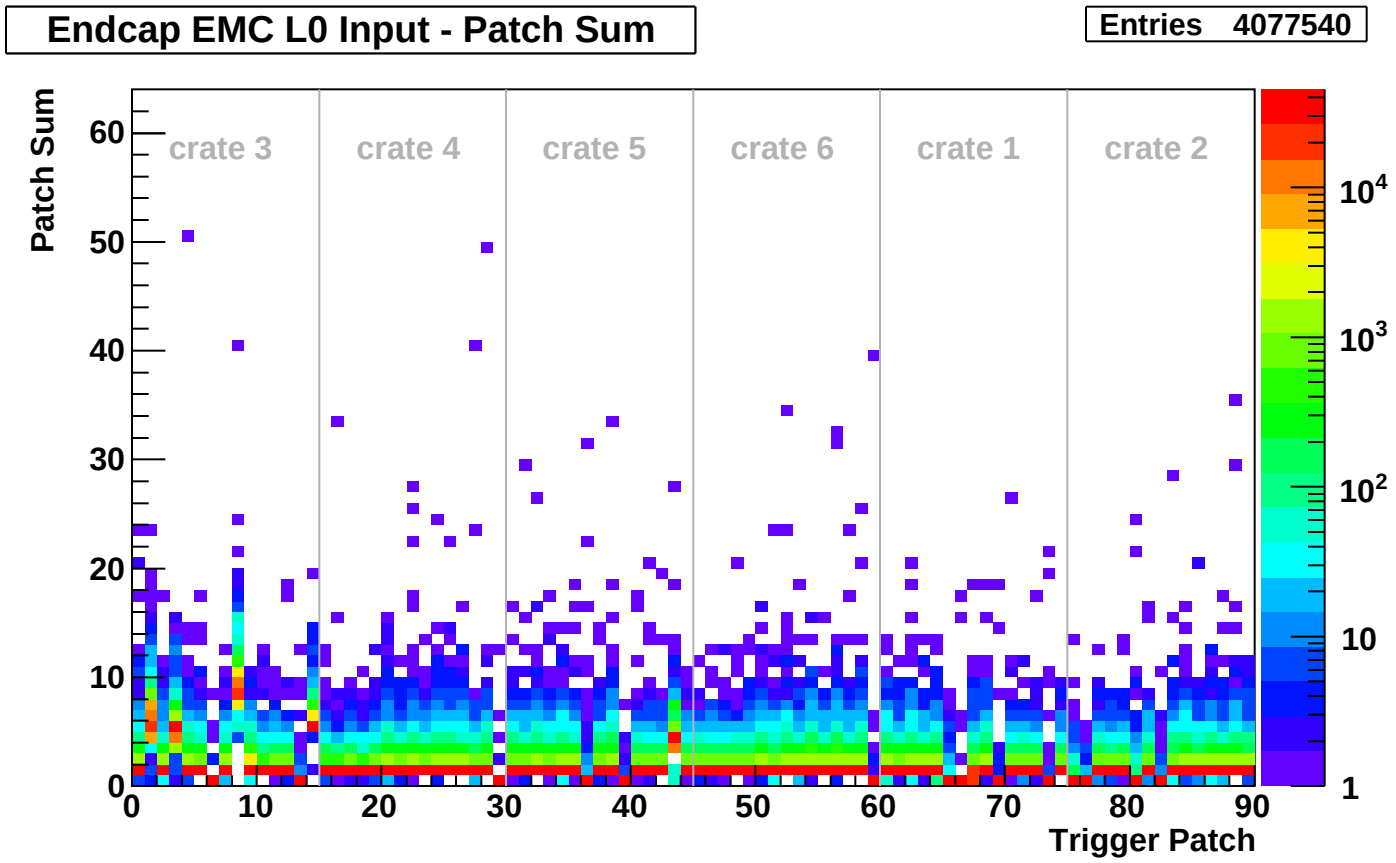
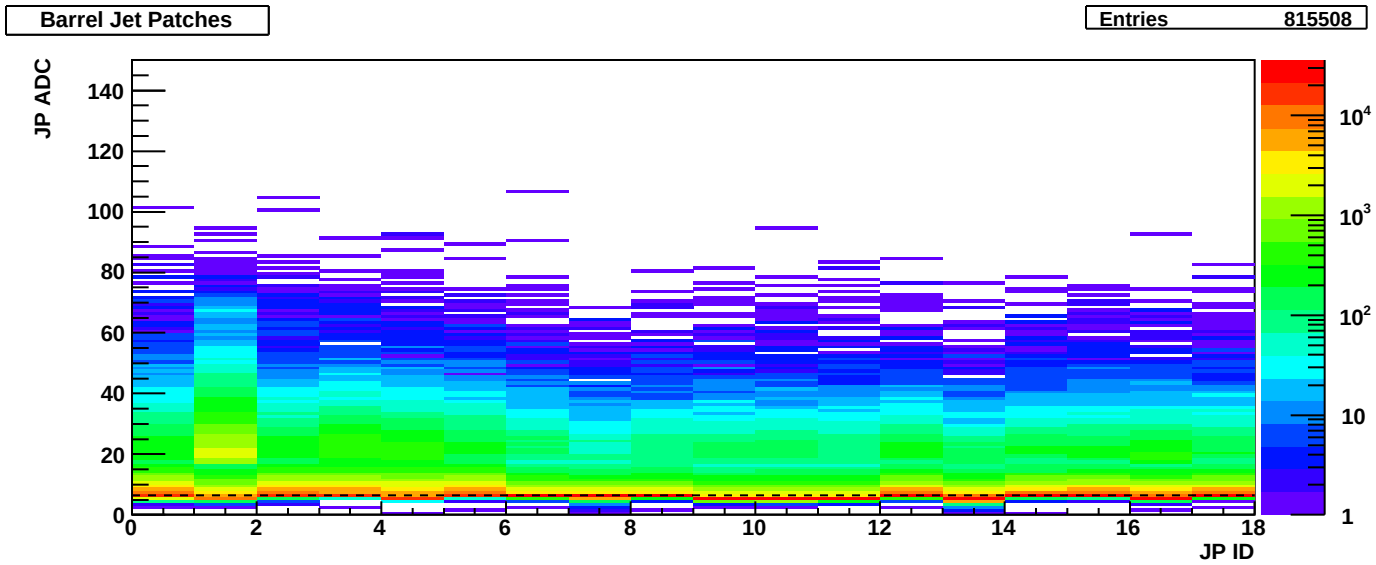
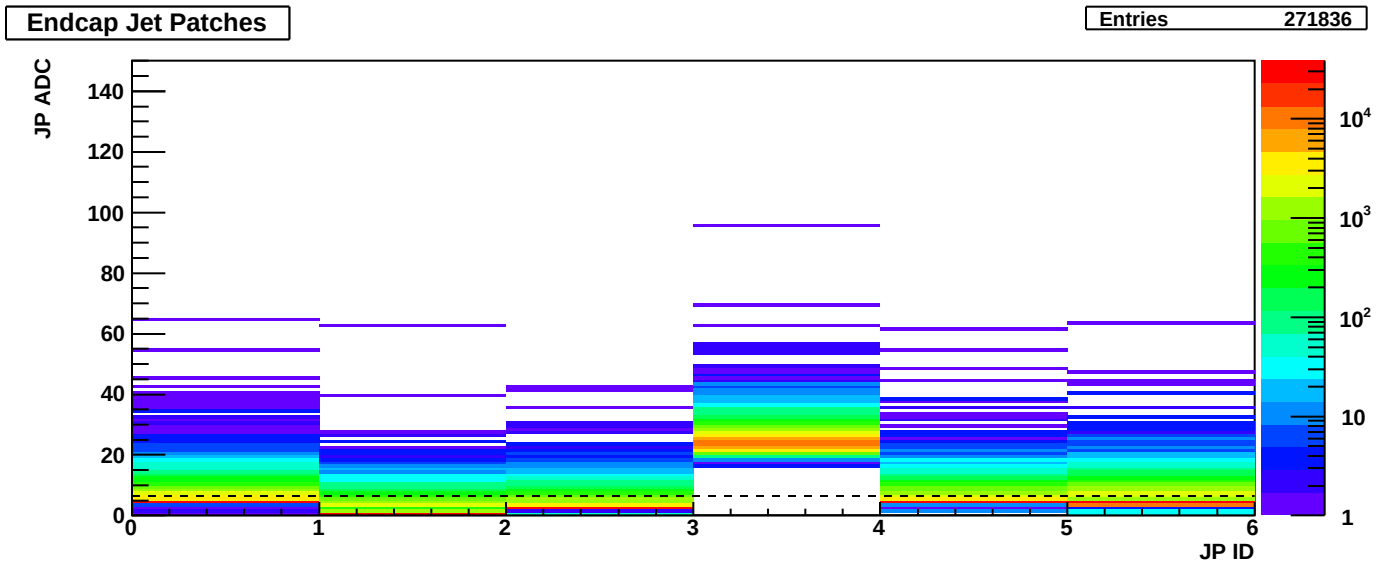

Barrel EMC L0 Input - High Tower
Entries 1.35918e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

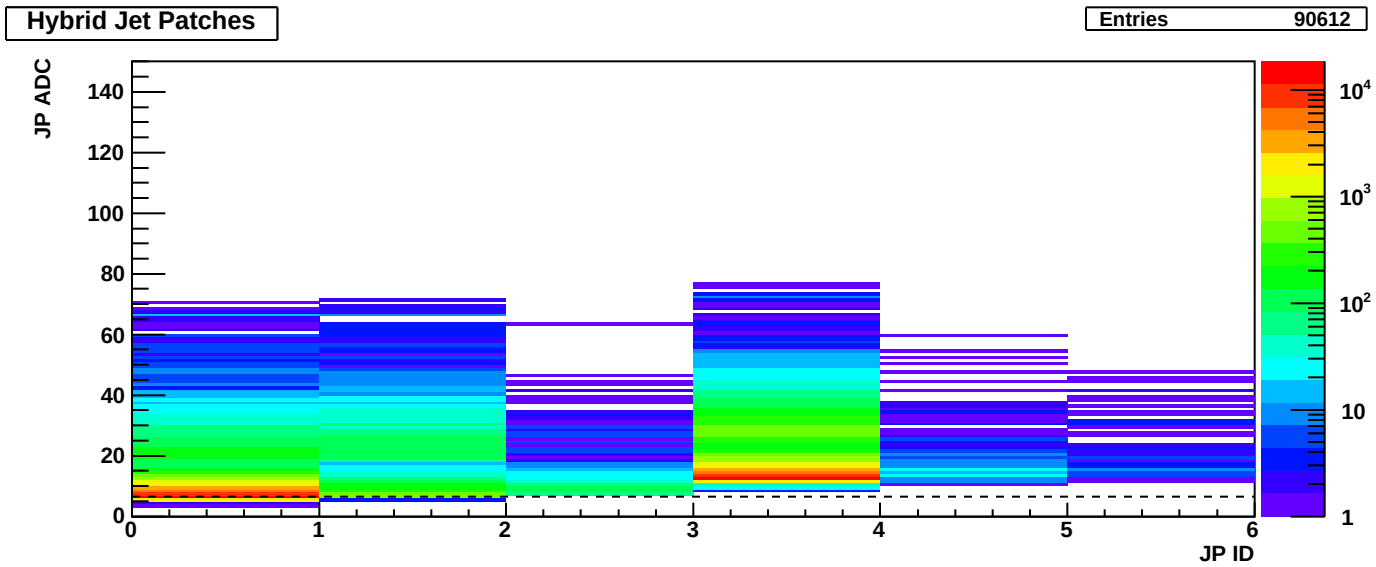

Barrel EMC L0 Input - Patch Sum
Entries 1.35918e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

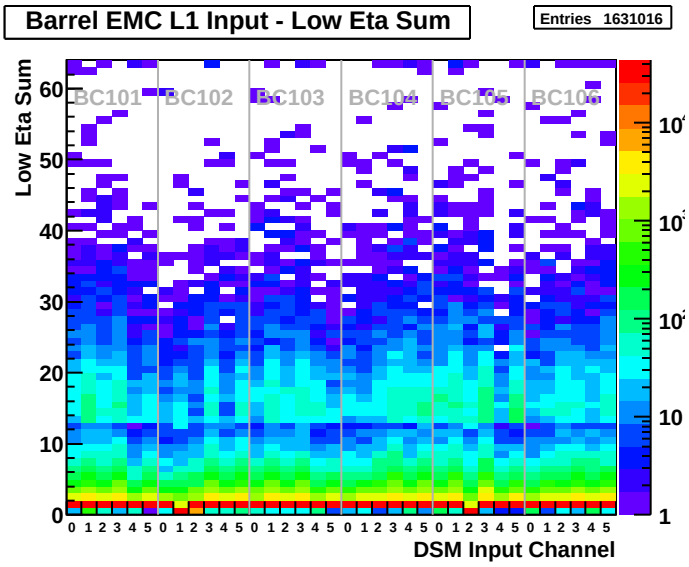

Endcap EMC L0 Input - High Tower
Entries 4077540
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

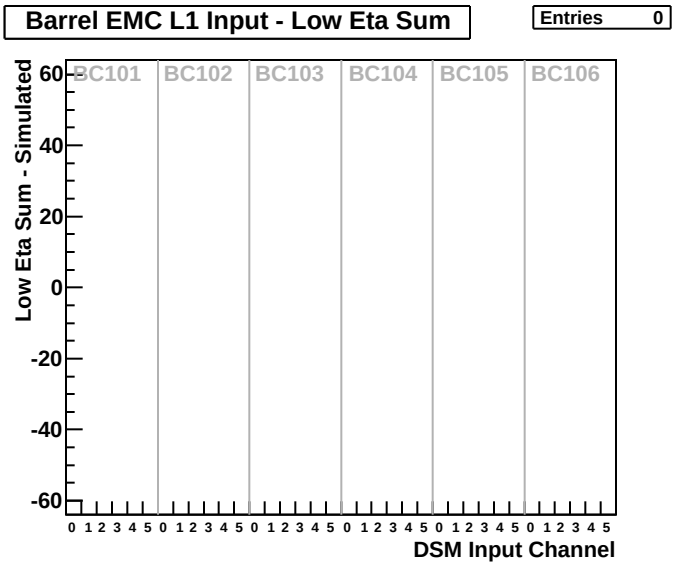

Endcap EMC L0 Input - Patch Sum
Entries 4077540
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

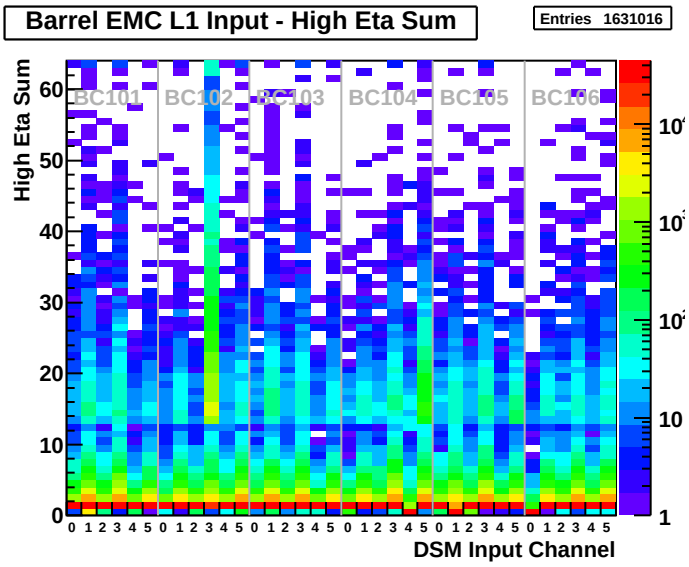

Barrel Jet Patches
Entries 815508
JP ADC
JP ID

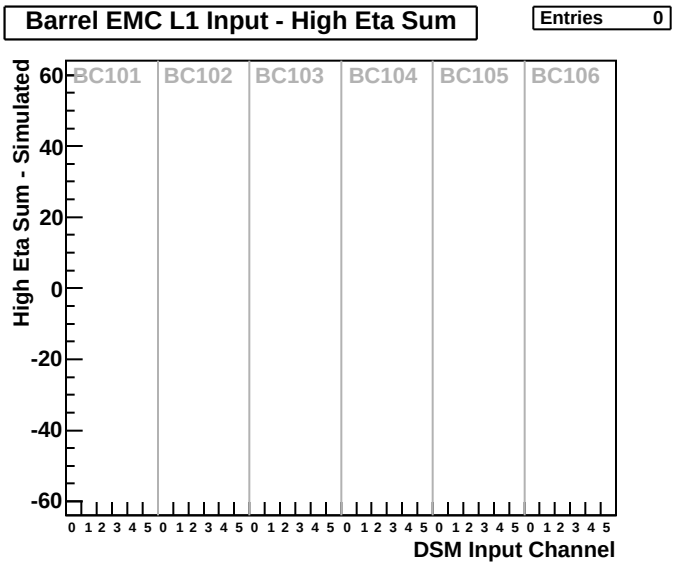

Endcap Jet Patches
Entries 271836
JP ADC
JP ID

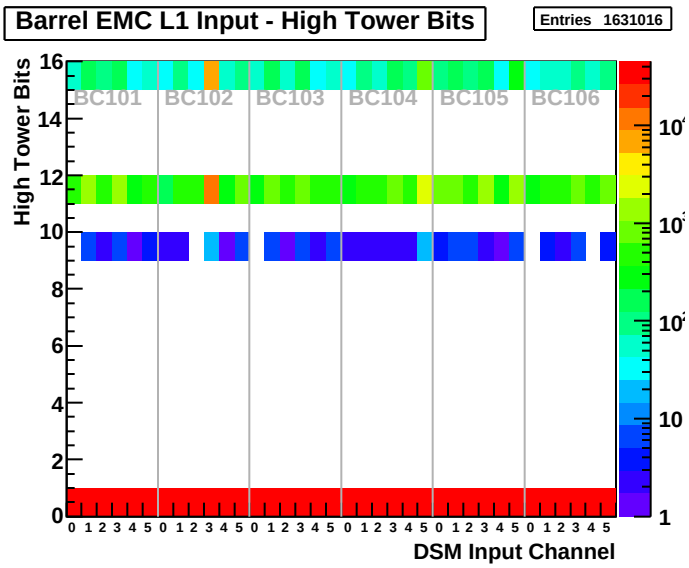

Hybrid Jet Patches
Entries 90612
JP ADC
JP ID

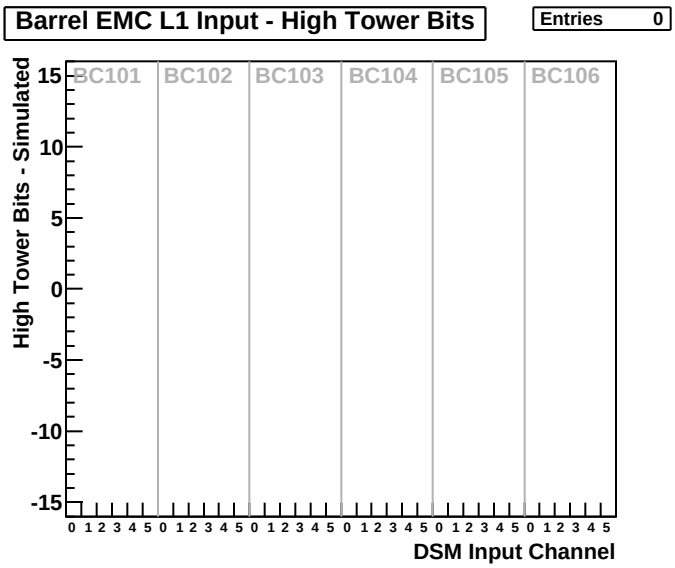

Barrel EMC L1 Input - Low Eta Sum
Entries 1631016
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

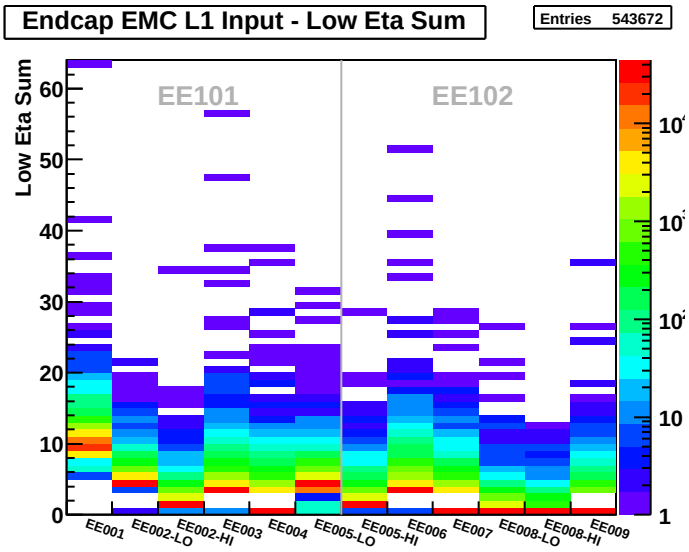

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

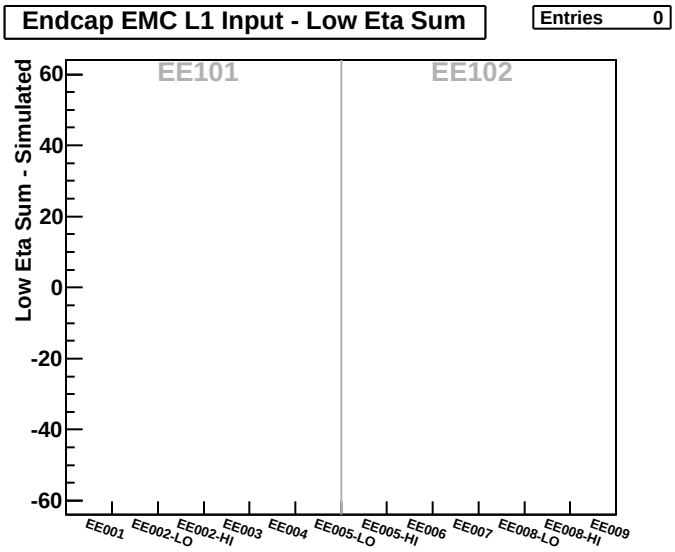

Barrel EMC L1 Input - High Eta Sum
Entries 1631016
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

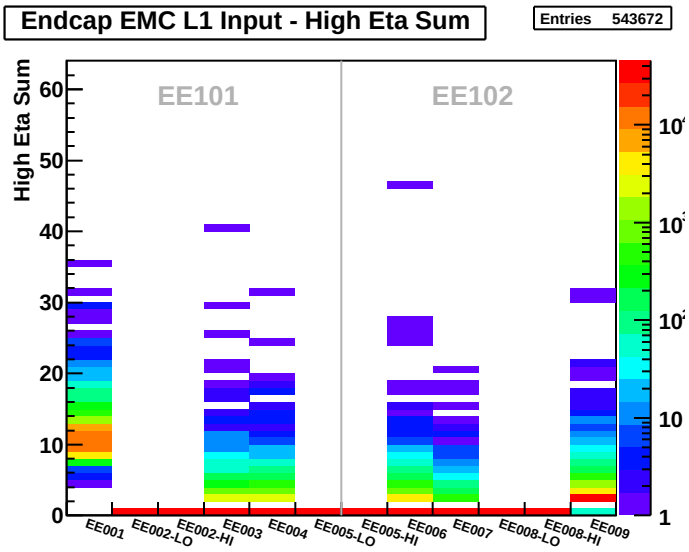

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

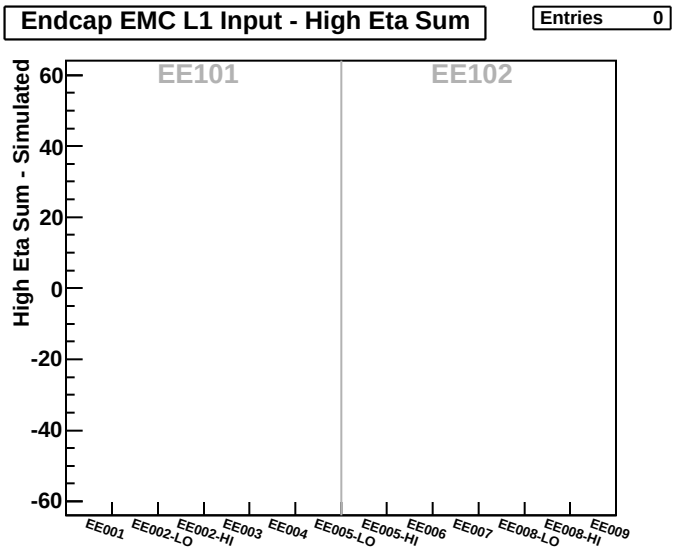

Barrel EMC L1 Input - High Tower Bits
Entries 1631016
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

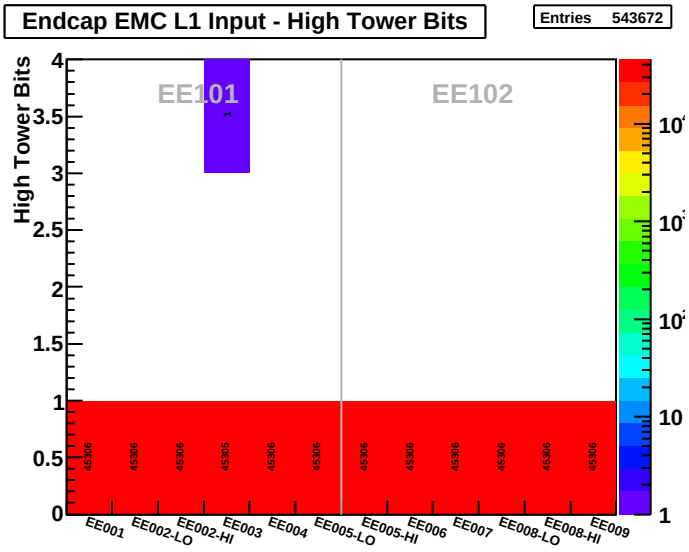

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

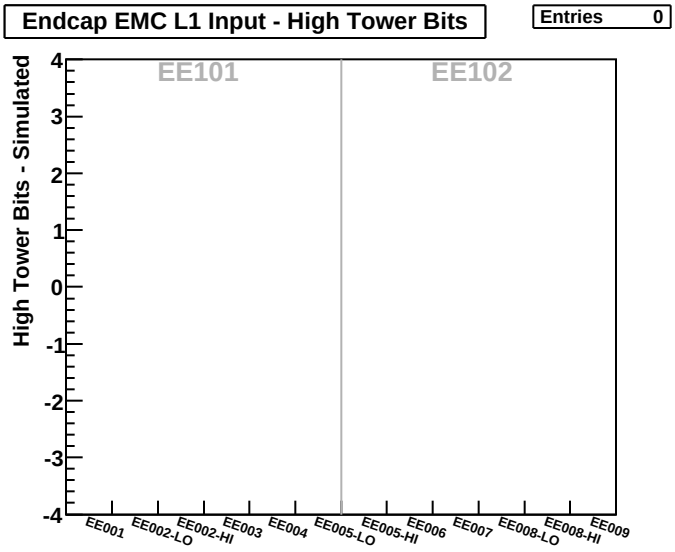

Endcap EMC L1 Input - Low Eta Sum
Entries 543672
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

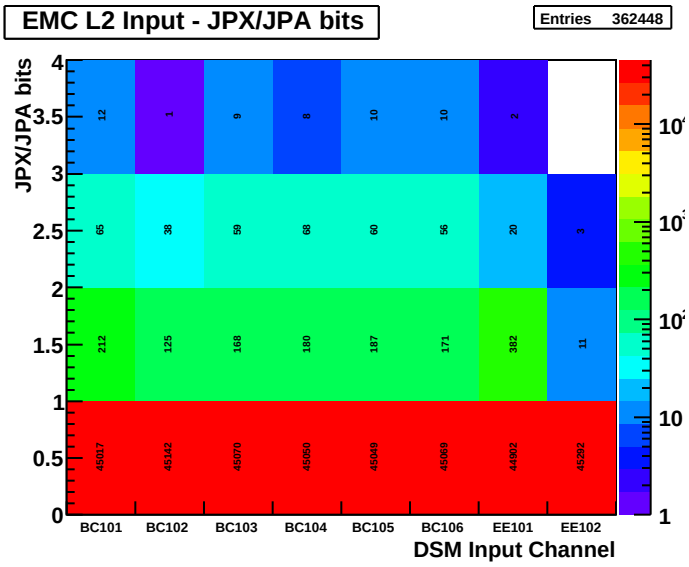

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

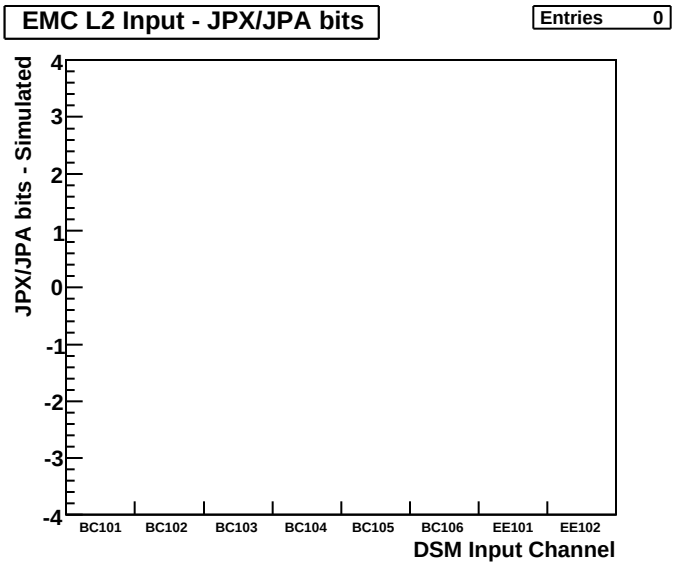

Endcap EMC L1 Input - High Eta Sum
Entries 543672
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

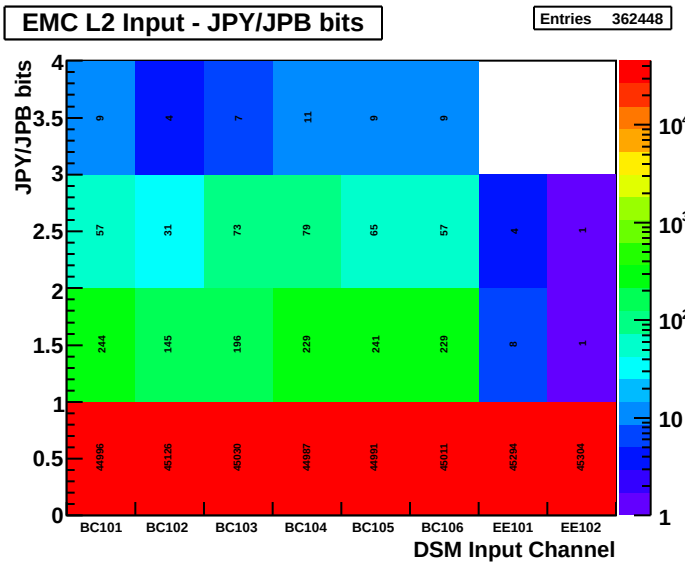

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

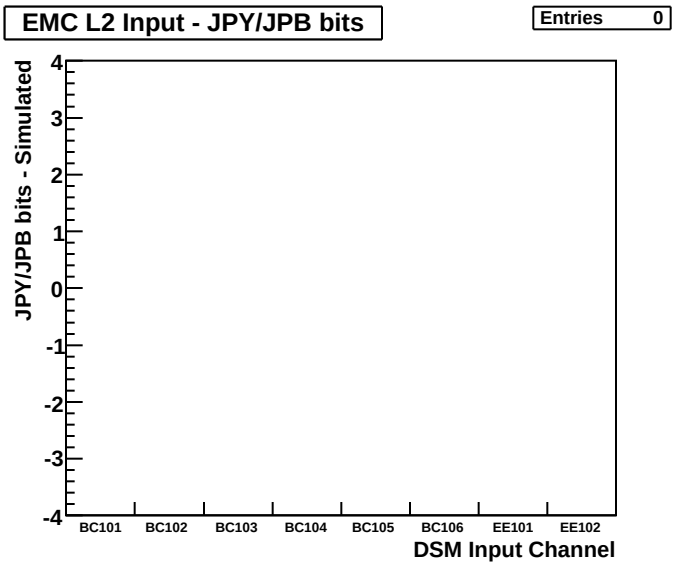

Endcap EMC L1 Input - High Tower Bits
Entries 543672
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

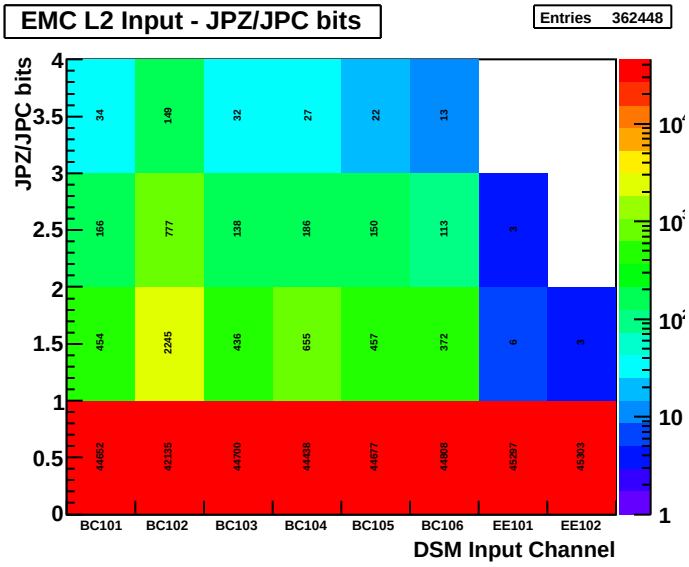

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

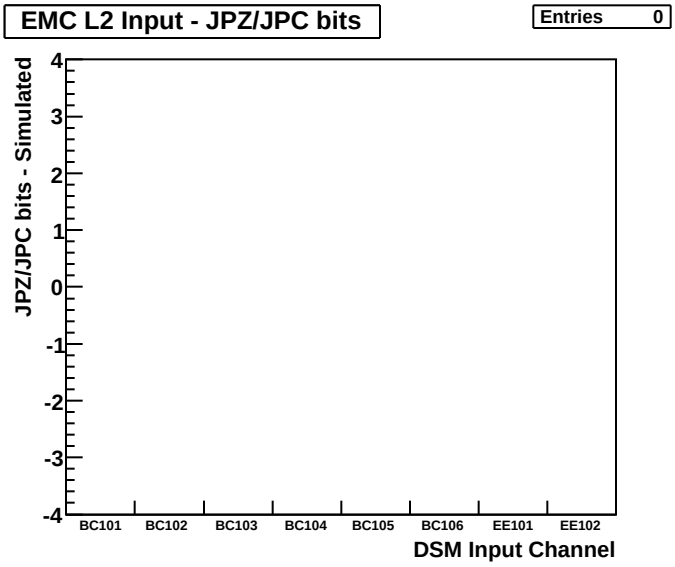

EMC L2 Input - JPX/JPA bits
Entries 362448
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
12 1 9 8 10 10 2
65 38 59 68 60 56 20 3
212 125 168 180 187 171 382 11
45017 45142 45070 45050 45049 45069 44902 45292

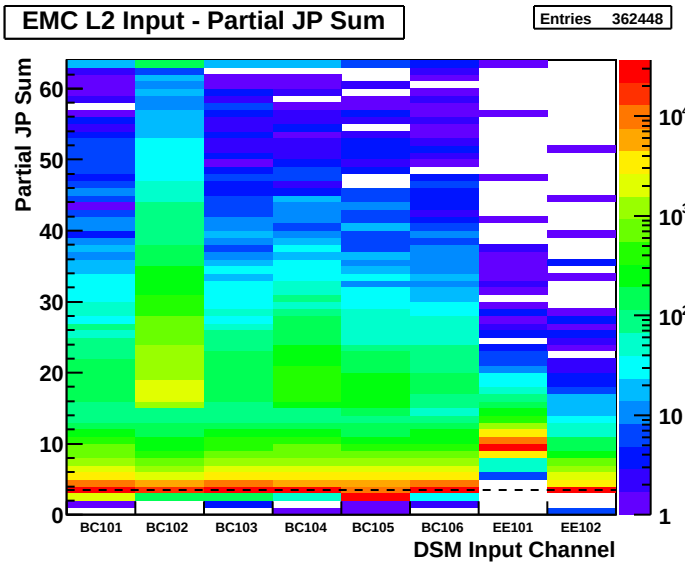

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

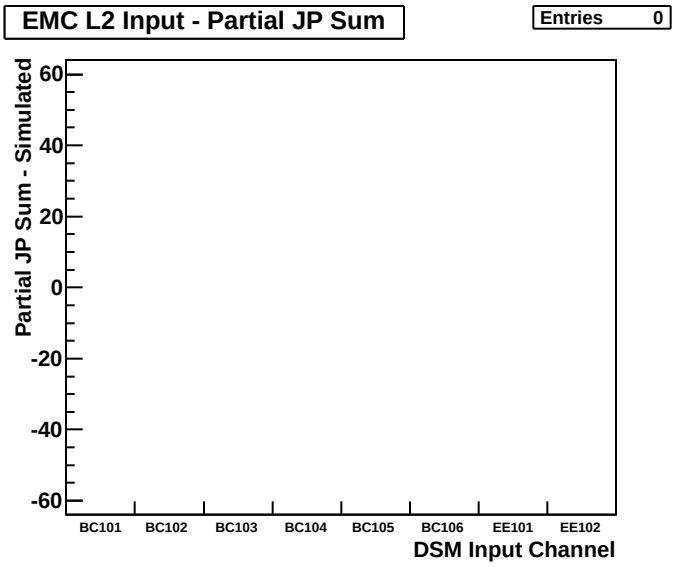

EMC L2 Input - JPY/JPB bits
Entries 362448
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
9 4 7 11 9 9
57 31 73 79 65 57 4 1
244 145 196 229 241 229 8 1
44996 45126 45030 44987 44991 45011 45294 45304

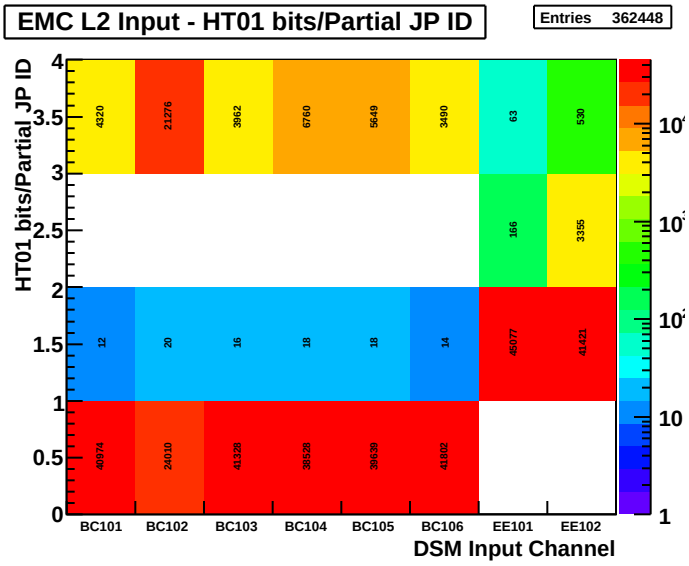

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

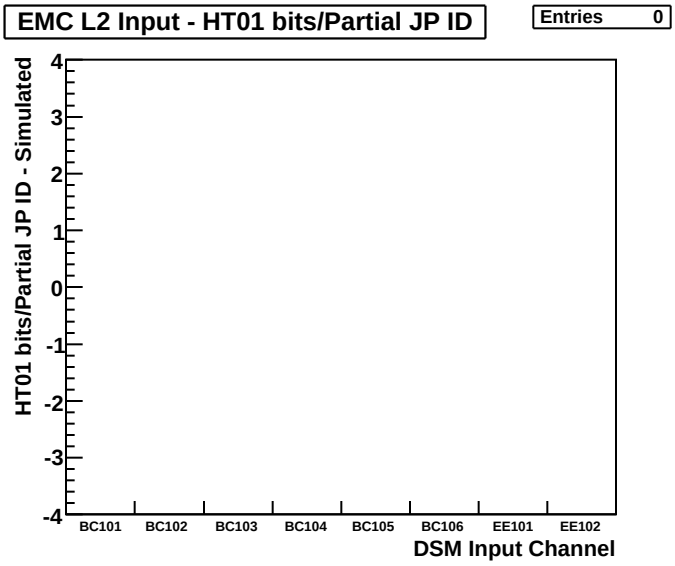

EMC L2 Input - JPZ/JPC bits
Entries 362448
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
34 149 32 27 22 13
166 777 138 186 150 113 3
454 2245 436 655 457 372 6 3
44652 42235 44700 44438 44677 44808 45397 45303

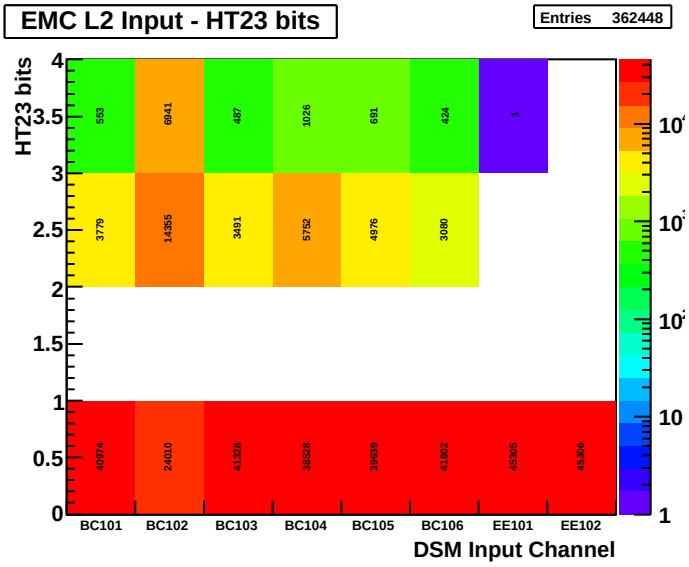

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

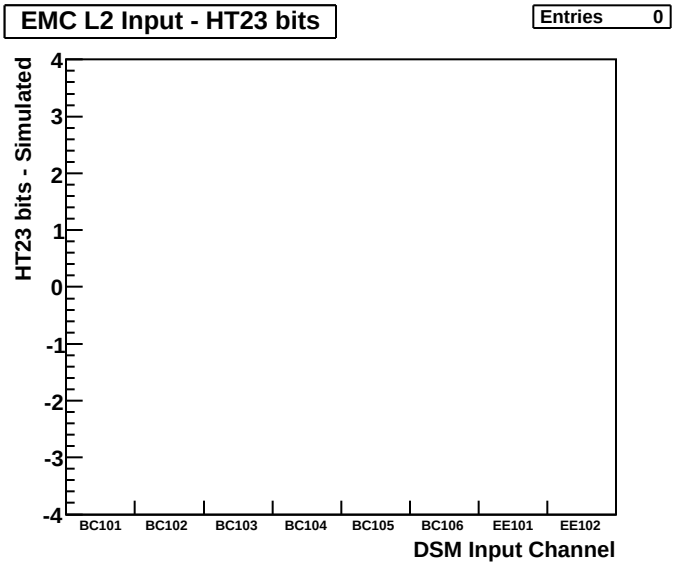

EMC L2 Input - Partial JP Sum
Entries 362448
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

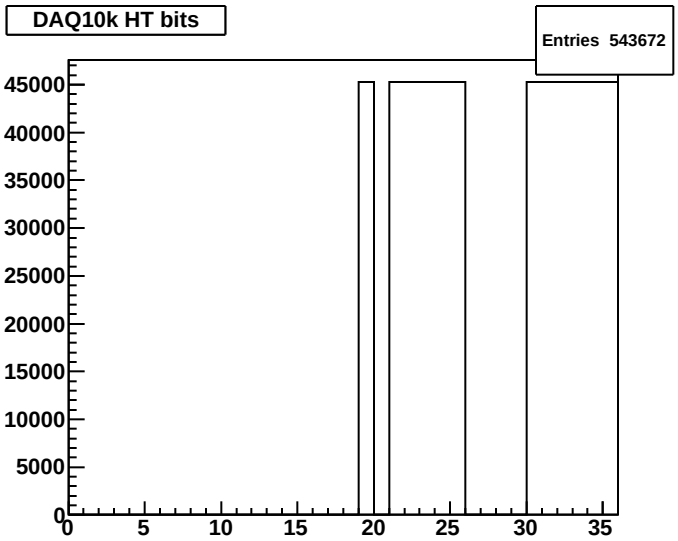

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

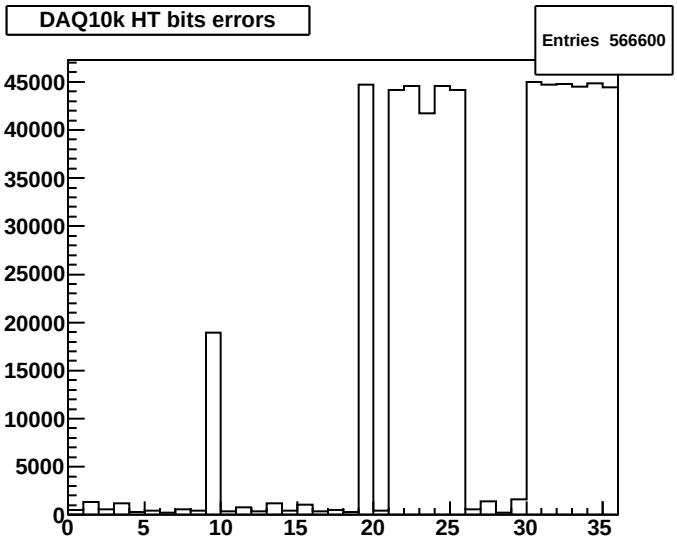

EMC L2 Input - HT01 bits/Partial JP ID
Entries 362448
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

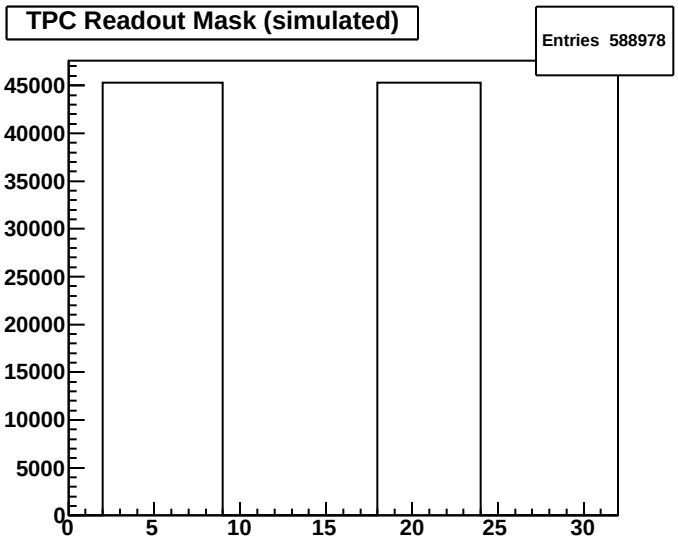

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

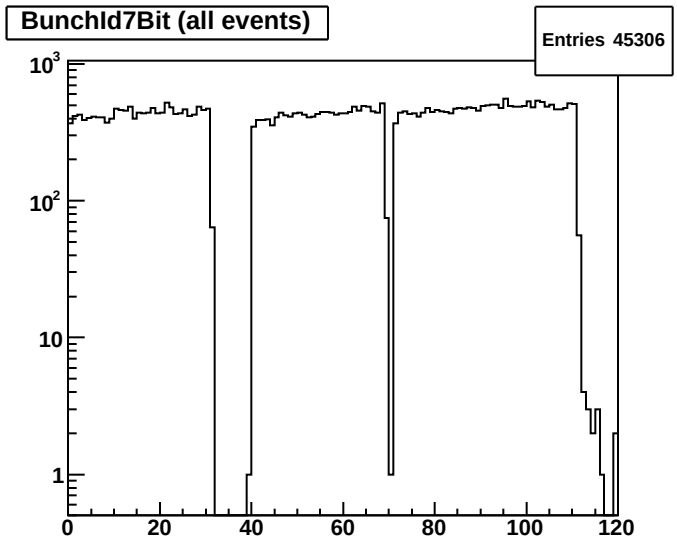

EMC L2 Input - HT23 bits
Entries 362448
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

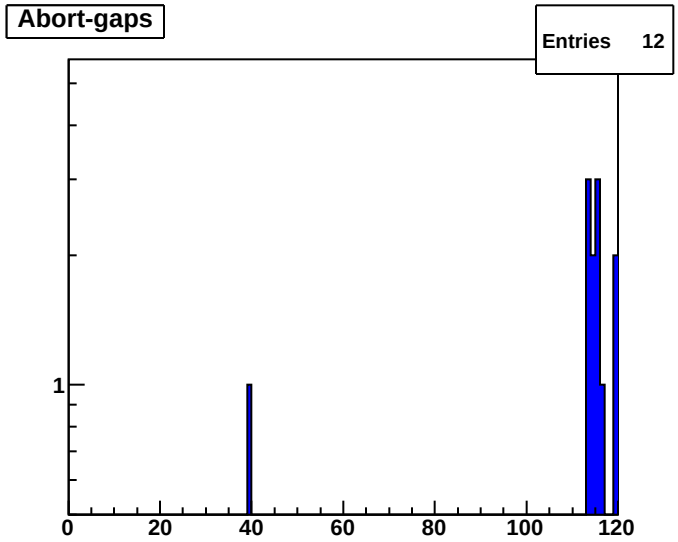

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

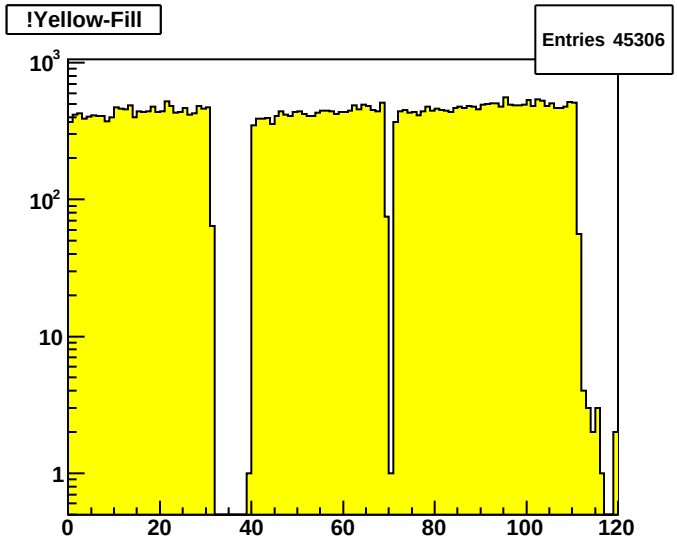

DAQ10k HT bits
Entries 543672

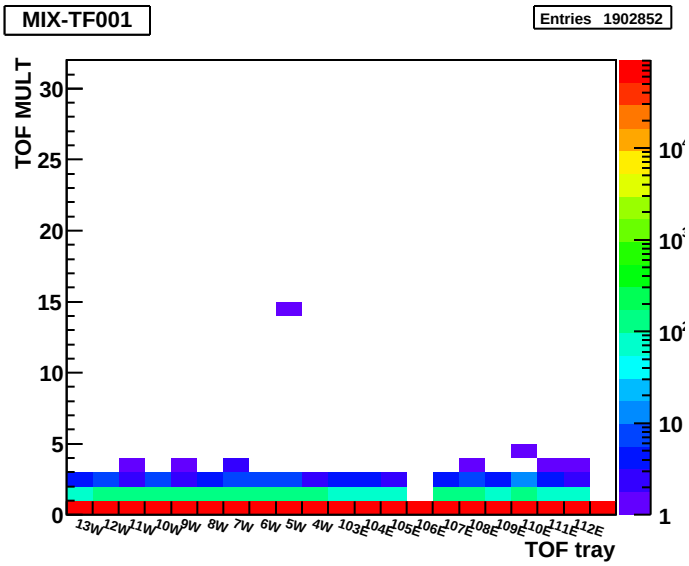

DAQ10k HT bits errors
Entries 566600

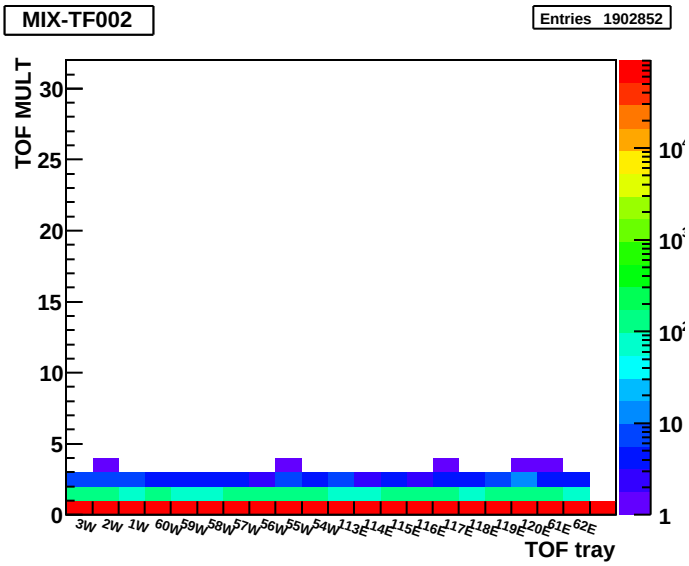

TPC Readout Mask (simulated)
Entries 588978

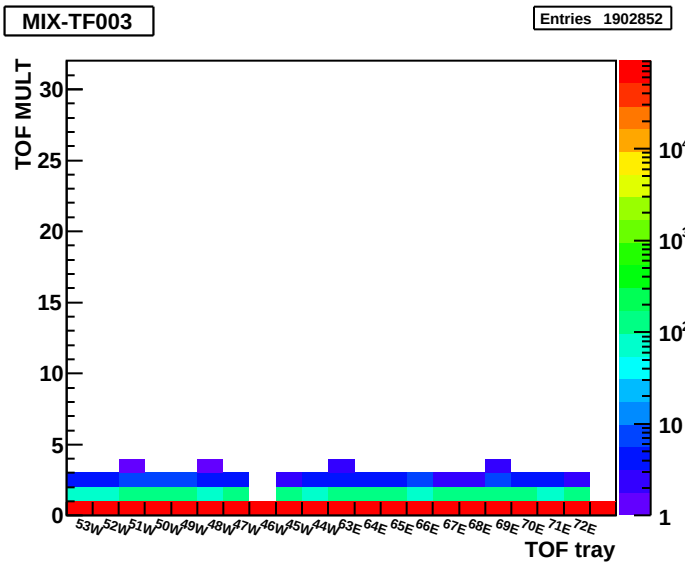

BunchId7Bit (all events)
Entries 45306

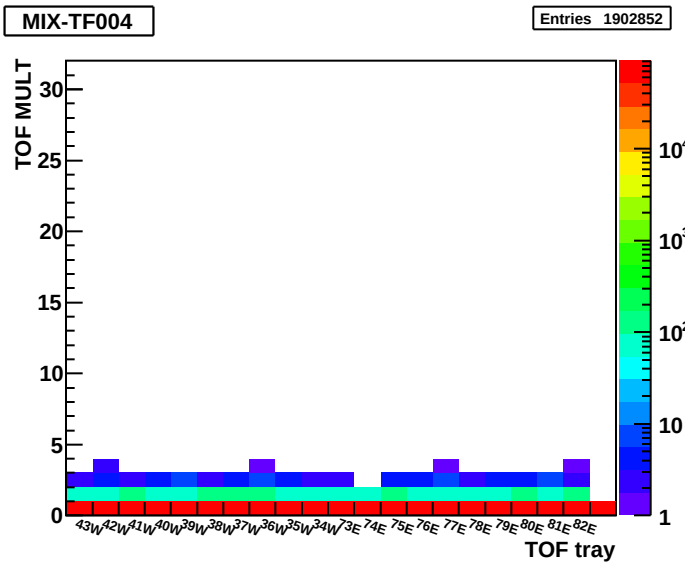

Abort-gaps
Entries 12

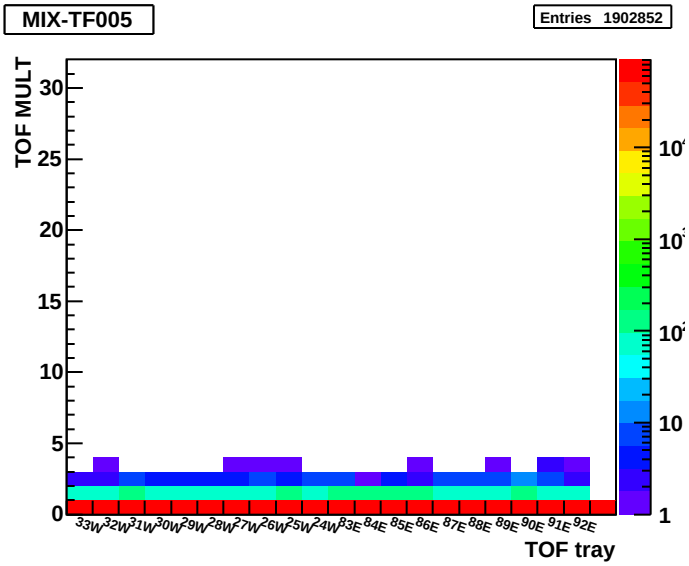

!Yellow-Fill
Entries 45306

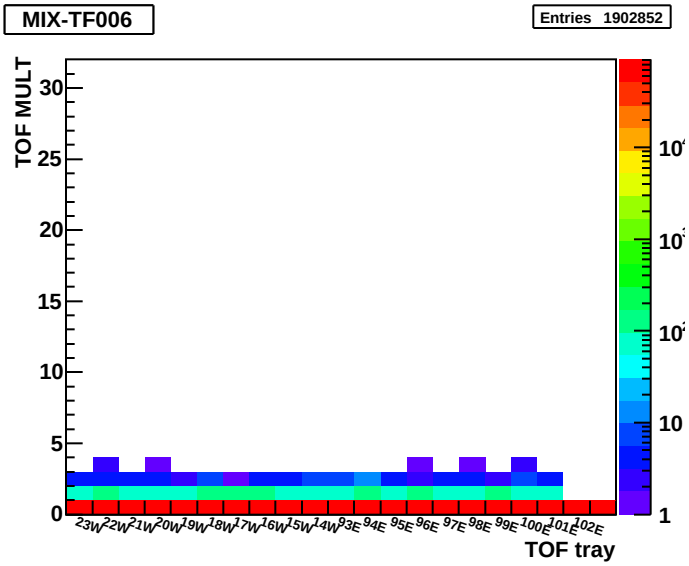

MIX-TF001
Entries 1902852
TOF MULT
TOF tray

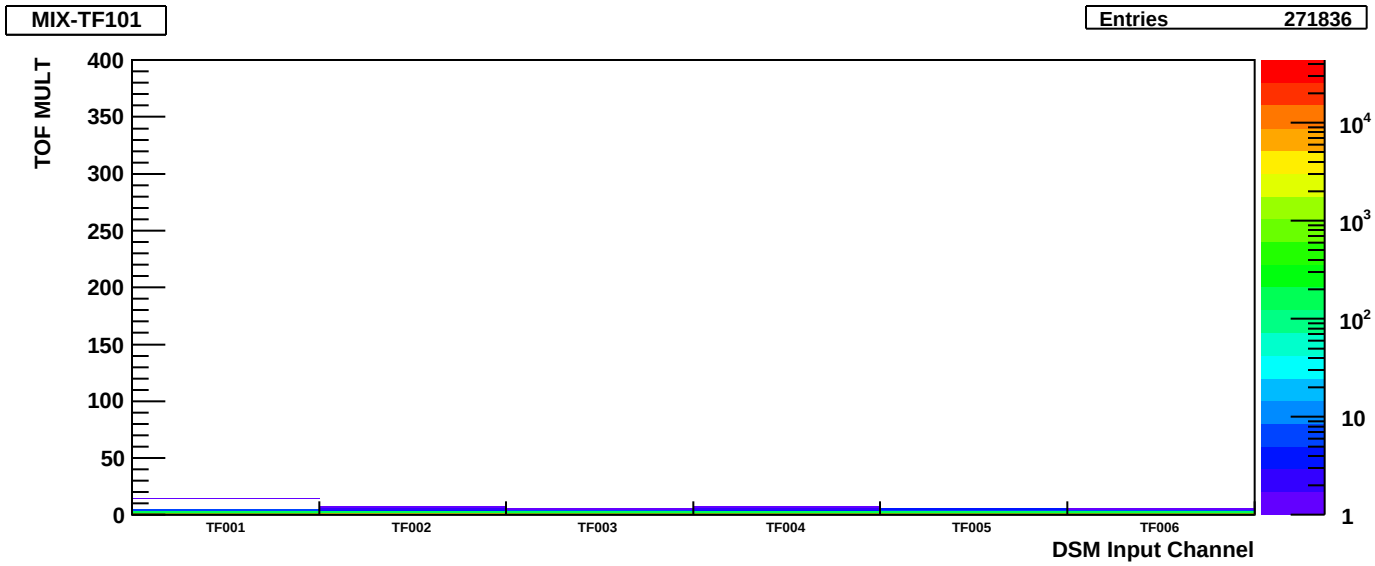

MIX-TF002
Entries 1902852
TOF MULT
TOF tray

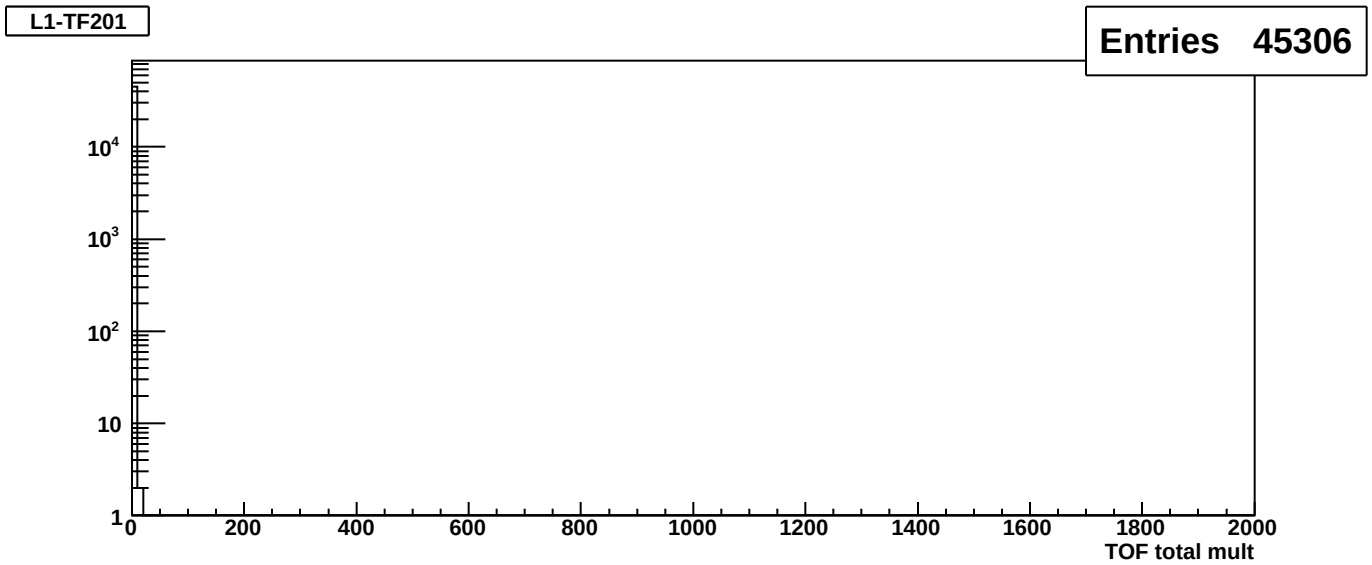

MIX-TF003
Entries 1902852
TOF MULT
TOF tray

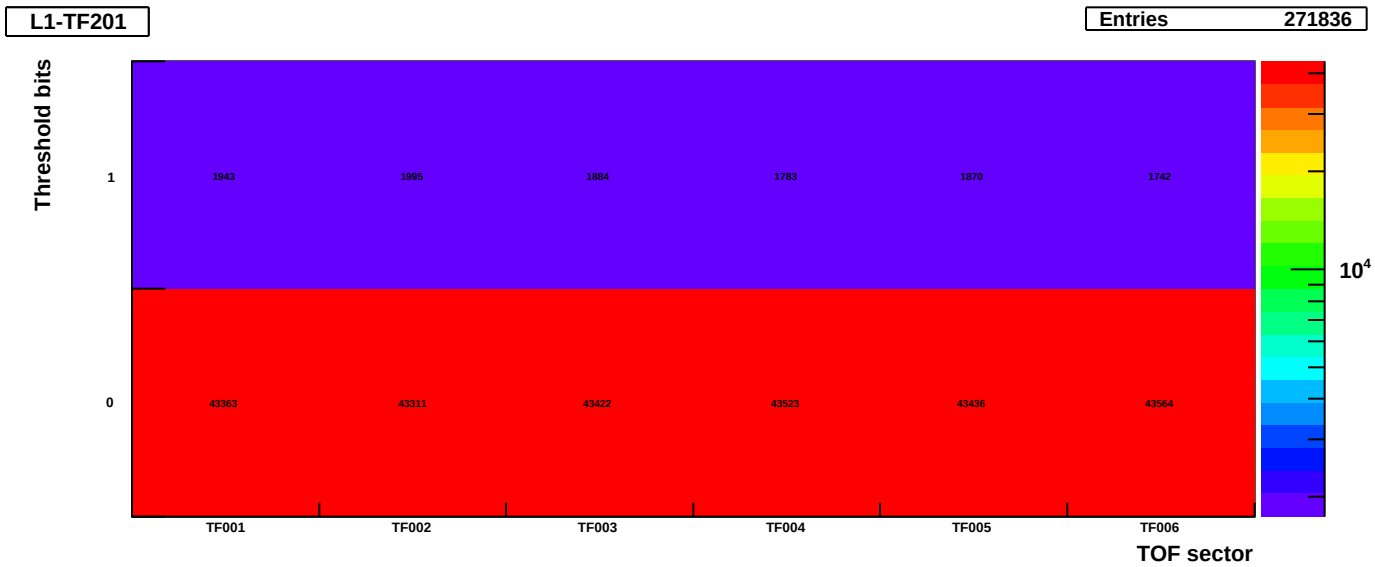

MIX-TF004
Entries 1902852
TOF MULT
TOF tray

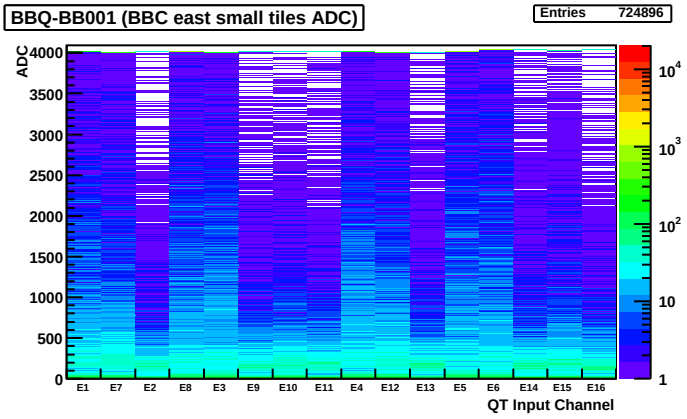

MIX-TF005
Entries 1902852
TOF MULT
TOF tray

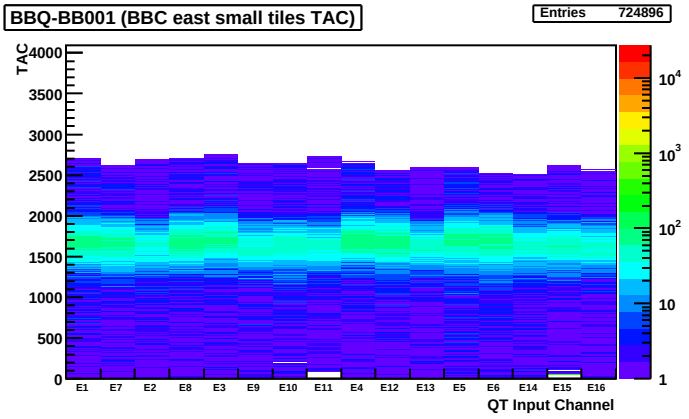

MIX-TF006
Entries 1902852
TOF MULT
TOF tray

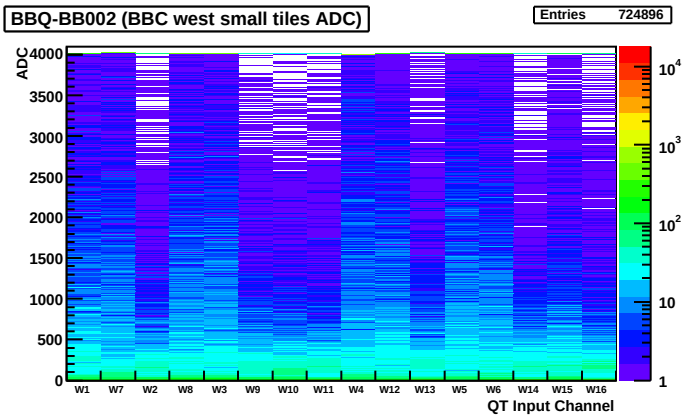

MIX-TF101
Entries 271836
TOF MULT
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

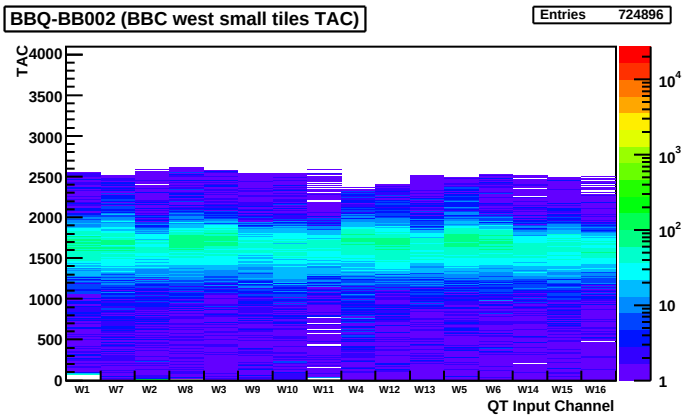

L1-TF201
Entries 45306
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
1600
1800
2000
TOF total mult

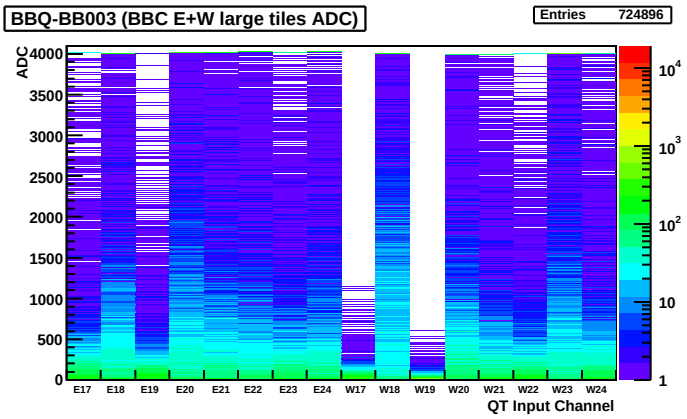

L1-TF201
Entries 271836
Threshold bits
1
0
1943
1995
1884
1783
1870
1742
43363
43311
43422
43523
43436
43564
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

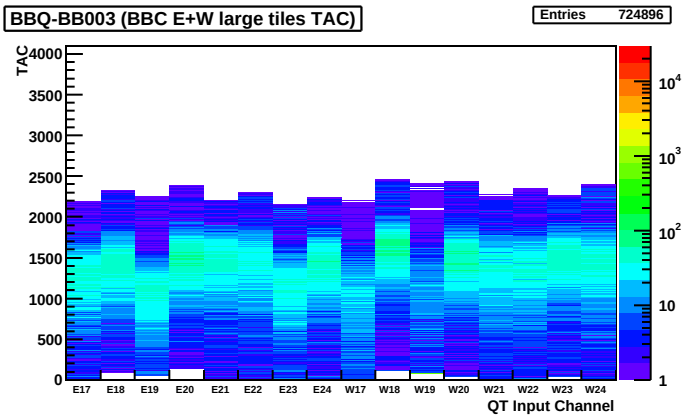

BBQ-BB001 (BBC east small tiles ADC)
Entries 724896
ADC
QT Input Channel

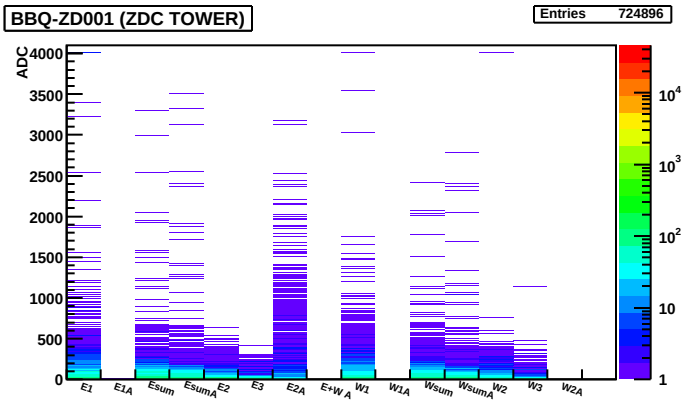

BBQ-BB001 (BBC east small tiles TAC)
Entries 724896
TAC
QT Input Channel

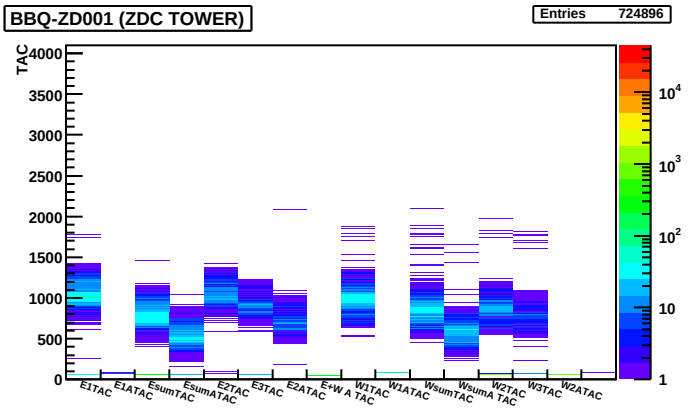

BBQ-BB002 (BBC west small tiles ADC)
Entries 724896
ADC
QT Input Channel

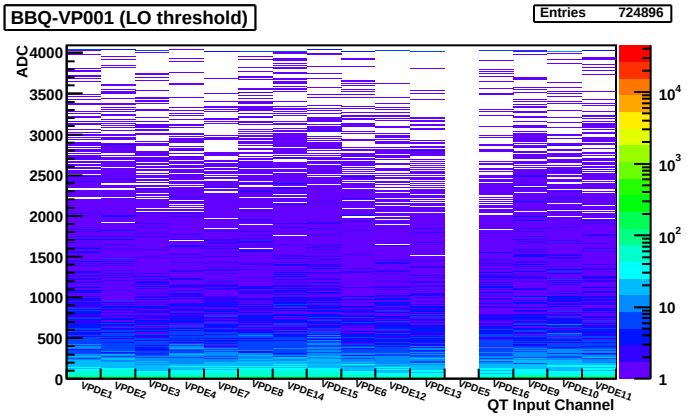

BBQ-BB002 (BBC west small tiles TAC)
Entries 724896
TAC
QT Input Channel

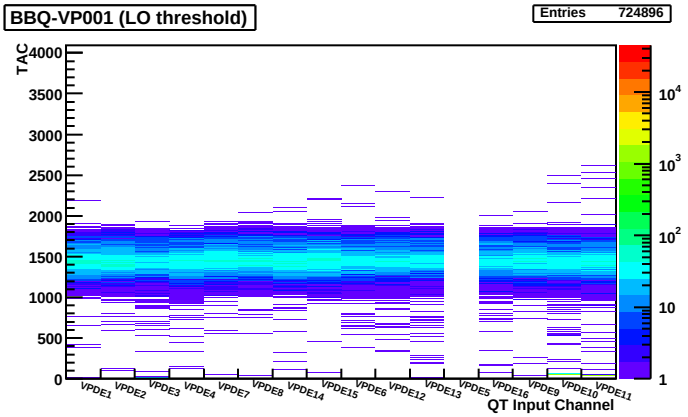

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 724896
ADC
QT Input Channel

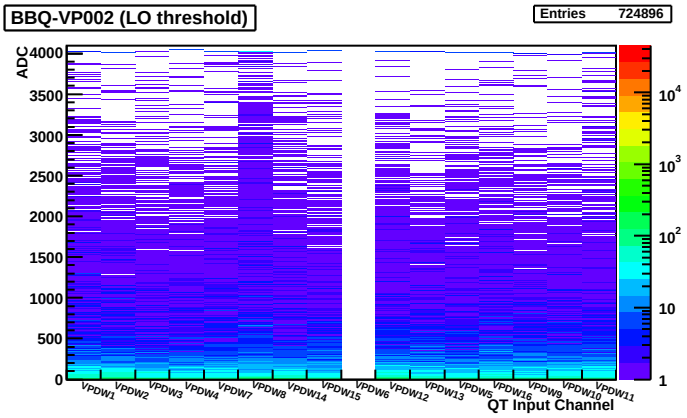

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 724896
TAC
QT Input Channel

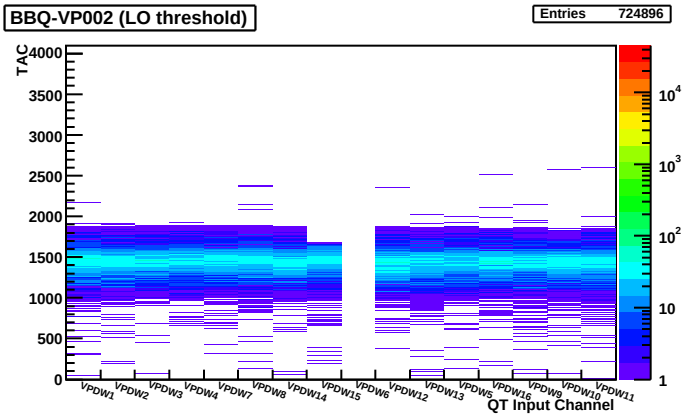

BBQ-ZD001 (ZDC TOWER)
Entries 724896
ADC

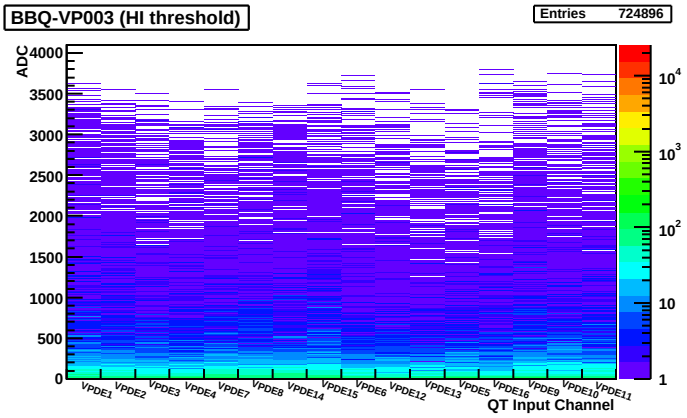

BBQ-ZD001 (ZDC TOWER)
Entries 724896
TAC

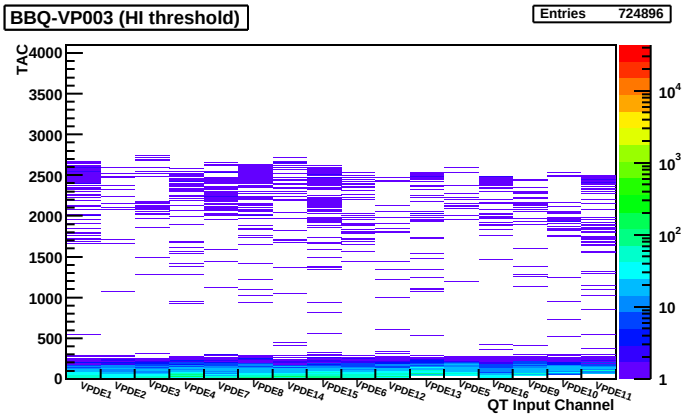

BBQ-VP001 (LO threshold)
Entries 724896
ADC
QT Input Channel

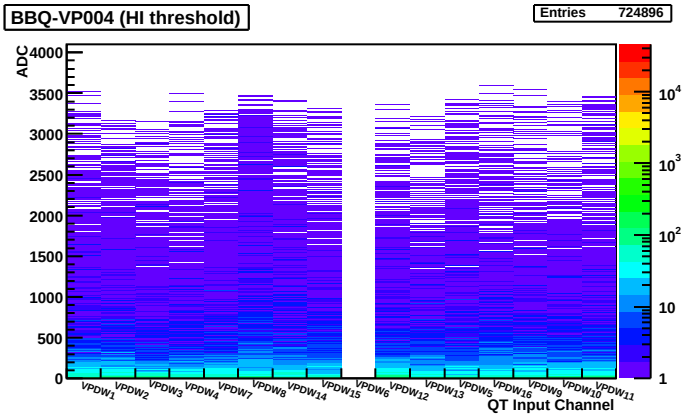

BBQ-VP001 (LO threshold)
Entries 724896
TAC
QT Input Channel

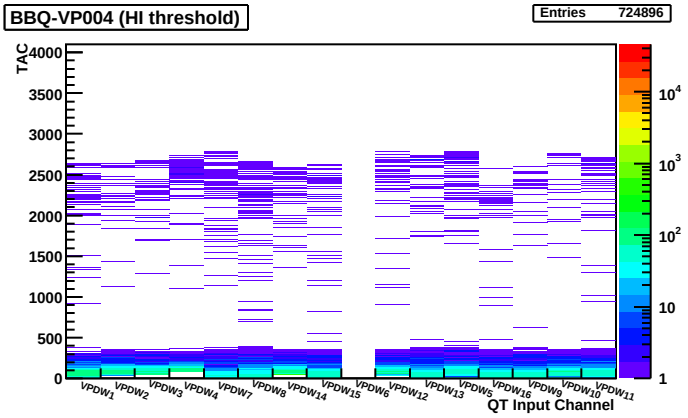

BBQ-VP002 (LO threshold)
Entries 724896
ADC
QT Input Channel

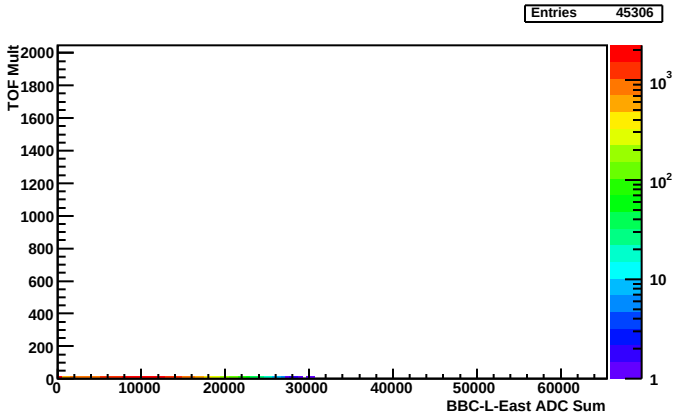

BBQ-VP002 (LO threshold)
Entries 724896
TAC
QT Input Channel

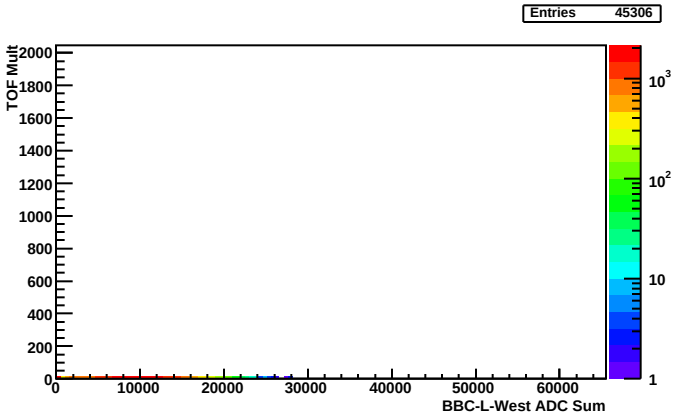

BBQ-VP003 (HI threshold)
Entries 724896
ADC
QT Input Channel

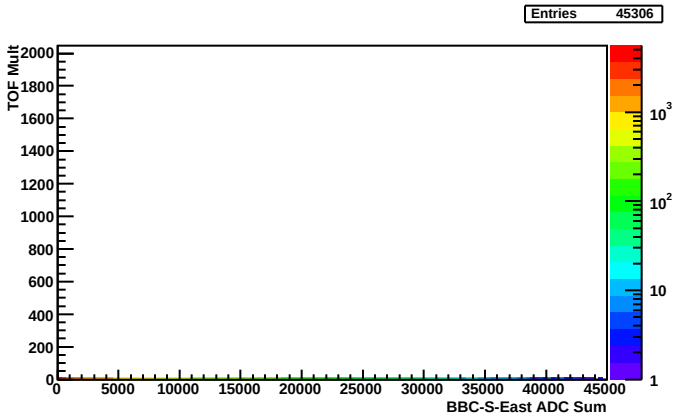

BBQ-VP003 (HI threshold)
Entries 724896
TAC
QT Input Channel

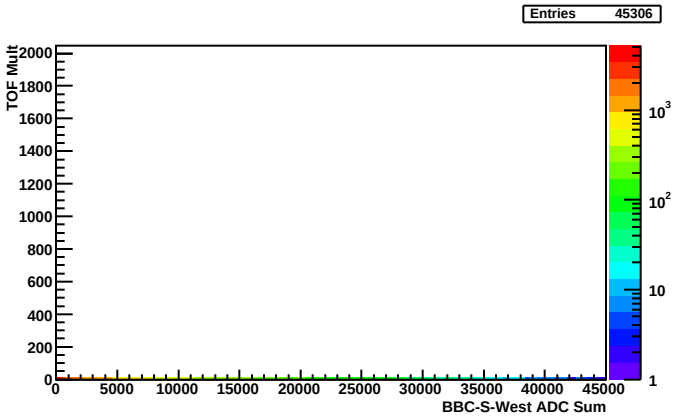

BBQ-VP004 (HI threshold)
Entries 724896
ADC
QT Input Channel

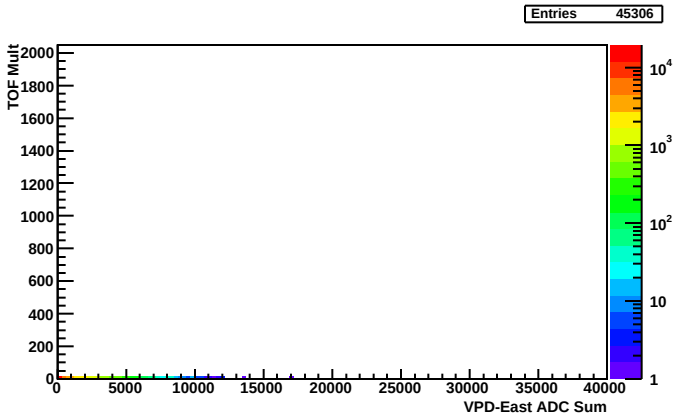

BBQ-VP004 (HI threshold)
Entries 724896
TAC
QT Input Channel

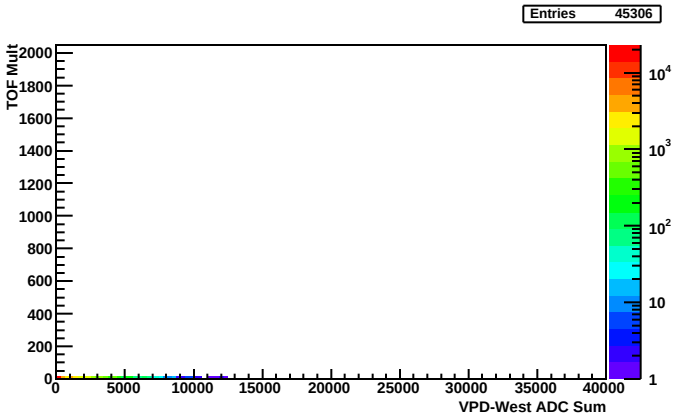

Entries 45306
TOF Mult
BBC-L-East ADC Sum

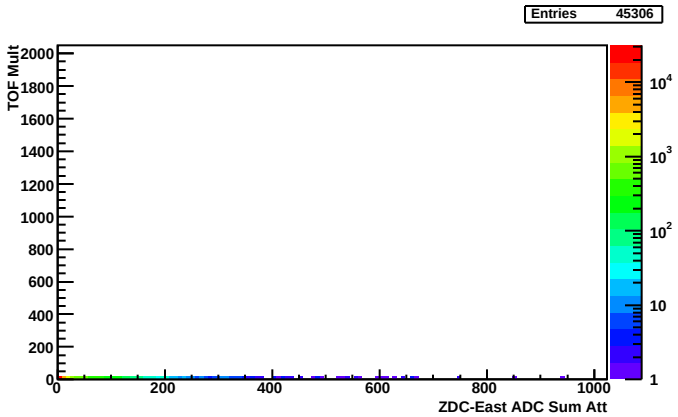

Entries 45306
TOF Mult
BBC-L-West ADC Sum

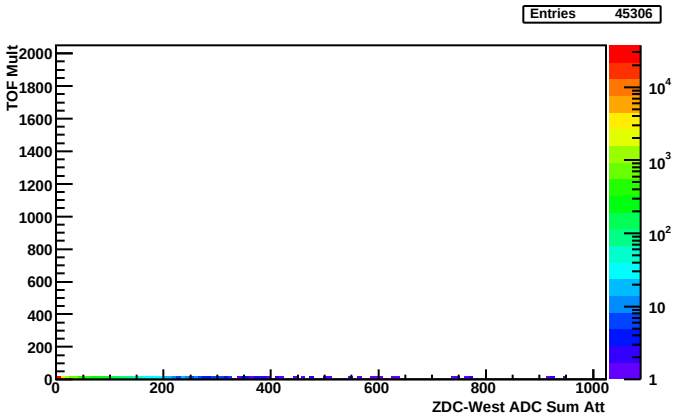

Entries 45306
TOF Mult
BBC-S-East ADC Sum

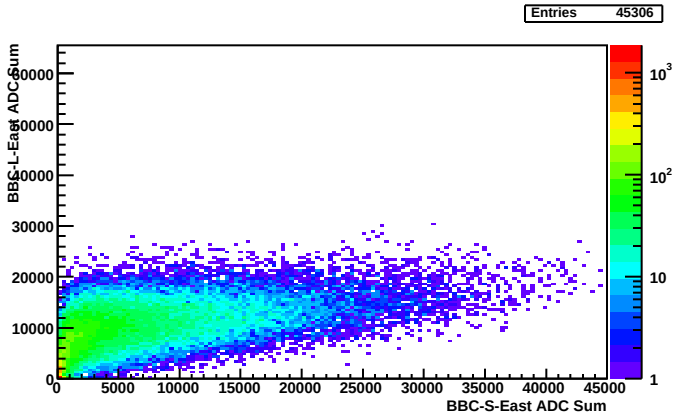

Entries 45306
TOF Mult
BBC-S-West ADC Sum

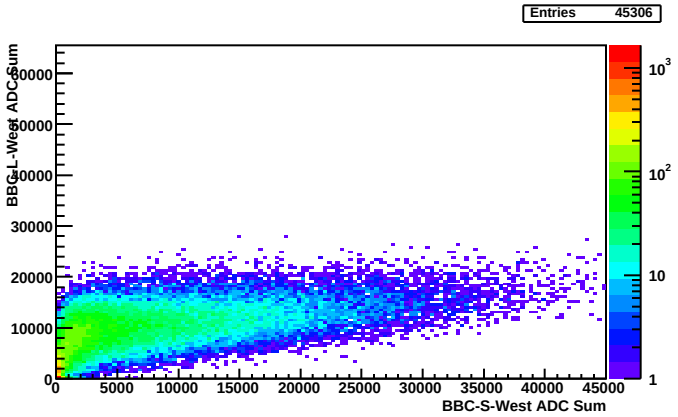

Entries 45306
TOF Mult
VPD-East ADC Sum

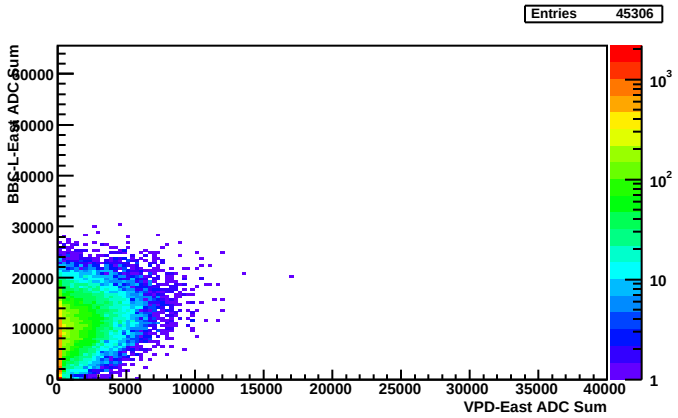

Entries 45306
TOF Mult
VPD-West ADC Sum

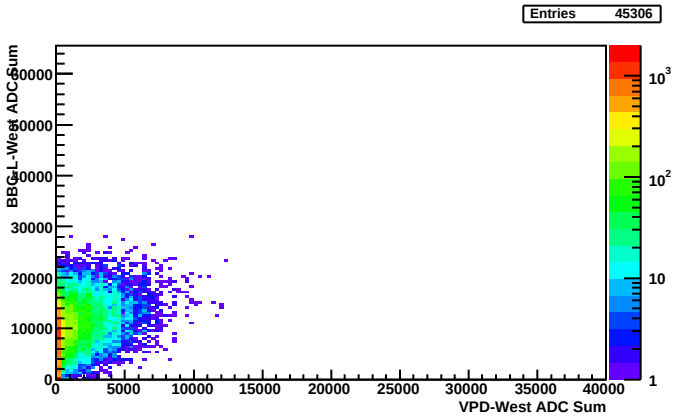

Entries 45306
TOF Mult
ZDC-East ADC Sum Att

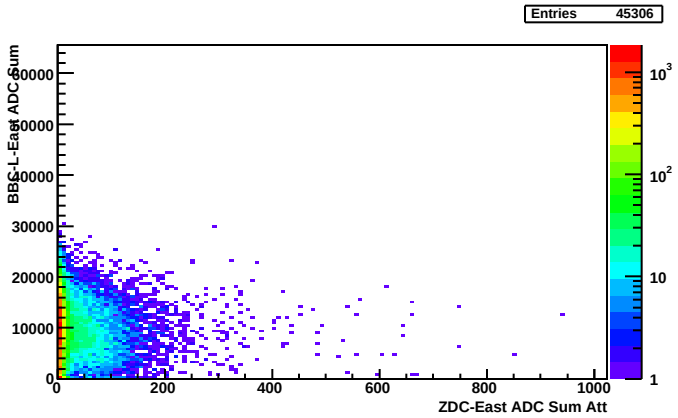

Entries 45306
TOF Mult
ZDC-West ADC Sum Att

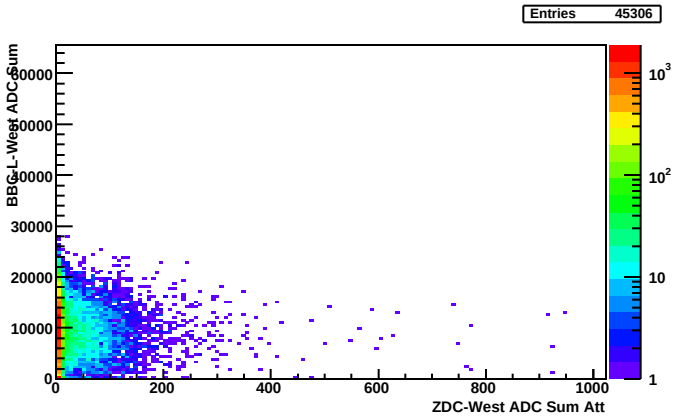

Entries 45306
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 45306
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 45306
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 45306
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 45306
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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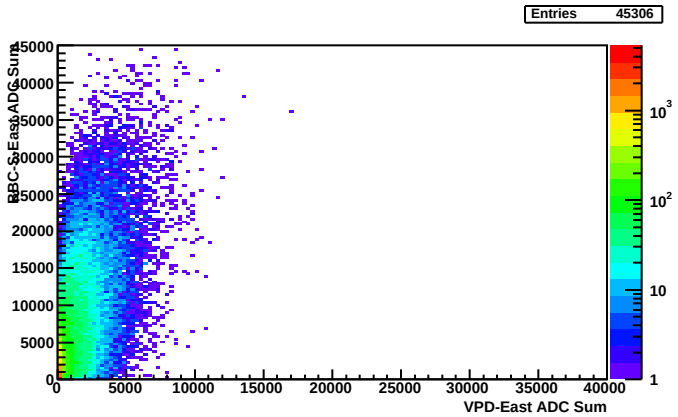
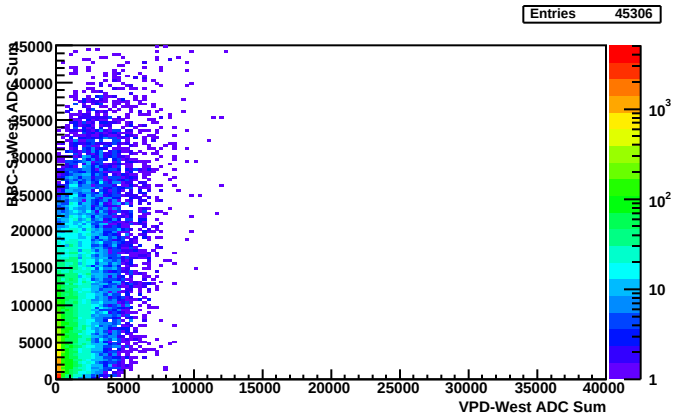
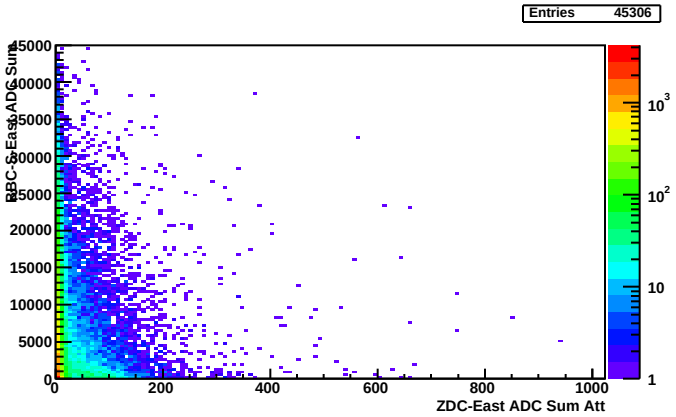
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 45306
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 45306
BBC-S-West ADC Sum
VPD-West ADC Sum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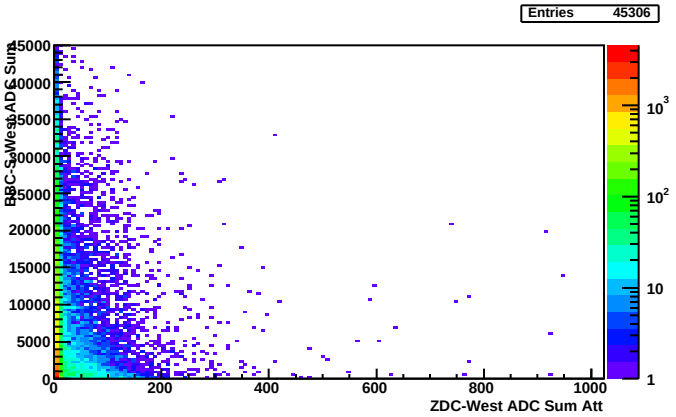
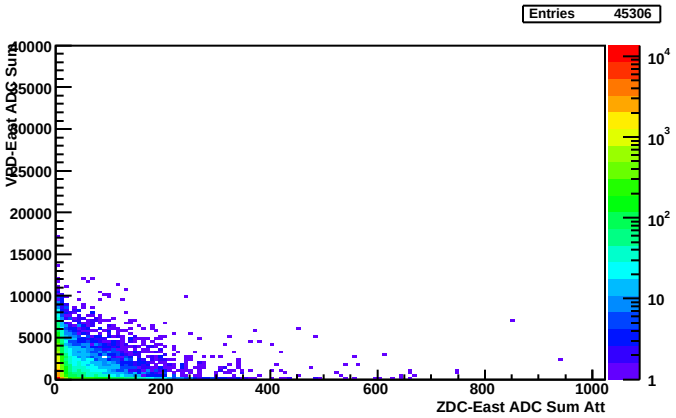
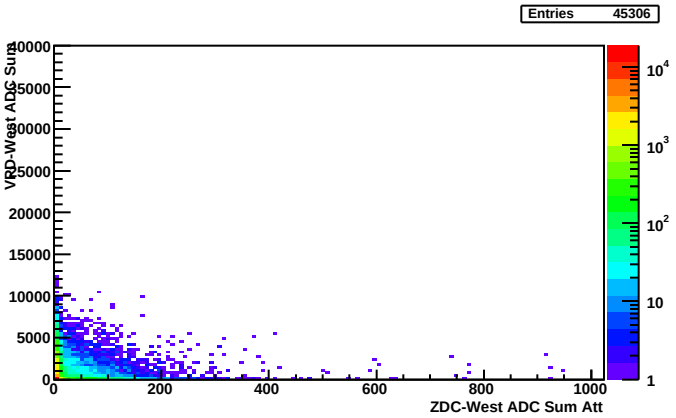
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 45306
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 45306
VPD-East ADC Sum
ZDC-East ADC Sum Att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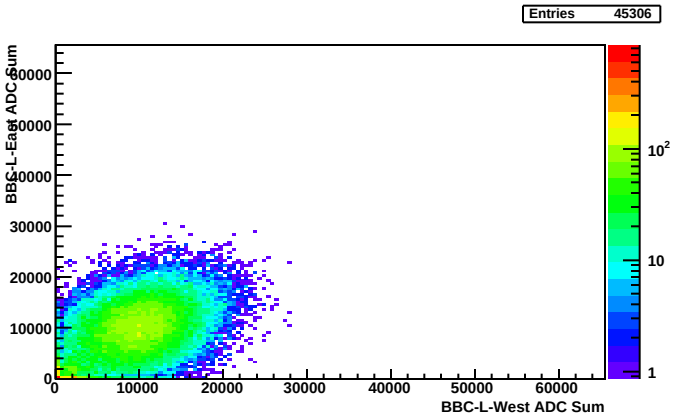
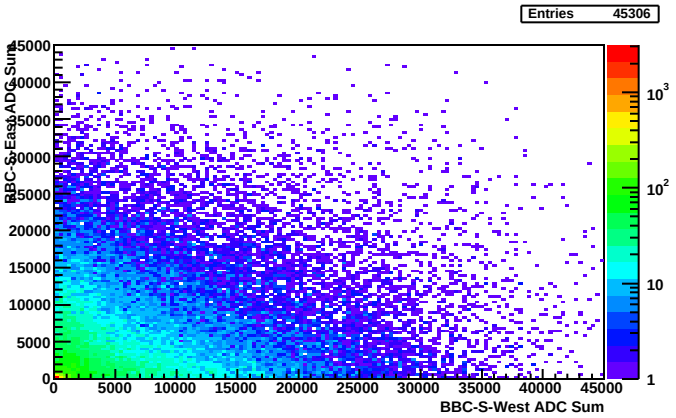
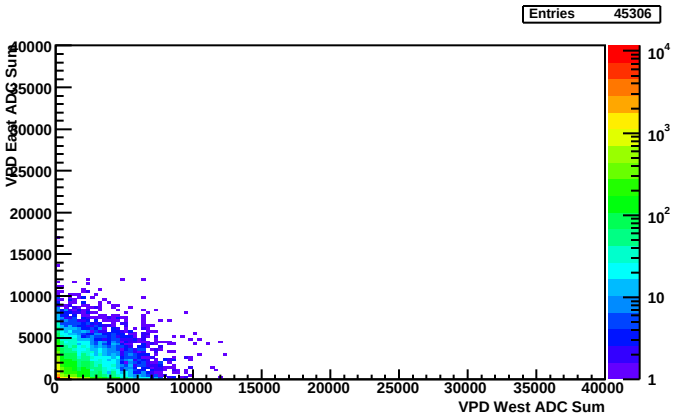
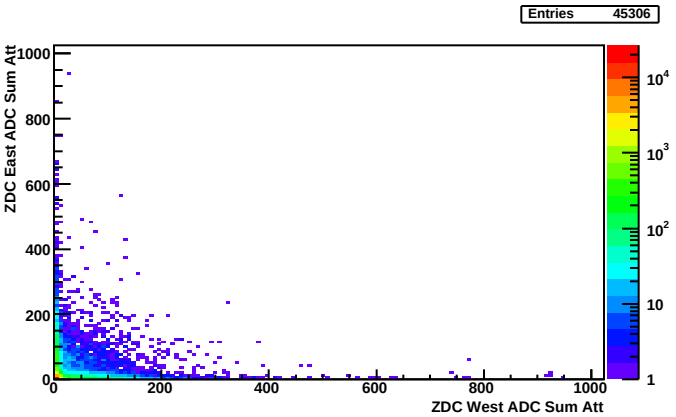
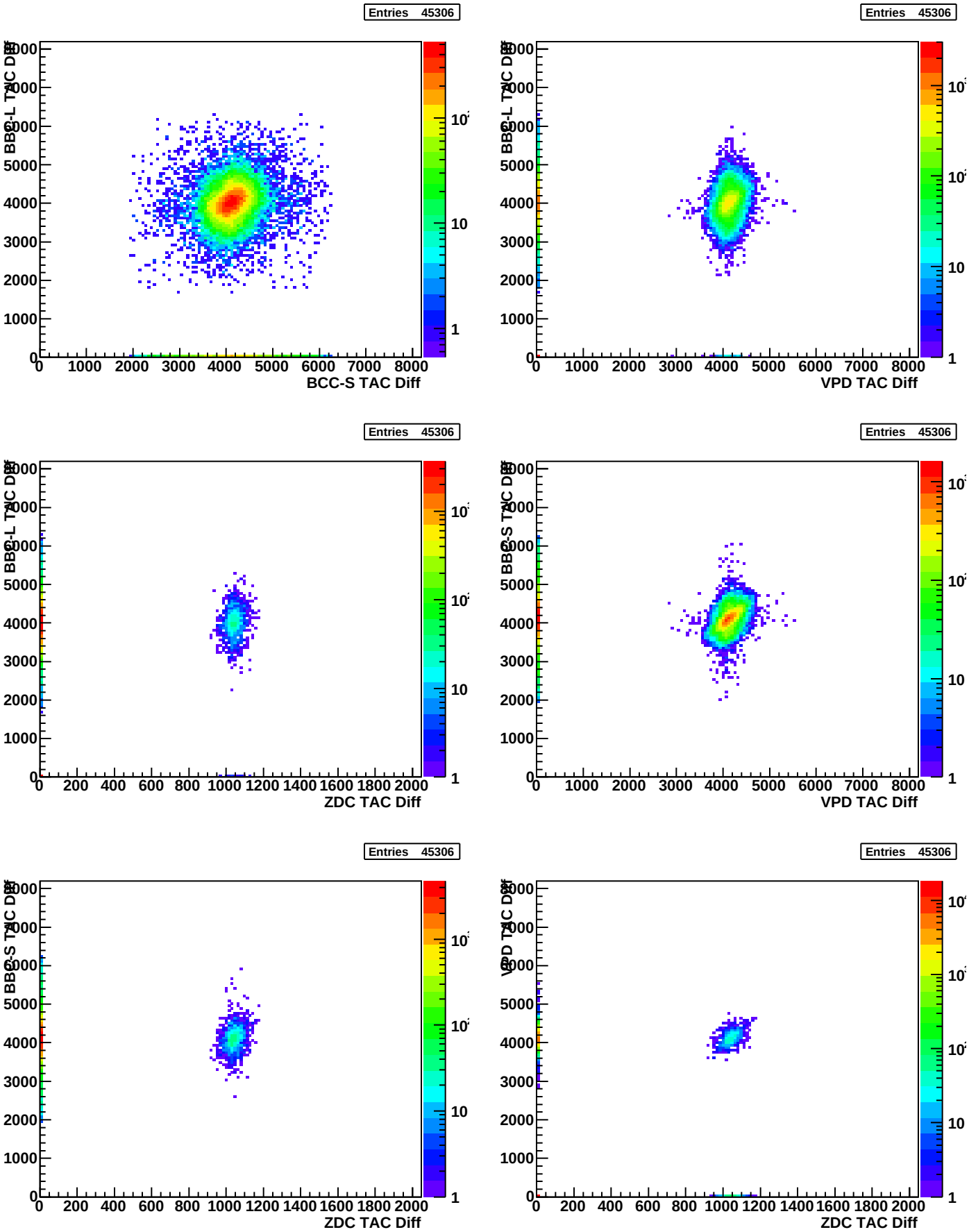
VPD-West ADC Sum
ZDC-West ADC Sum Att

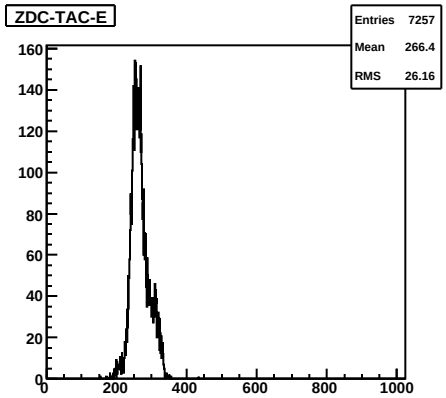

Entries 45306
BBC-L-East ADC Sum
BBC-L-West ADC Sum

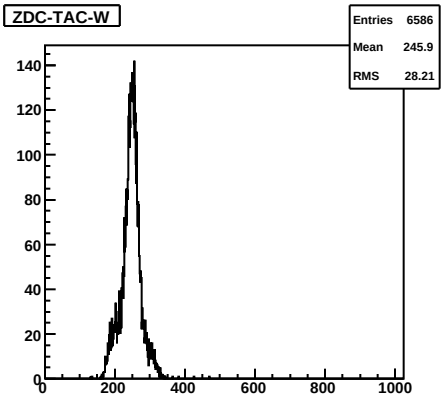

Entries 45306
BBC-S-East ADC Sum
BBC-S-West ADC Sum

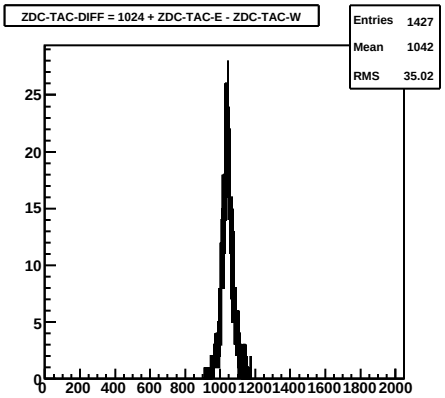

Entries 45306
VPD East ADC Sum
VPD West ADC Sum

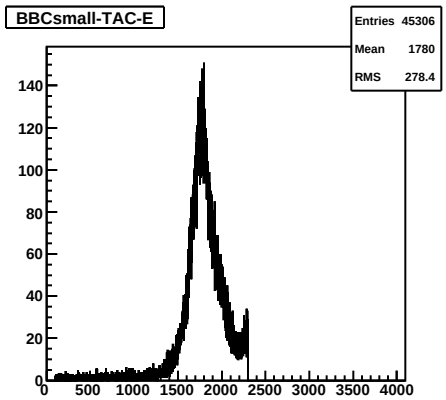

Entries 45306
ZDC East ADC Sum Att
ZDC West ADC Sum Att

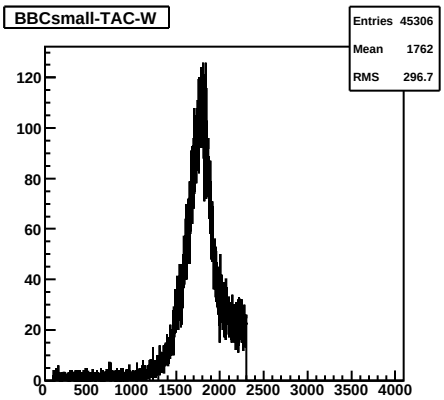

Entries 45306
BBC-L TAC Diff
BCC-S TAC Diff
Entries 45306
BBC-L TAC Diff
VPD TAC Diff
Entries 45306
BBC-L TAC Diff
ZDC TAC Diff
Entries 45306
BBC-S TAC Diff
VPD TAC Diff
Entries 45306
BBC-S TAC Diff
ZDC TAC Diff
Entries 45306
VPD TAC Diff
ZDC TAC Diff

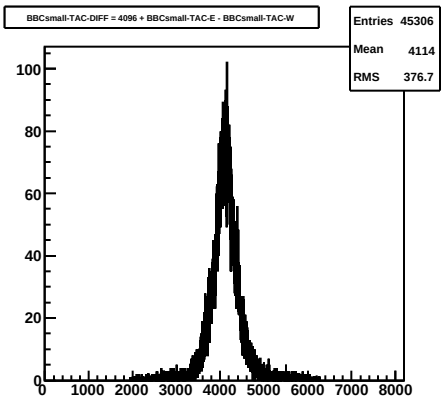

ZDC-TAC-E
Entries 7257
Mean 266.4
RMS 26.16

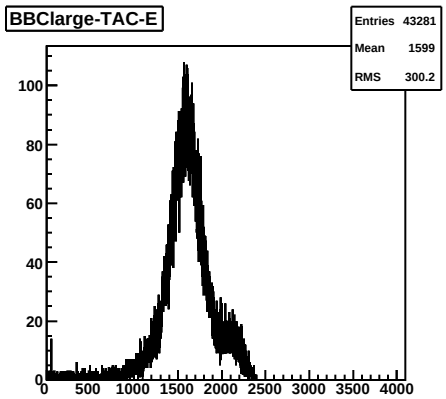

ZDC-TAC-W
Entries 6586
Mean 245.9
RMS 28.21

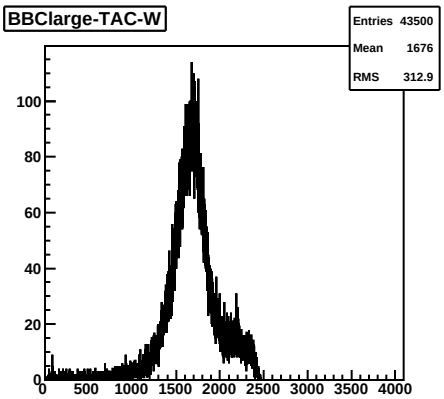

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1427
Mean 1042
RMS 35.02

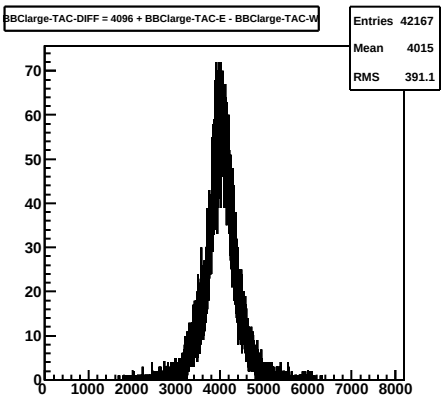

BBCsmall-TAC-E
Entries 45306
Mean 1780
RMS 278.4

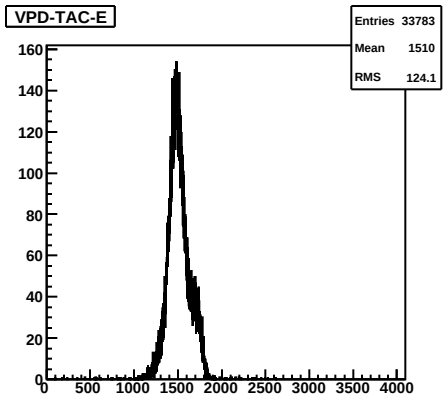

BBCsmall-TAC-W
Entries 45306
Mean 1762
RMS 296.7

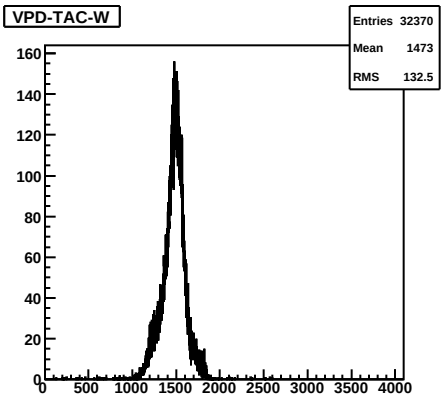

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 45306
Mean 4114
RMS 376.7

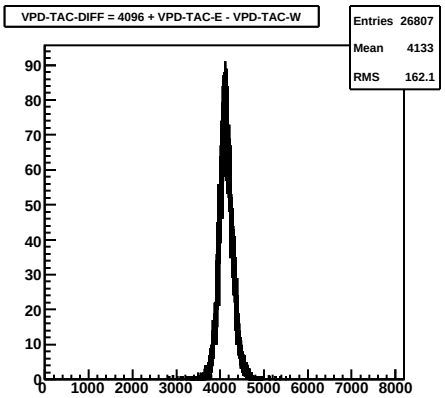

BBClarge-TAC-E
Entries 43281
Mean 1599
RMS 300.2

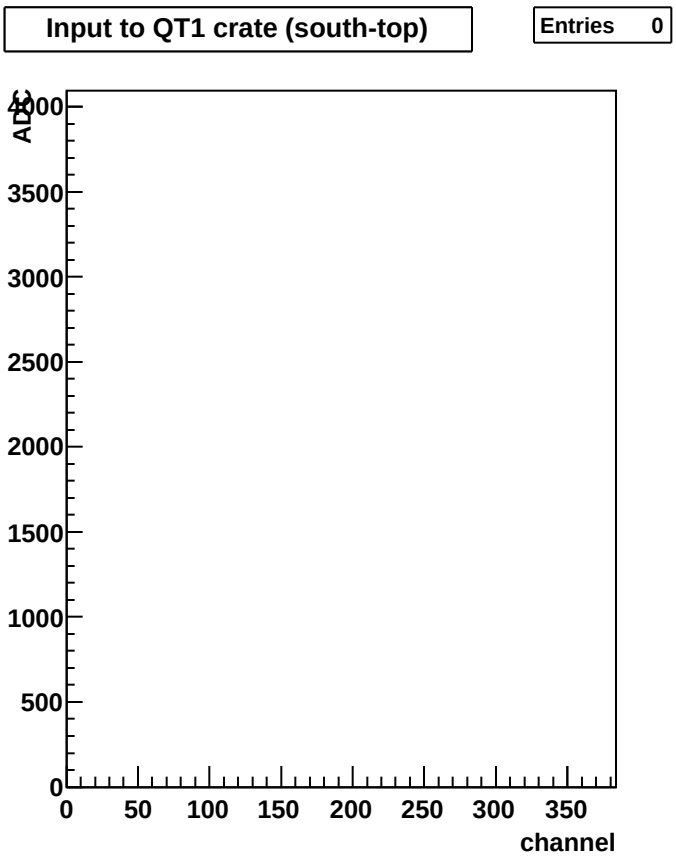

BBClarge-TAC-W
Entries 43500
Mean 1676
RMS 312.9

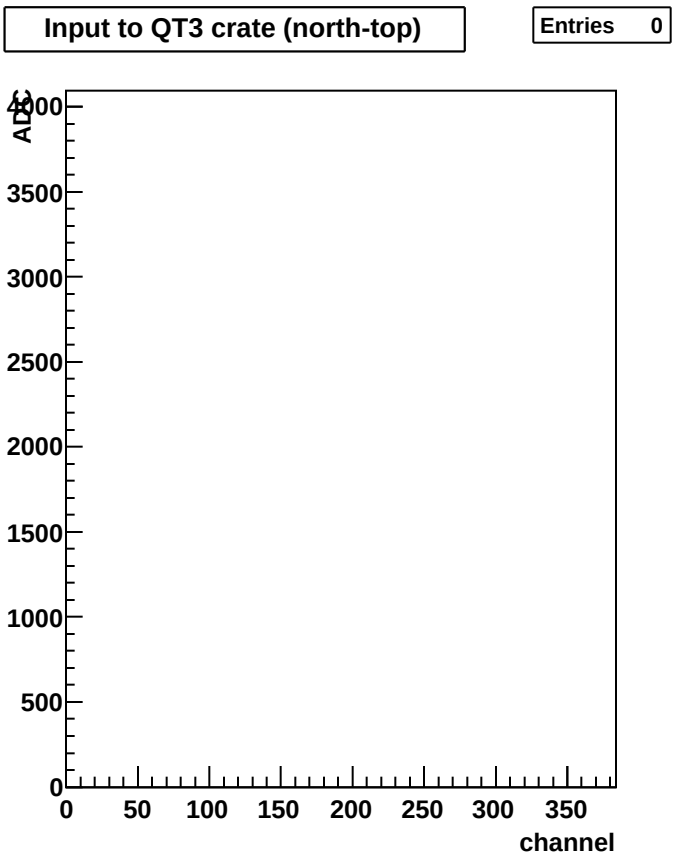

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 42167
Mean 4015
RMS 391.1

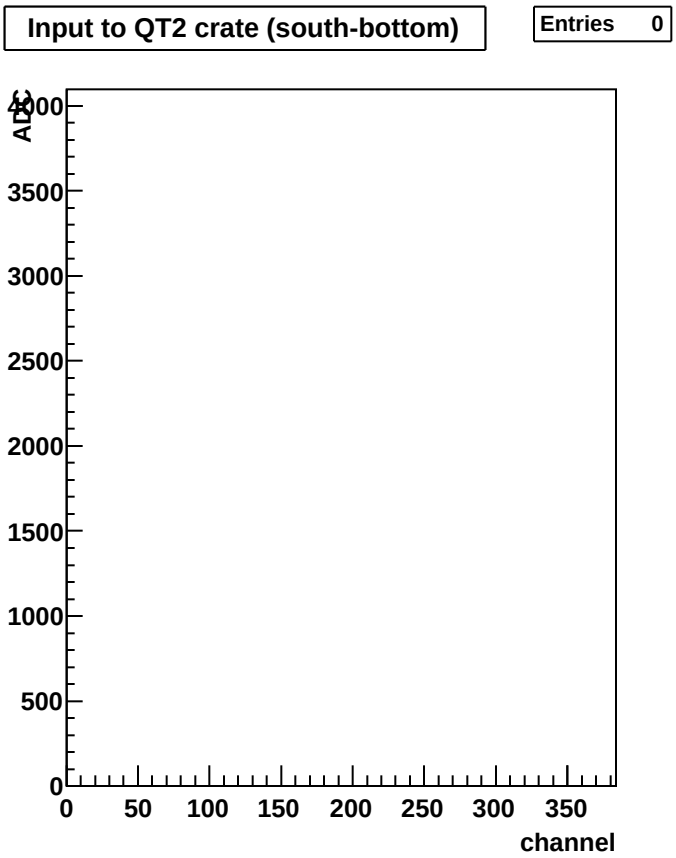

VPD-TAC-E
Entries 33783
Mean 1510
RMS 124.1

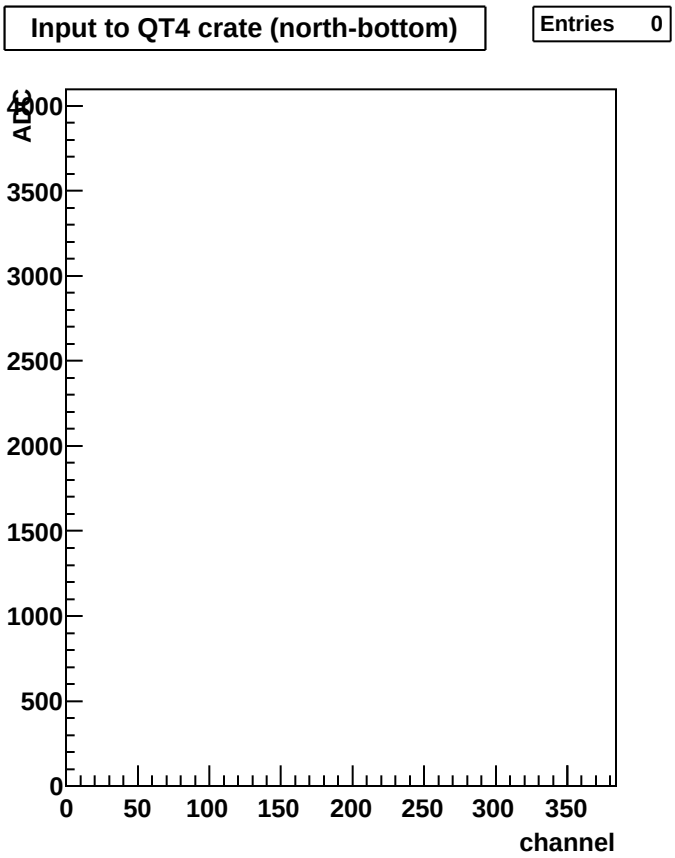

VPD-TAC-W
Entries 32370
Mean 1473
RMS 132.5

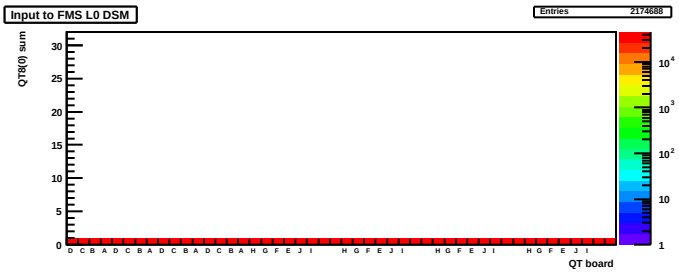

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 26807
Mean 4133
RMS 162.1

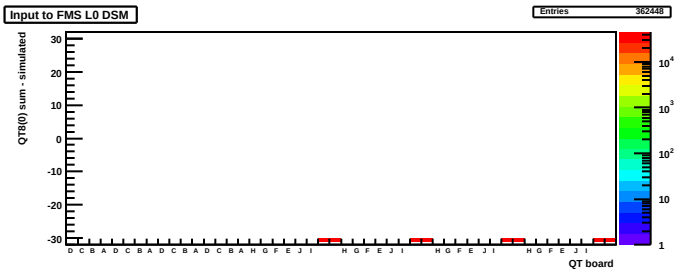

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

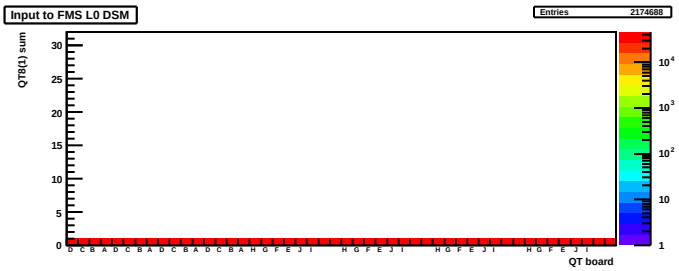

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

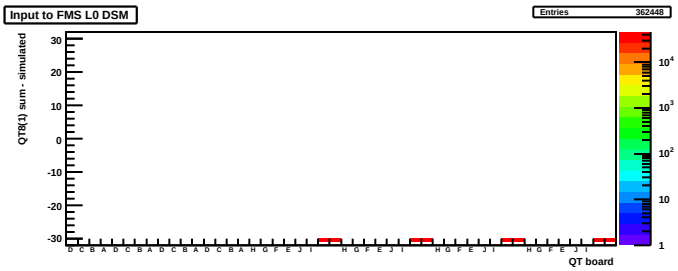

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

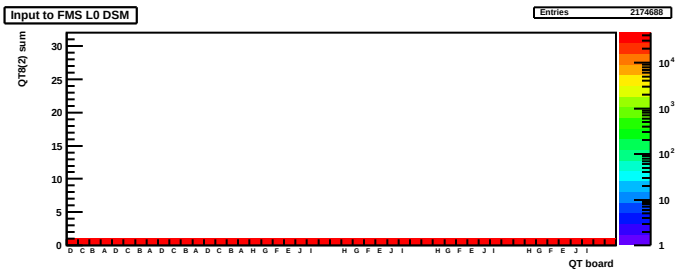

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

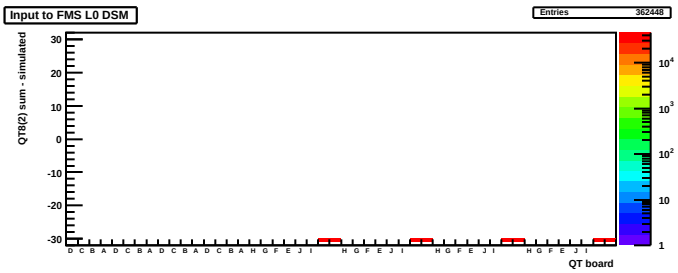

Input to FMS L0 DSM
Entries 2174688
QT8(0) sum
QT board

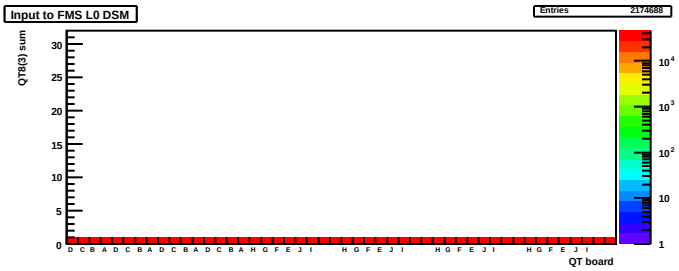

Input to FMS L0 DSM
Entries 362448
QT8(0) sum - simulated
QT board

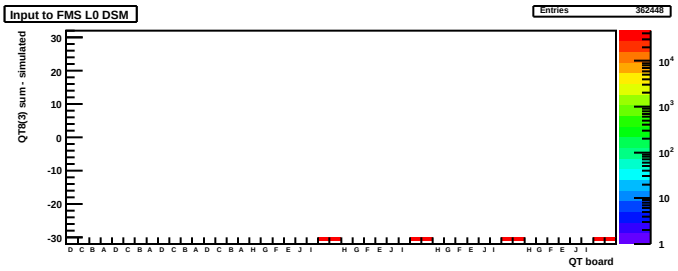

Input to FMS L0 DSM
Entries 2174688
QT8(1) sum
QT board

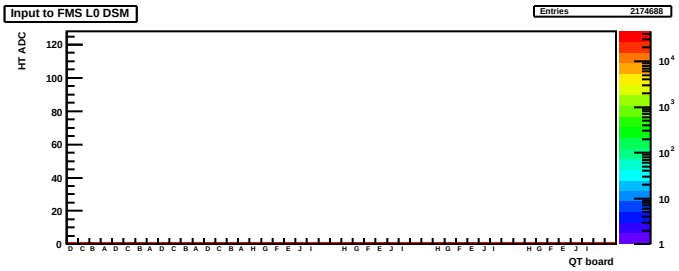

Input to FMS L0 DSM
Entries 362448
QT8(1) sum - simulated
QT board

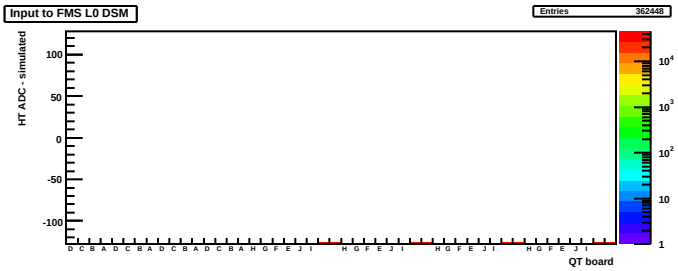

Input to FMS L0 DSM
Entries 2174688
QT8(2) sum
QT board

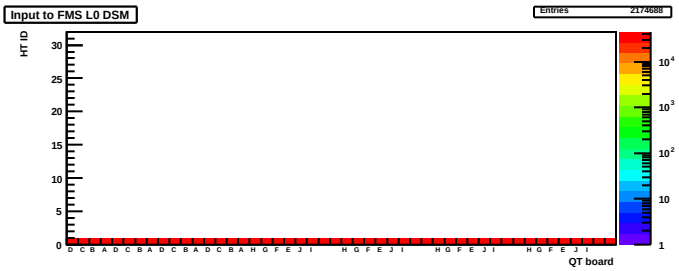

Input to FMS L0 DSM
Entries 362448
QT8(2) sum - simulated
QT board

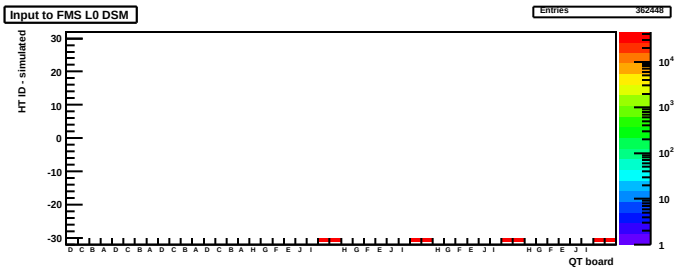

Input to FMS L0 DSM
Entries 2174688
QT8(3) sum
QT board

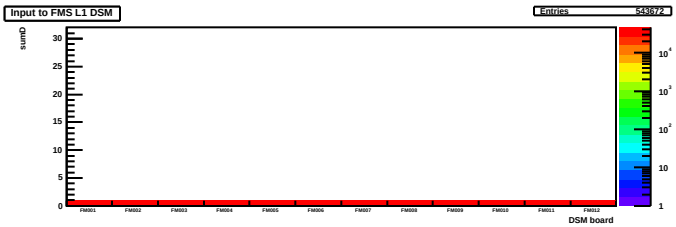

Input to FMS L0 DSM
Entries 362448
QT8(3) sum - simulated
QT board

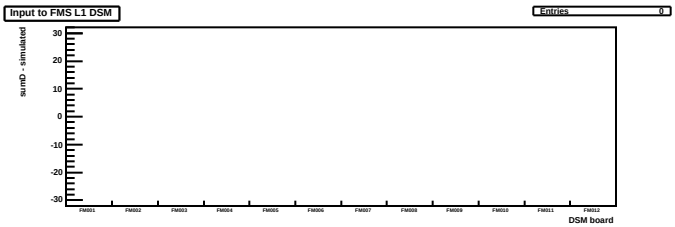

Input to FMS L0 DSM
Entries 2174688
HT ADC
QT board

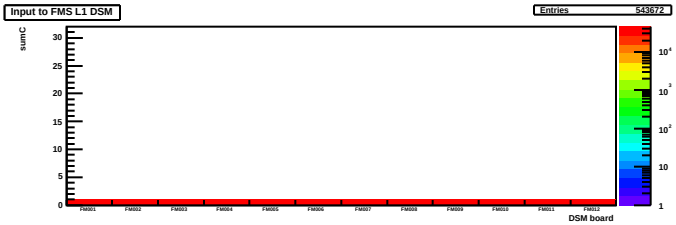

Input to FMS L0 DSM
Entries 362448
HT ADC - simulated
QT board

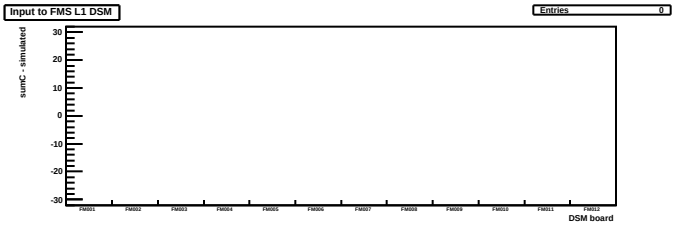

Input to FMS L0 DSM
Entries 2174688
HT ID
QT board

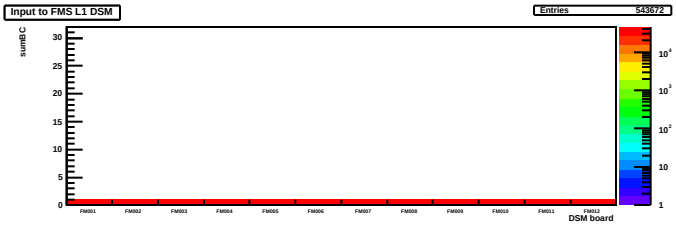

Input to FMS L0 DSM
Entries 362448
HT ID - simulated
QT board

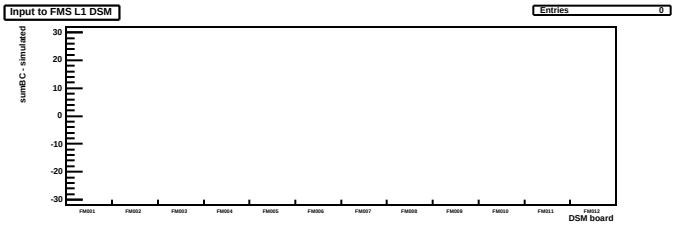

Input to FMS L1 DSM
Entries 543672
sumD
DSM board

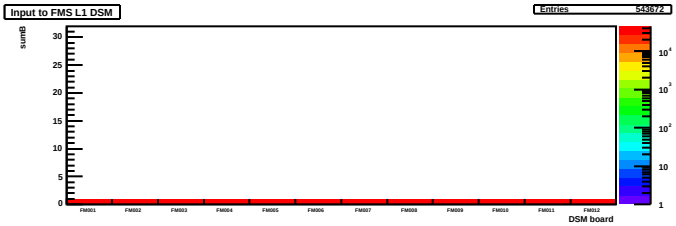

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

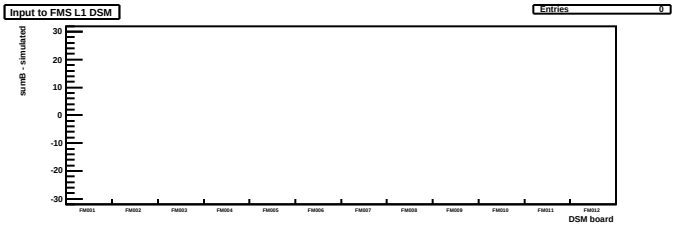

Input to FMS L1 DSM
Entries 543672
sumC
DSM board

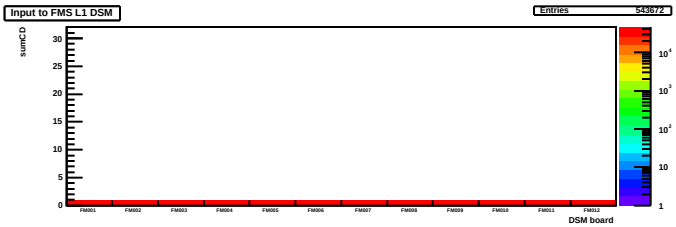

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

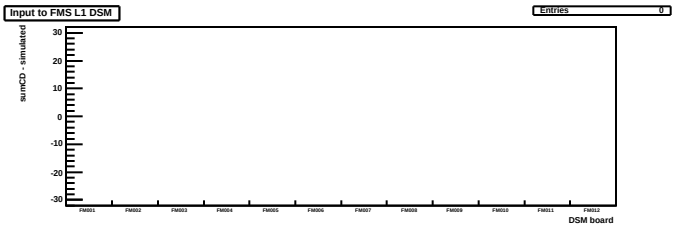

Input to FMS L1 DSM
Entries 543672
sumBC
DSM board

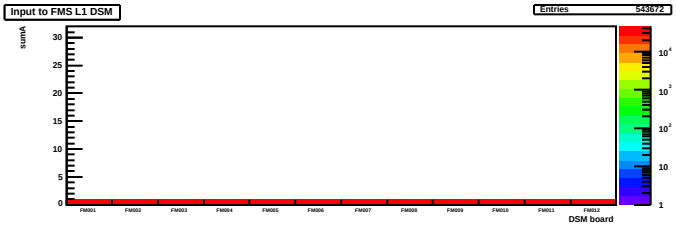

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

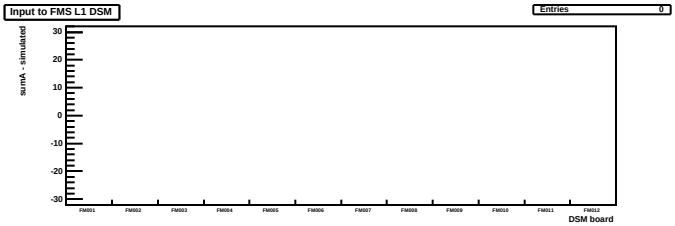

Input to FMS L1 DSM
Entries 543672
sumB
DSM board

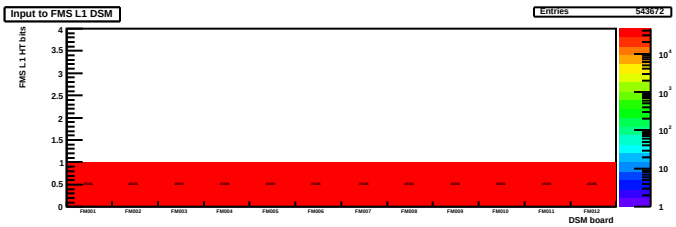

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

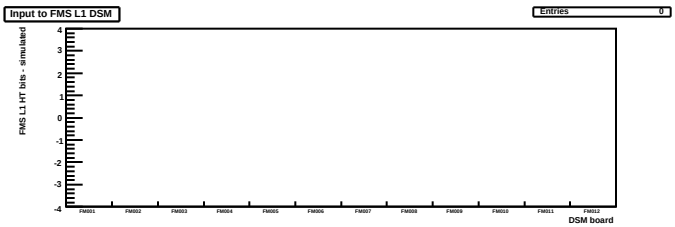

Input to FMS L1 DSM
Entries 543672
sumCD
DSM board

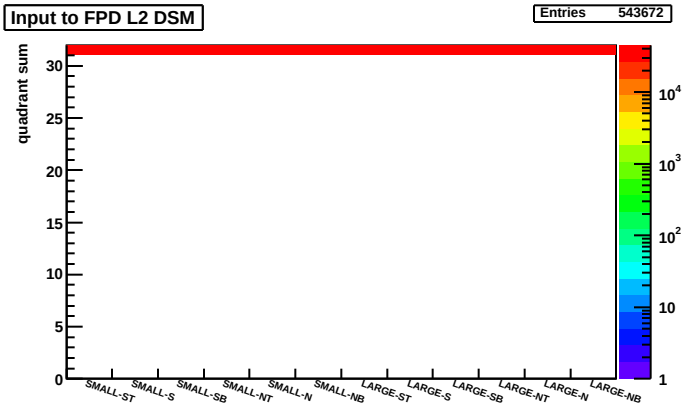

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

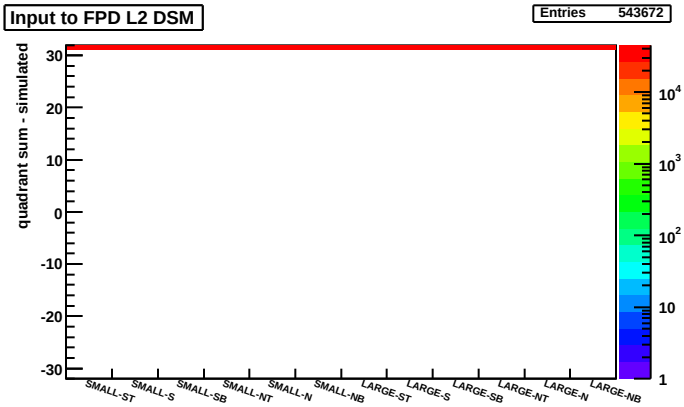

Input to FMS L1 DSM
Entries 543672
sumA
DSM board

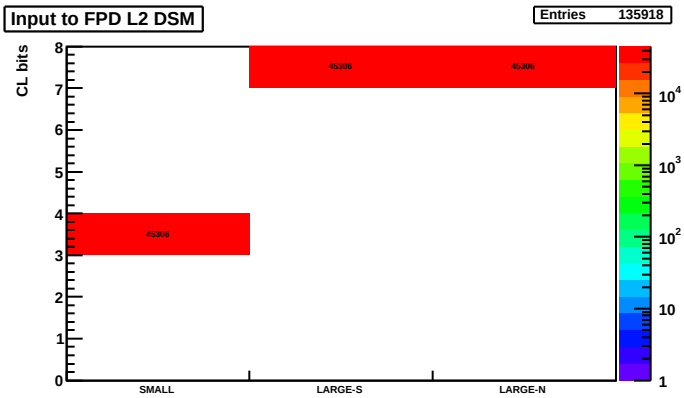

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

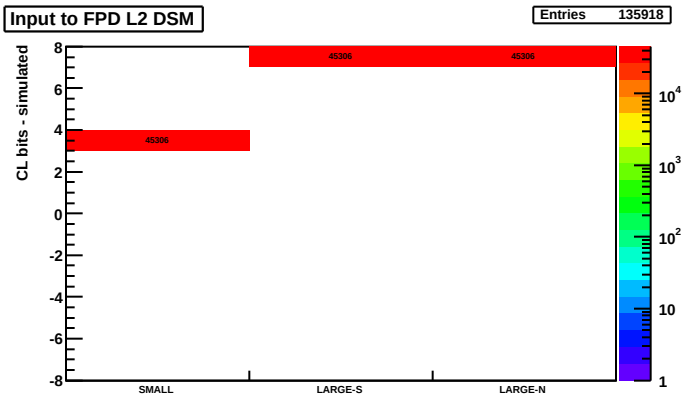

Input to FMS L1 DSM
Entries 543672
FMS L1 HT bits
DSM board

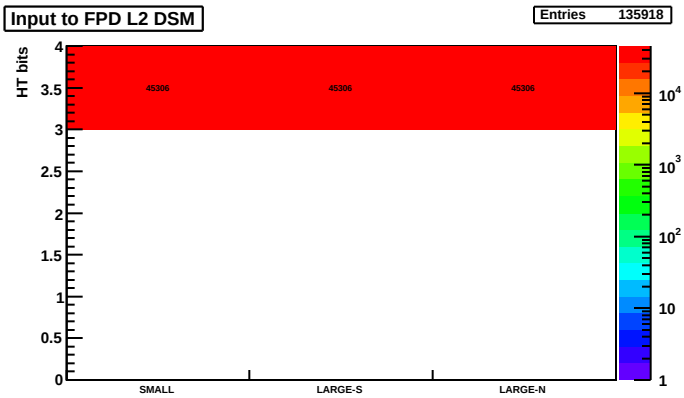

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

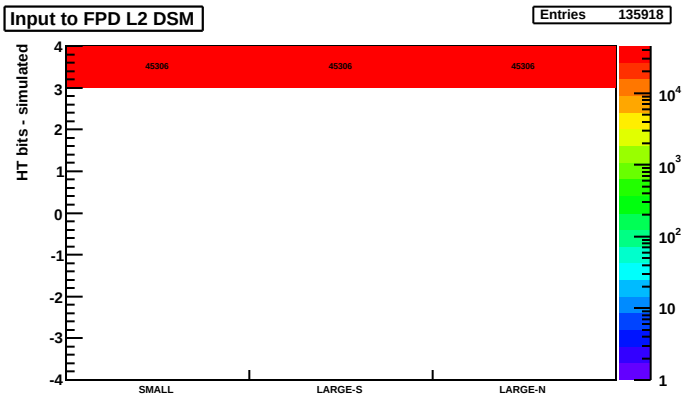

Input to FPD L2 DSM
Entries 543672
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

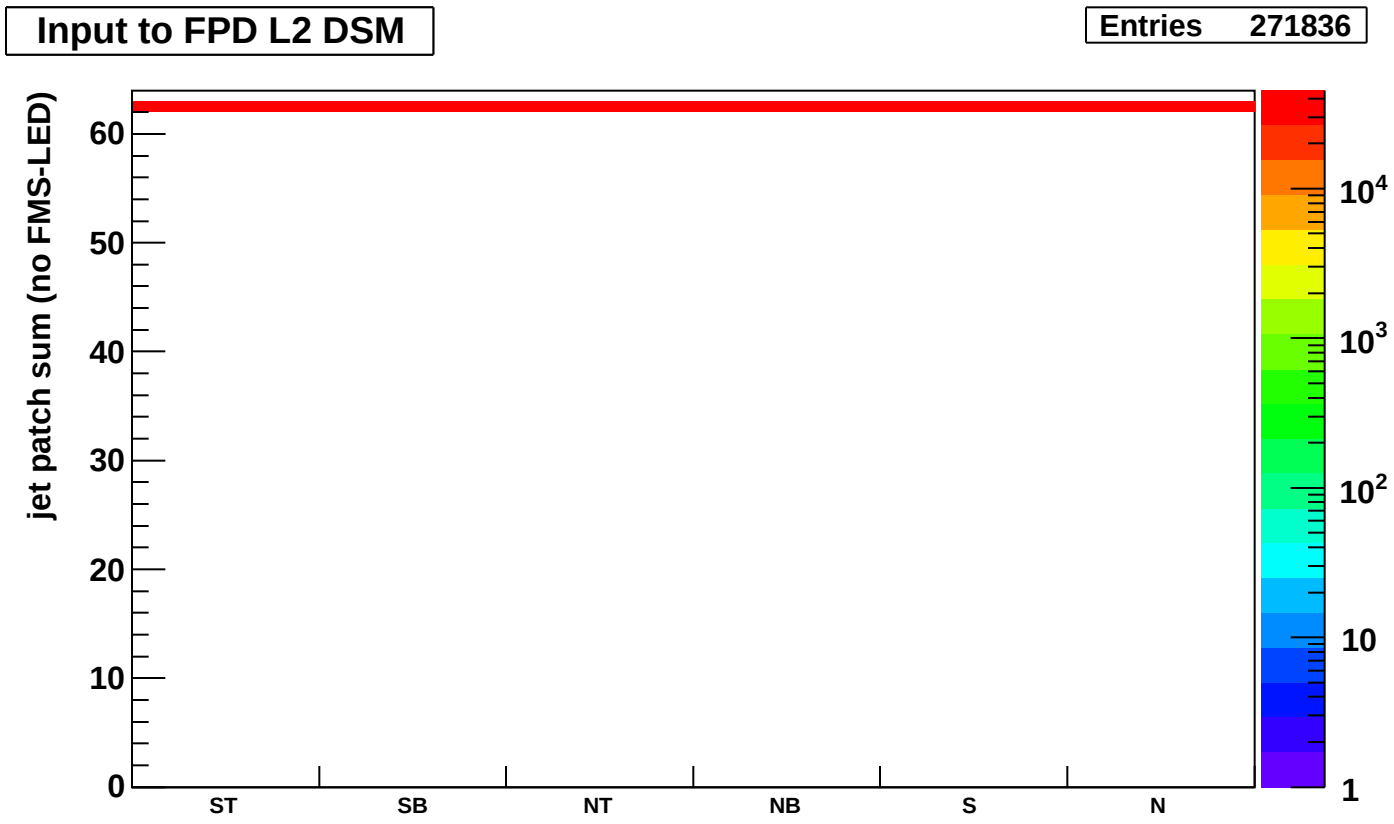

Input to FPD L2 DSM
Entries 543672
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

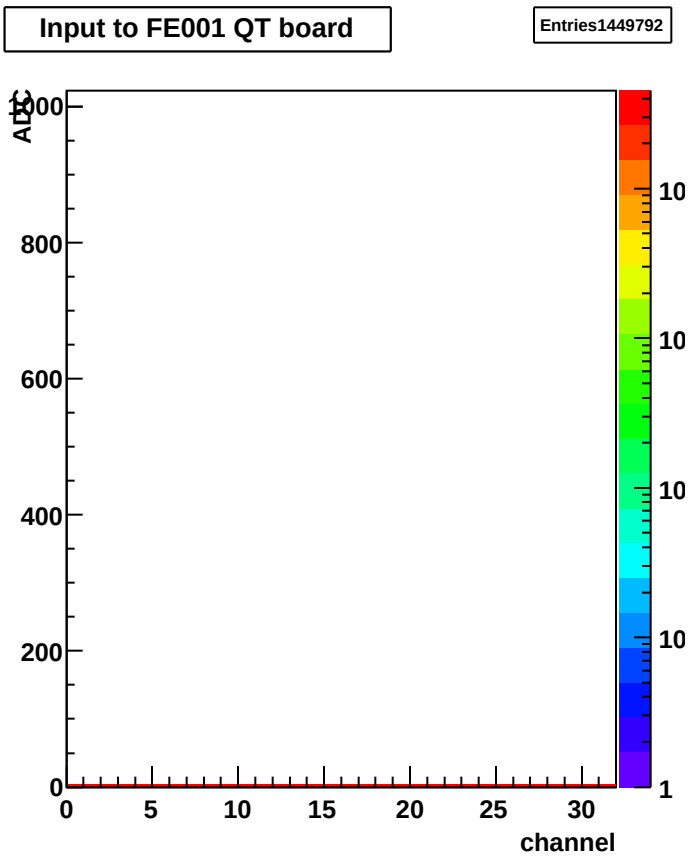

Input to FPD L2 DSM
Entries 135918
CL bits
45306
45306
45306
SMALL
LARGE-S
LARGE-N

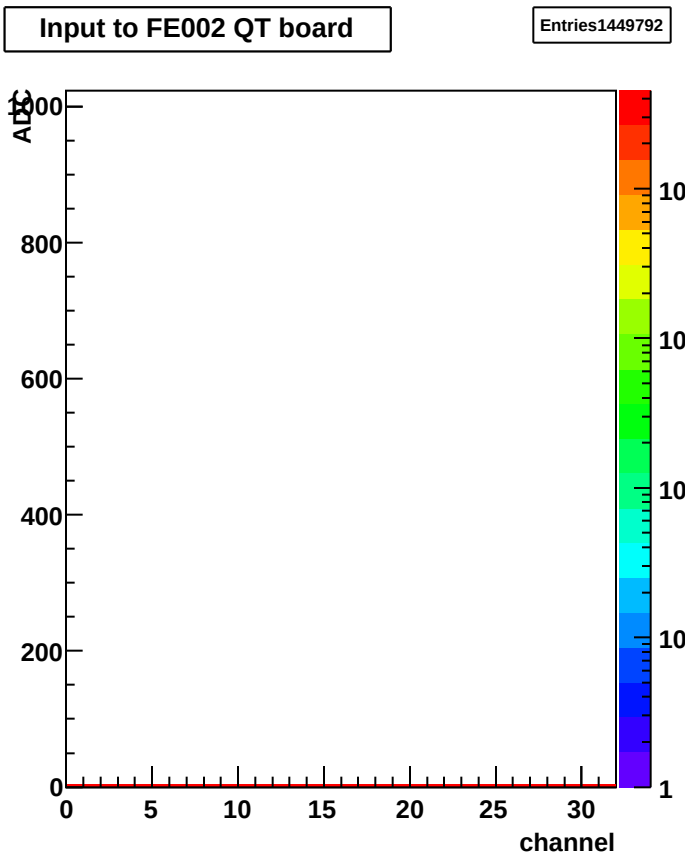

Input to FPD L2 DSM
Entries 135918
CL bits - simulated
45306
45306
45306
SMALL
LARGE-S
LARGE-N

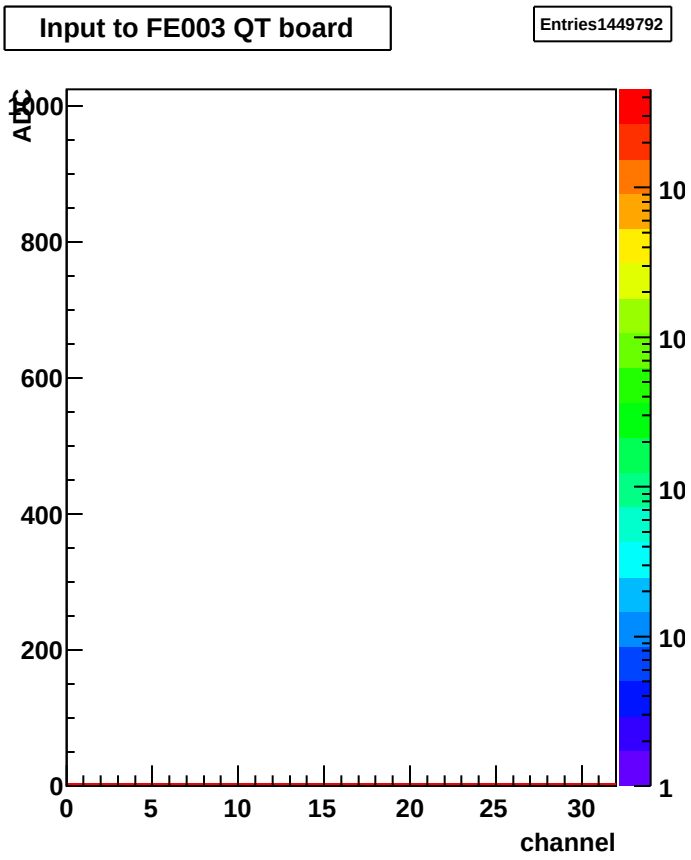

Input to FPD L2 DSM
Entries 135918
HT bits
45306
45306
45306
SMALL
LARGE-S
LARGE-N

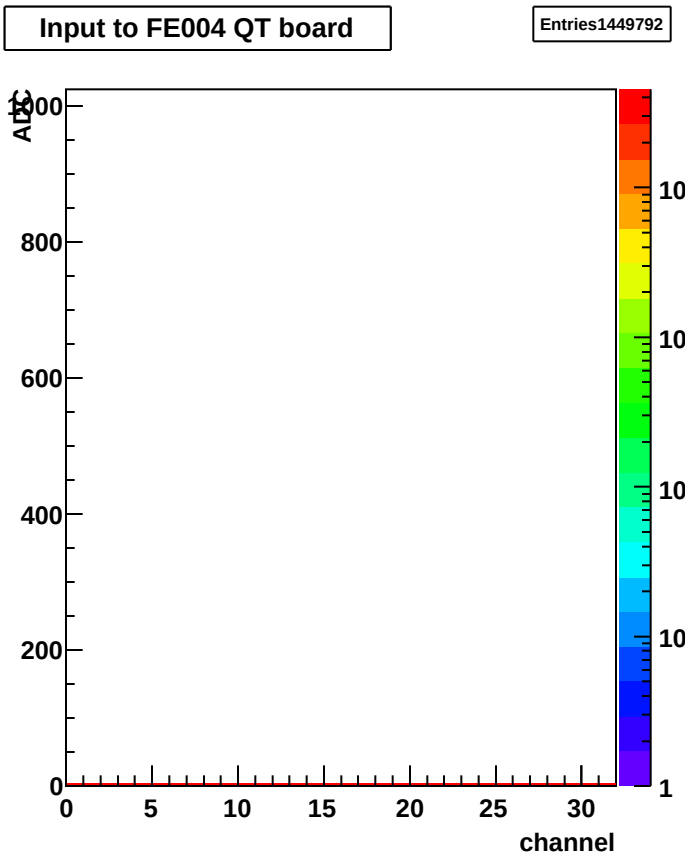

Input to FPD L2 DSM
Entries 135918
HT bits - simulated
45306
45306
45306
SMALL
LARGE-S
LARGE-N

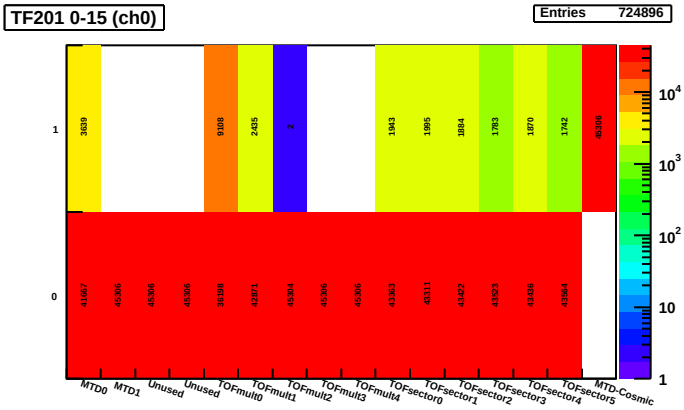

Input to FPD L2 DSM
Entries 271836
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

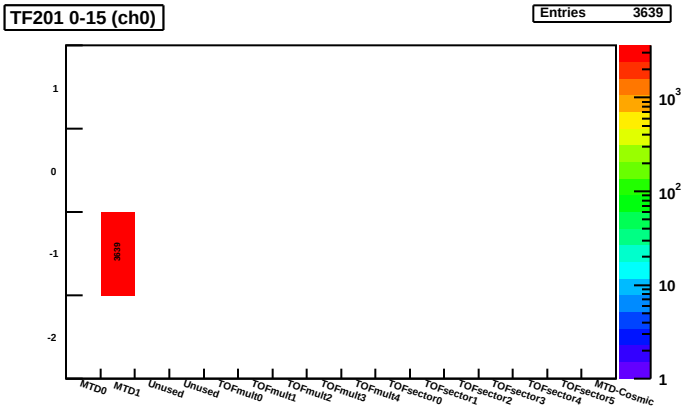

Input to FE001 QT board
Entries1449792
ADC
channel

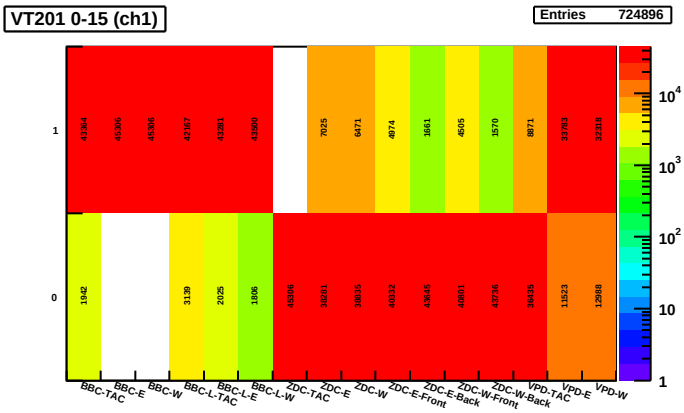

Input to FE002 QT board
Entries1449792
ADC
channel

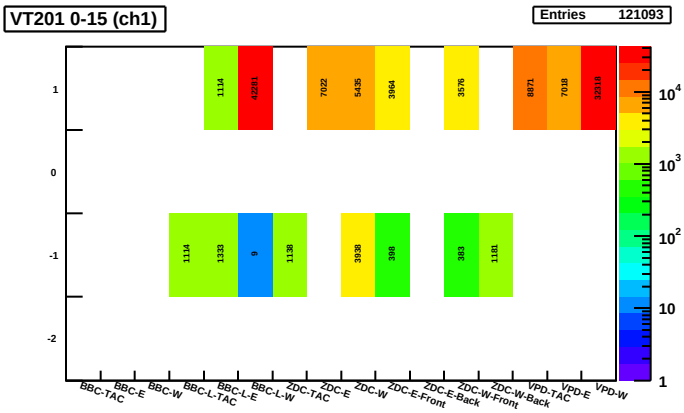

Input to FE003 QT board
Entries1449792
ADC
channel

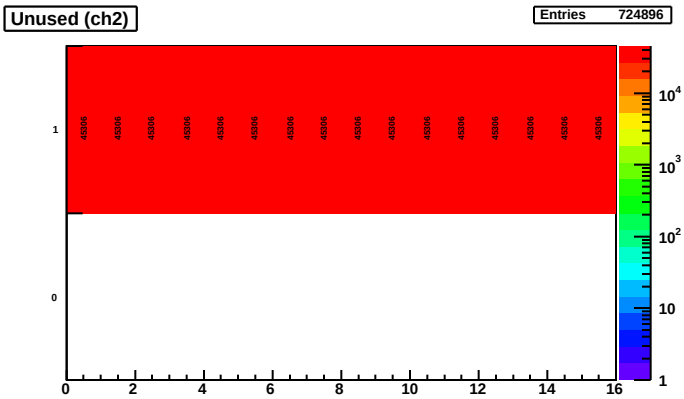

Input to FE004 QT board
Entries1449792
ADC
channel

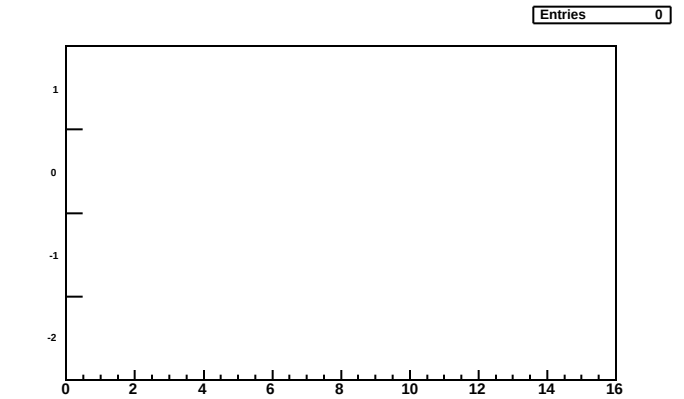

TF201 0-15 (ch0)
Entries 724896
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

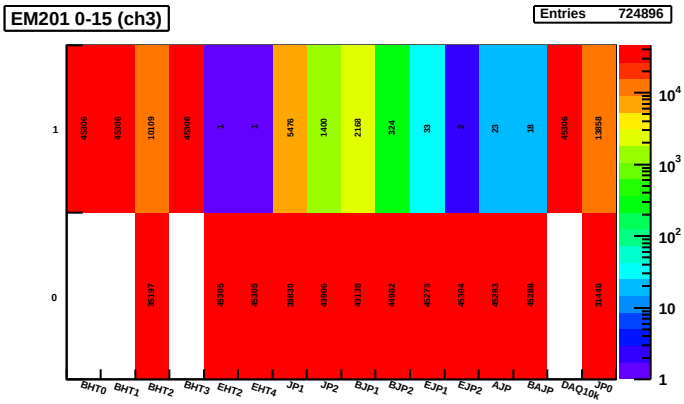

TF201 0-15 (ch0)
Entries 3639
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 724896
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 121093
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 724896


Entries 0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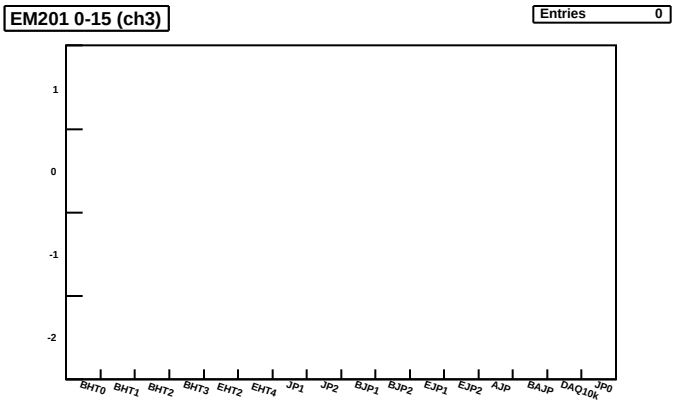
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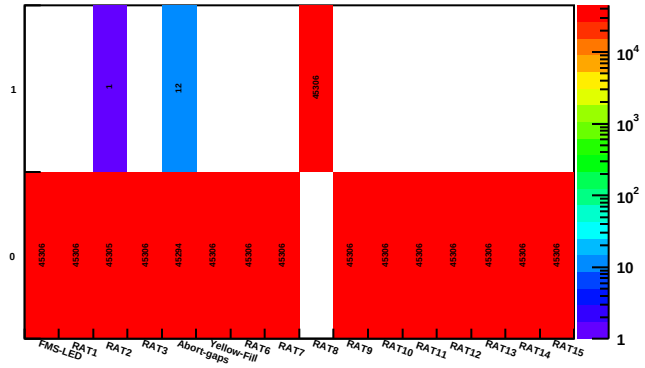
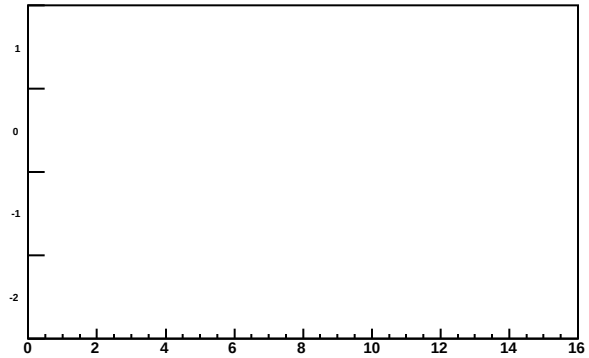
RAT board (ch4)





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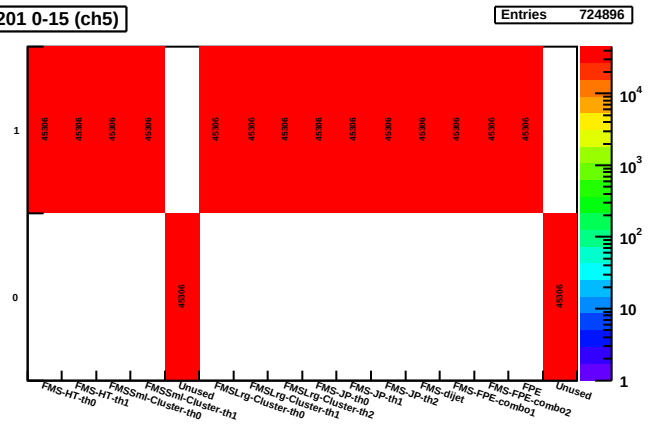

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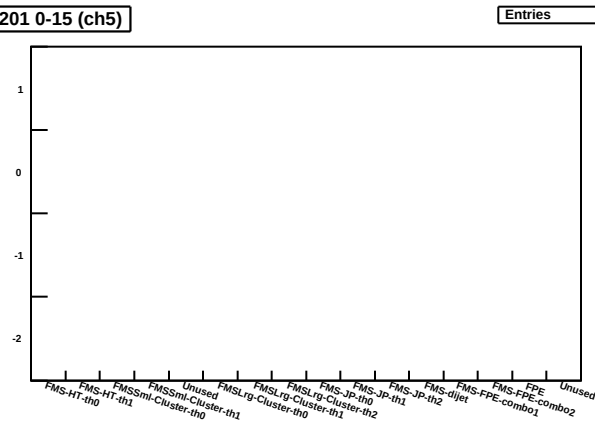

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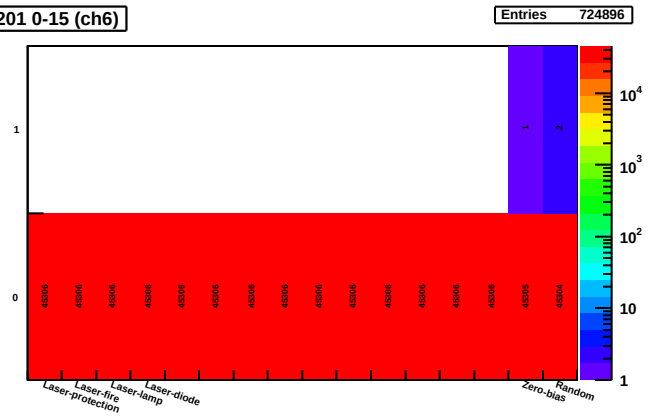

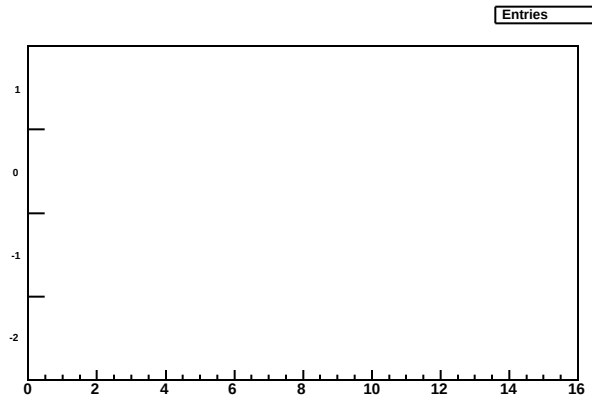
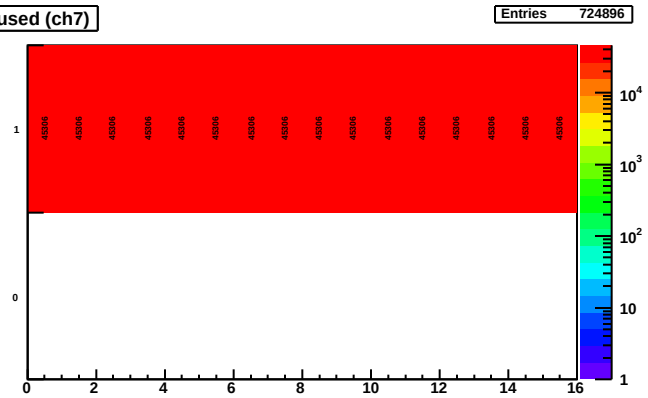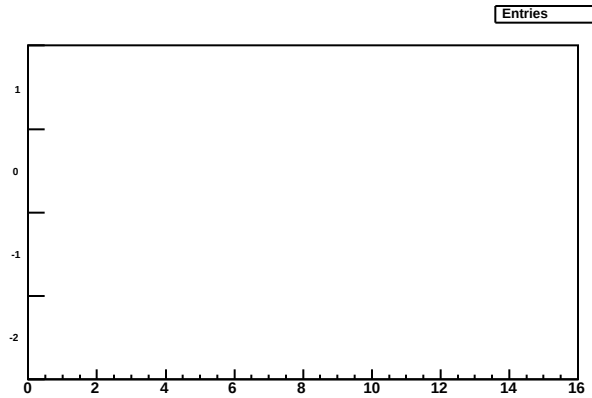
Unused (ch7)