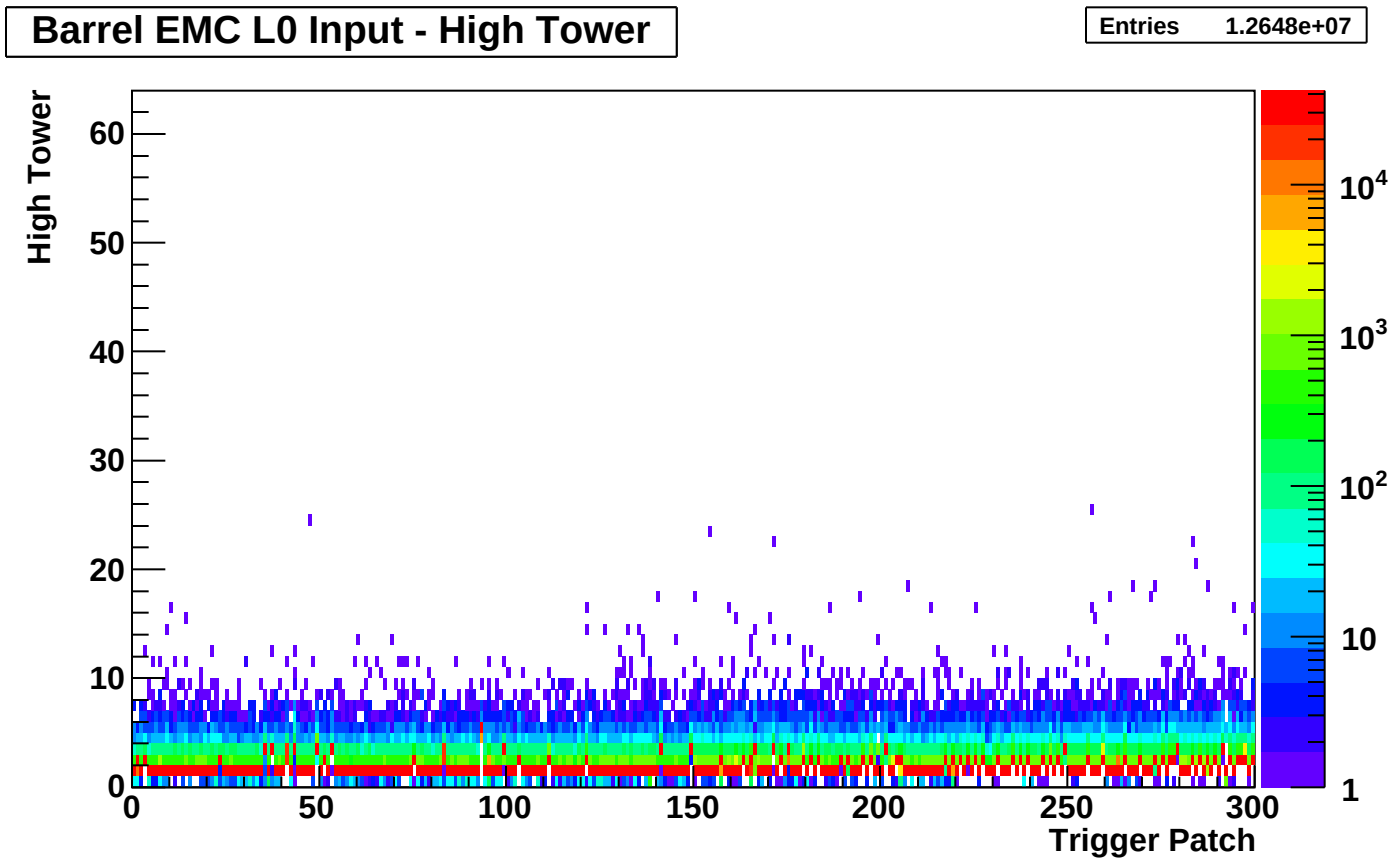
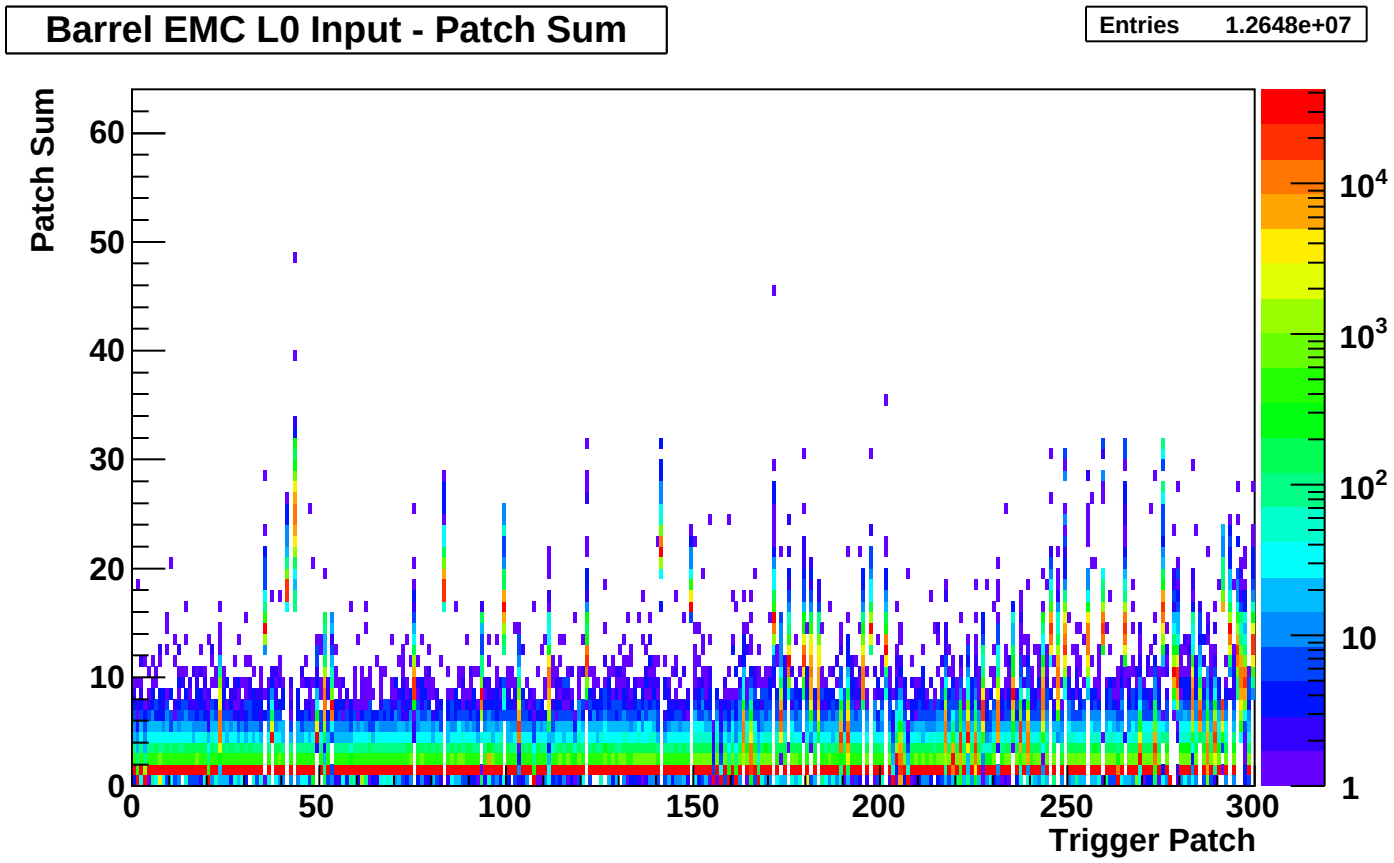
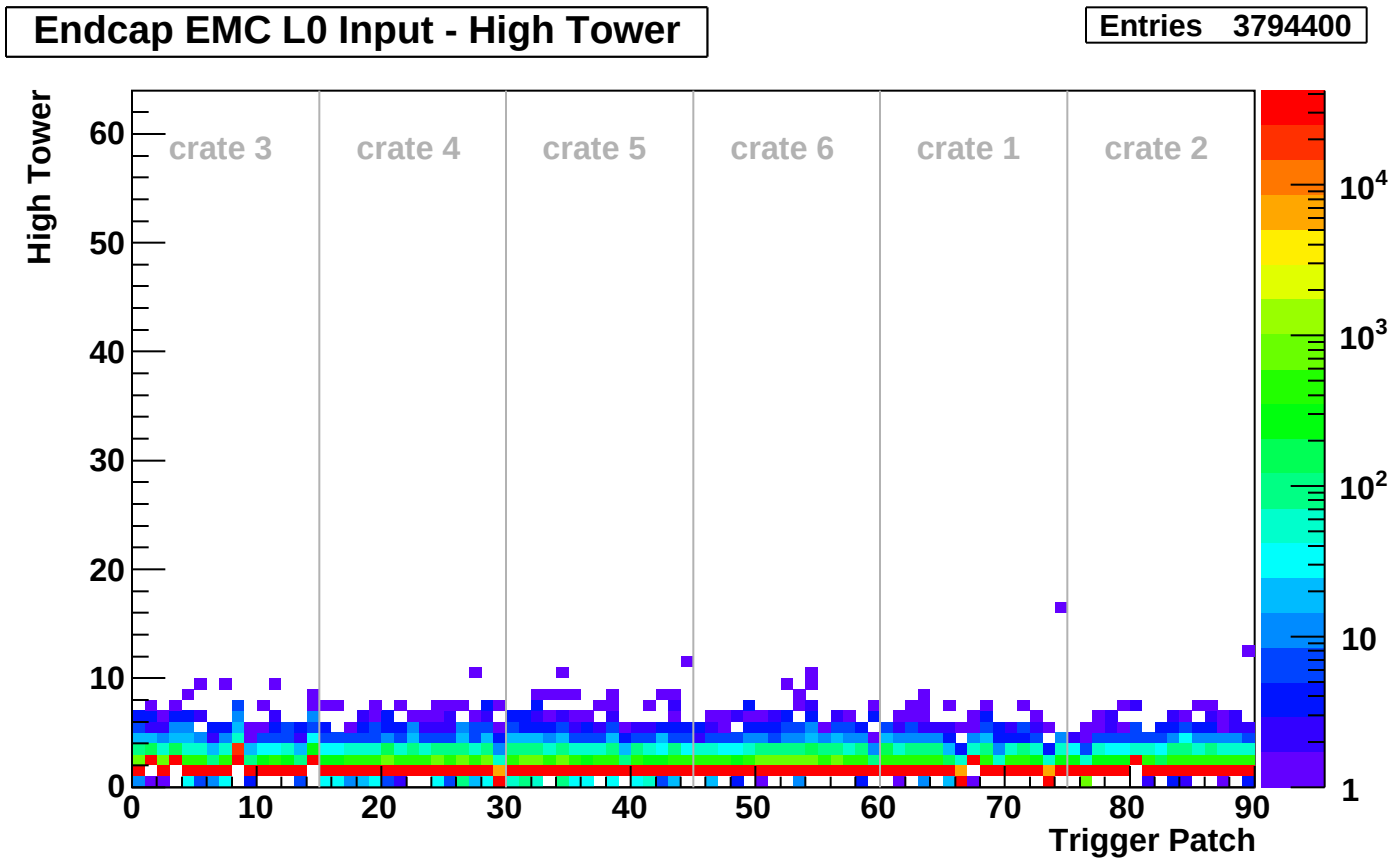
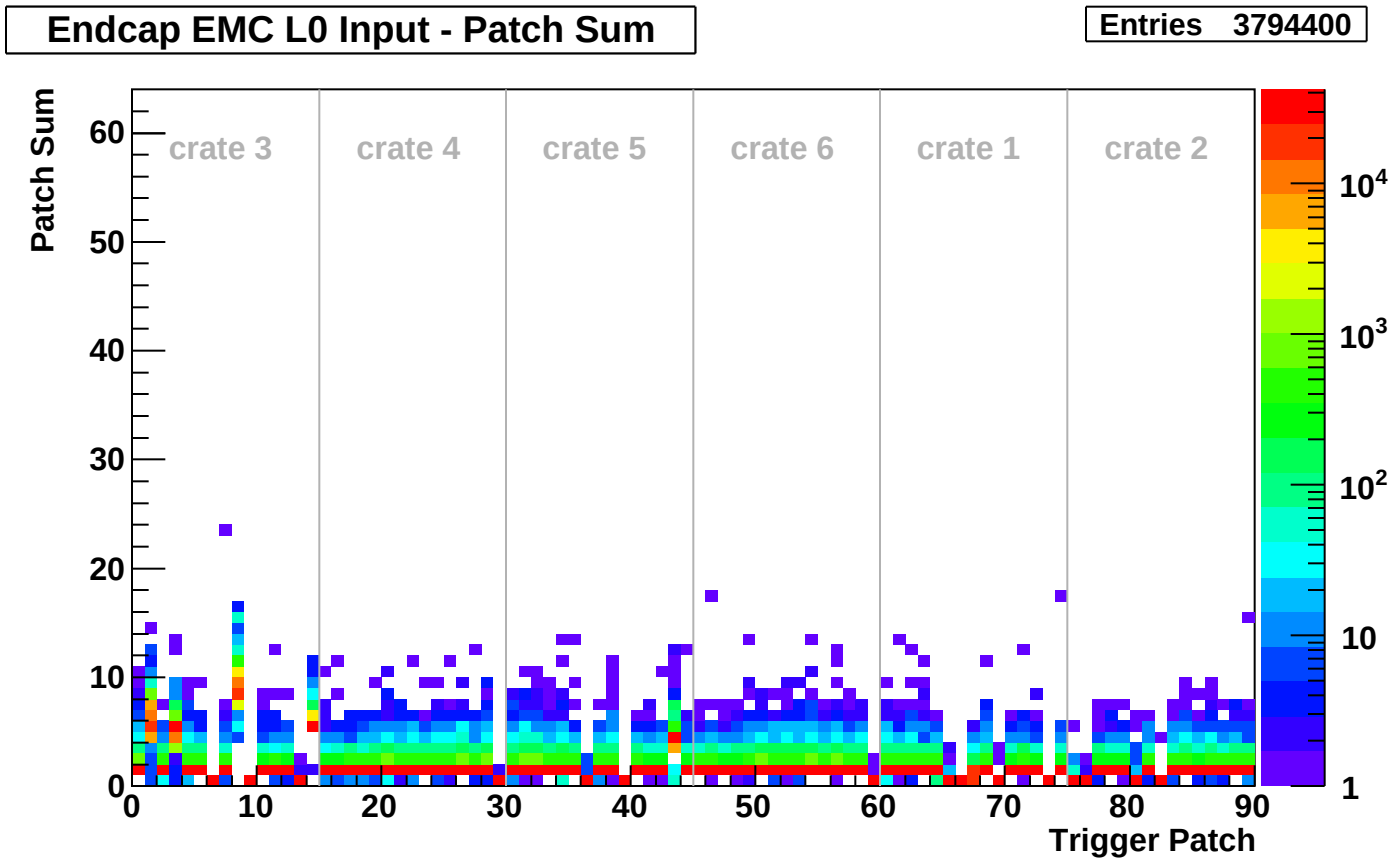
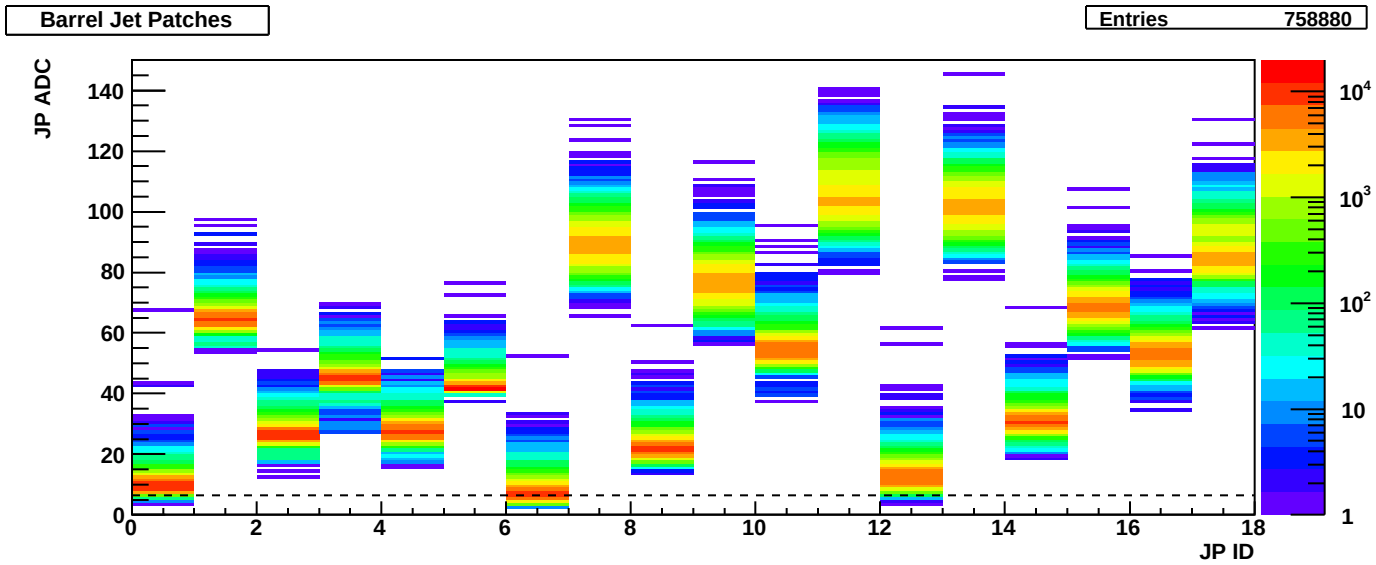
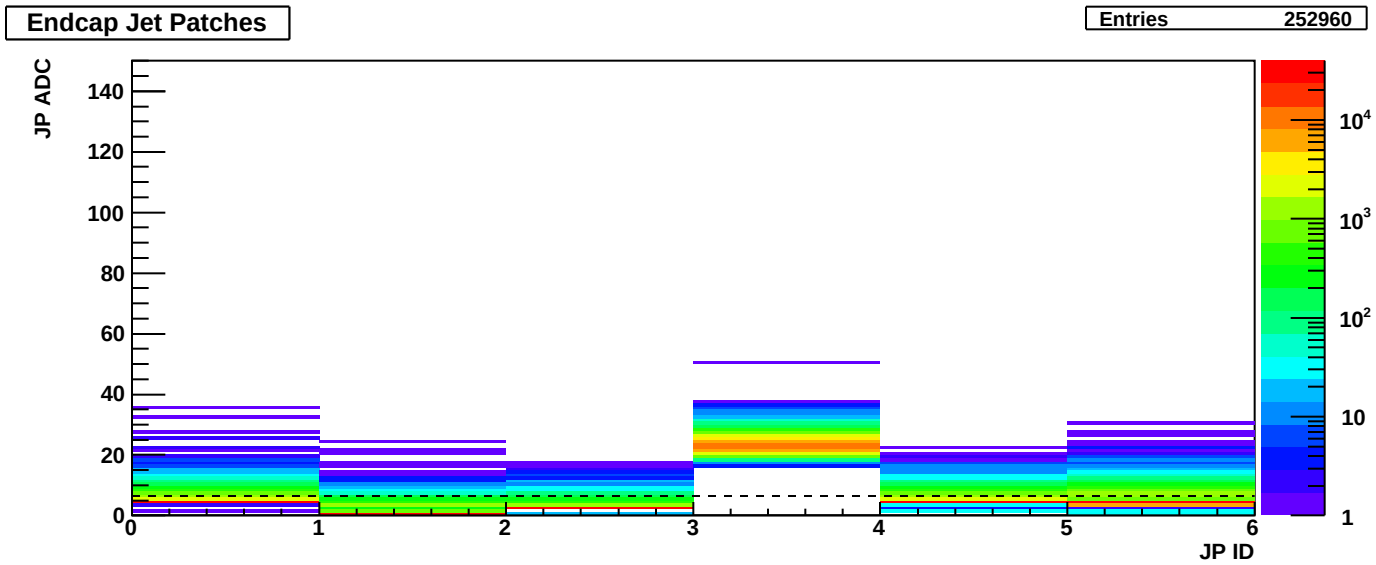

Barrel EMC L0 Input - High Tower
Entries 1.2648e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

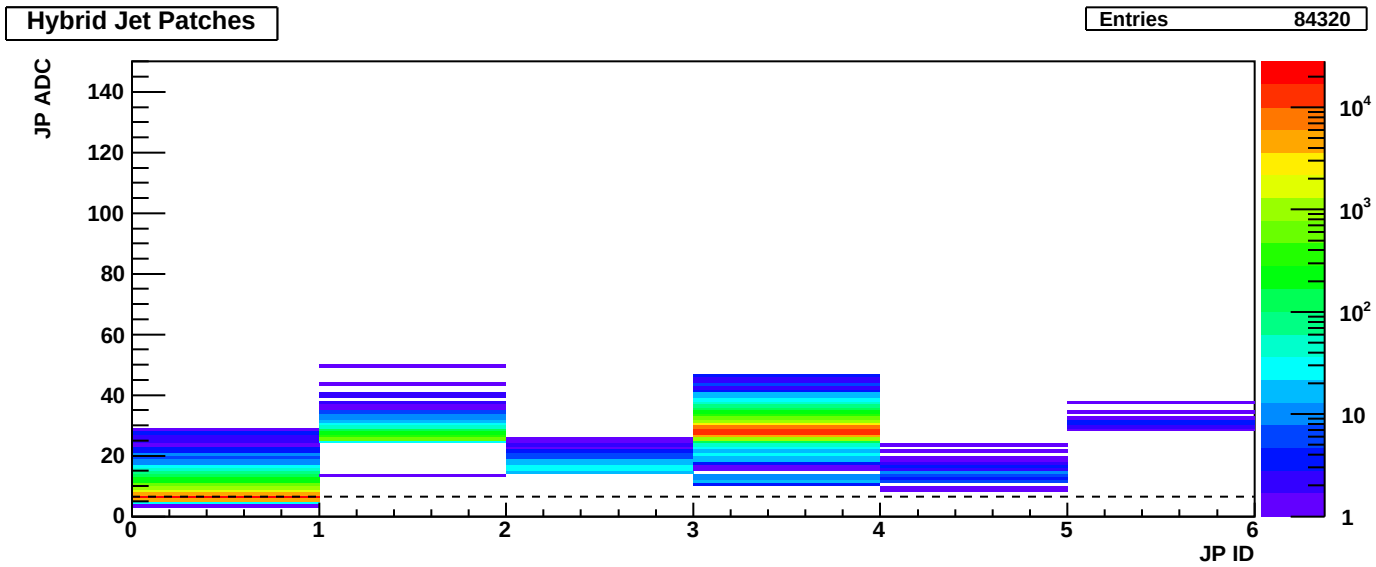

Barrel EMC L0 Input - Patch Sum
Entries 1.2648e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

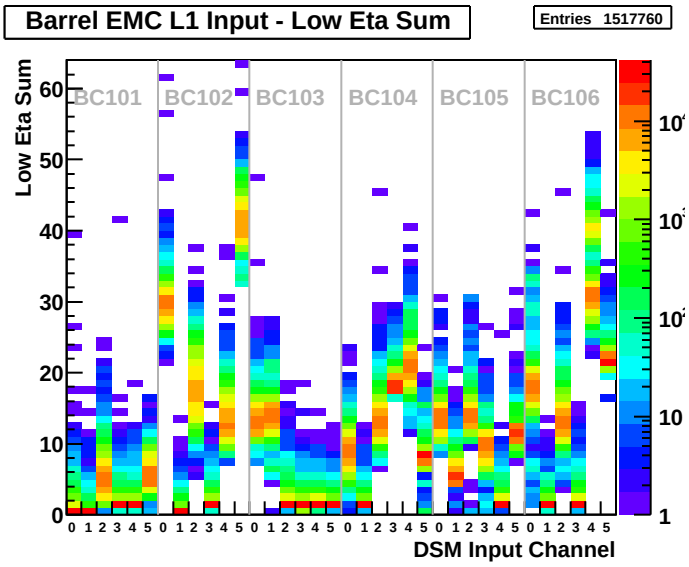

Endcap EMC L0 Input - High Tower
Entries 3794400
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

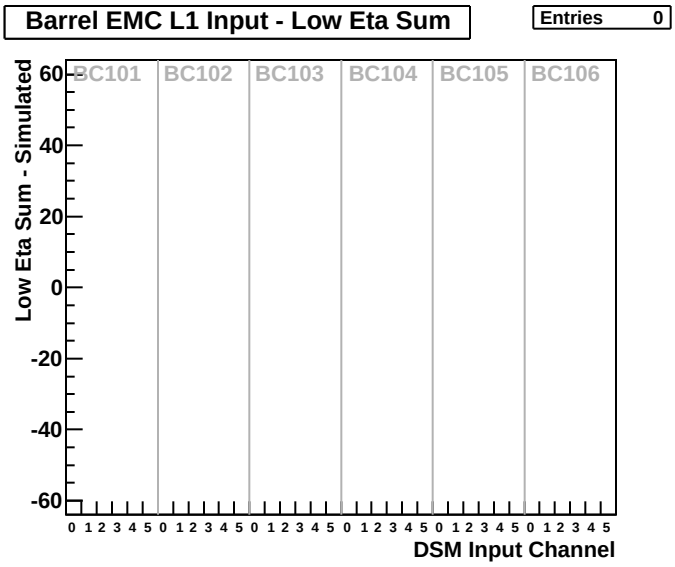

Endcap EMC L0 Input - Patch Sum
Entries 3794400
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

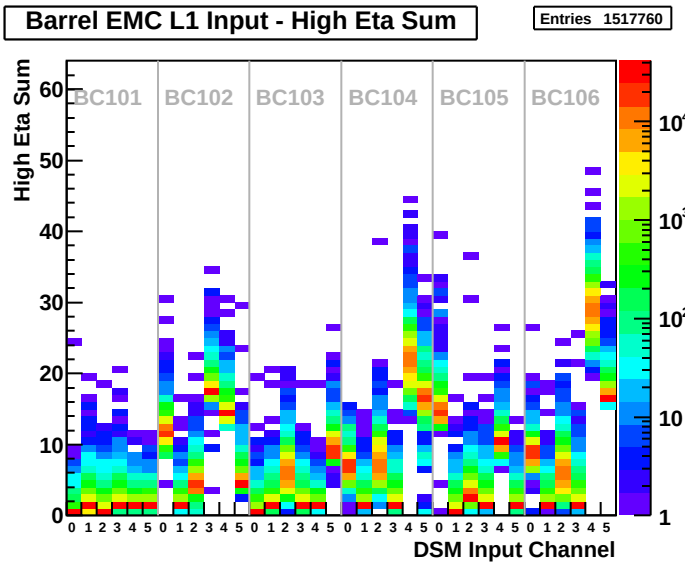

Barrel Jet Patches
Entries 758880
JP ADC
JP ID

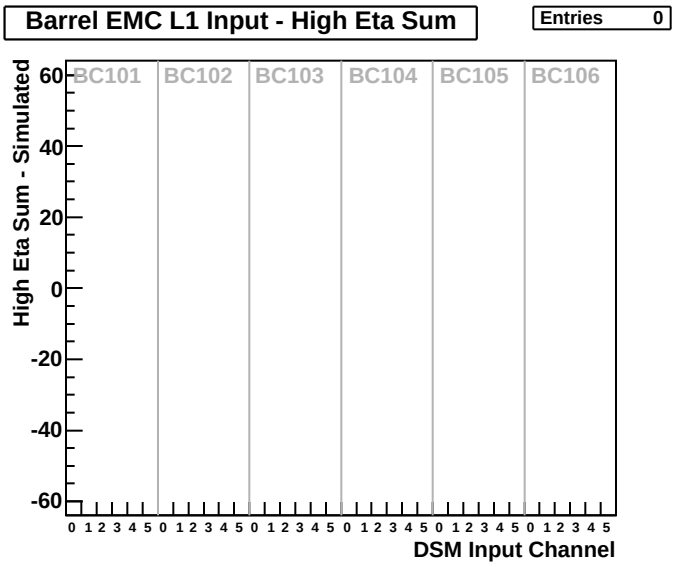

Endcap Jet Patches
Entries 252960
JP ADC
JP ID

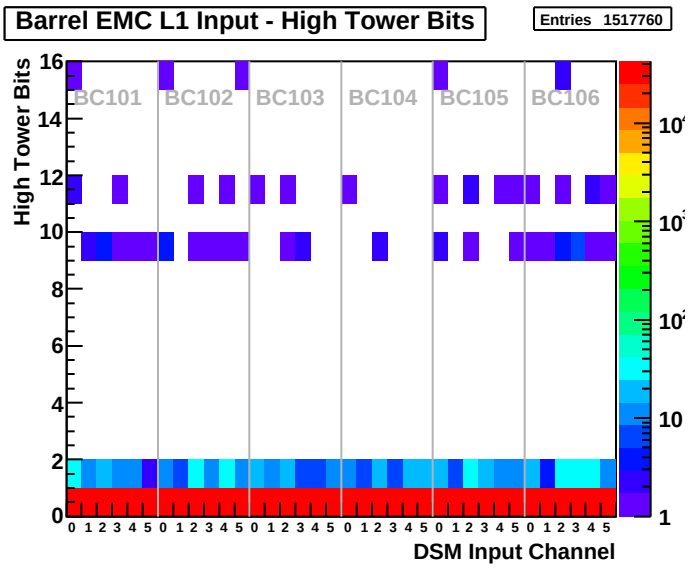

Hybrid Jet Patches
Entries 84320
JP ADC
JP ID

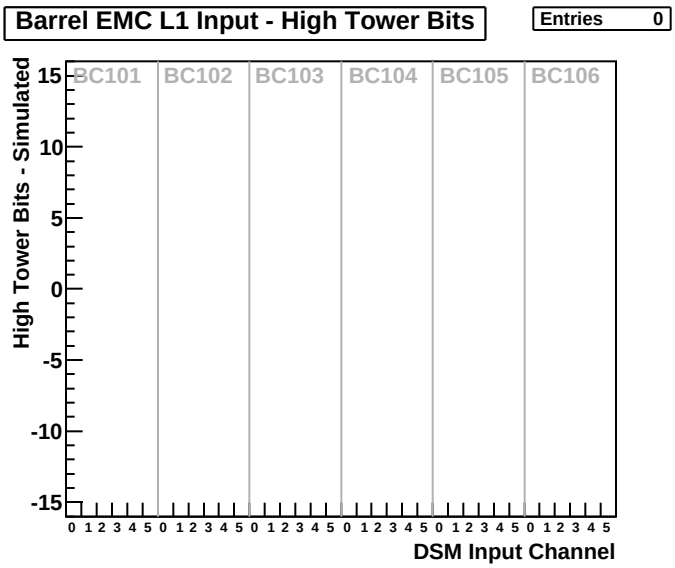

Barrel EMC L1 Input - Low Eta Sum
Entries 1517760
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

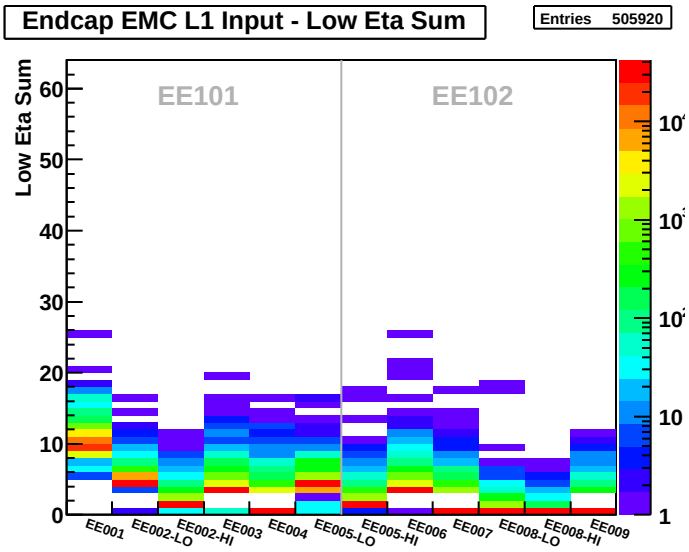

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

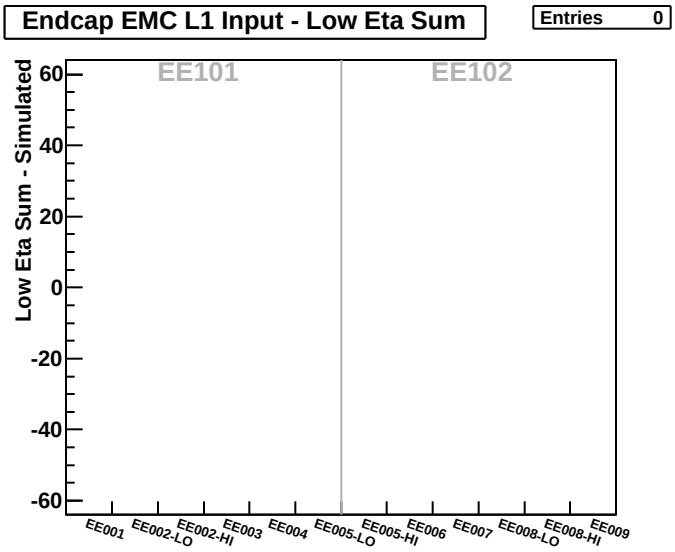

Barrel EMC L1 Input - High Eta Sum
Entries 1517760
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

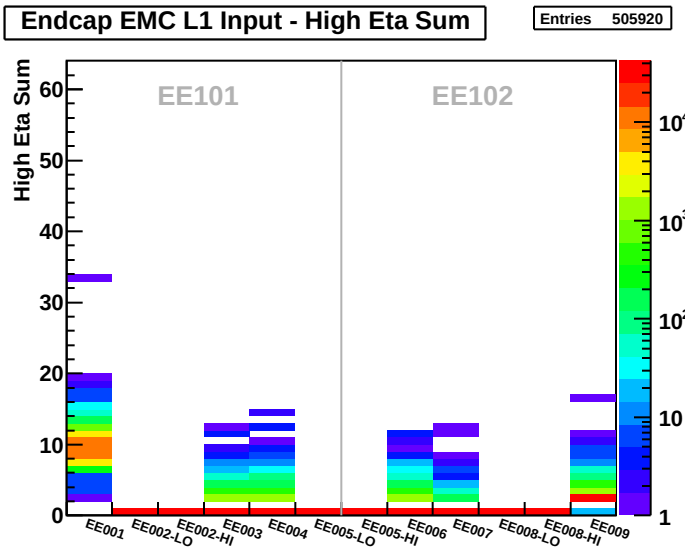

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

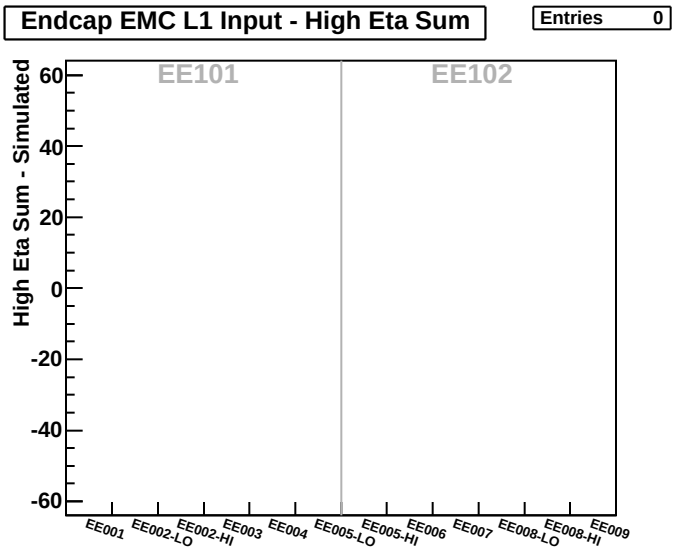

Barrel EMC L1 Input - High Tower Bits
Entries 1517760
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

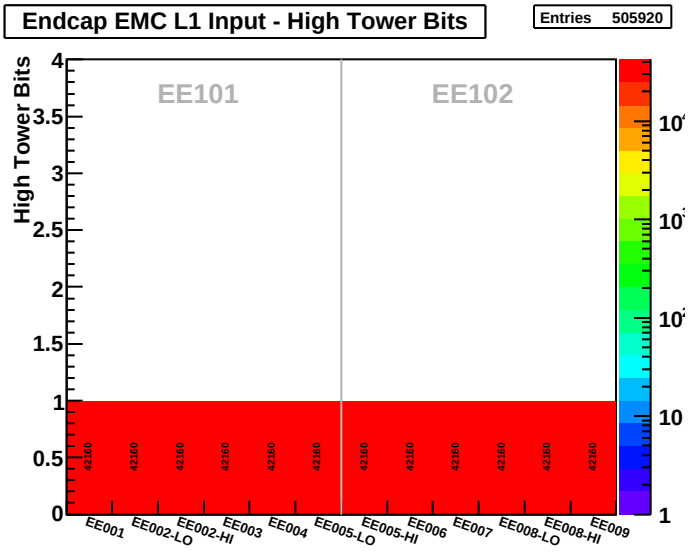

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

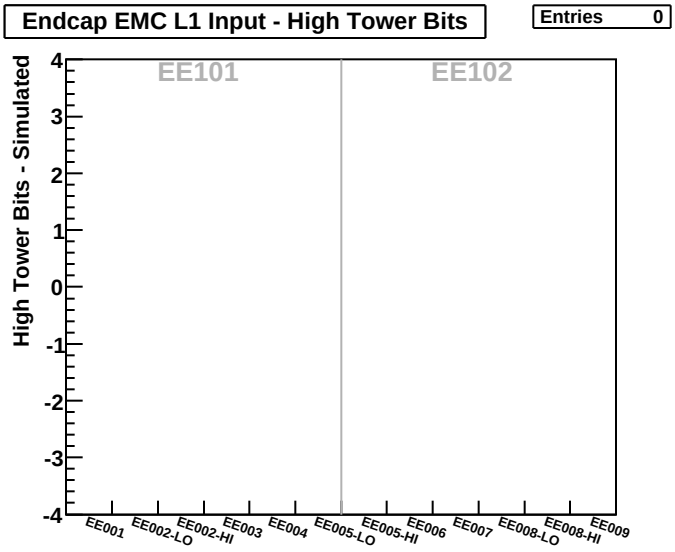

Endcap EMC L1 Input - Low Eta Sum
Entries 505920
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

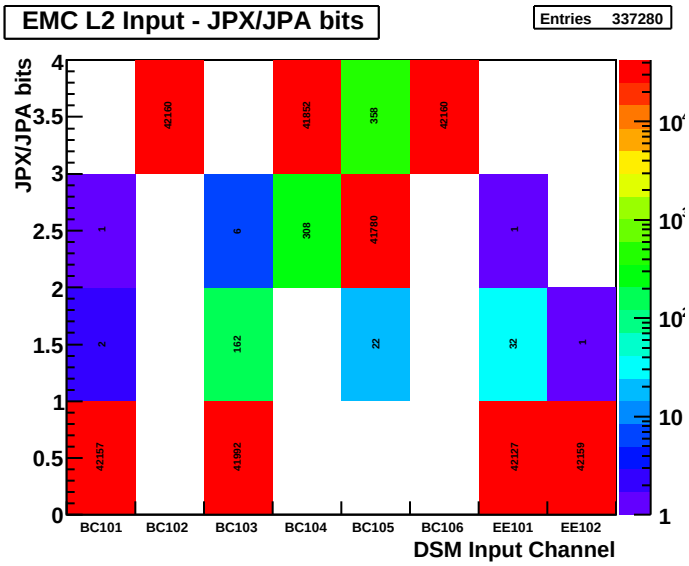

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

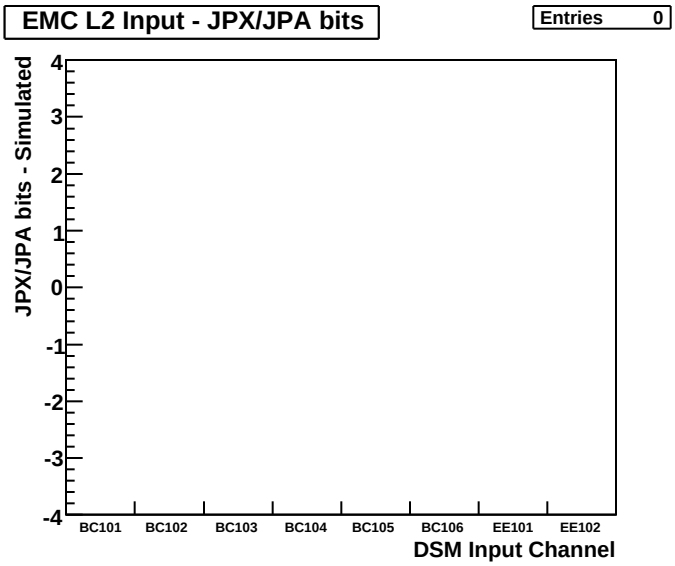

Endcap EMC L1 Input - High Eta Sum
Entries 505920
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

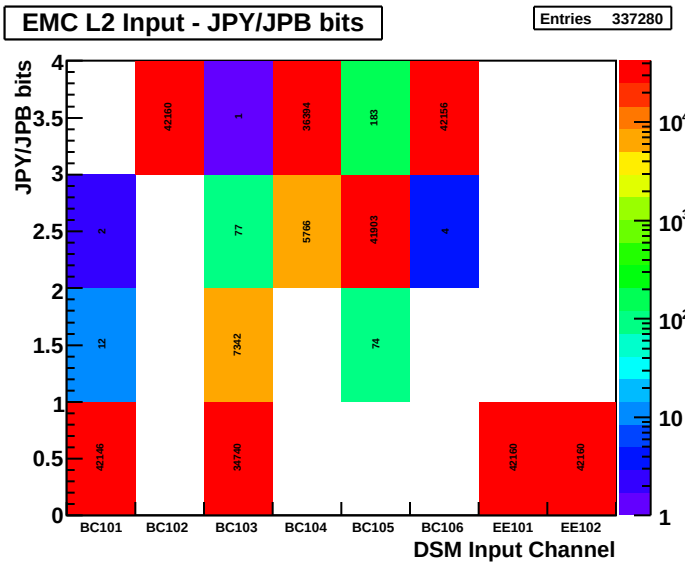

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

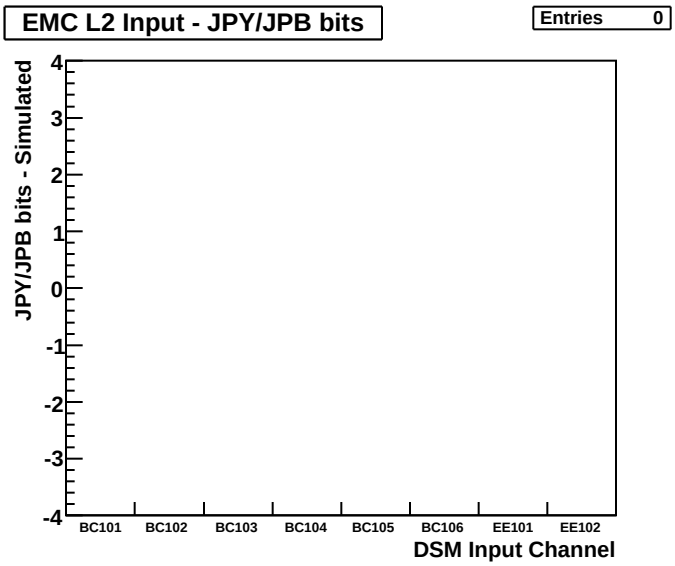

Endcap EMC L1 Input - High Tower Bits
Entries 505920
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

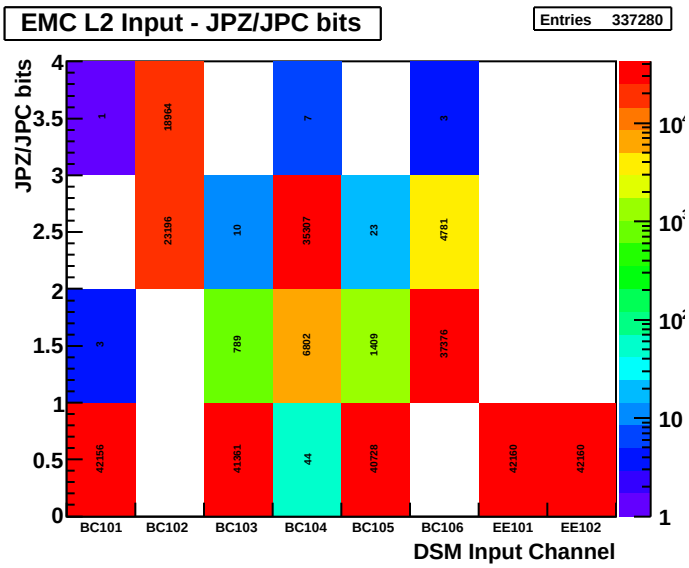

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

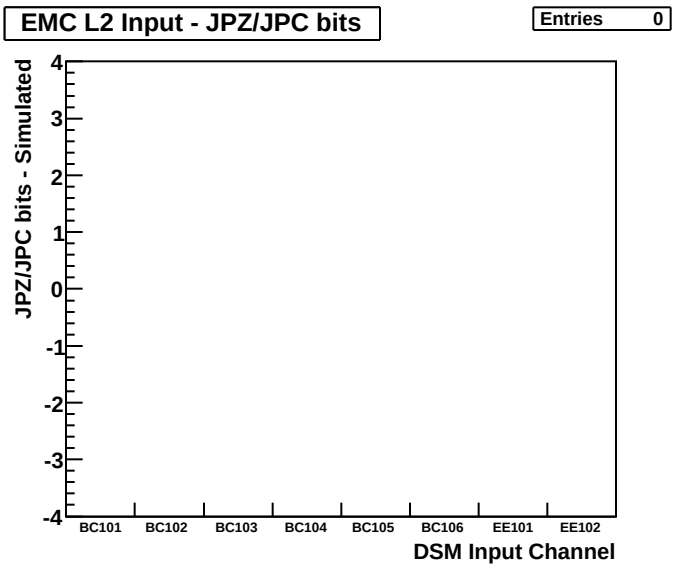

EMC L2 Input - JPX/JPA bits
Entries 337280
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

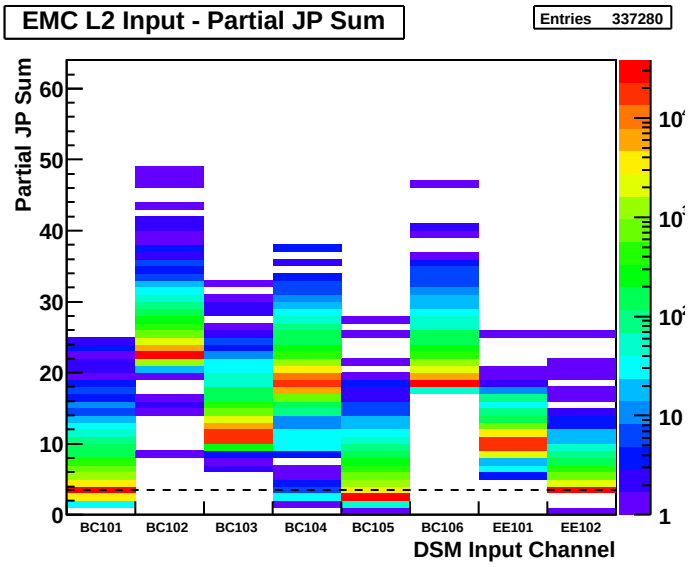

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

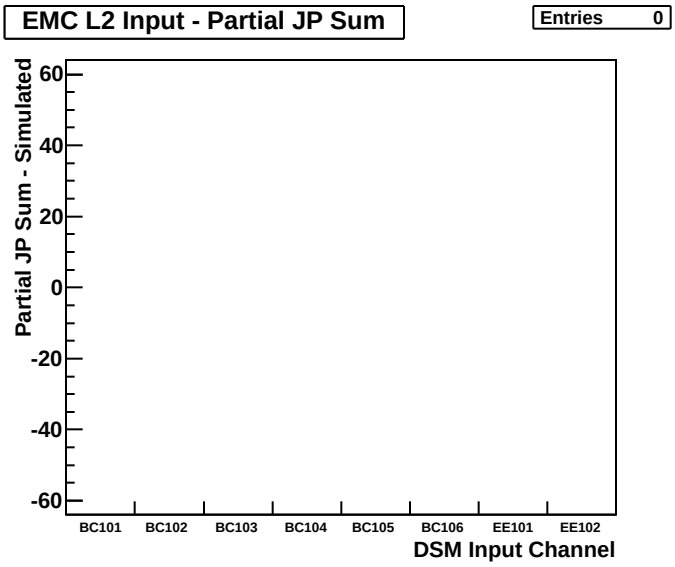

EMC L2 Input - JPY/JPB bits
Entries 337280
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

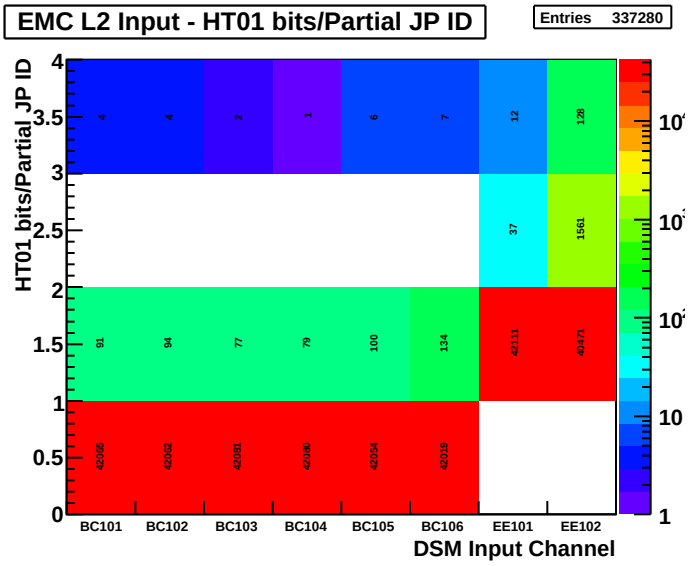

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

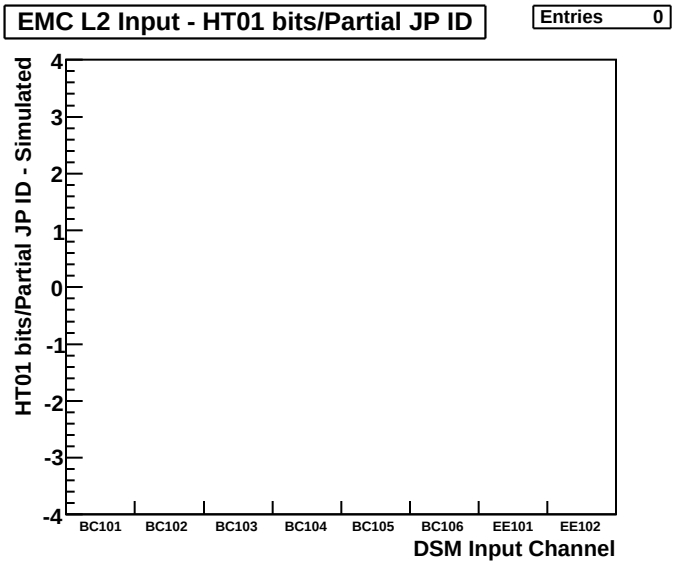

EMC L2 Input - JPZ/JPC bits
Entries 337280
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

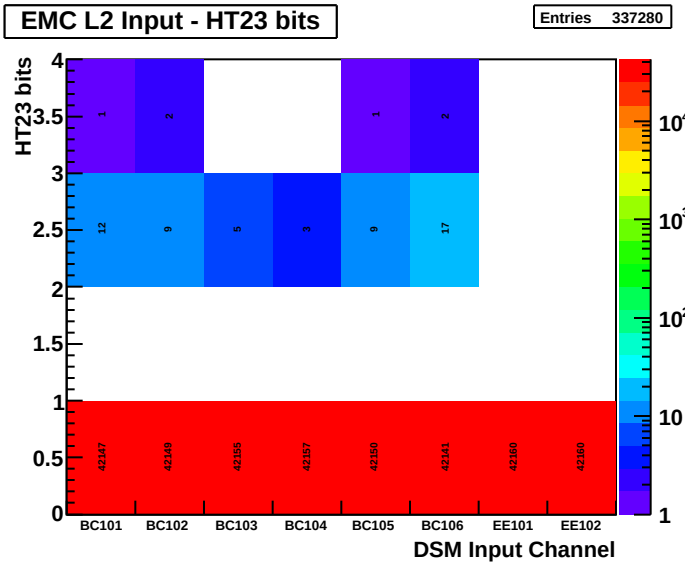

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

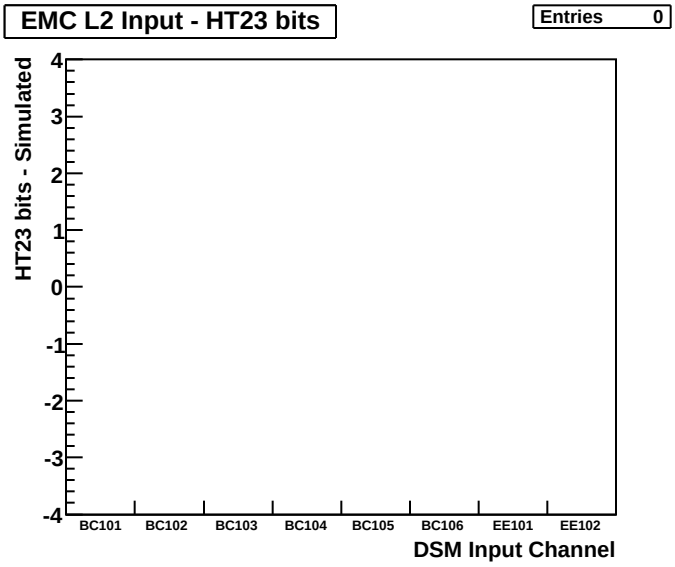

EMC L2 Input - Partial JP Sum
Entries 337280
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

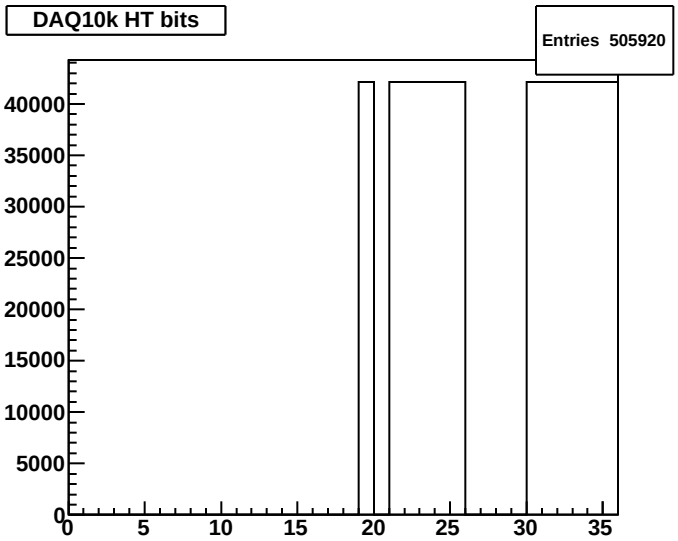

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

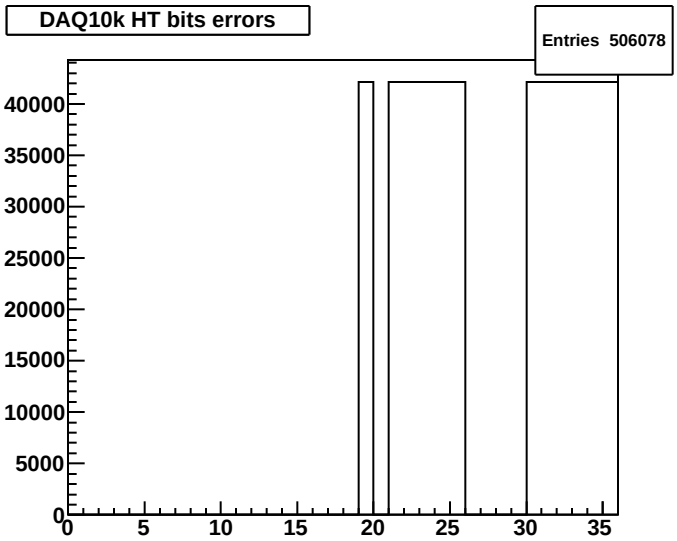

EMC L2 Input - HT01 bits/Partial JP ID
Entries 337280
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

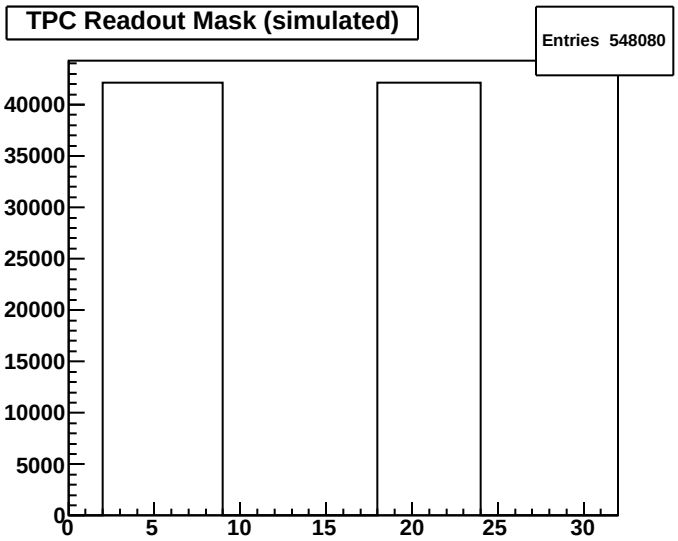

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

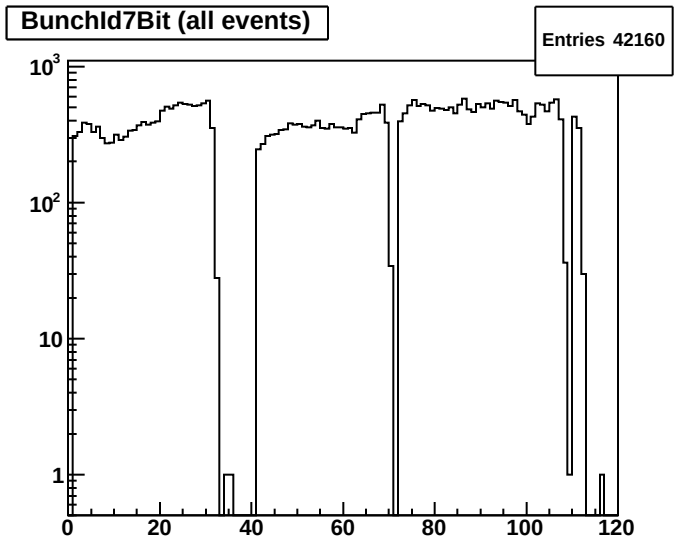

EMC L2 Input - HT23 bits
Entries 337280
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

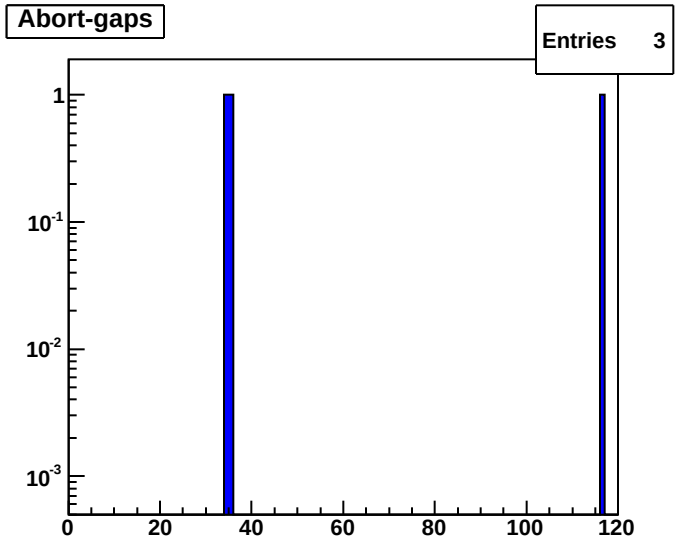

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

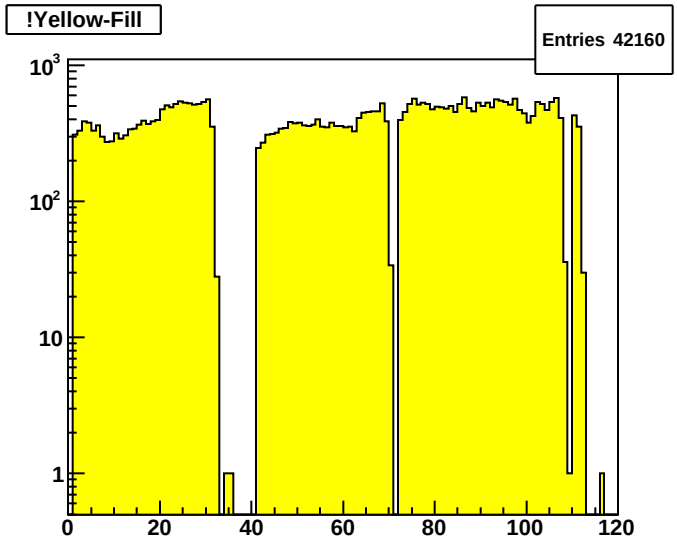

DAQ10k HT bits
Entries 505920

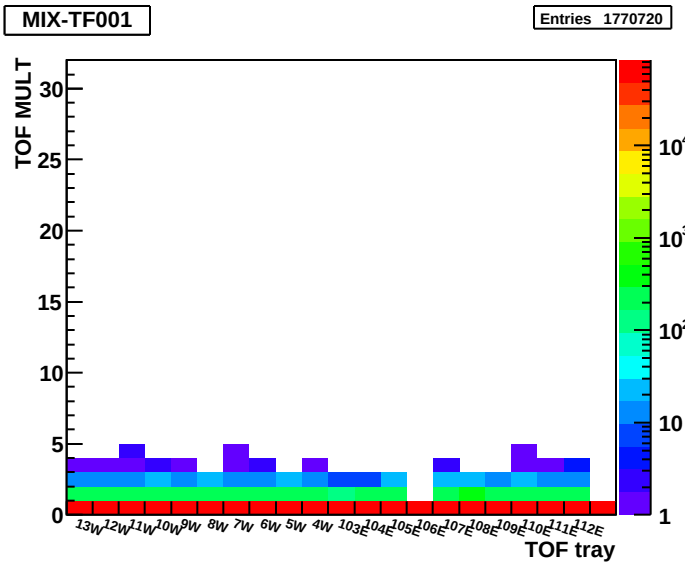

DAQ10k HT bits errors
Entries 506078

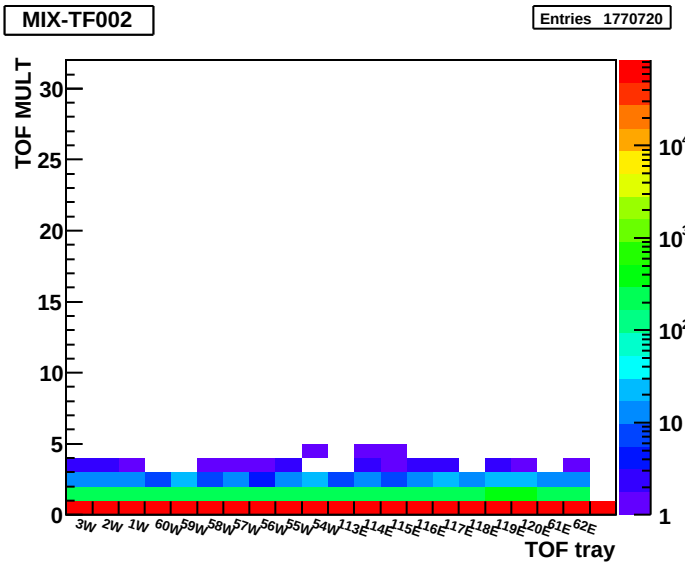

TPC Readout Mask (simulated)
Entries 548080

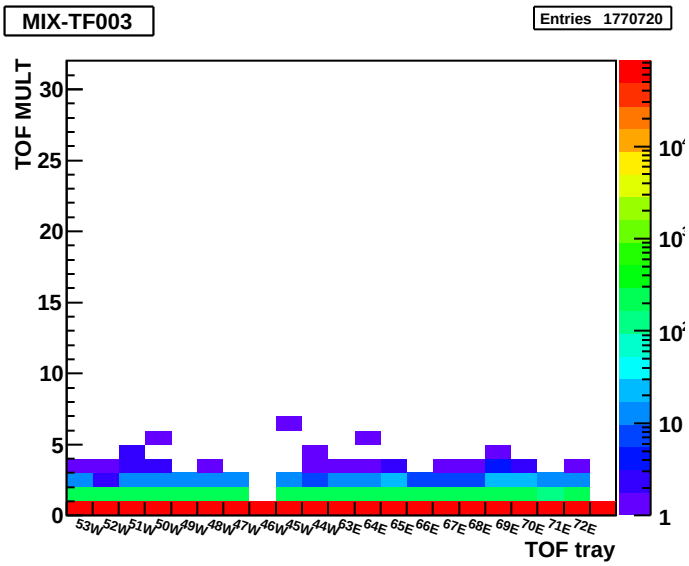

BunchId7Bit (all events)
Entries 42160

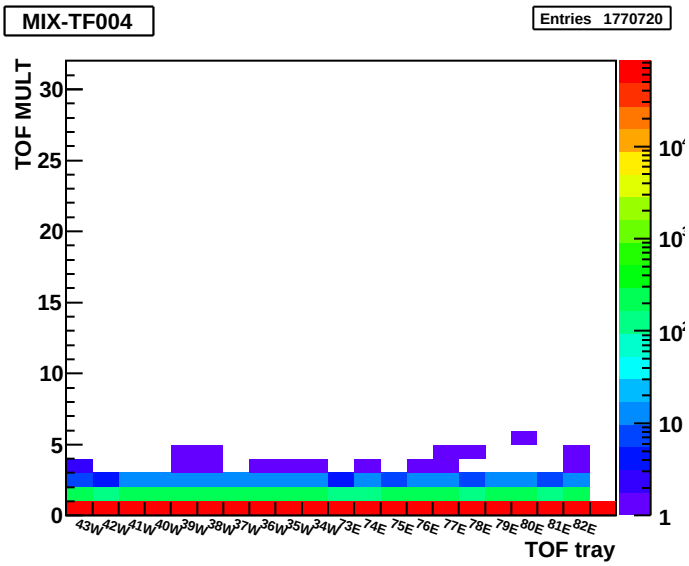

Abort-gaps
Entries 3

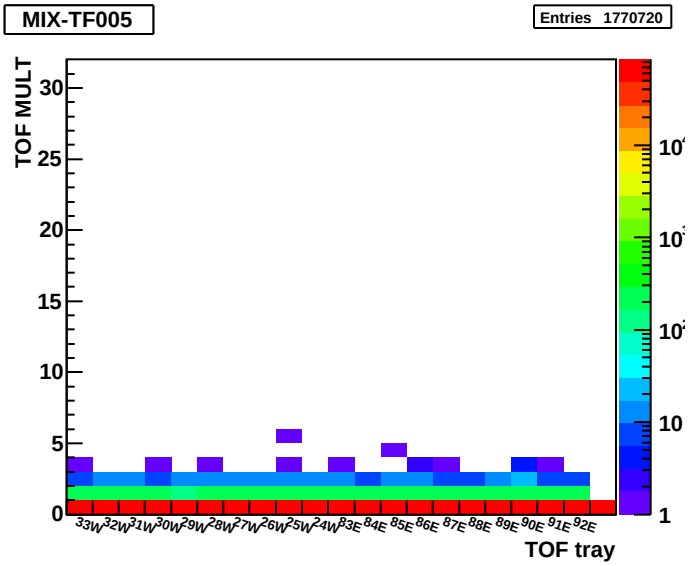

!Yellow-Fill
Entries 42160

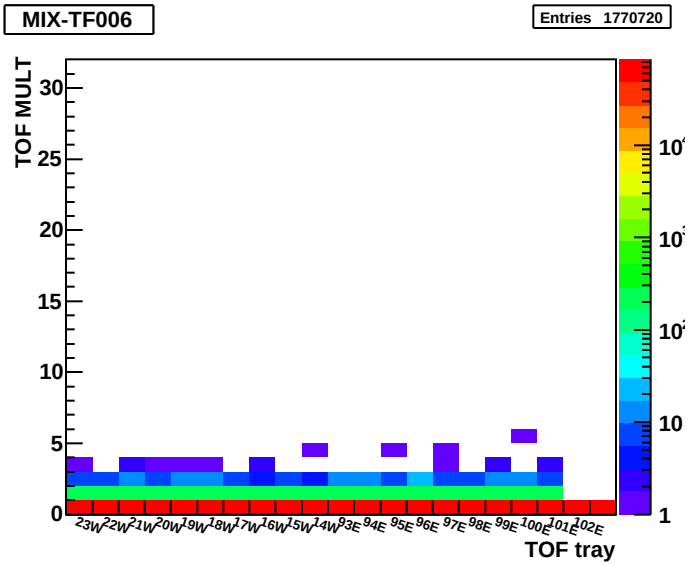

MIX-TF001
Entries 1770720
TOF MULT
TOF tray

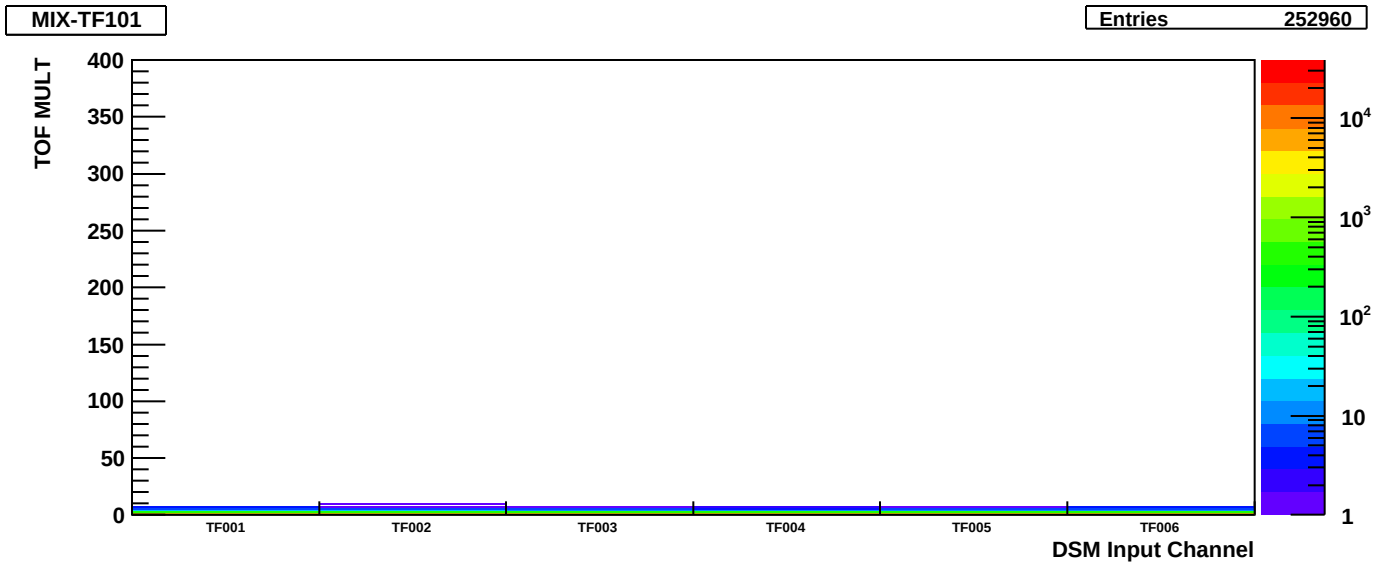

MIX-TF002
Entries 1770720
TOF MULT
TOF tray

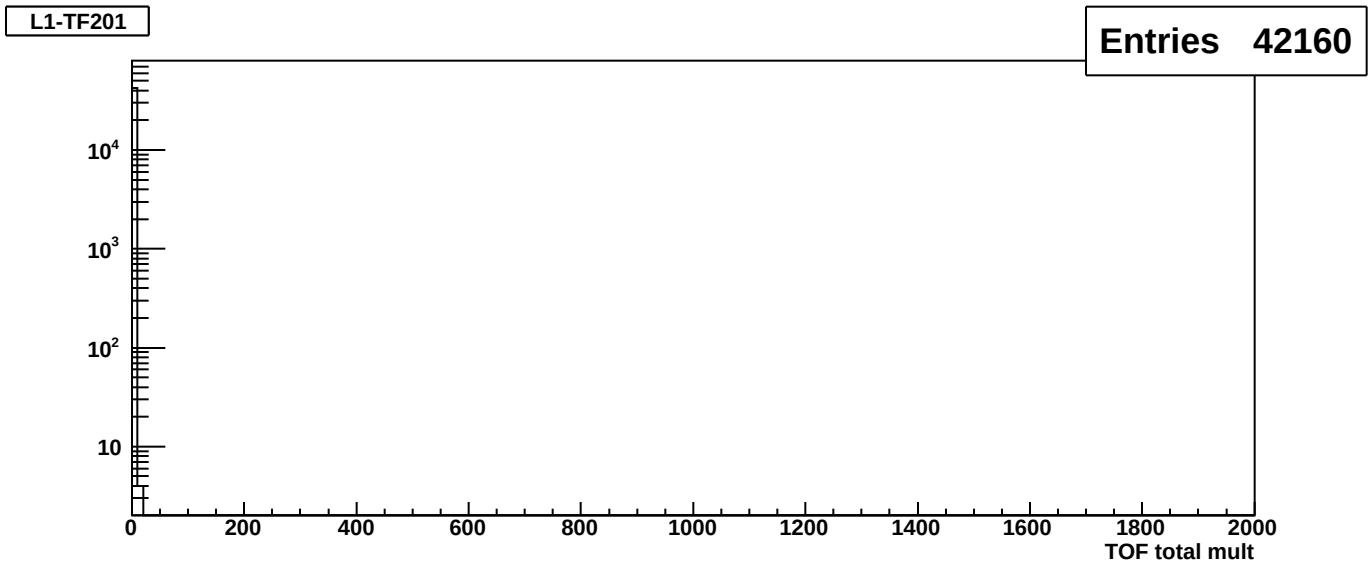

MIX-TF003
Entries 1770720
TOF MULT
TOF tray

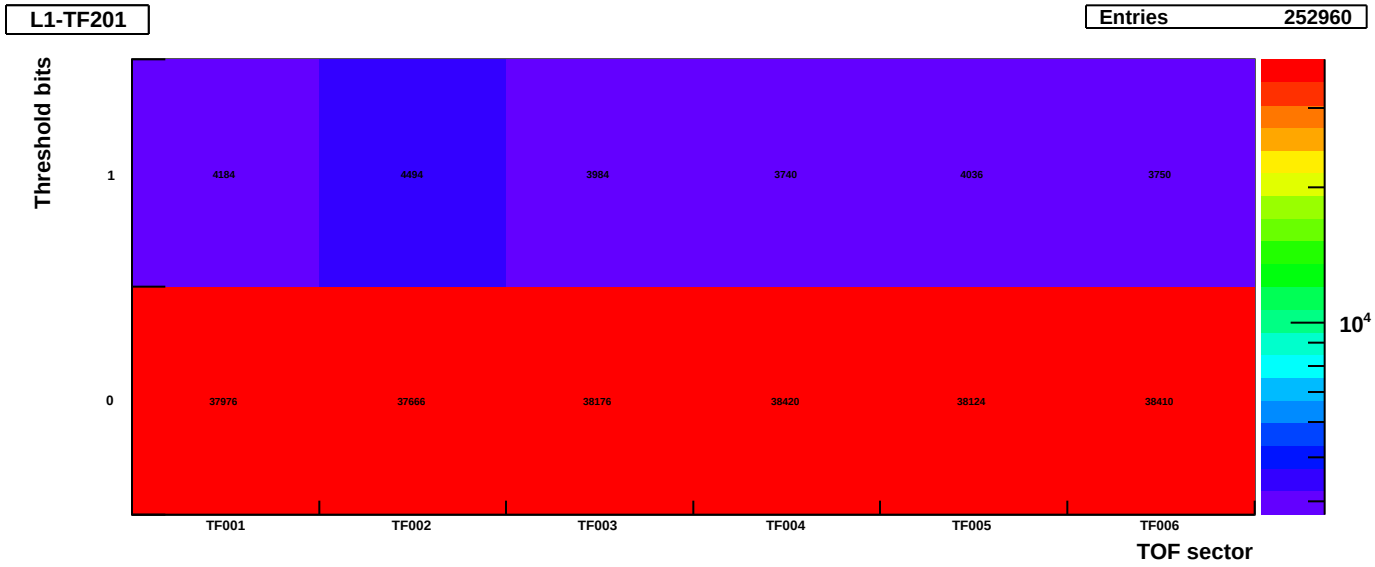

MIX-TF004
Entries 1770720
TOF MULT
TOF tray

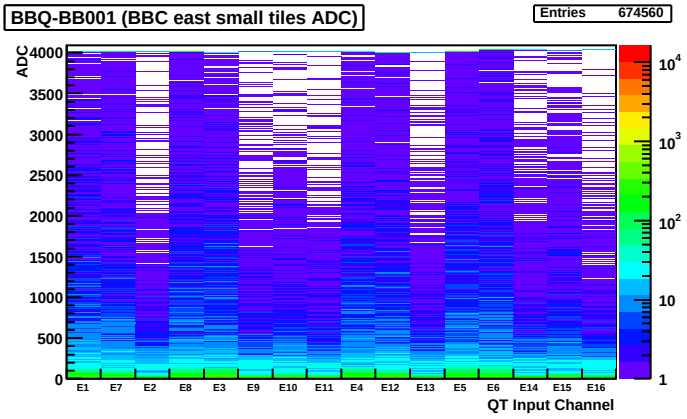

MIX-TF005
Entries 1770720
TOF MULT
TOF tray

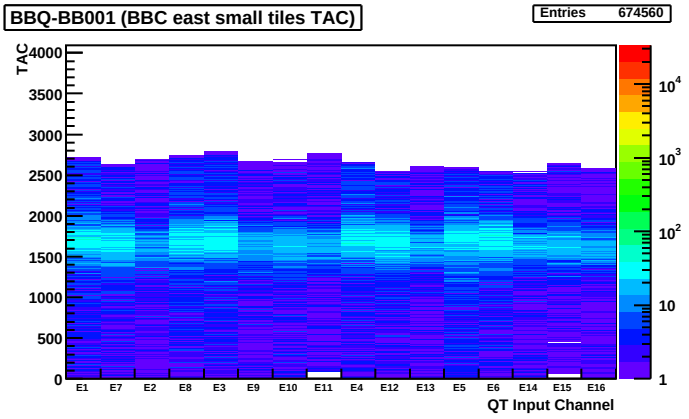

MIX-TF006
Entries 1770720
TOF MULT
TOF tray

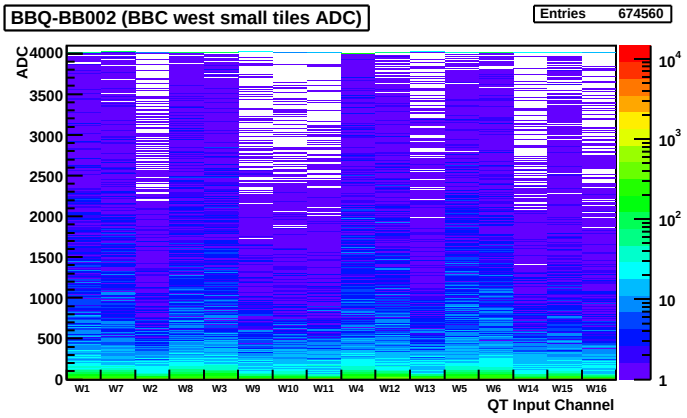

MIX-TF101
Entries 252960
TOF MULT
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

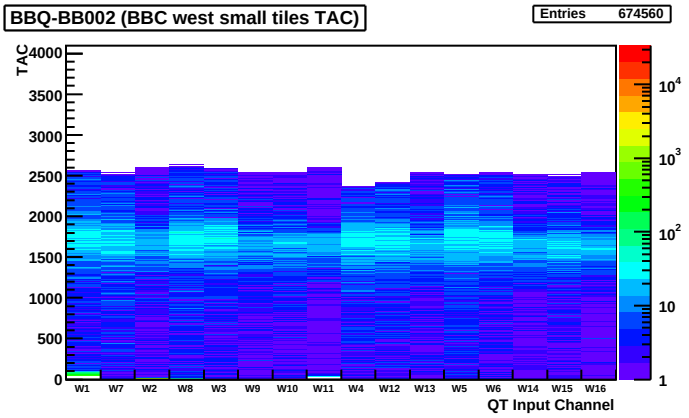

L1-TF201
Entries 42160
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000
TOF total mult

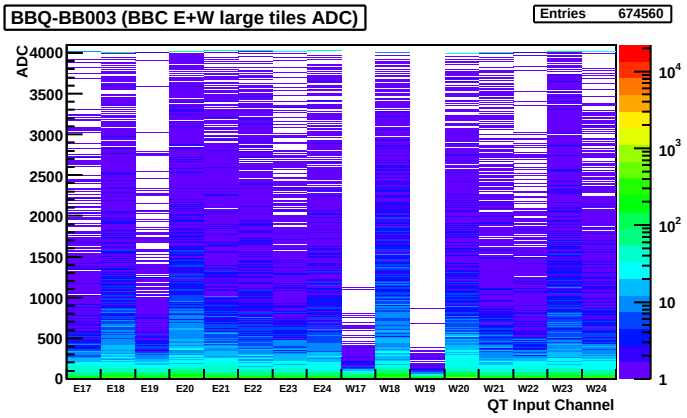

L1-TF201
Entries 252960
Threshold bits
1
0
4184
4494
3984
3740
4036
3750
37976
37666
38176
38420
38124
38410
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

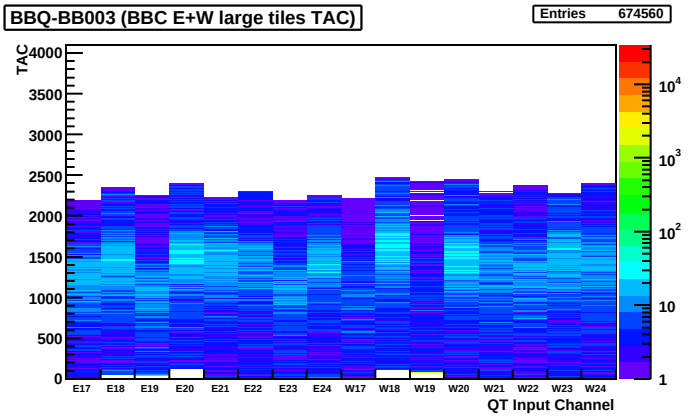

BBQ-BB001 (BBC east small tiles ADC)
Entries 674560
ADC
QT Input Channel

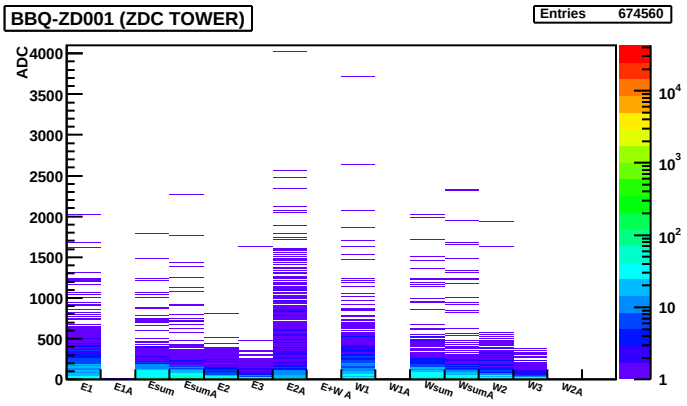

BBQ-BB001 (BBC east small tiles TAC)
Entries 674560
TAC
QT Input Channel

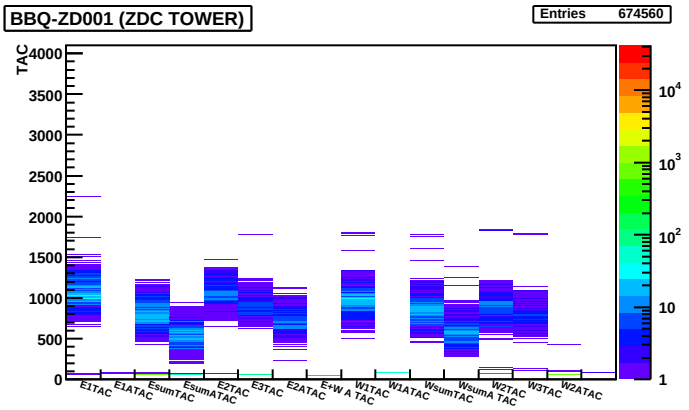

BBQ-BB002 (BBC west small tiles ADC)
Entries 674560
ADC
QT Input Channel

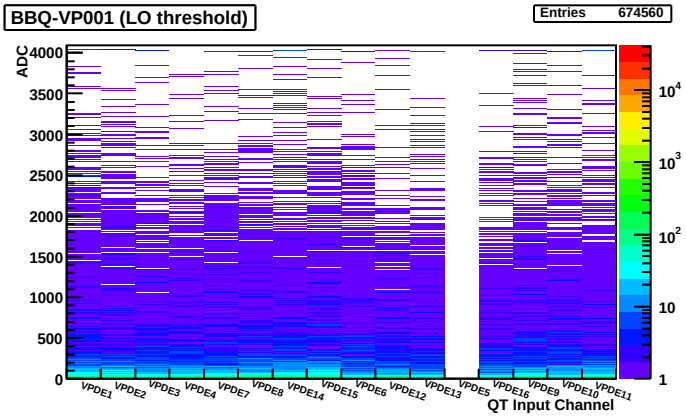

BBQ-BB002 (BBC west small tiles TAC)
Entries 674560
TAC
QT Input Channel

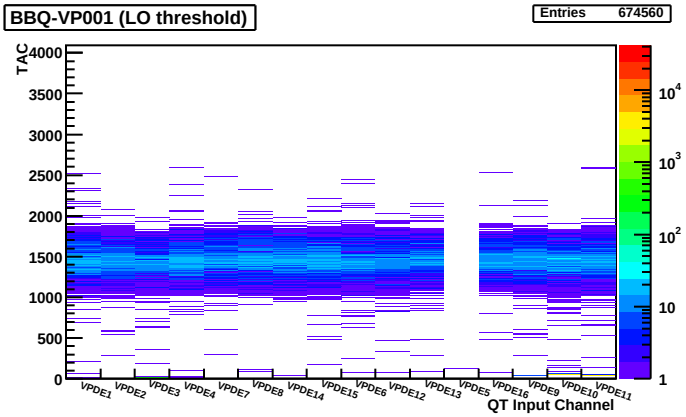

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 674560
ADC
QT Input Channel

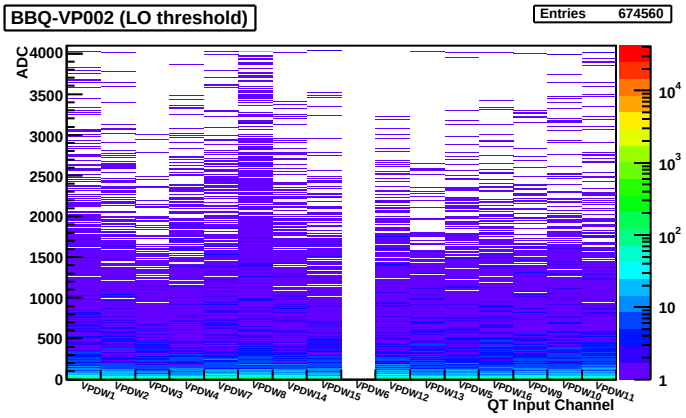

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 674560
TAC
QT Input Channel

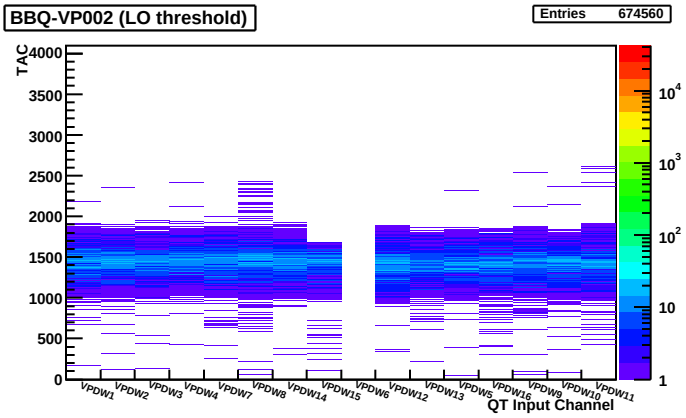

BBQ-ZD001 (ZDC TOWER)
Entries 674560
ADC

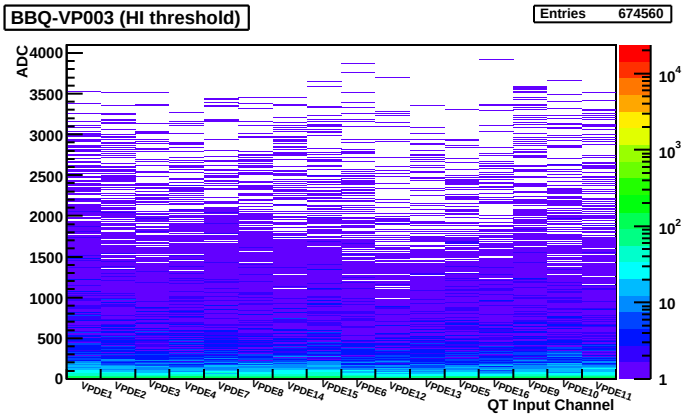

BBQ-ZD001 (ZDC TOWER)
Entries 674560
TAC

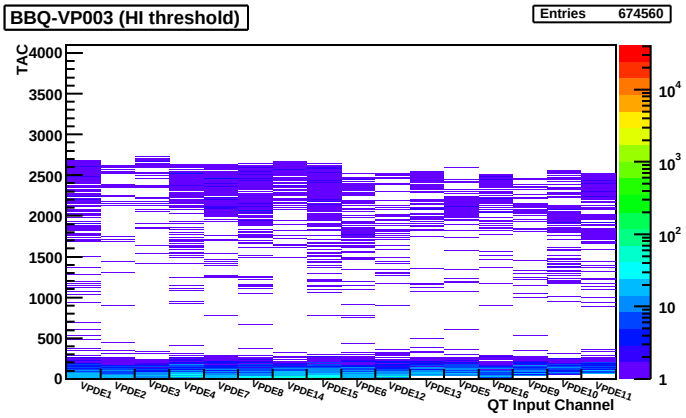

BBQ-VP001 (LO threshold)
Entries 674560
ADC
QT Input Channel

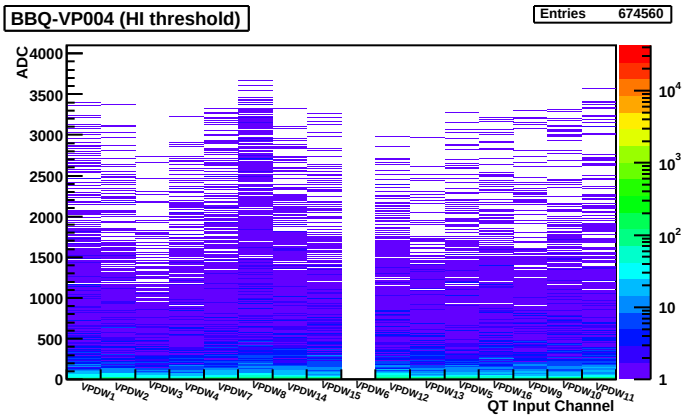

BBQ-VP001 (LO threshold)
Entries 674560
TAC
QT Input Channel

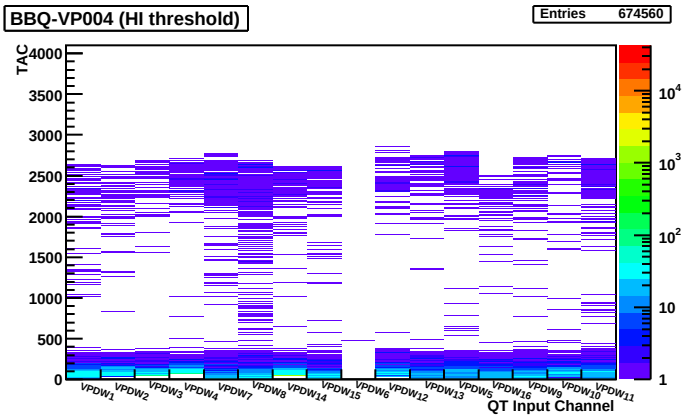

BBQ-VP002 (LO threshold)
Entries 674560
ADC
QT Input Channel

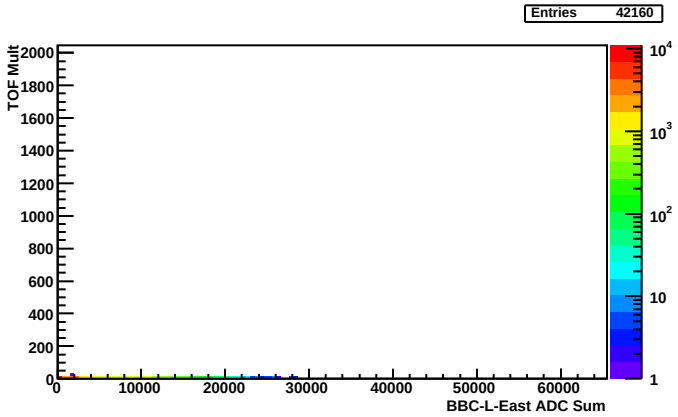

BBQ-VP002 (LO threshold)
Entries 674560
TAC
QT Input Channel

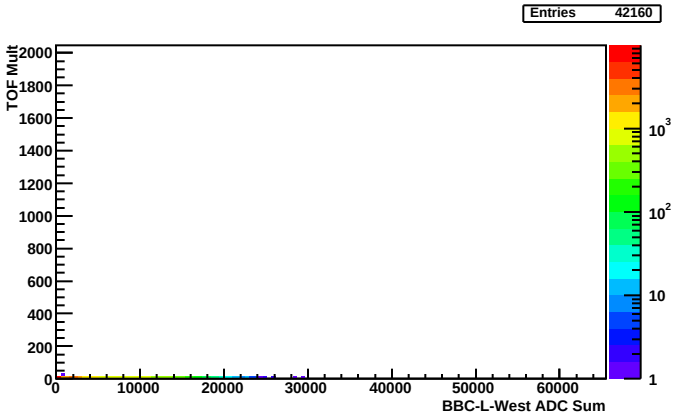

BBQ-VP003 (HI threshold)
Entries 674560
ADC
QT Input Channel

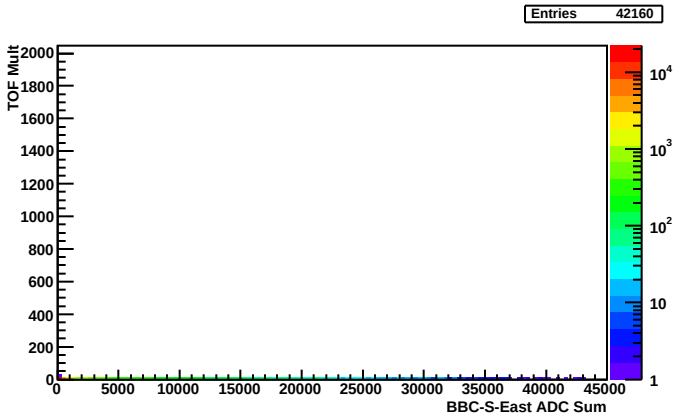

BBQ-VP003 (HI threshold)
Entries 674560
TAC
QT Input Channel

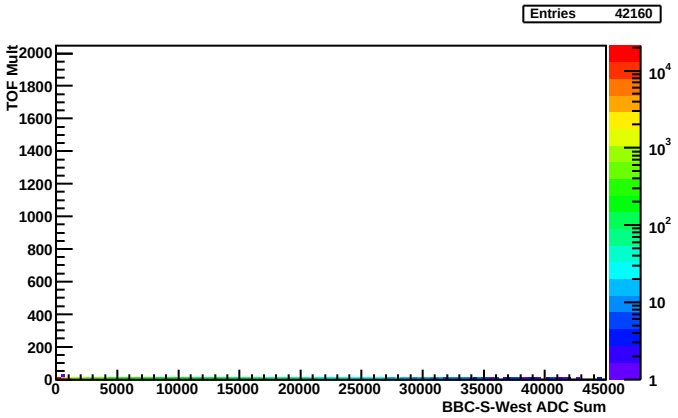

BBQ-VP004 (HI threshold)
Entries 674560
ADC
QT Input Channel

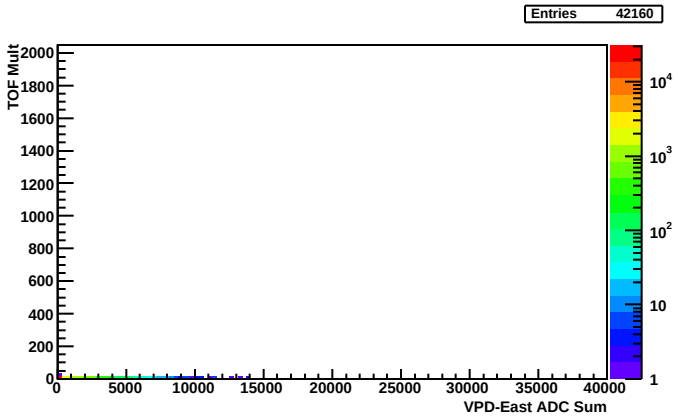

BBQ-VP004 (HI threshold)
Entries 674560
TAC
QT Input Channel

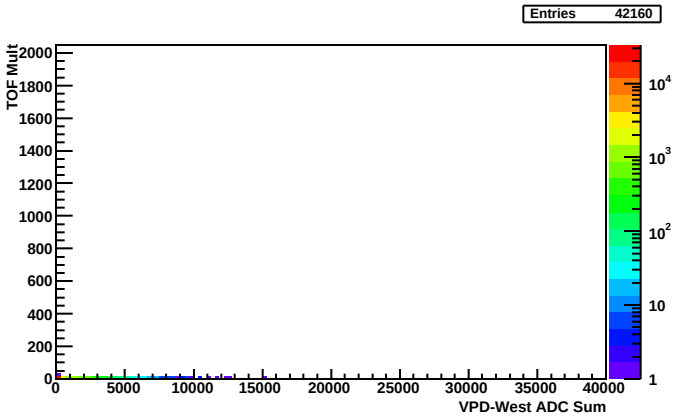

Entries 42160
TOF Mult
BBC-L-East ADC Sum

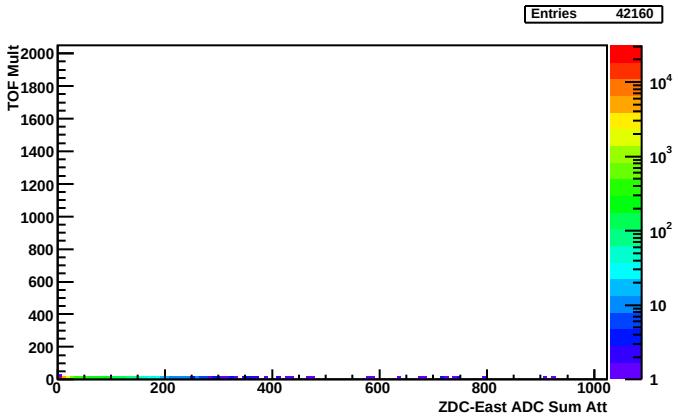

Entries 42160
TOF Mult
BBC-L-West ADC Sum

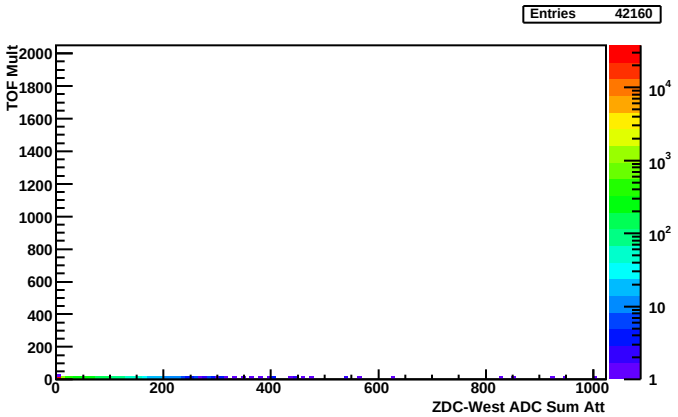

Entries 42160
TOF Mult
BBC-S-East ADC Sum

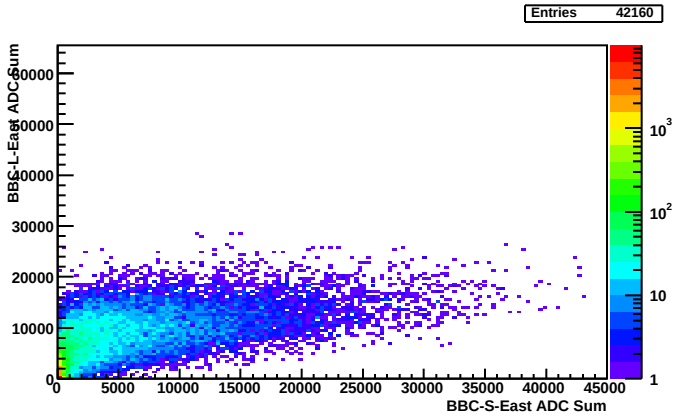

Entries 42160
TOF Mult
BBC-S-West ADC Sum

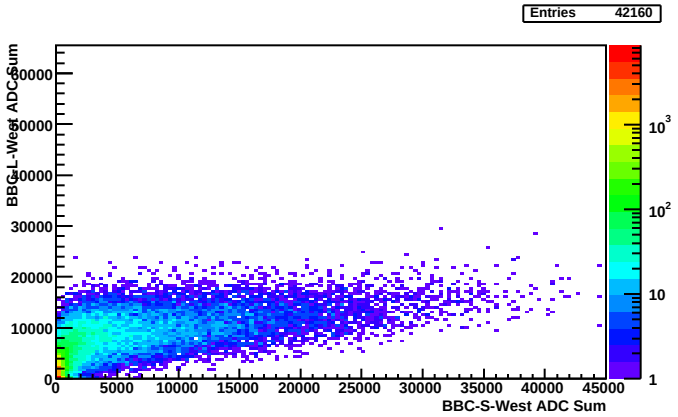

Entries 42160
TOF Mult
VPD-East ADC Sum

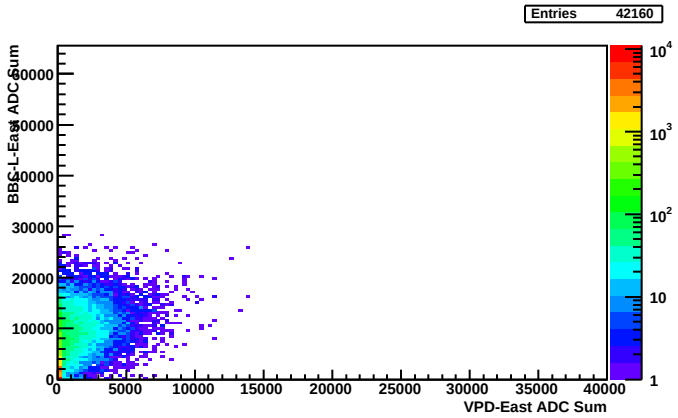

Entries 42160
TOF Mult
VPD-West ADC Sum

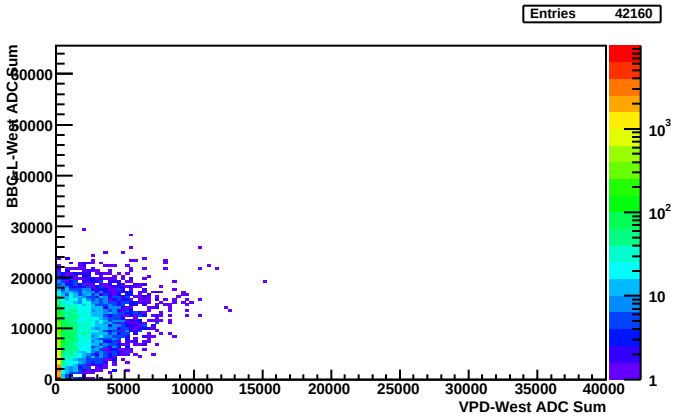

Entries 42160
TOF Mult
ZDC-East ADC Sum Att

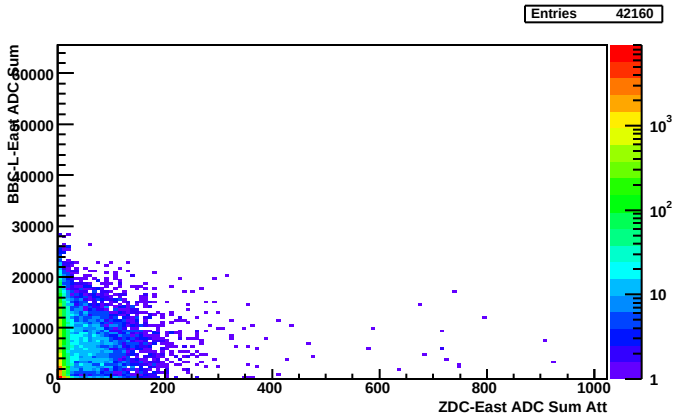

Entries 42160
TOF Mult
ZDC-West ADC Sum Att

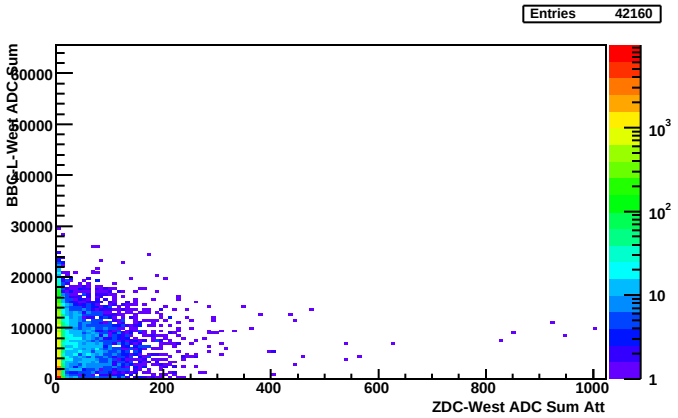

Entries 42160
BBC-L-East ADC Sum
BBC-S-East ADC Sum

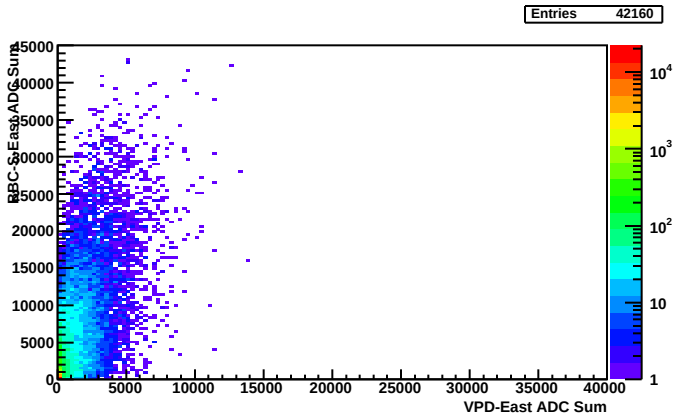

Entries 42160
BBC-L-West ADC Sum
BBC-S-West ADC Sum

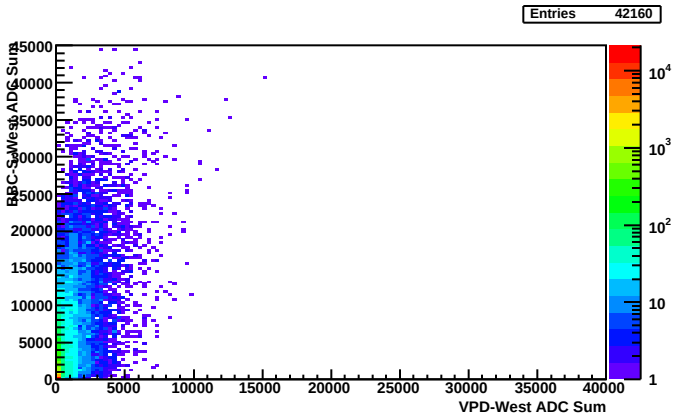

Entries 42160
BBC-L-East ADC Sum
VPD-East ADC Sum

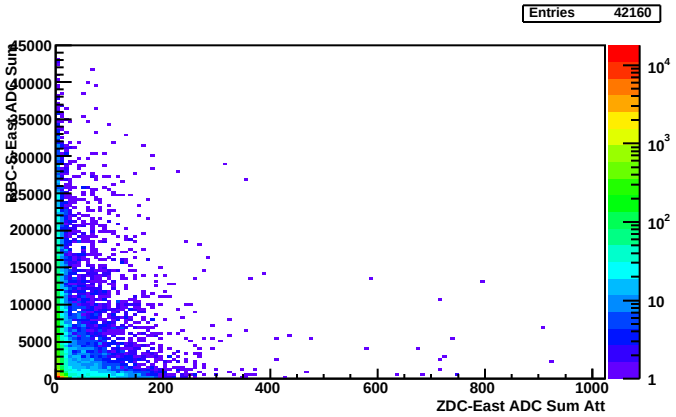

Entries 42160
BBC-L-West ADC Sum
VPD-West ADC Sum

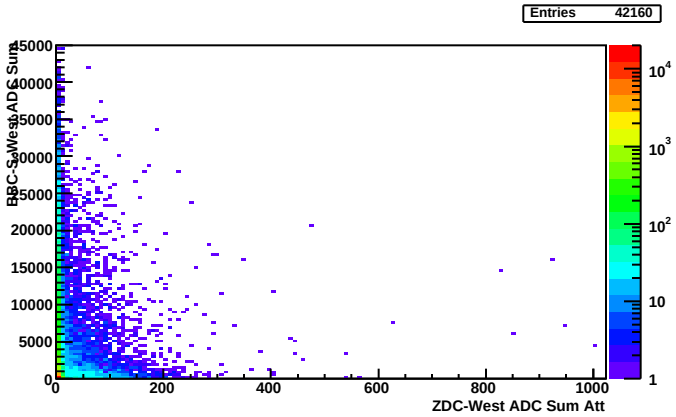

Entries 42160
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

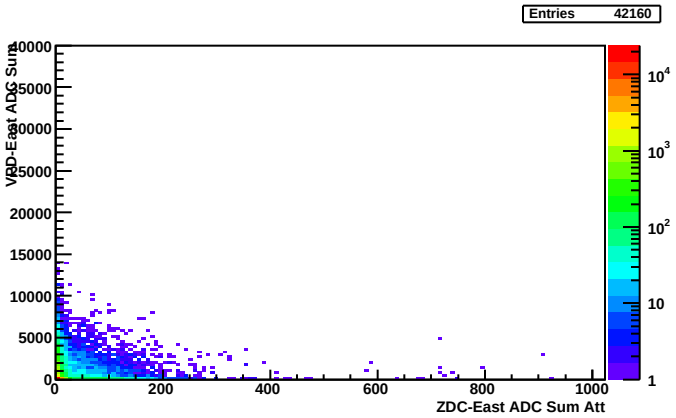

Entries 42160
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

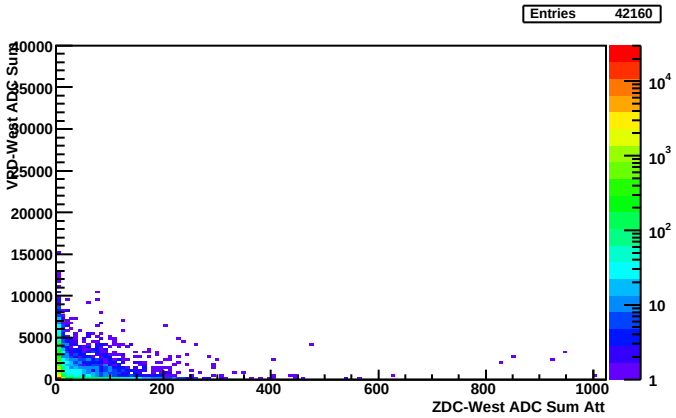

Entries 42160
BBC-S-East ADC Sum
VPD-East ADC Sum

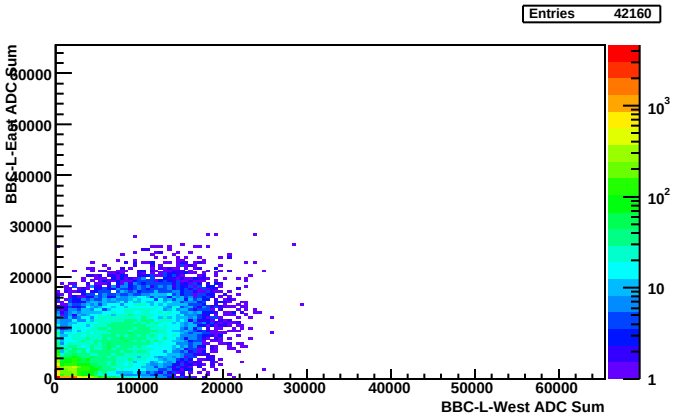

Entries 42160
BBC-S-West ADC Sum
VPD-West ADC Sum

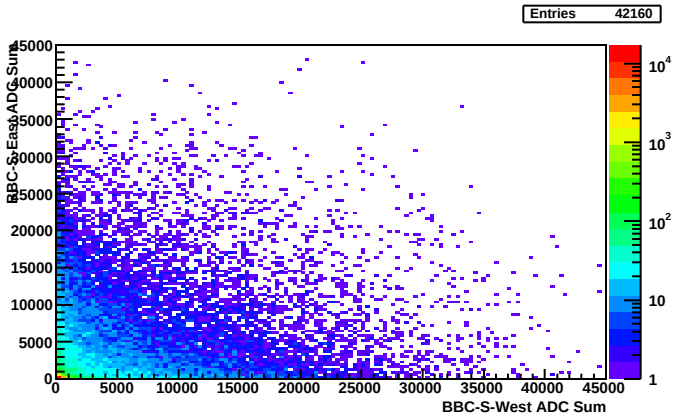

Entries 42160
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

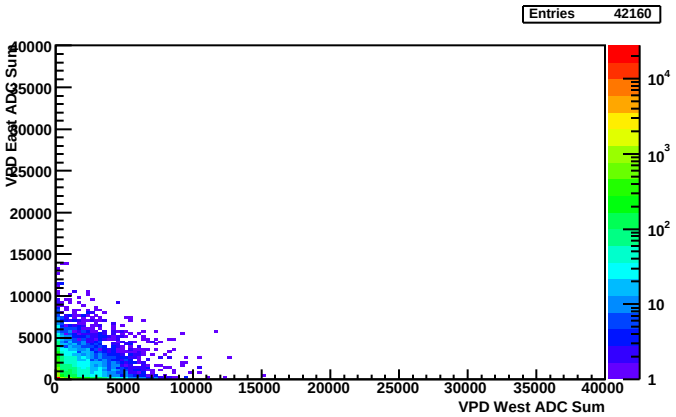

Entries 42160
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

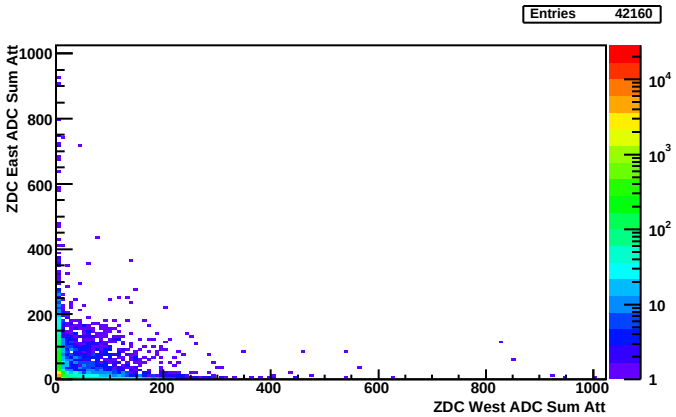

Entries 42160
VPD-East ADC Sum
ZDC-East ADC Sum Att

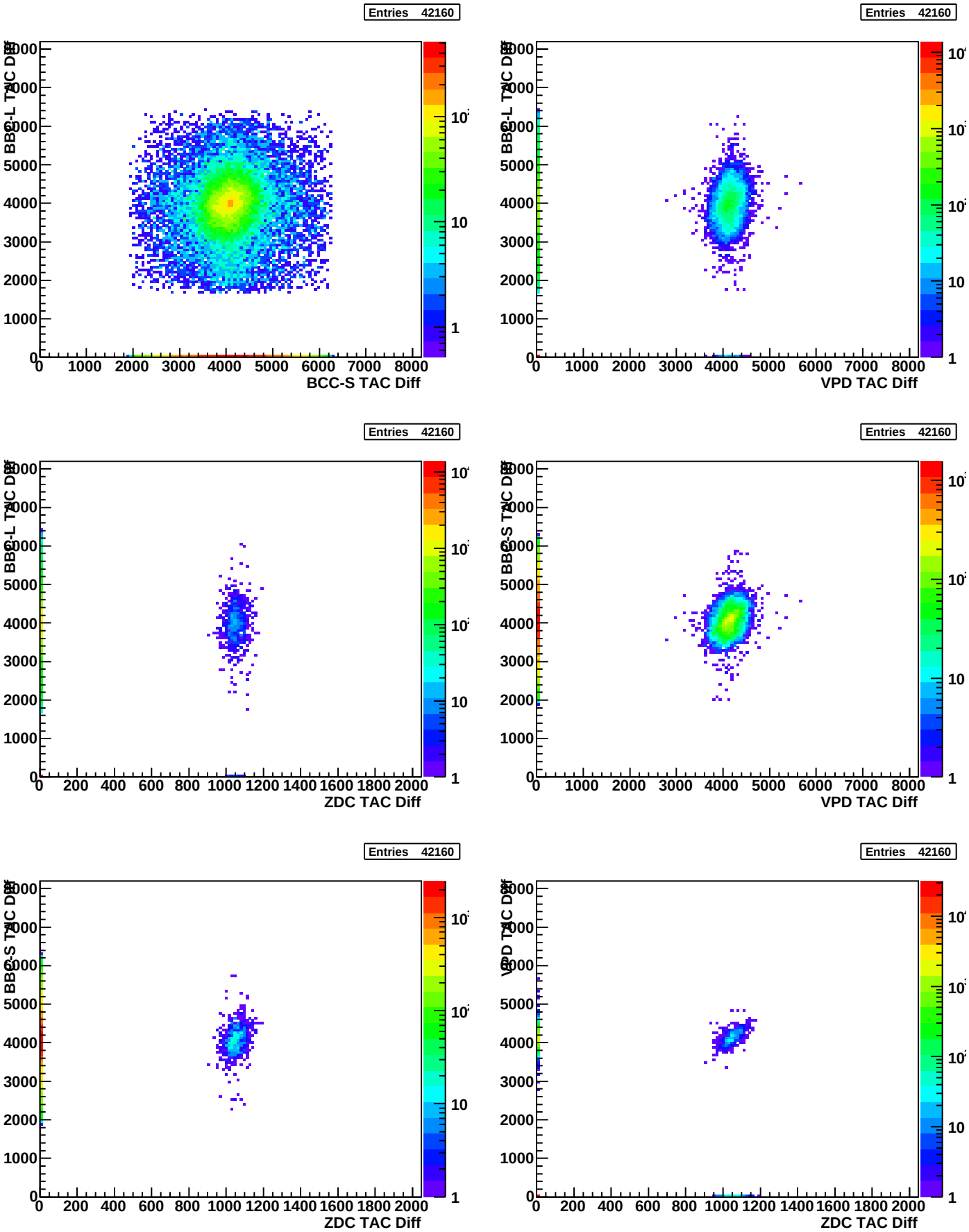

Entries 42160
VPD-West ADC Sum
ZDC-West ADC Sum Att

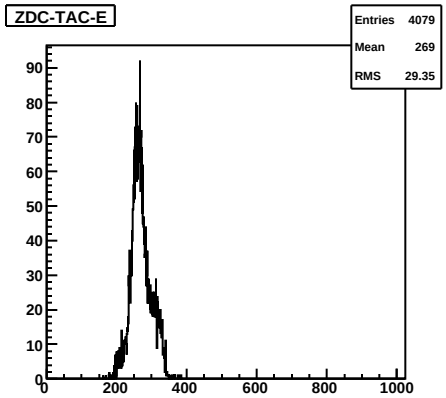

Entries 42160
BBC-L-East ADC Sum
BBC-L-West ADC Sum

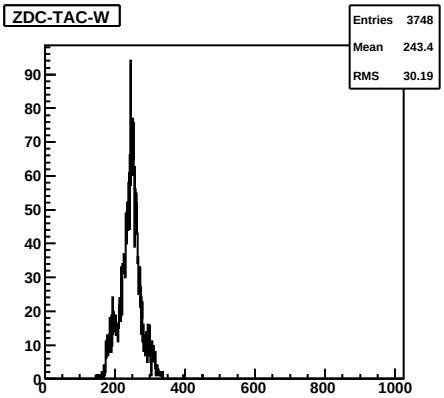

Entries 42160
BBC-S-East ADC Sum
BBC-S-West ADC Sum

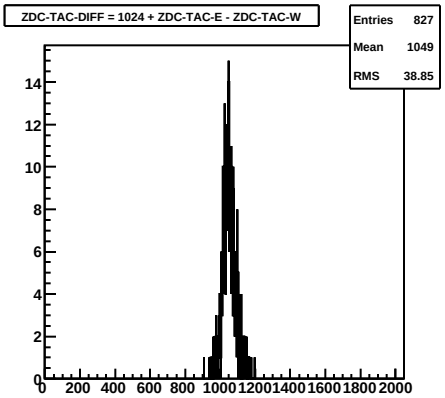

Entries 42160
VPD East ADC Sum
VPD West ADC Sum

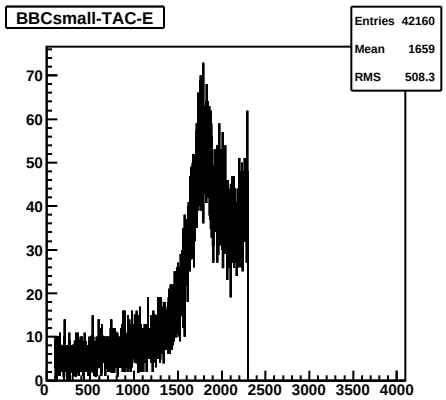

Entries 42160
ZDC East ADC Sum Att
ZDC West ADC Sum Att

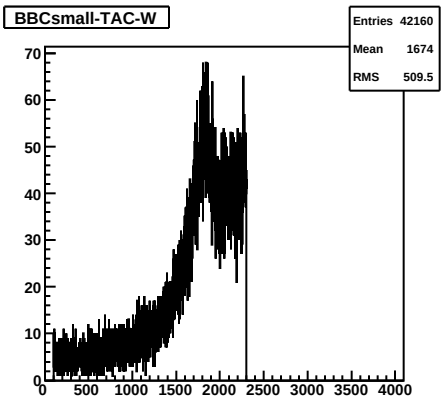

Entries 42160
BBC-L TAC Diff
BCC-S TAC Diff
Entries 42160
BBC-L TAC Diff
VPD TAC Diff
Entries 42160
BBC-L TAC Diff
ZDC TAC Diff
Entries 42160
BBC-S TAC Diff
VPD TAC Diff
Entries 42160
BBC-S TAC Diff
ZDC TAC Diff
Entries 42160
VPD TAC Diff
ZDC TAC Diff

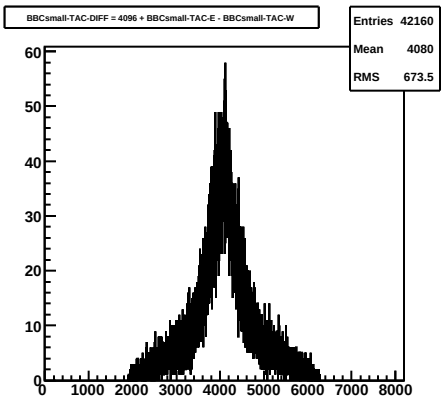

ZDC-TAC-E
Entries 4079
Mean 269
RMS 29.35

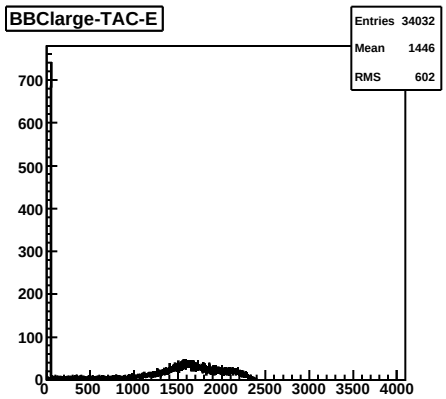

ZDC-TAC-W
Entries 3748
Mean 243.4
RMS 30.19

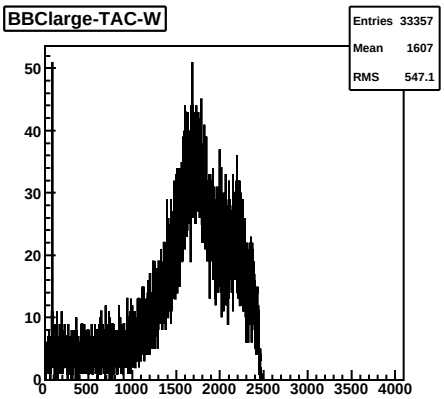

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 827
Mean 1049
RMS 38.85

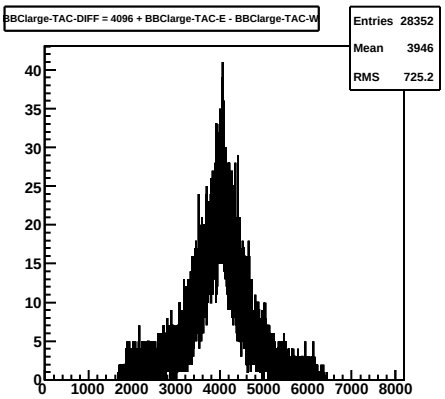

BBCsmall-TAC-E
Entries 42160
Mean 1659
RMS 508.3

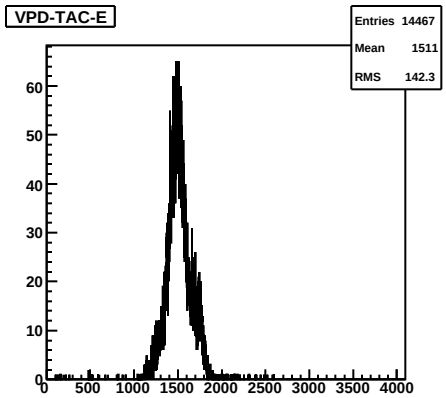

BBCsmall-TAC-W
Entries 42160
Mean 1674
RMS 509.5

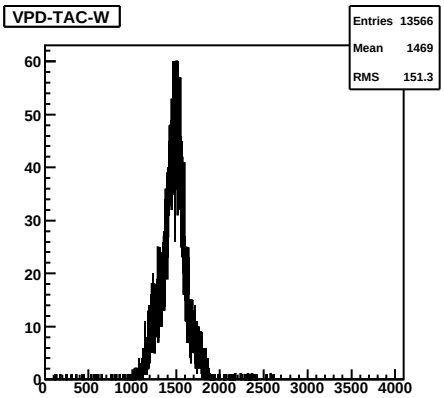

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 42160
Mean 4080
RMS 673.5

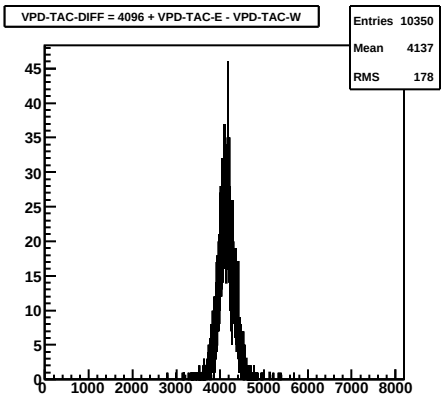

BBClarge-TAC-E
Entries 34032
Mean 1446
RMS 602

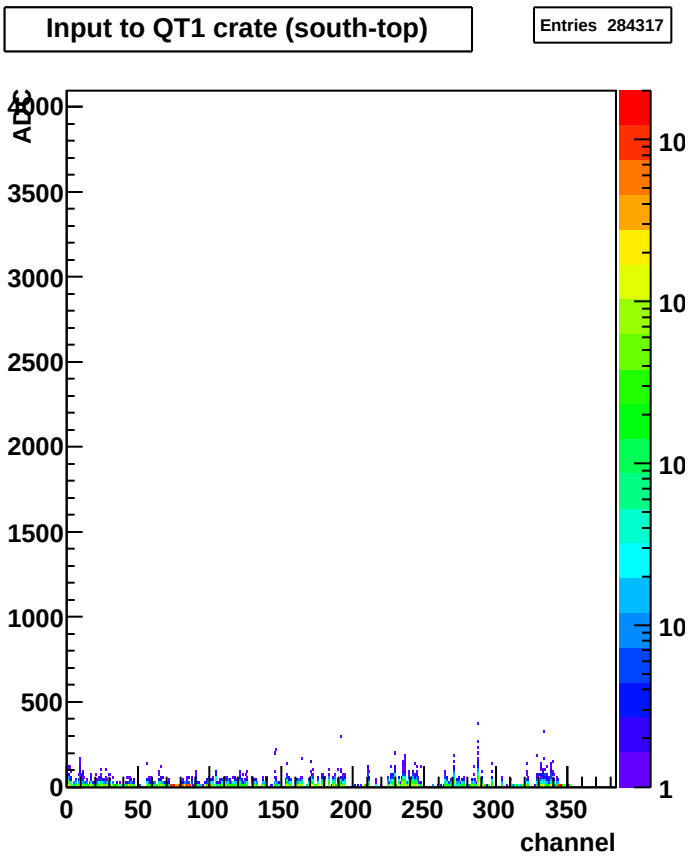

BBClarge-TAC-W
Entries 33357
Mean 1607
RMS 547.1

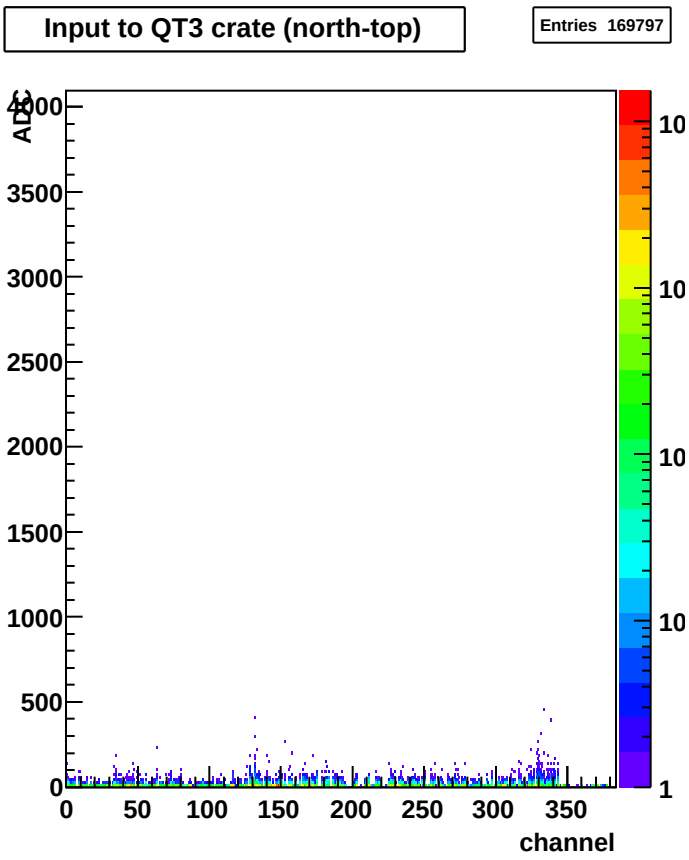

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 28352
Mean 3946
RMS 725.2

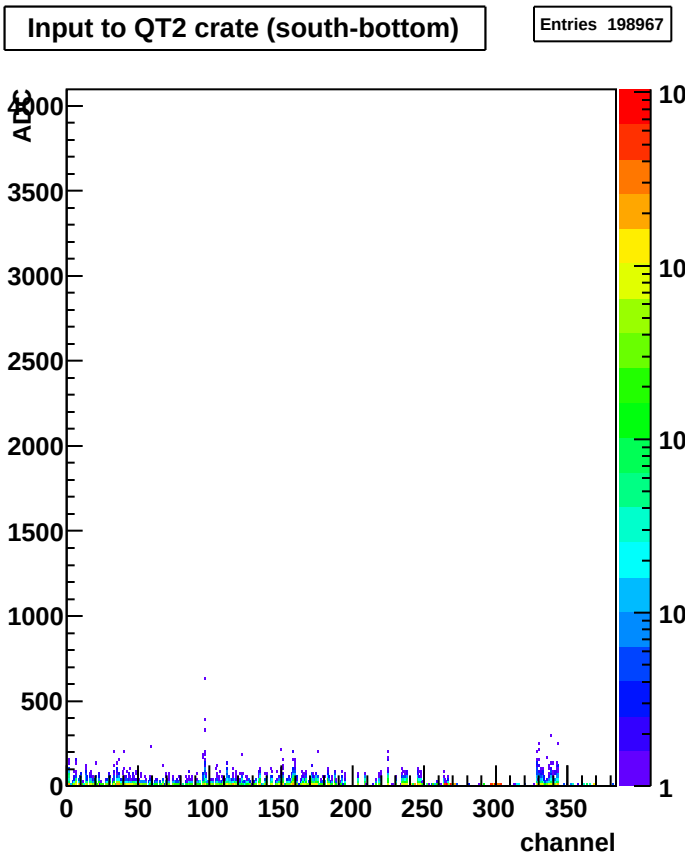

VPD-TAC-E
Entries 14467
Mean 1511
RMS 142.3

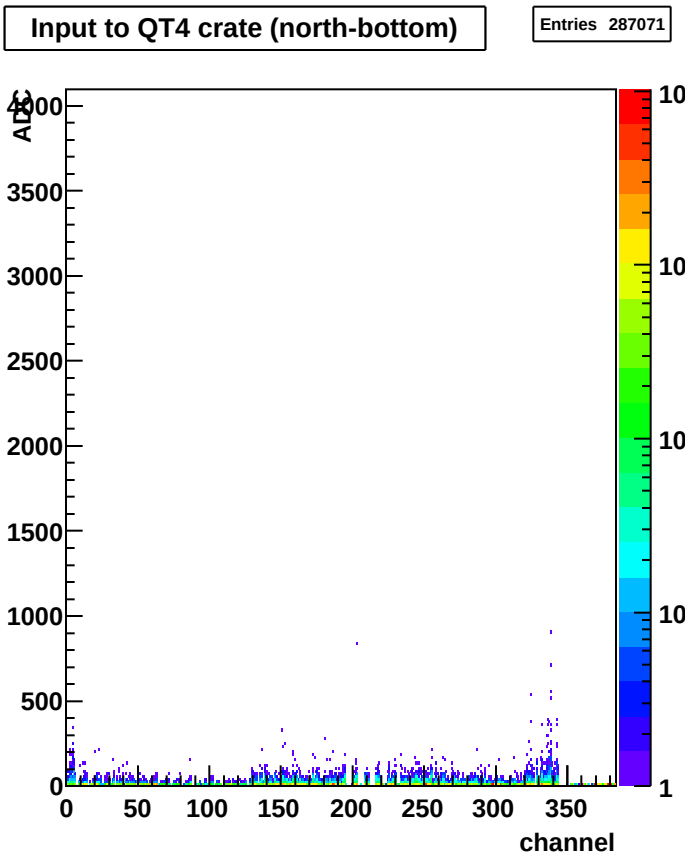

VPD-TAC-W
Entries 13566
Mean 1469
RMS 151.3

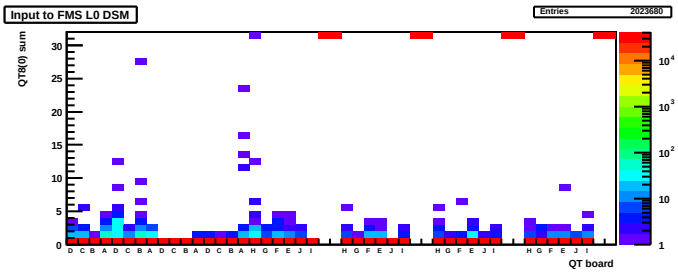

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 10350
Mean 4137
RMS 178

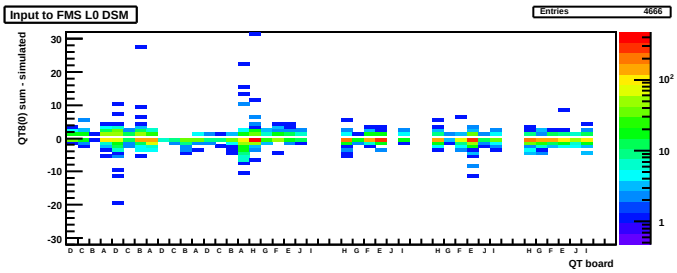

Input to QT1 crate (south-top)
Entries 284317
ADC
channel

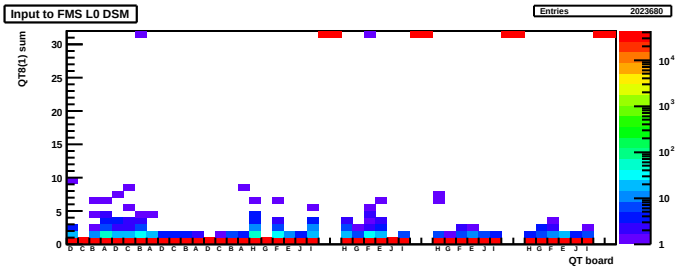

Input to QT3 crate (north-top)
Entries 169797
ADC
channel

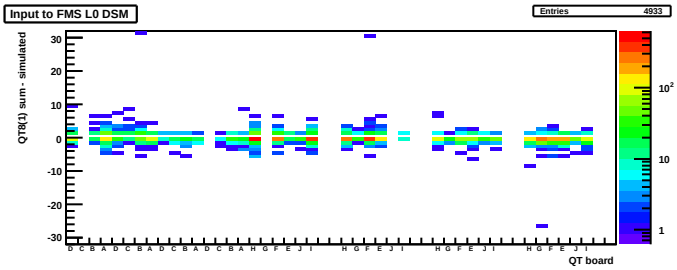

Input to QT2 crate (south-bottom)
Entries 198967
ADC
channel

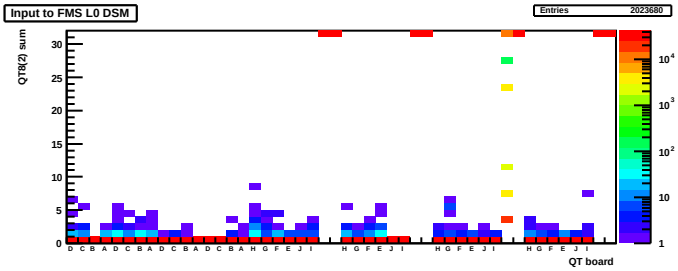

Input to QT4 crate (north-bottom)
Entries 287071
ADC
channel

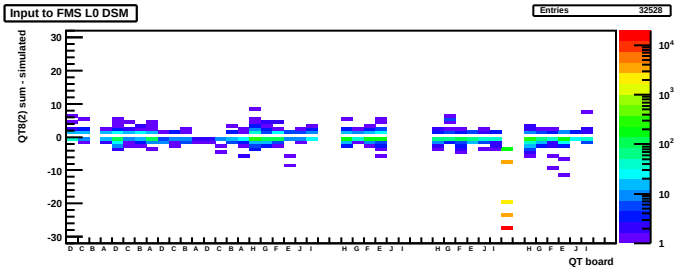

Input to FMS L0 DSM
Entries 2023680
QT8(0) sum
QT board

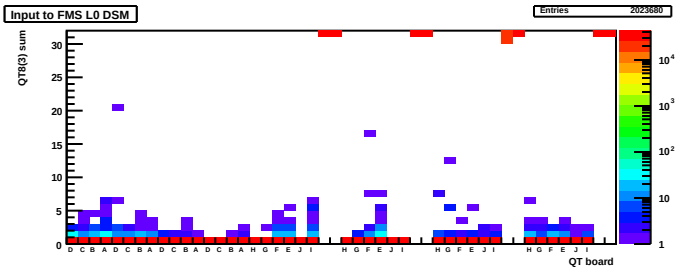

Input to FMS L0 DSM
Entries 4666
QT8(0) sum - simulated
QT board

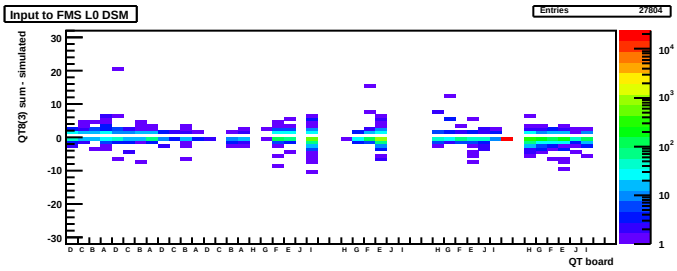

Input to FMS L0 DSM
Entries 2023680
QT8(1) sum
QT board

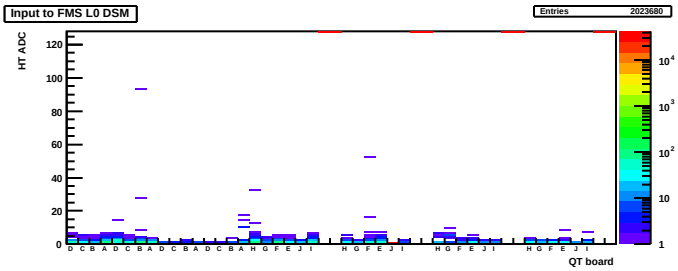

Input to FMS L0 DSM
Entries 4933
QT8(1) sum - simulated
QT board

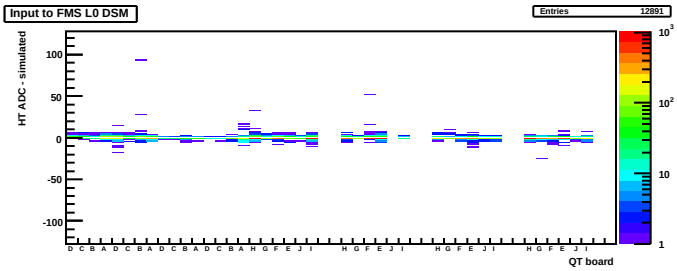

Input to FMS L0 DSM
Entries 2023680
QT8(2) sum
QT board

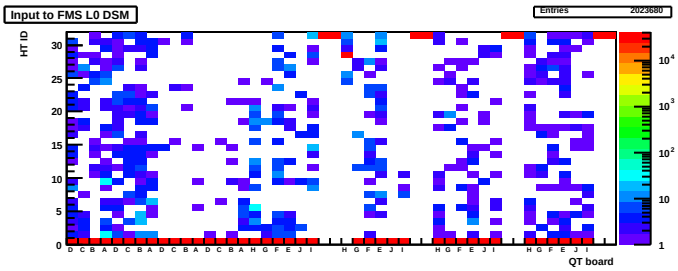

Input to FMS L0 DSM
Entries 32528
QT8(2) sum - simulated
QT board

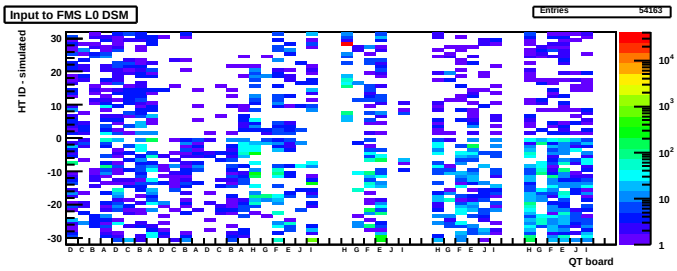

Input to FMS L0 DSM
Entries 2023680
QT8(3) sum
QT board

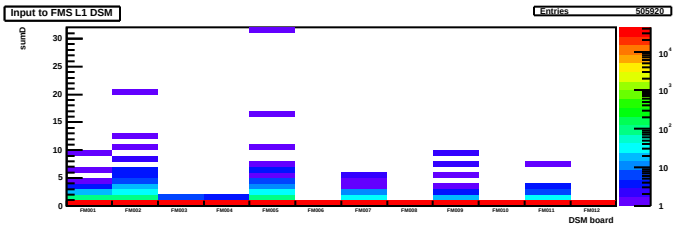

Input to FMS L0 DSM
Entries 27804
QT8(3) sum - simulated
QT board

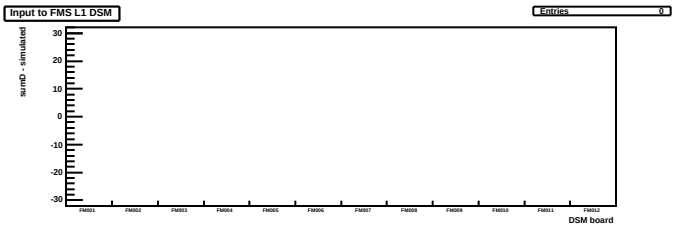

Input to FMS L0 DSM
Entries 2023680
HT ADC
QT board

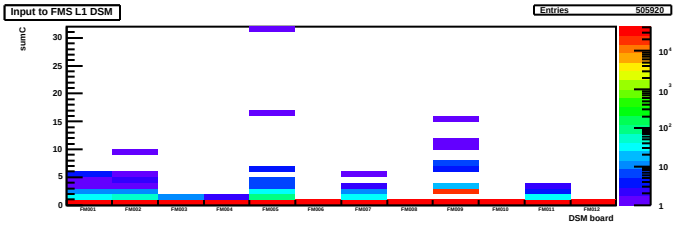

Input to FMS L0 DSM
Entries 12891
HT ADC - simulated
QT board

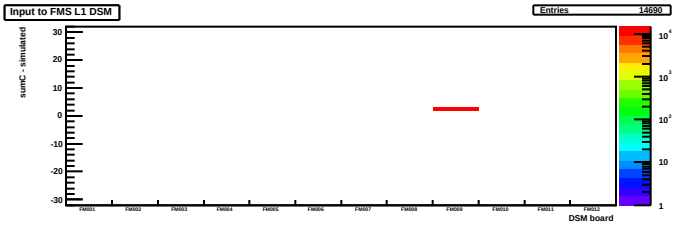

Input to FMS L0 DSM
Entries 2023680
HT ID
QT board

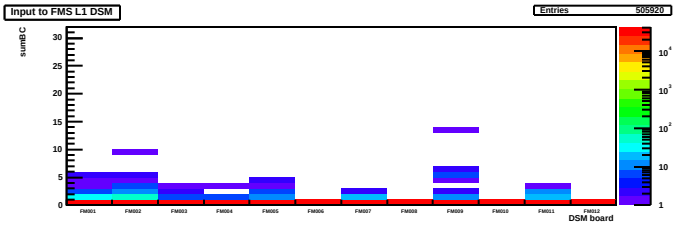

Input to FMS L0 DSM
Entries 54163
HT ID - simulated
QT board

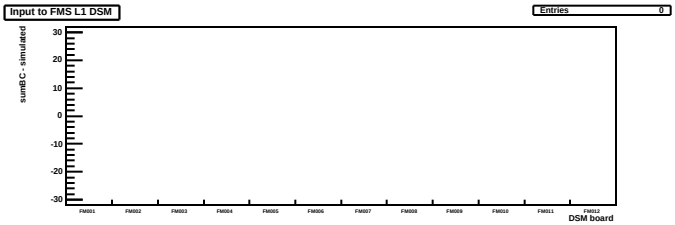

Input to FMS L1 DSM
Entries 505920
sumD
DSM board

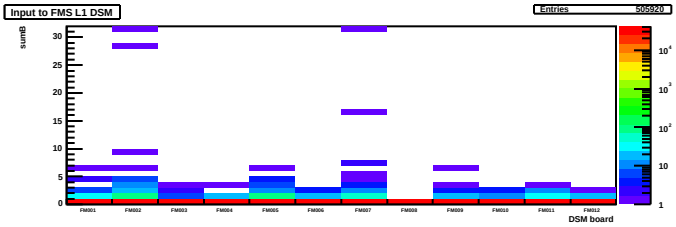

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

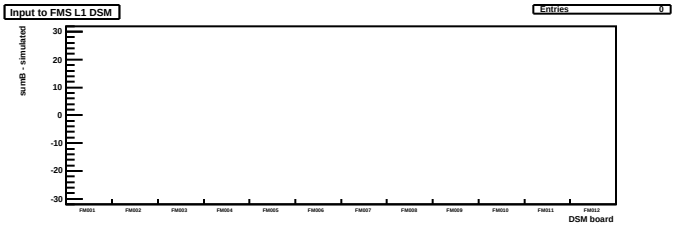

Input to FMS L1 DSM
Entries 505920
sumC
DSM board

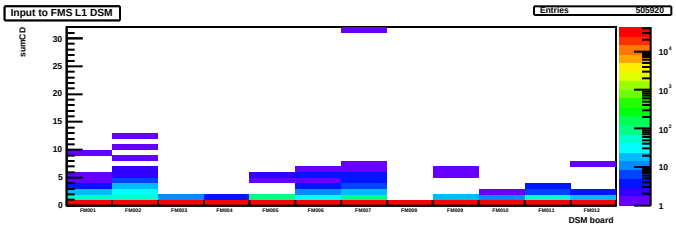

Input to FMS L1 DSM
Entries 14690
sumC - simulated
DSM board

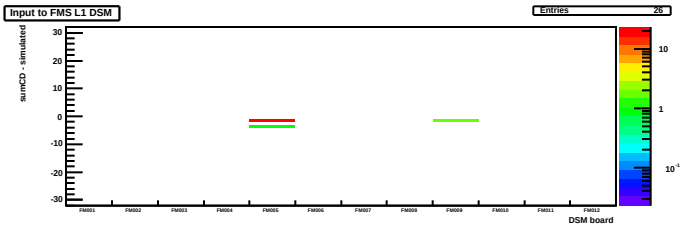

Input to FMS L1 DSM
Entries 505920
sumBC
DSM board

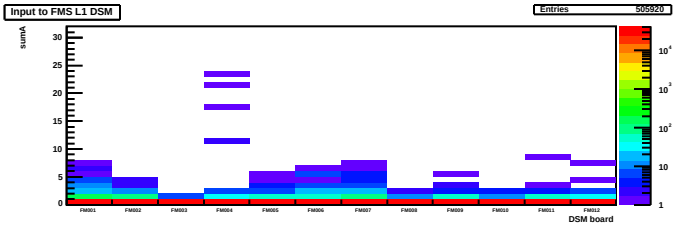

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

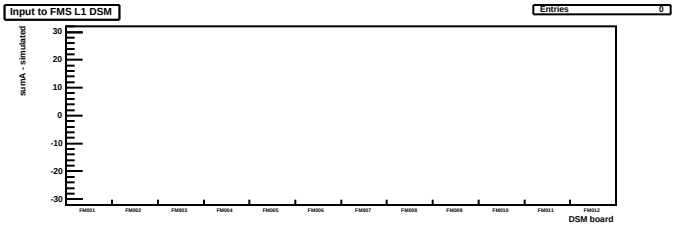

Input to FMS L1 DSM
Entries 505920
sumB
DSM board

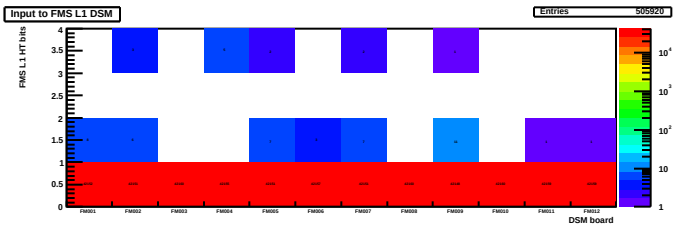

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

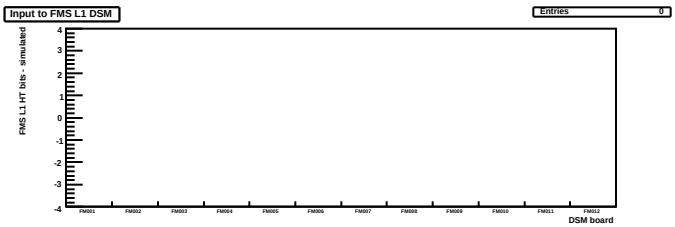

Input to FMS L1 DSM
Entries 505920
sumCD
DSM board

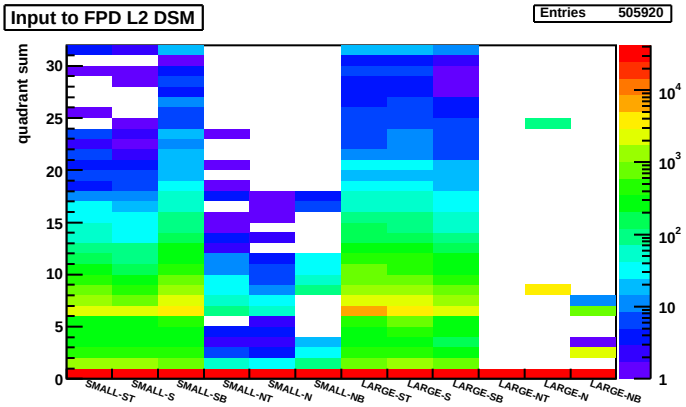

Input to FMS L1 DSM
Entries 26
sumCD - simulated
DSM board

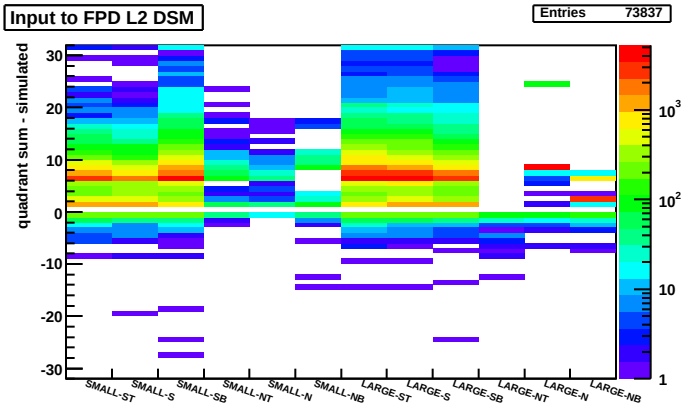

Input to FMS L1 DSM
Entries 505920
sumA
DSM board

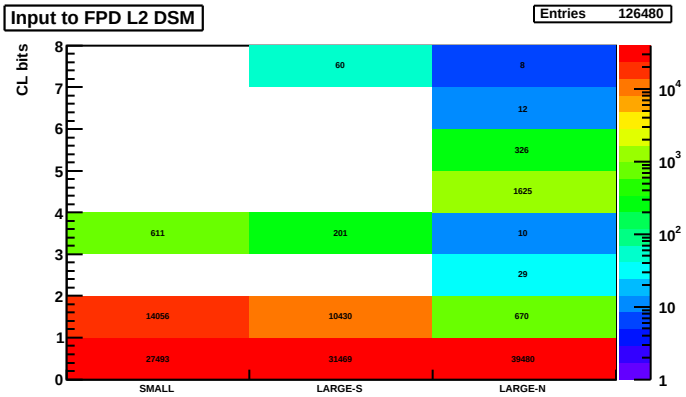

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

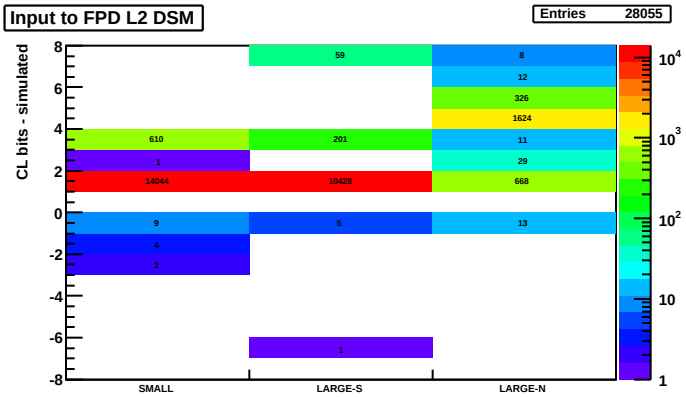

Input to FMS L1 DSM
Entries 505920
FMS L1 HT bits
DSM board

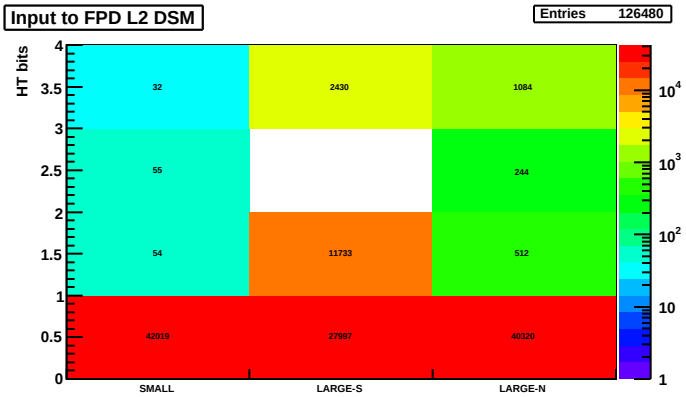

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

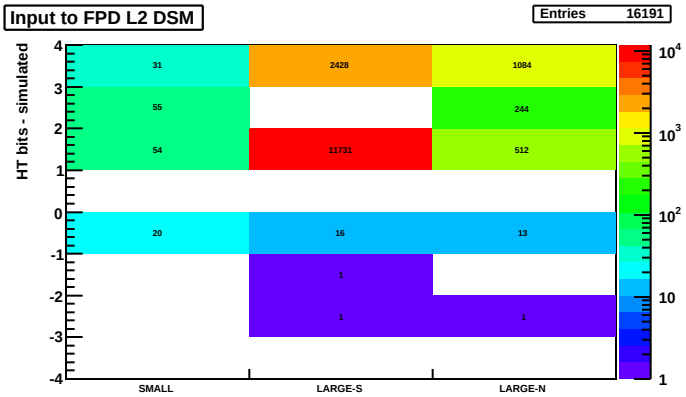

Input to FPD L2 DSM
Entries 505920
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

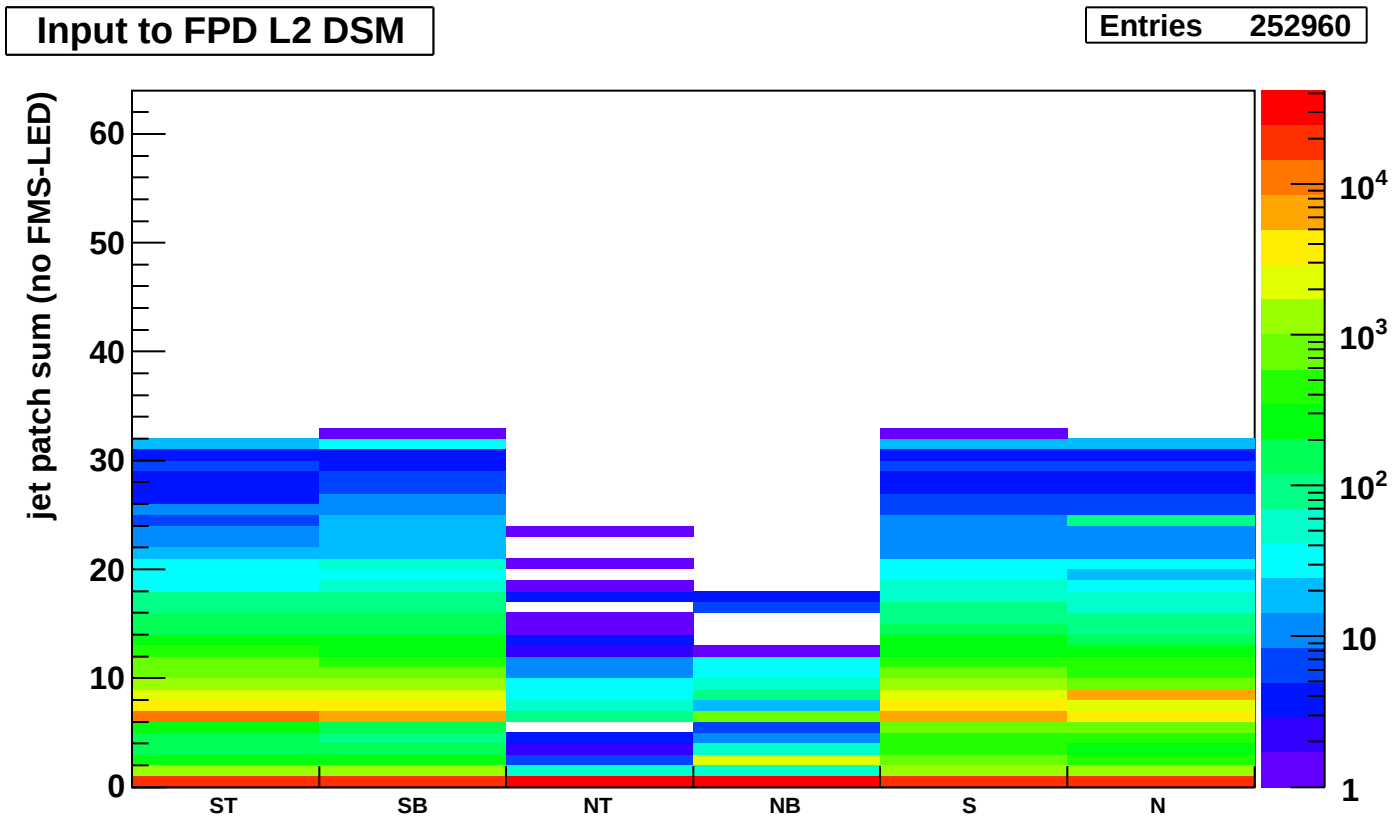

Input to FPD L2 DSM
Entries 73837
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

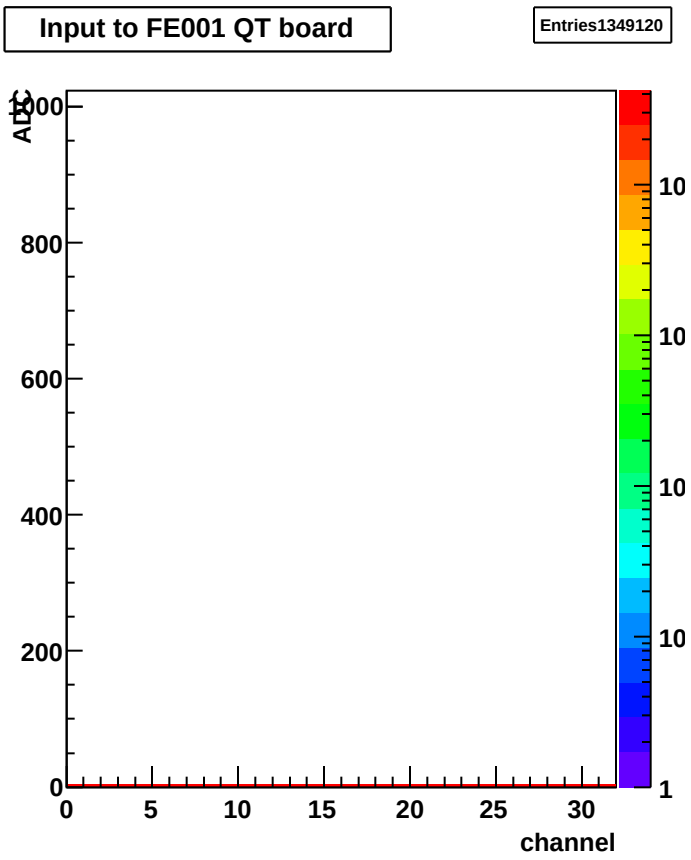

Input to FPD L2 DSM
Entries 126480
CL bits
SMALL
LARGE-S
LARGE-N

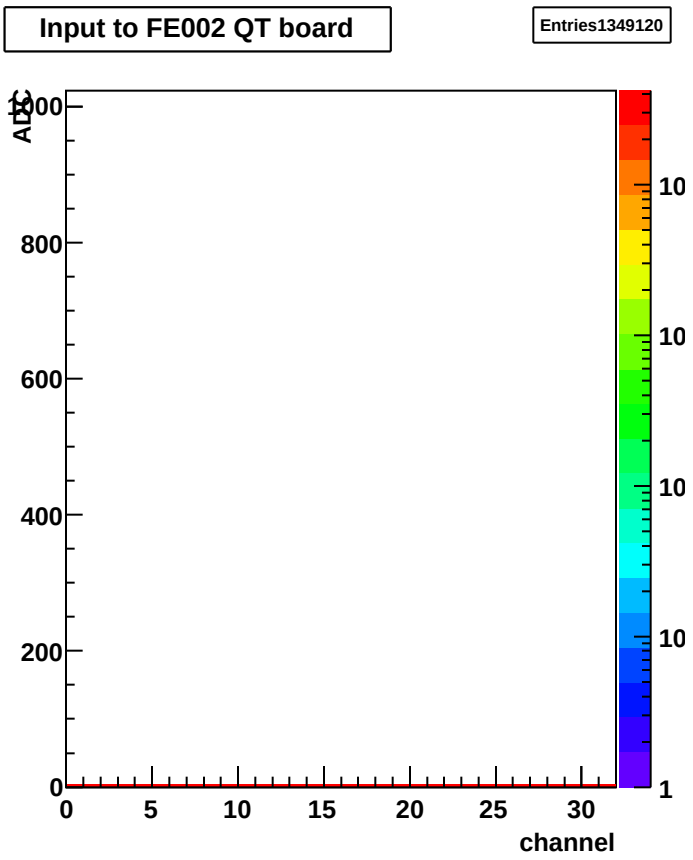

Input to FPD L2 DSM
Entries 28055
CL bits - simulated
SMALL
LARGE-S
LARGE-N

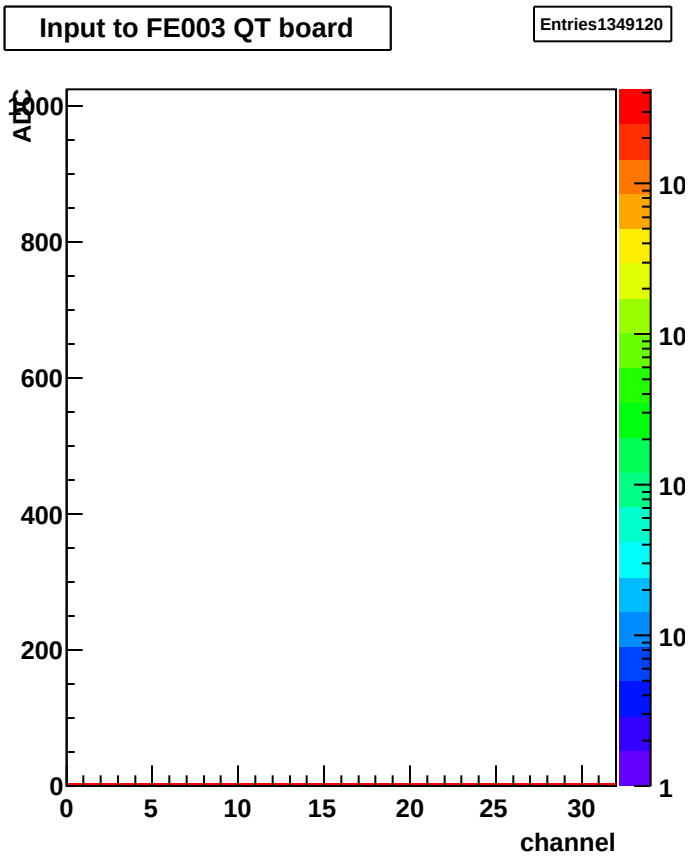

Input to FPD L2 DSM
Entries 126480
HT bits
SMALL
LARGE-S
LARGE-N

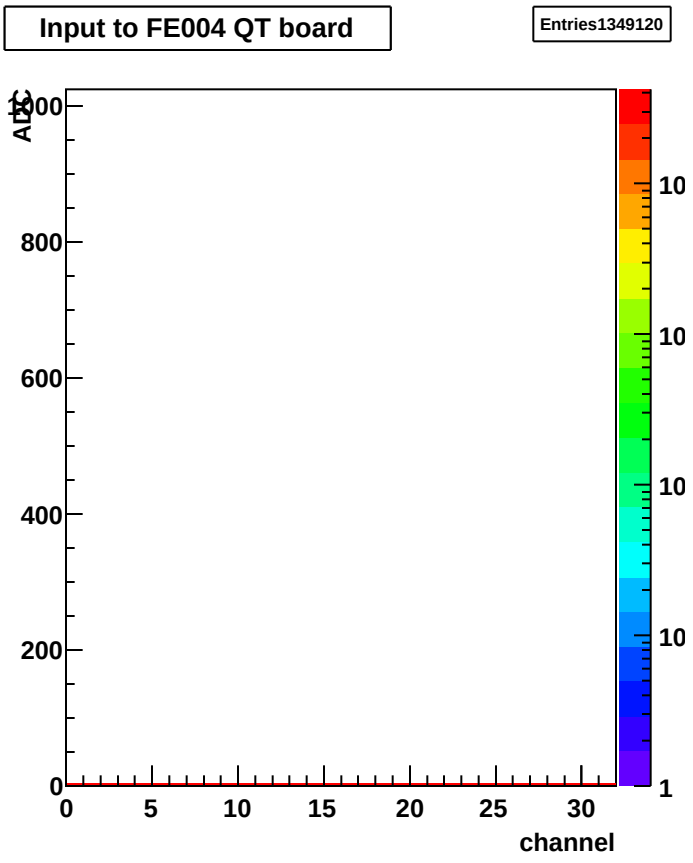

Input to FPD L2 DSM
Entries 16191
HT bits - simulated
SMALL
LARGE-S
LARGE-N

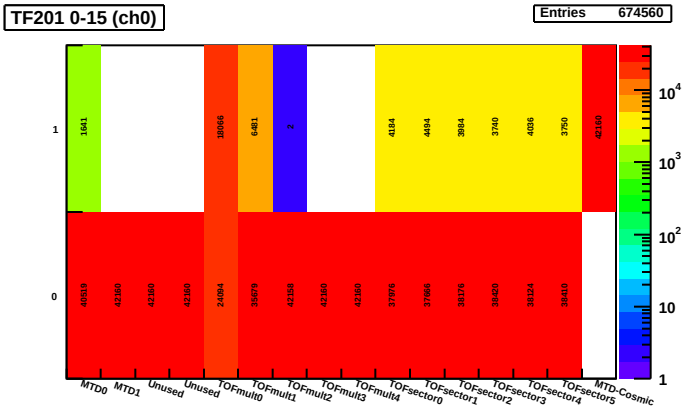

Input to FPD L2 DSM
Entries 252960
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

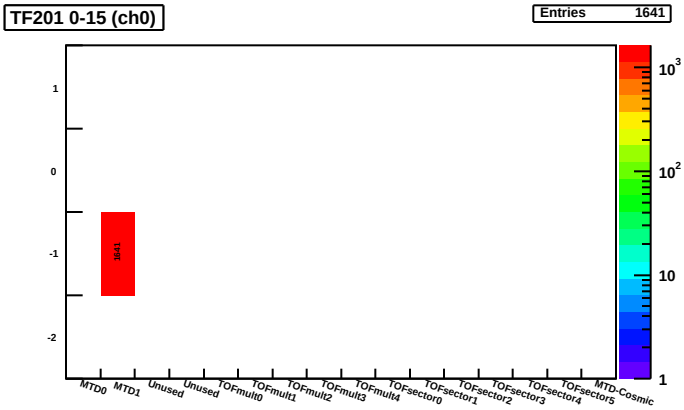

Input to FE001 QT board
Entries1349120
ADC
channel

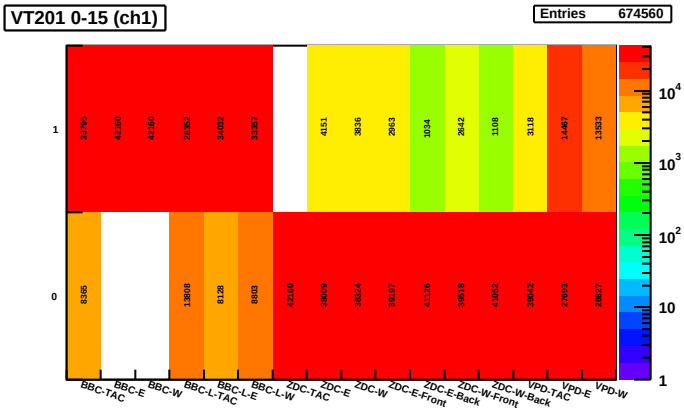

Input to FE002 QT board
Entries1349120
ADC
channel

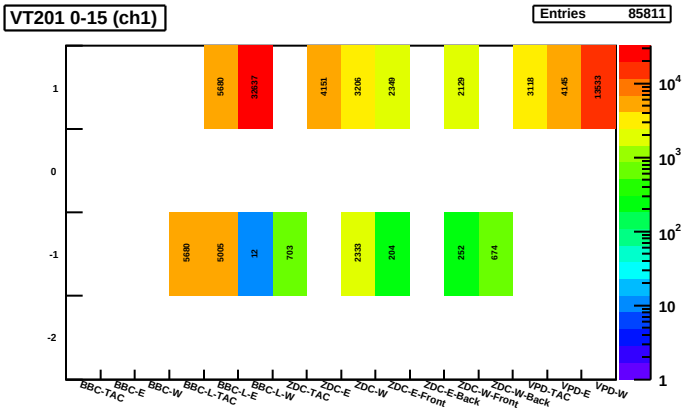

Input to FE003 QT board
Entries1349120
ADC
channel

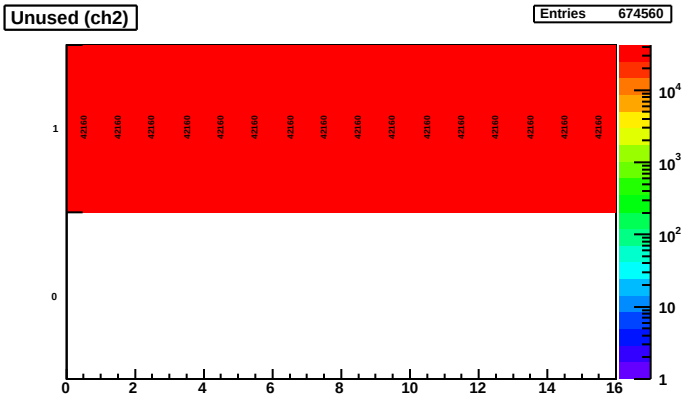

Input to FE004 QT board
Entries1349120
ADC
channel

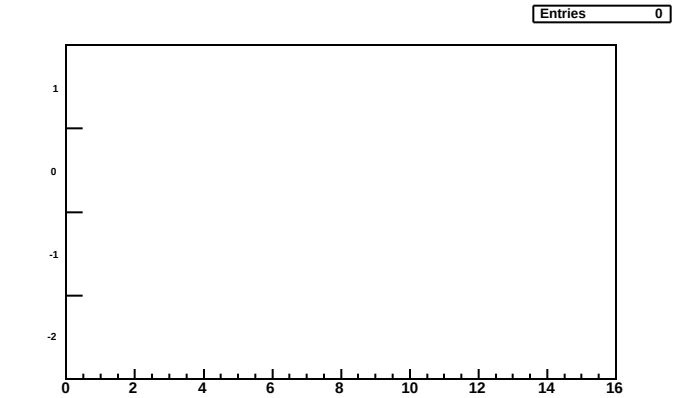

TF201 0-15 (ch0)
Entries 674560
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

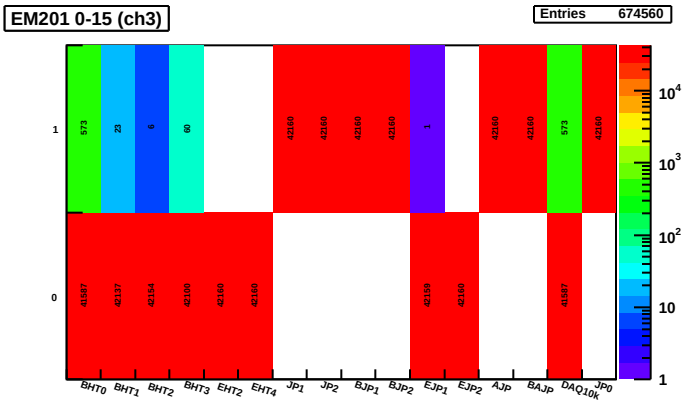

TF201 0-15 (ch0)
Entries 1641
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

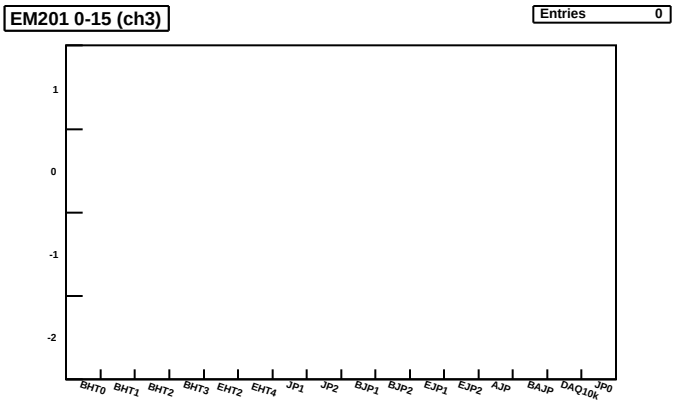

VT201 0-15 (ch1)
Entries 674560
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 85811
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 674560


Entries 0


EM201 0-15 (ch3)
Entries 674560
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
Entries 0
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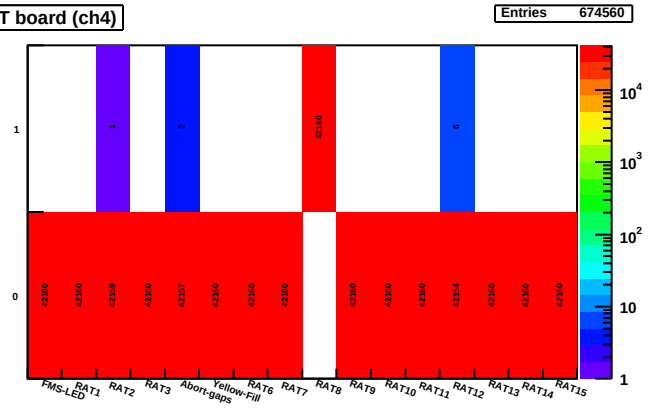
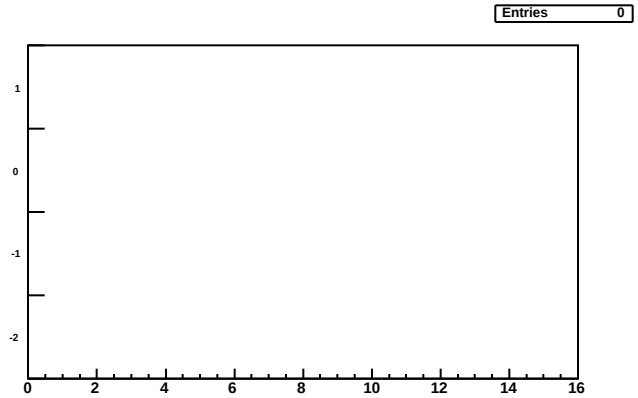
RAT board (ch4)





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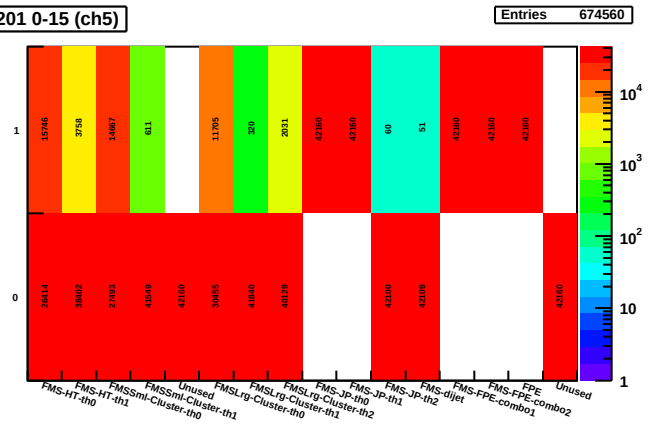

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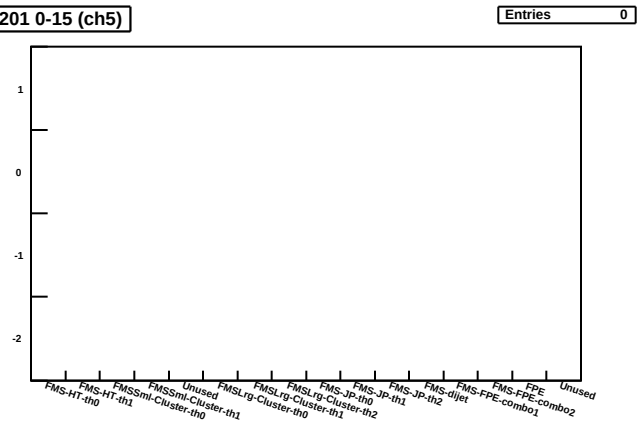
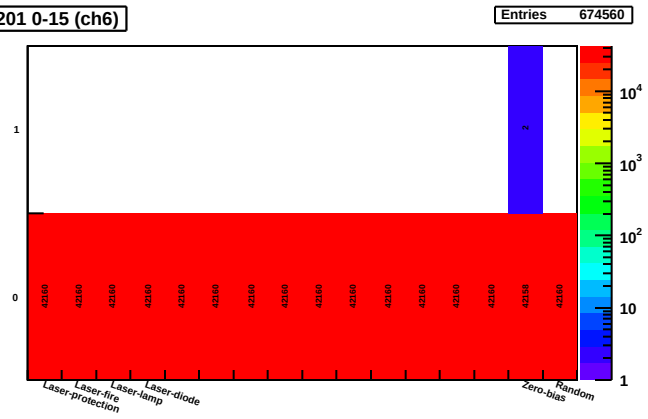
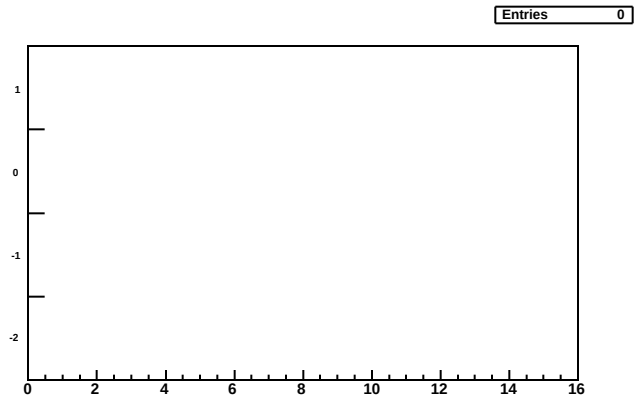
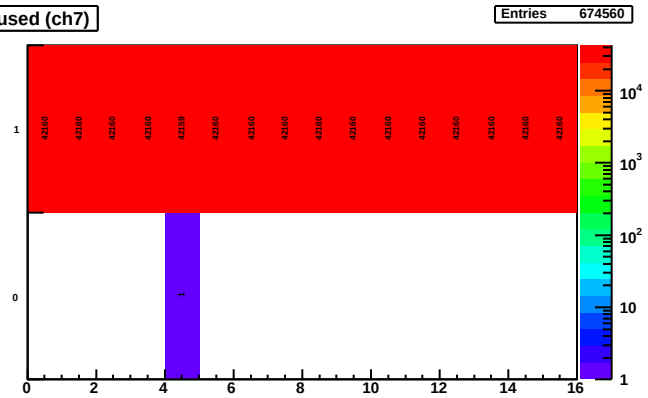
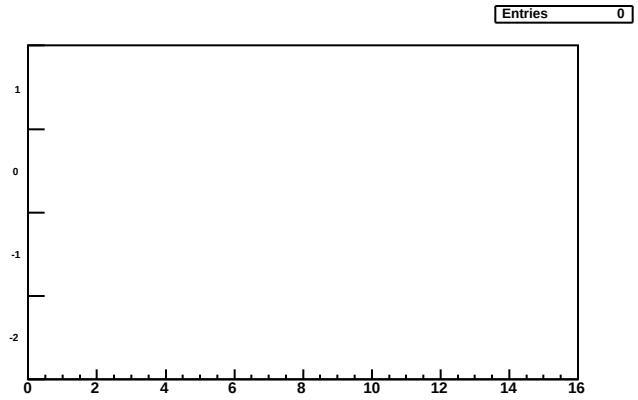


ST201 0-15 (ch6)





Unused (ch7)