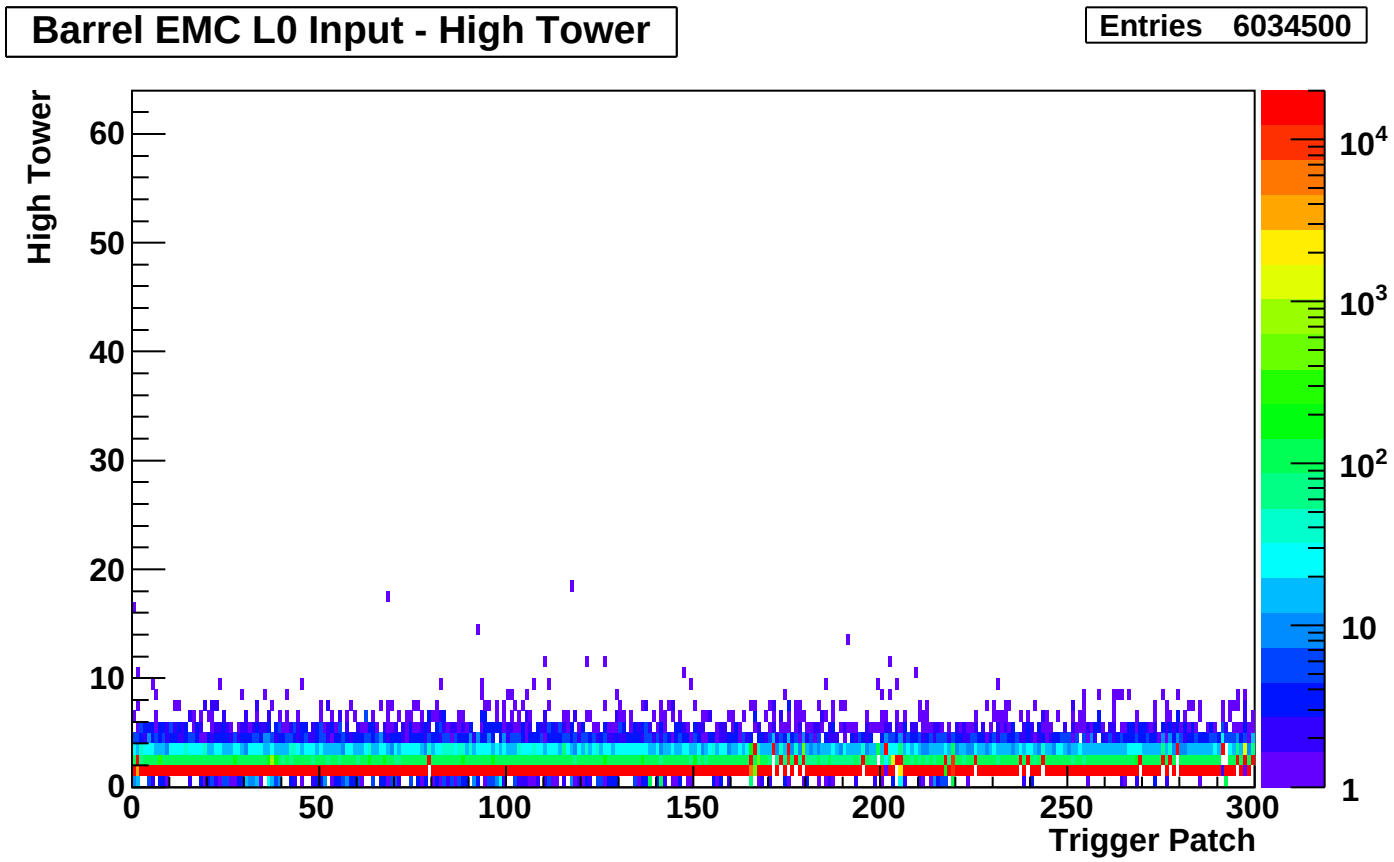
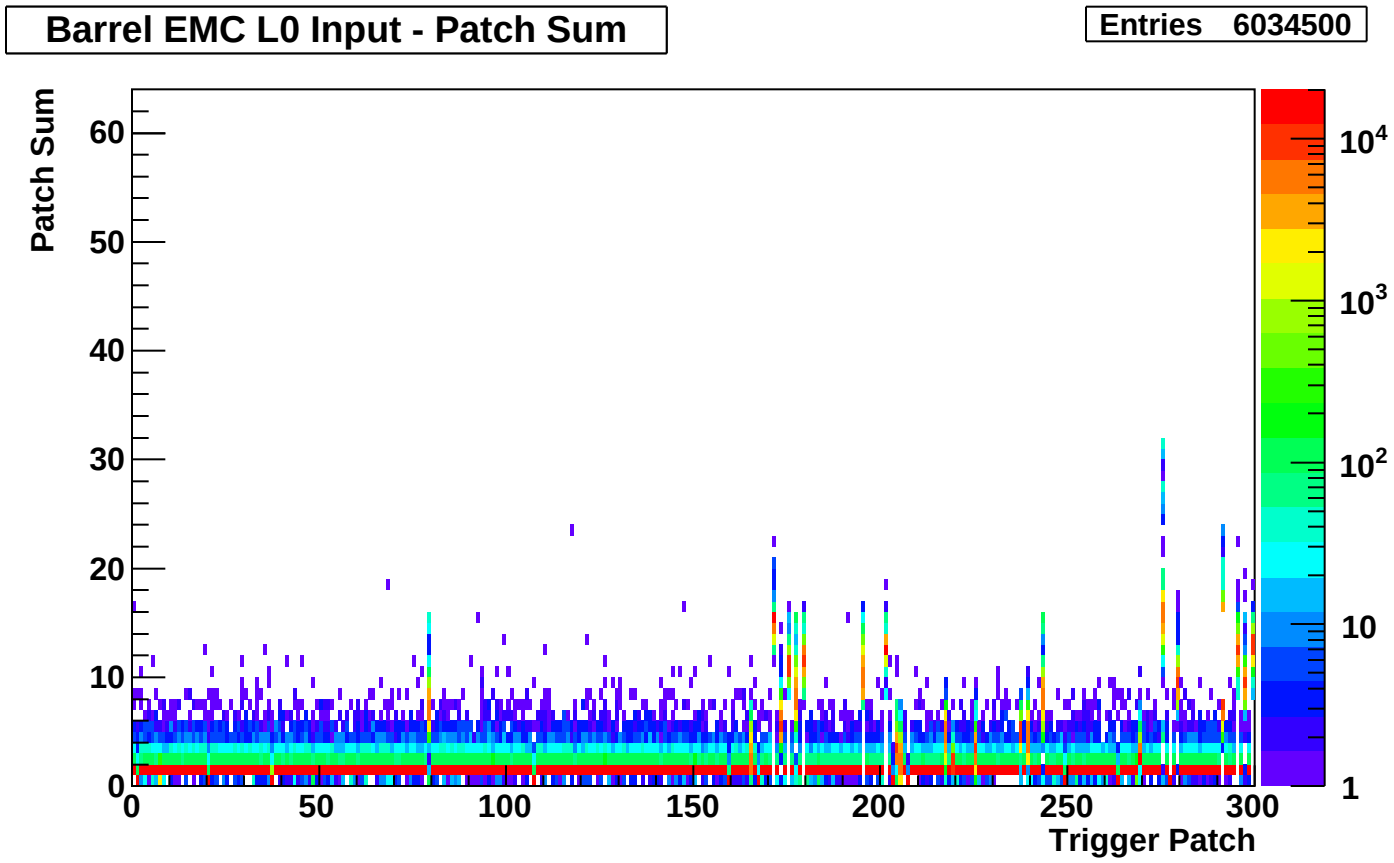
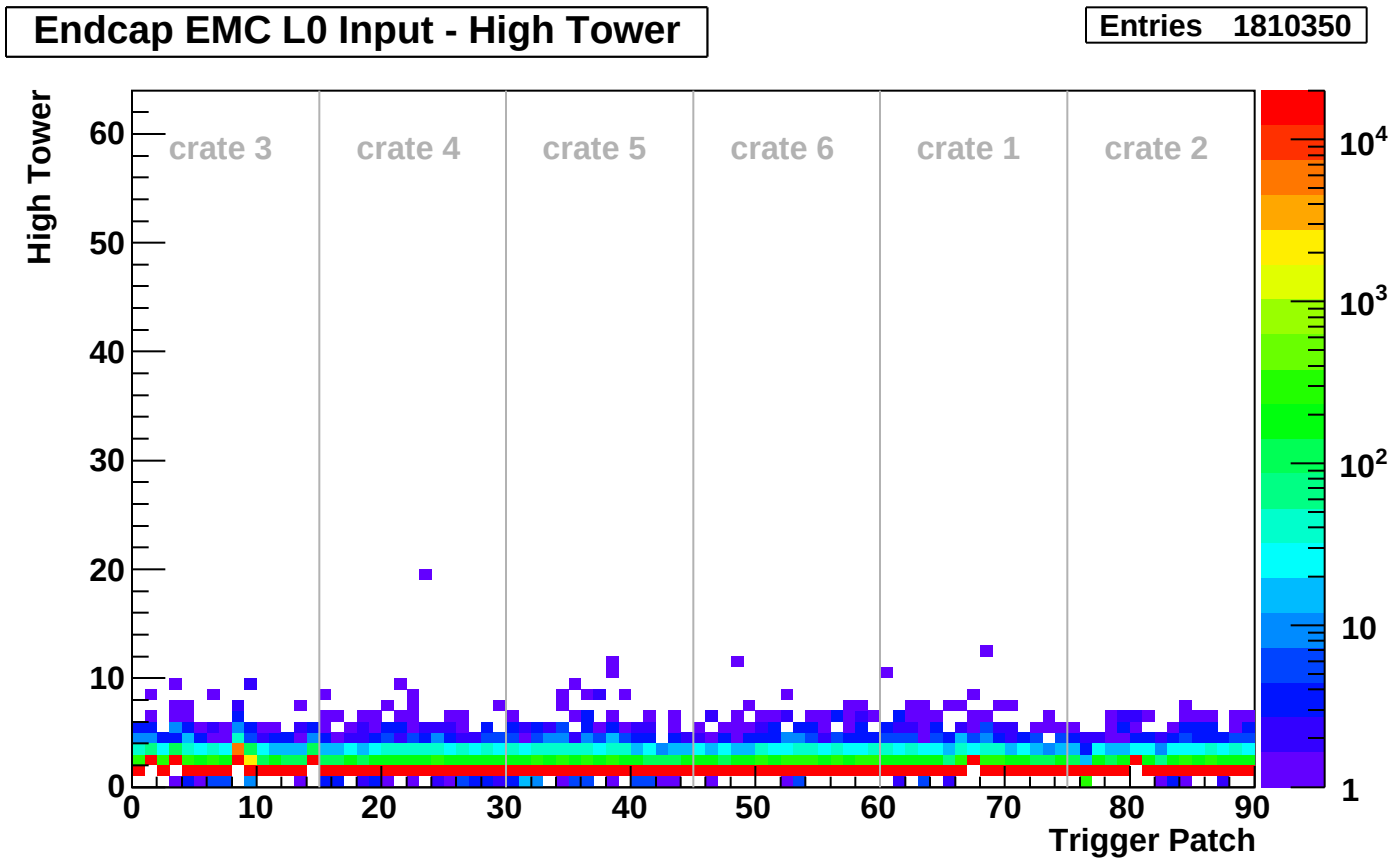
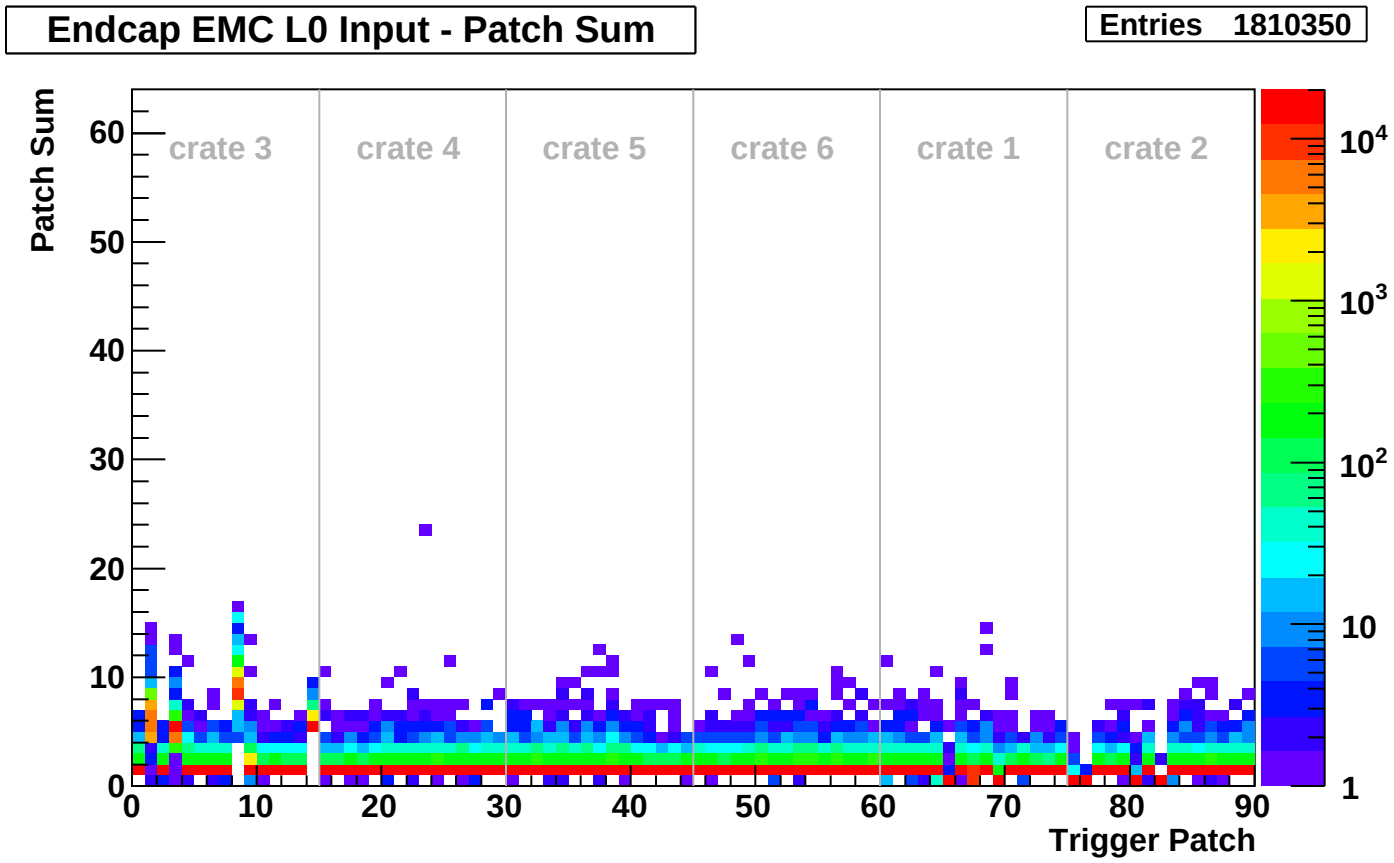
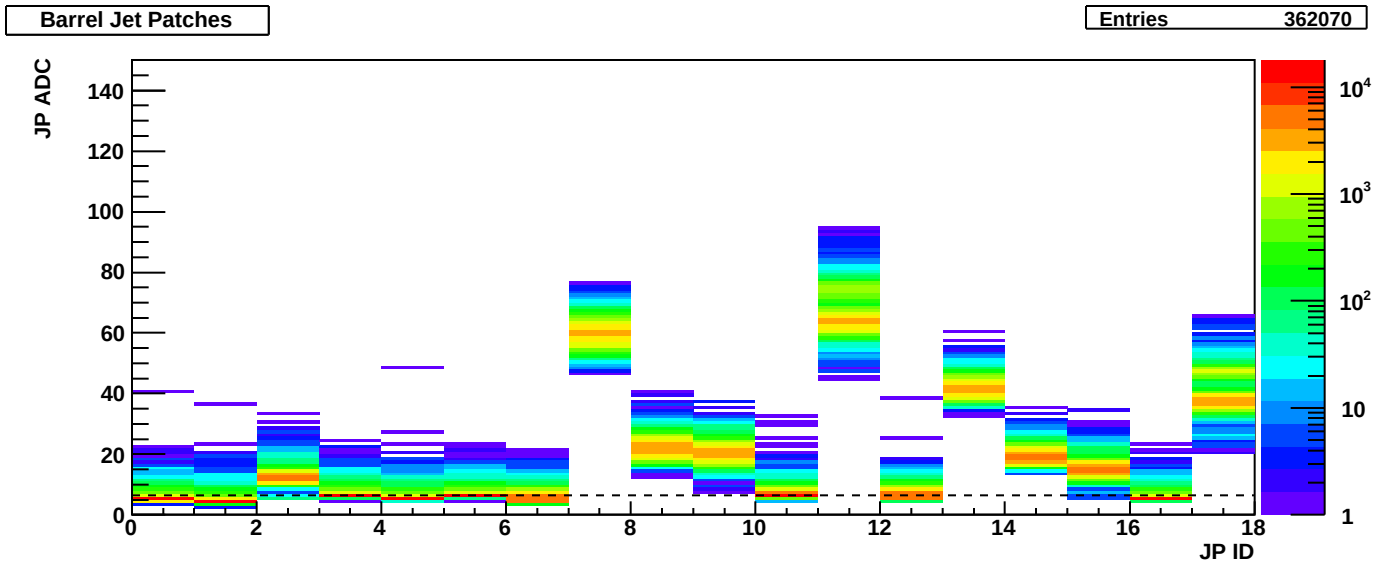
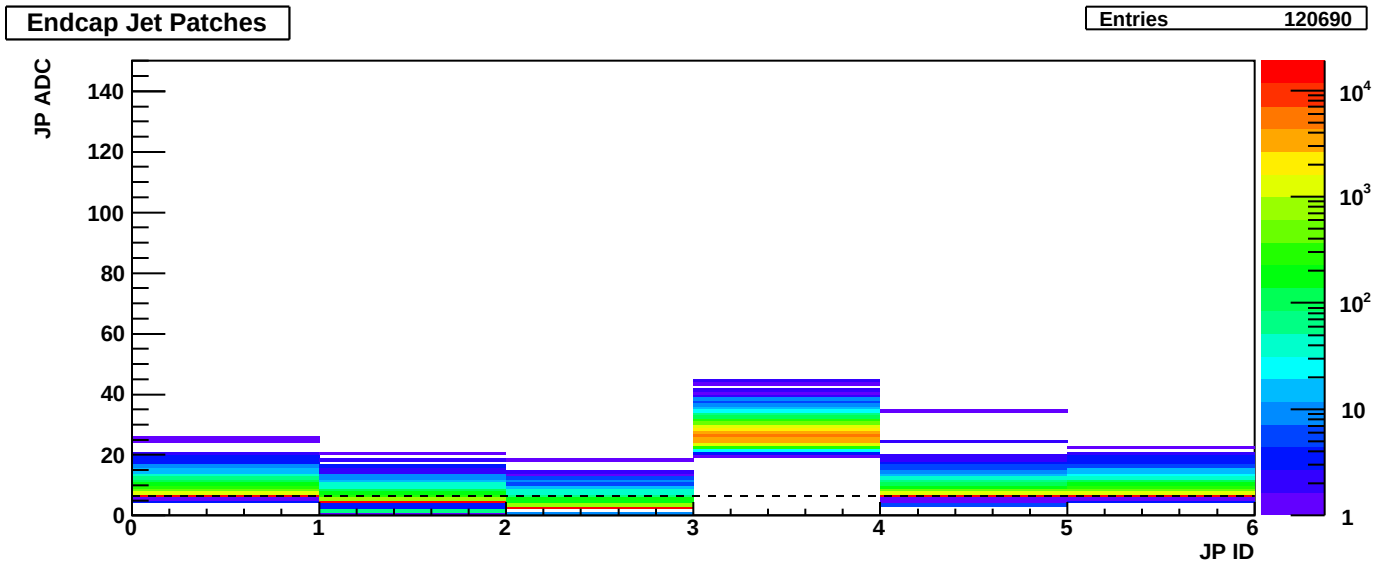

Barrel EMC L0 Input - High Tower
Entries 6034500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

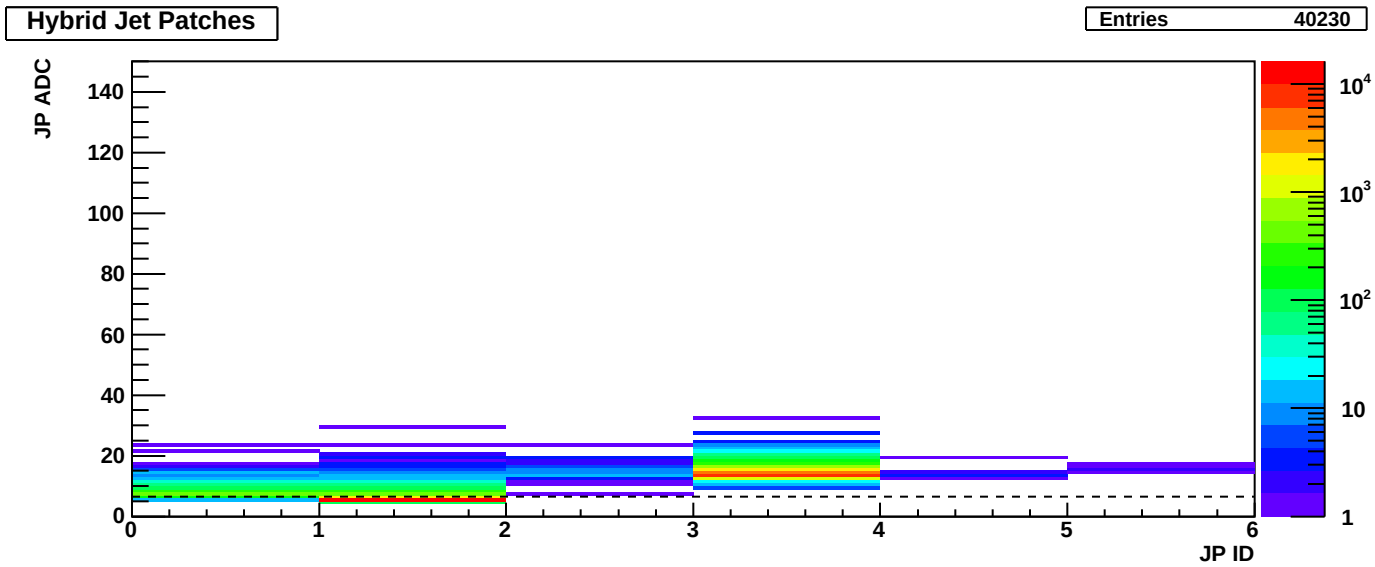

Barrel EMC L0 Input - Patch Sum
Entries 6034500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

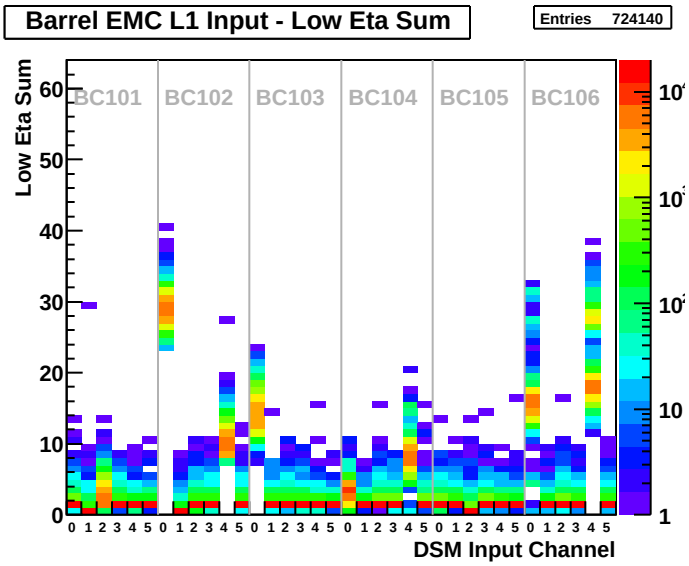

Endcap EMC L0 Input - High Tower
Entries 1810350
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

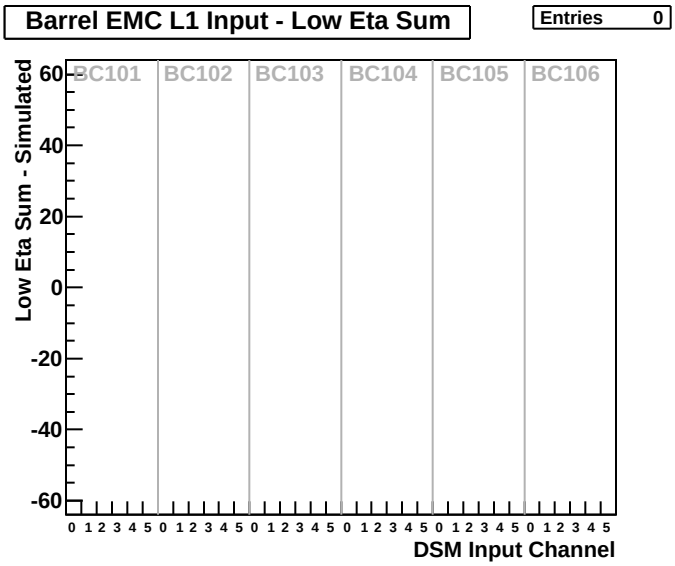

Endcap EMC L0 Input - Patch Sum
Entries 1810350
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

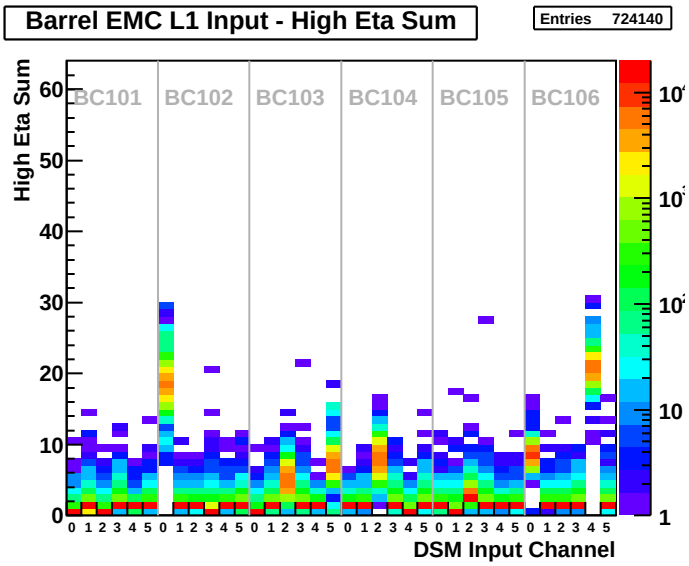

Barrel Jet Patches
Entries 362070
JP ADC
JP ID

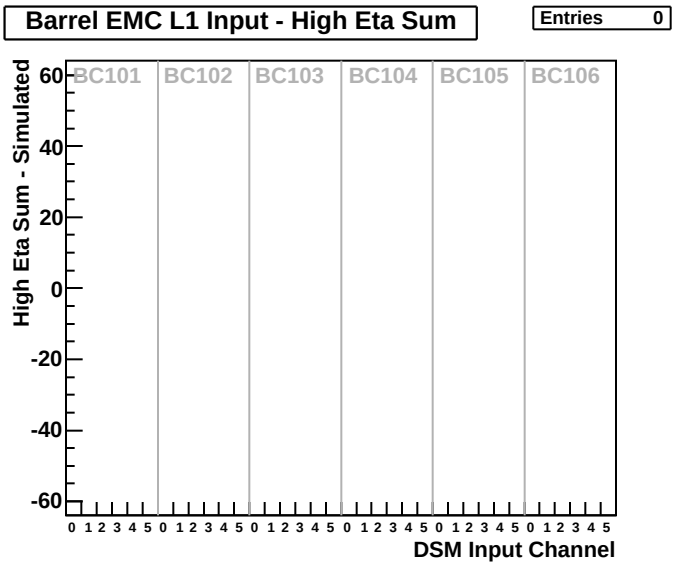

Endcap Jet Patches
Entries 120690
JP ADC
JP ID

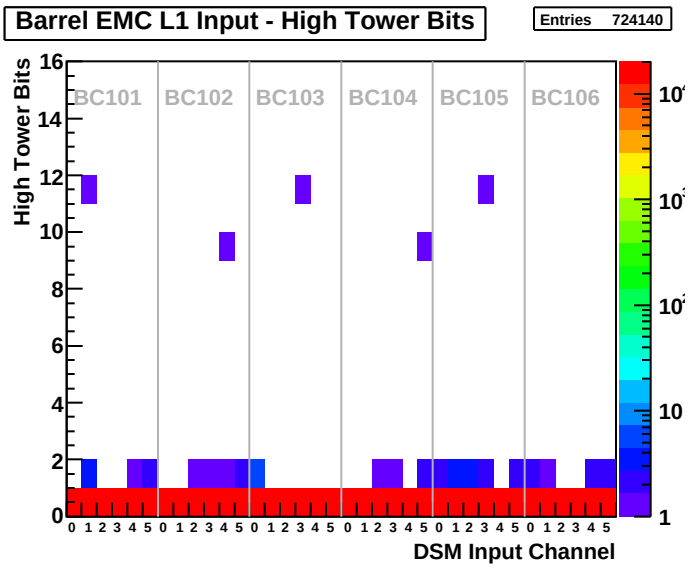

Hybrid Jet Patches
Entries 40230
JP ADC
JP ID

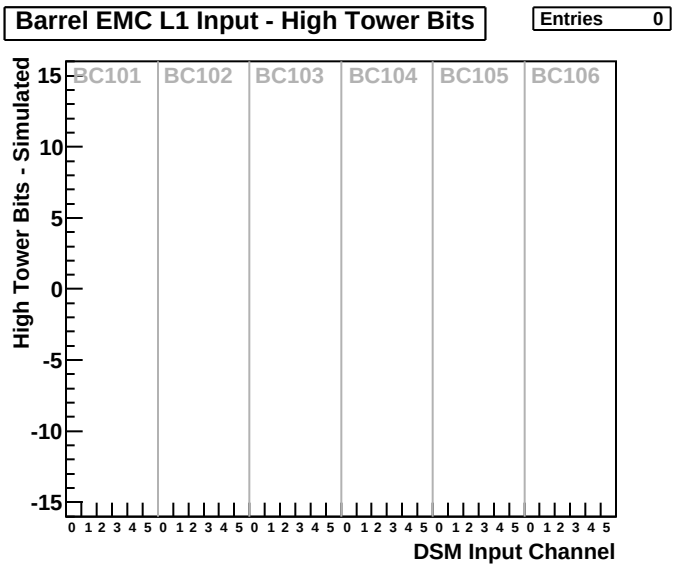

Barrel EMC L1 Input - Low Eta Sum
Entries 724140
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

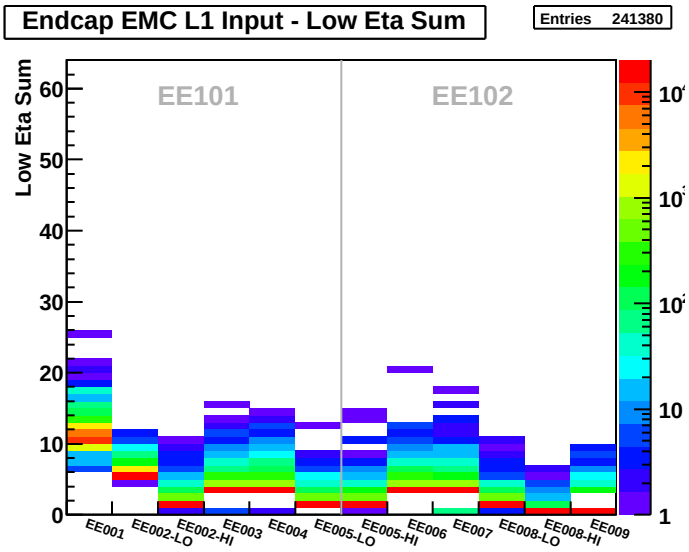

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

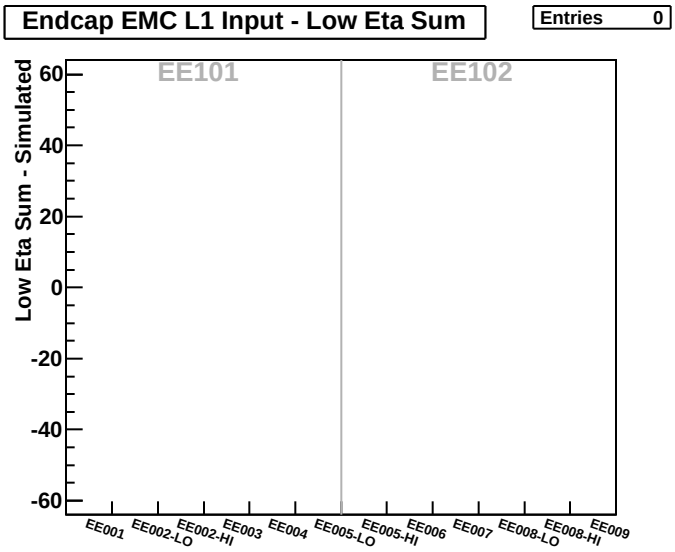

Barrel EMC L1 Input - High Eta Sum
Entries 724140
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

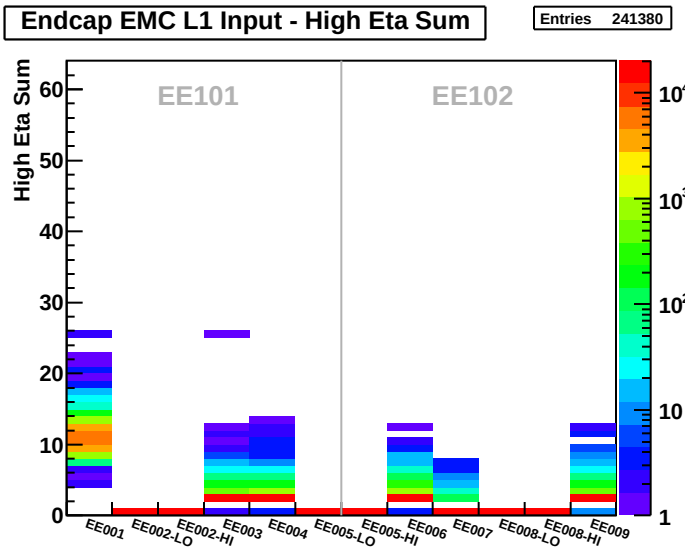

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

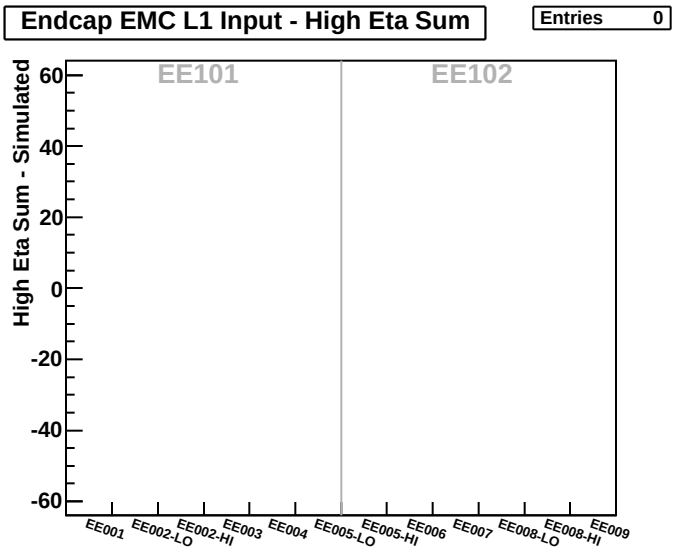

Barrel EMC L1 Input - High Tower Bits
Entries 724140
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

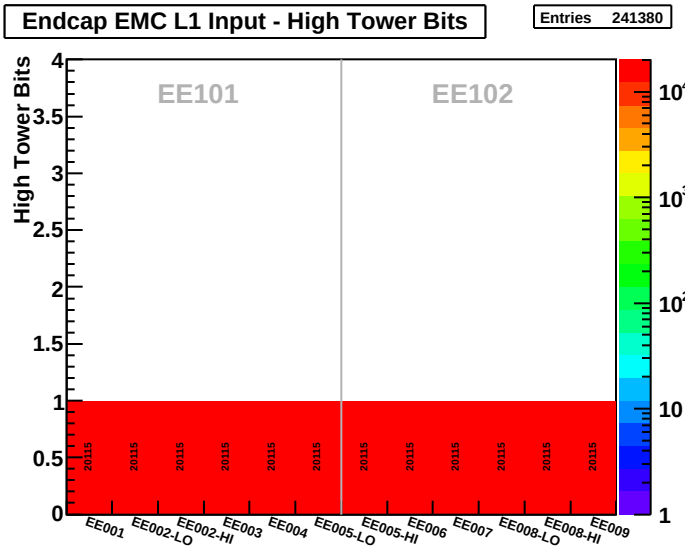

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

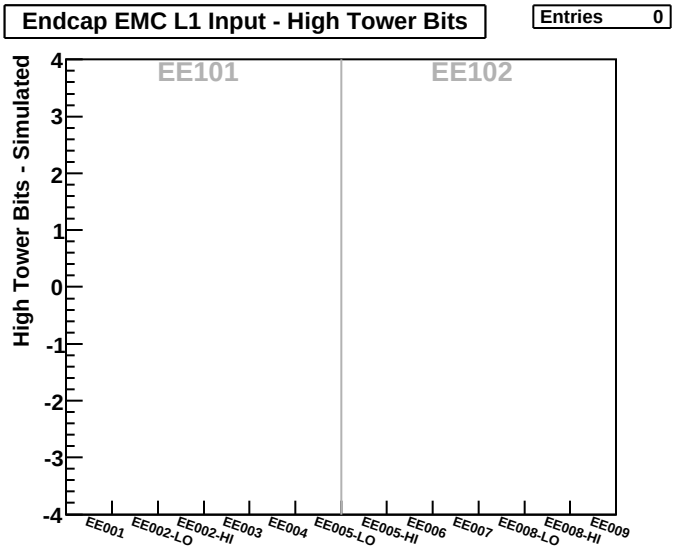

Endcap EMC L1 Input - Low Eta Sum
Entries 241380
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

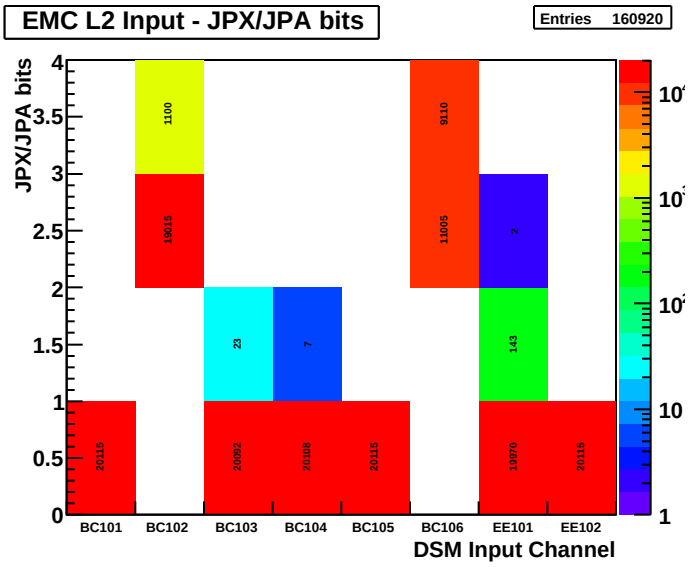

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

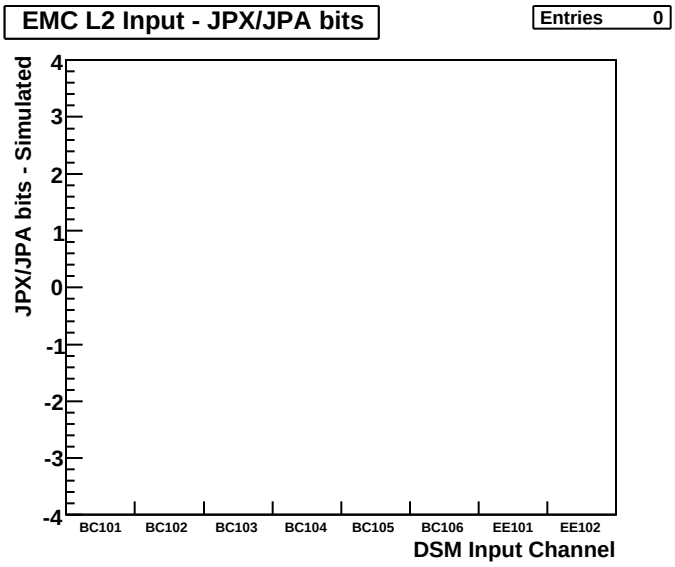

Endcap EMC L1 Input - High Eta Sum
Entries 241380
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

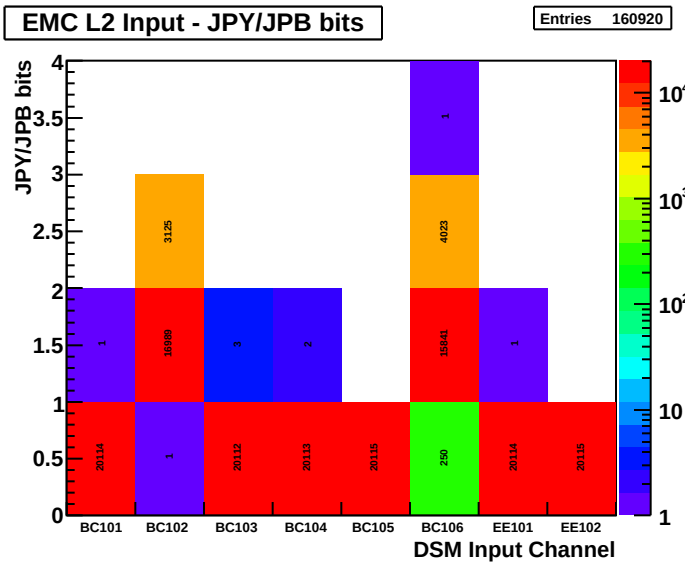

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

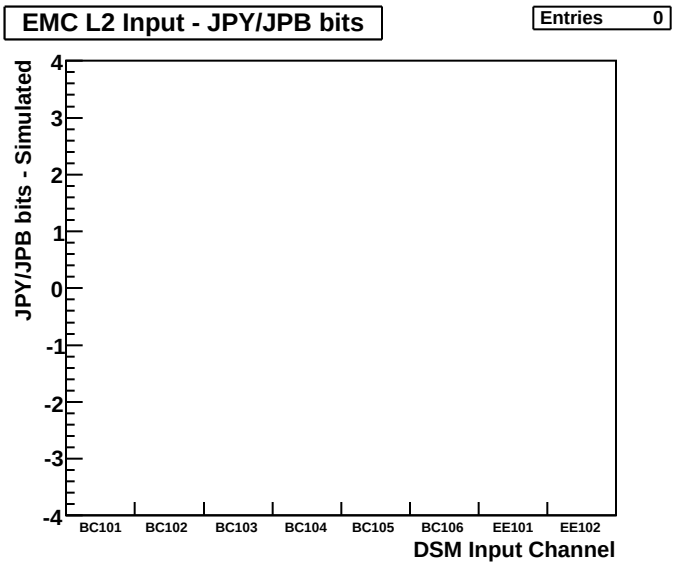

Endcap EMC L1 Input - High Tower Bits
Entries 241380
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

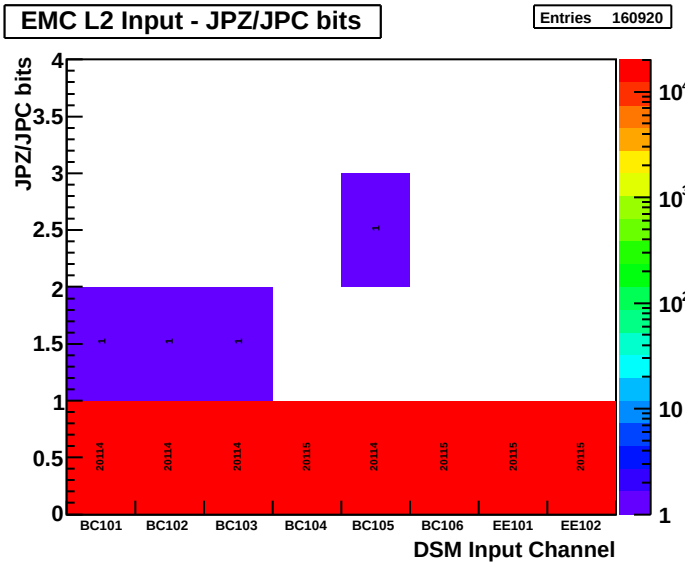

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

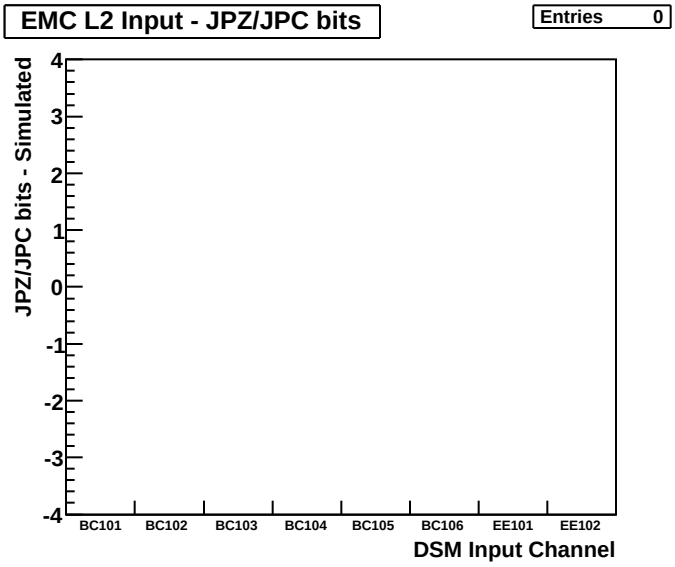

EMC L2 Input - JPX/JPA bits
Entries 160920
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

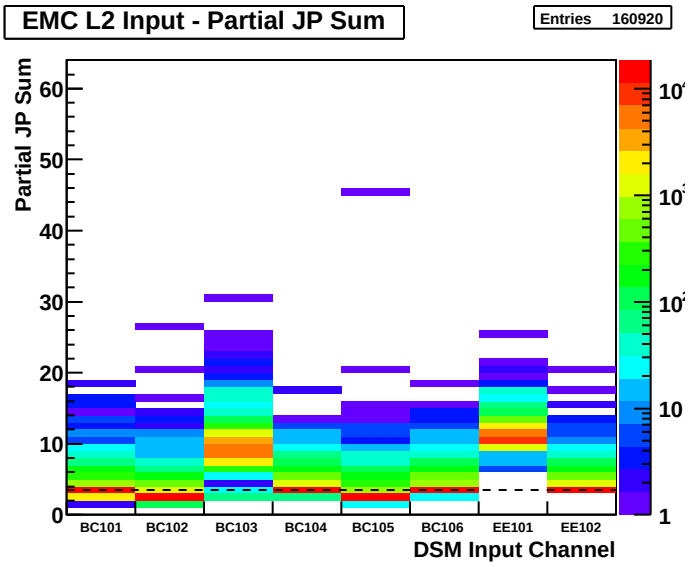

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

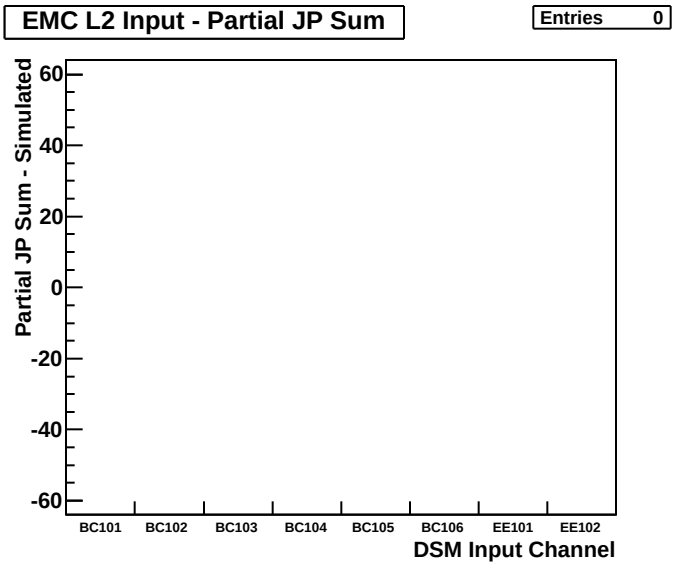

EMC L2 Input - JPY/JPB bits
Entries 160920
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

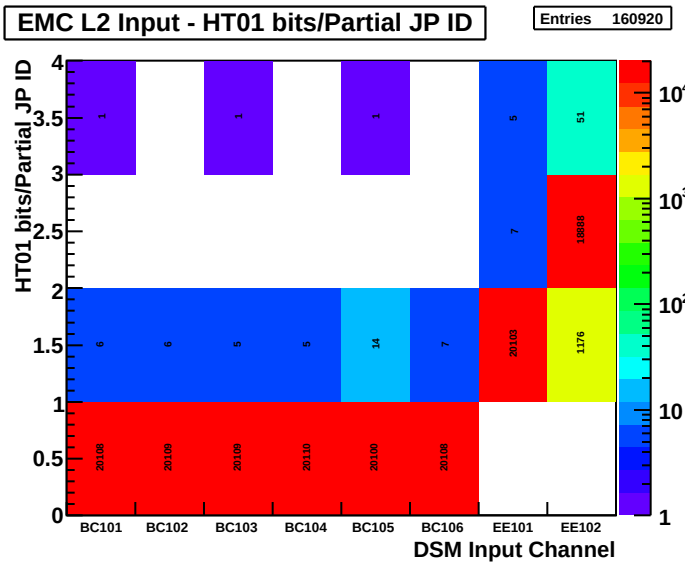

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

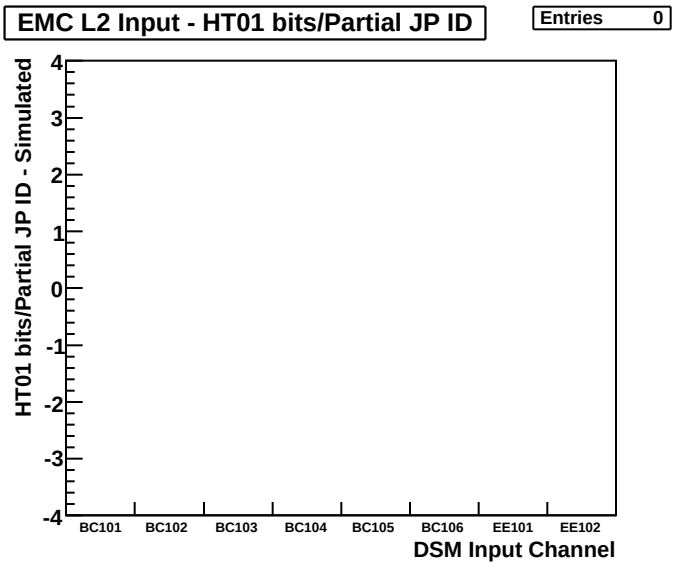

EMC L2 Input - JPZ/JPC bits
Entries 160920
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

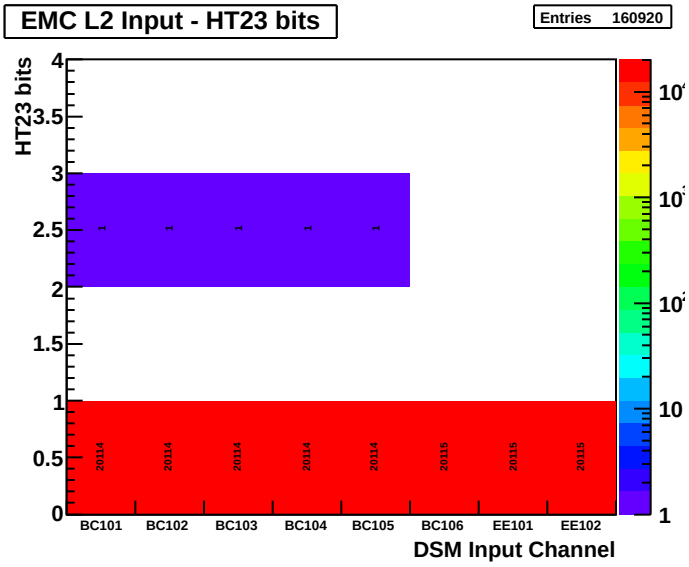

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

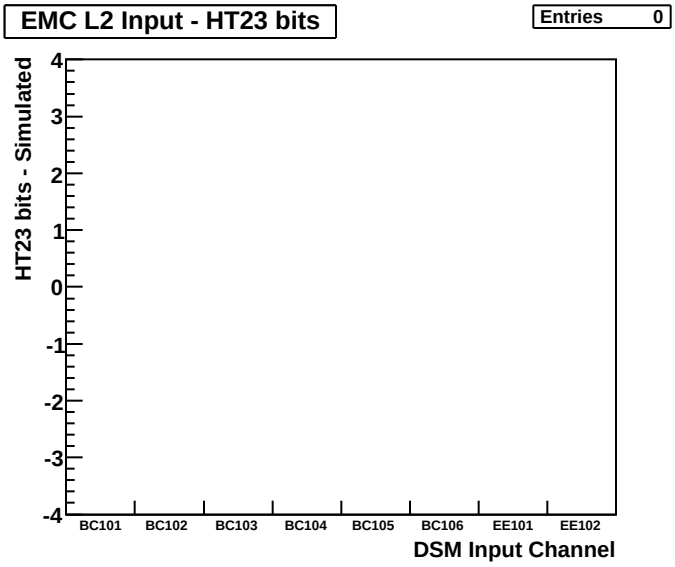

EMC L2 Input - Partial JP Sum
Entries 160920
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

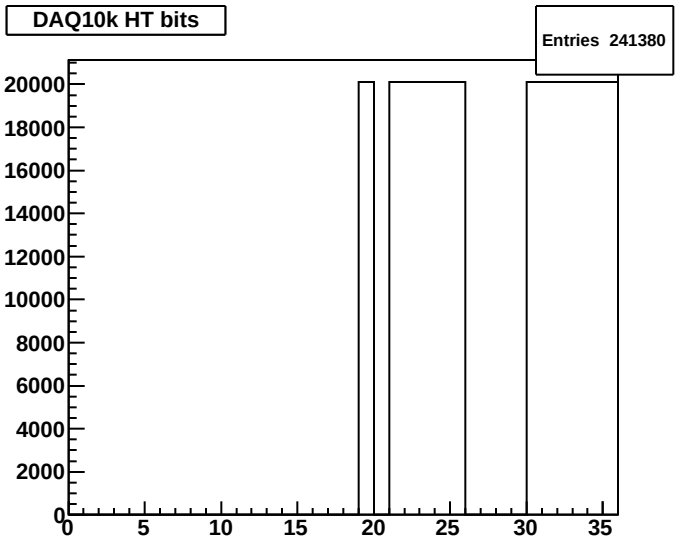

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

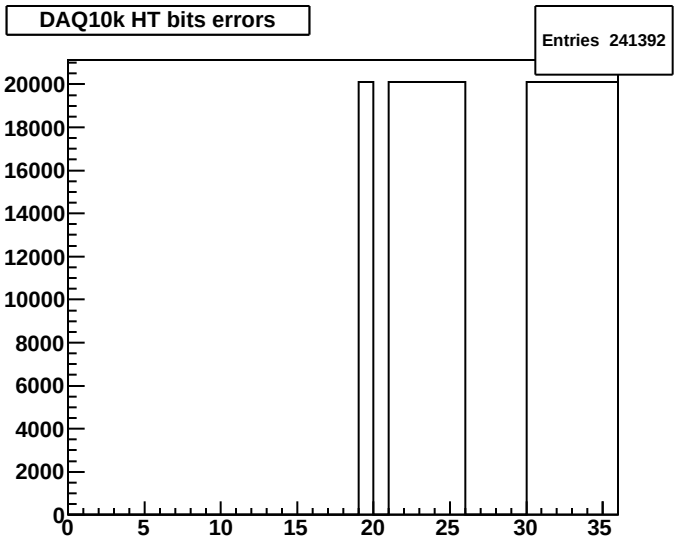

EMC L2 Input - HT01 bits/Partial JP ID
Entries 160920
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

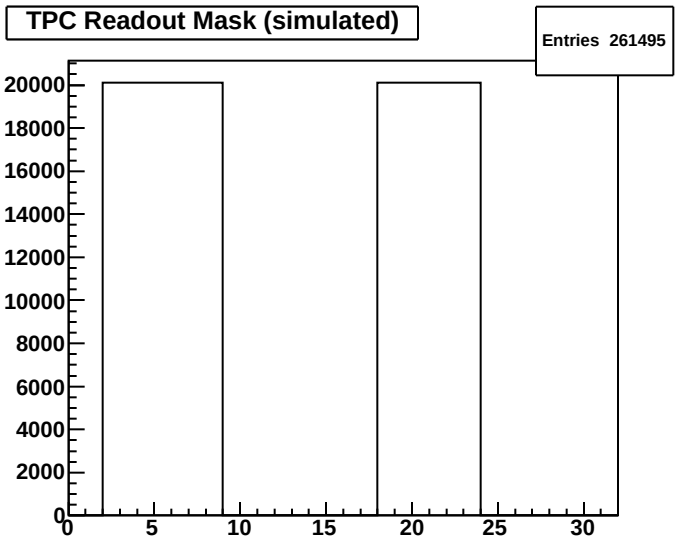

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

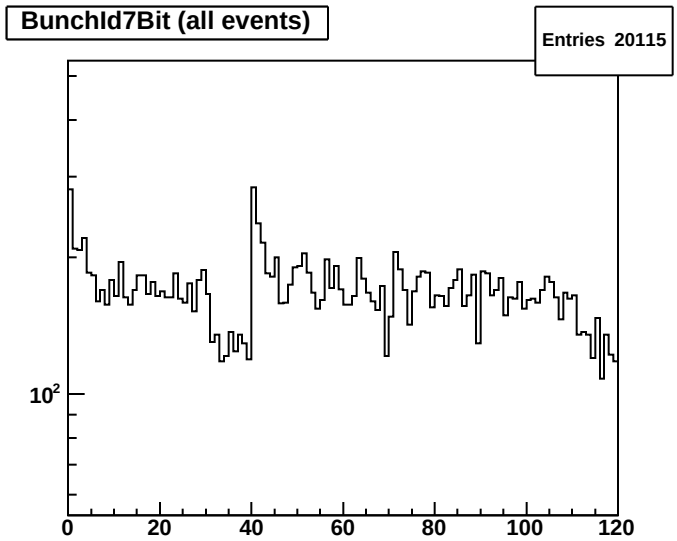

EMC L2 Input - HT23 bits
Entries 160920
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

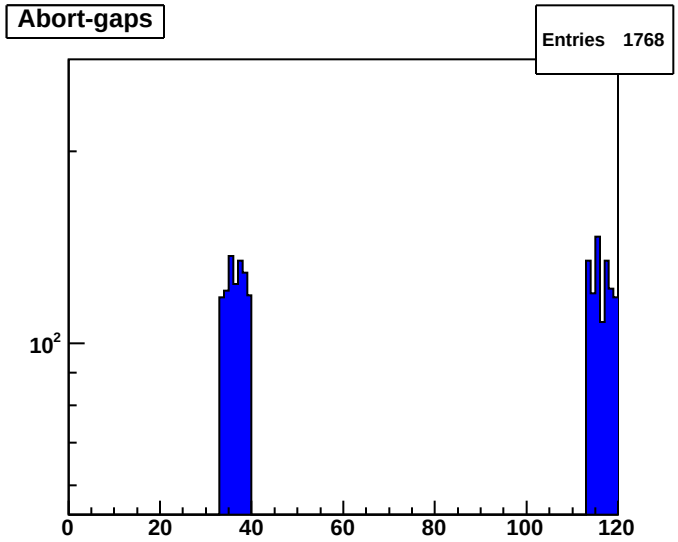

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

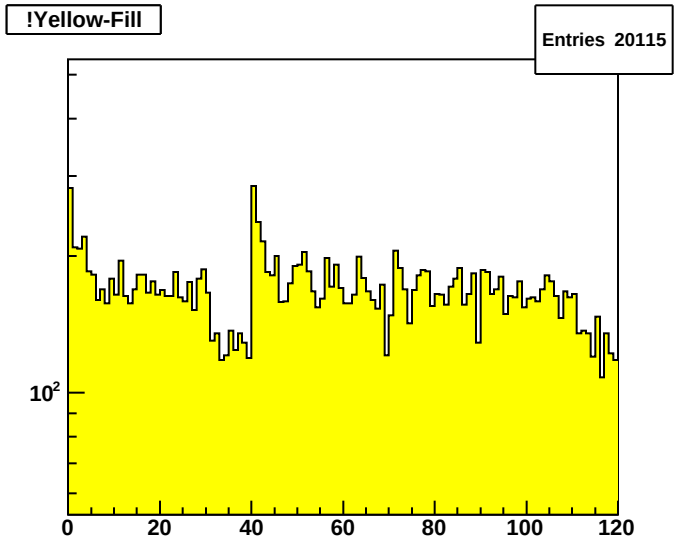

DAQ10k HT bits
Entries 241380

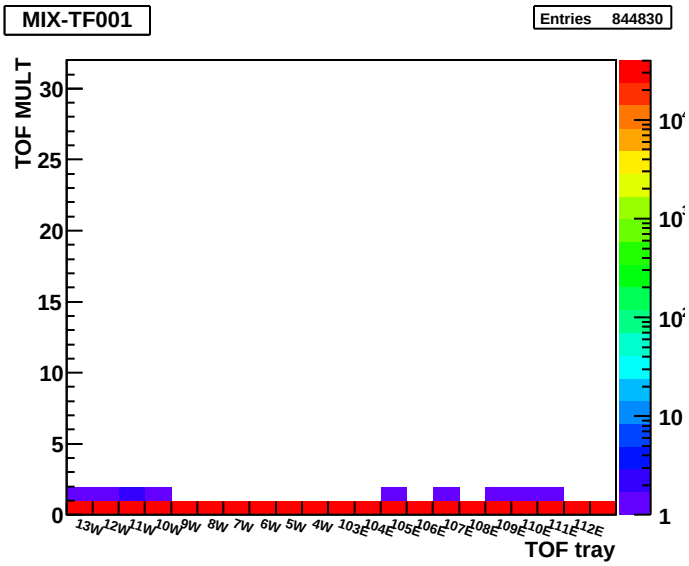

DAQ10k HT bits errors
Entries 241392

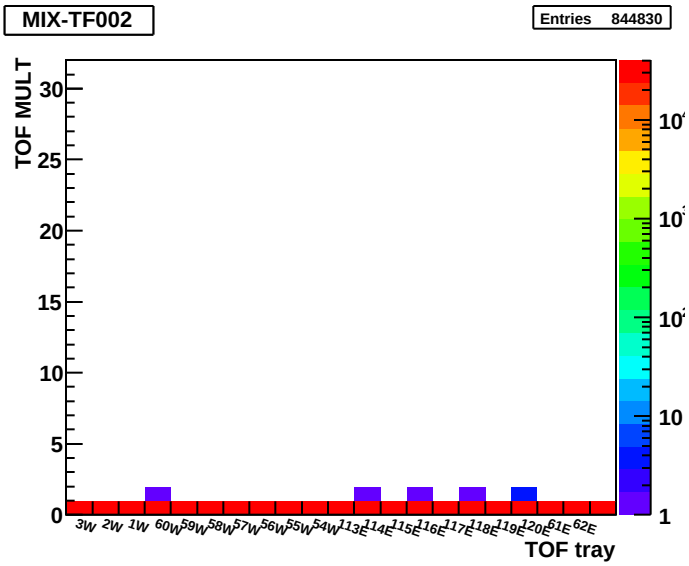

TPC Readout Mask (simulated)
Entries 261495

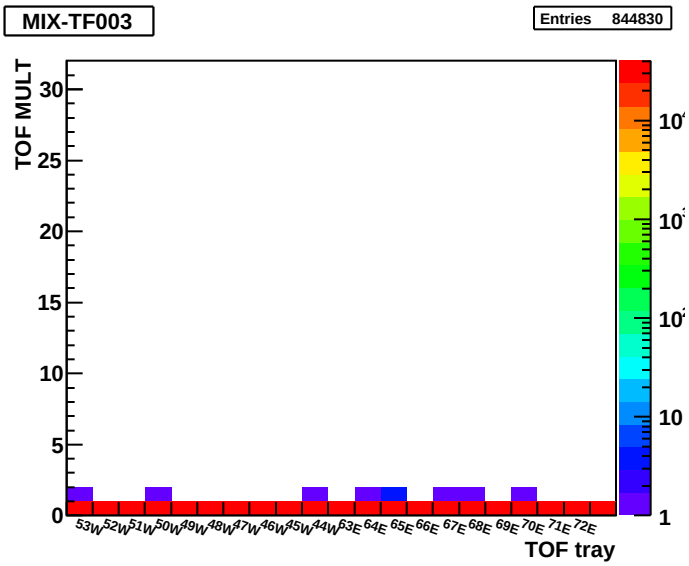

BunchId7Bit (all events)
Entries 20115

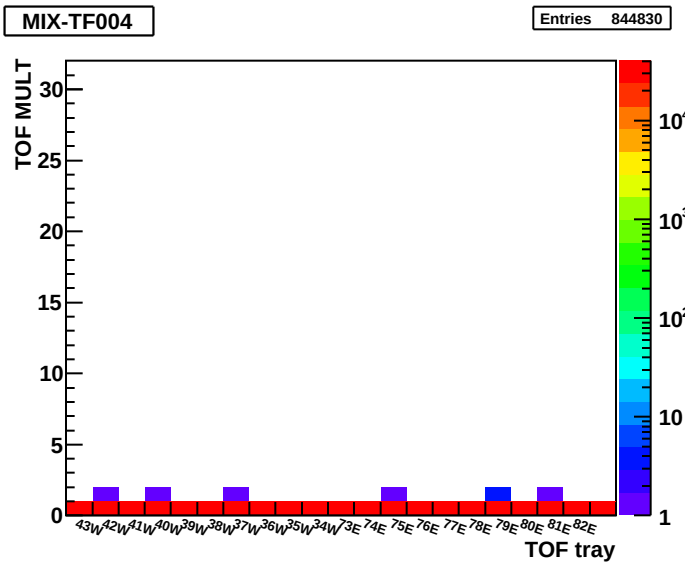

Abort-gaps
Entries 1768

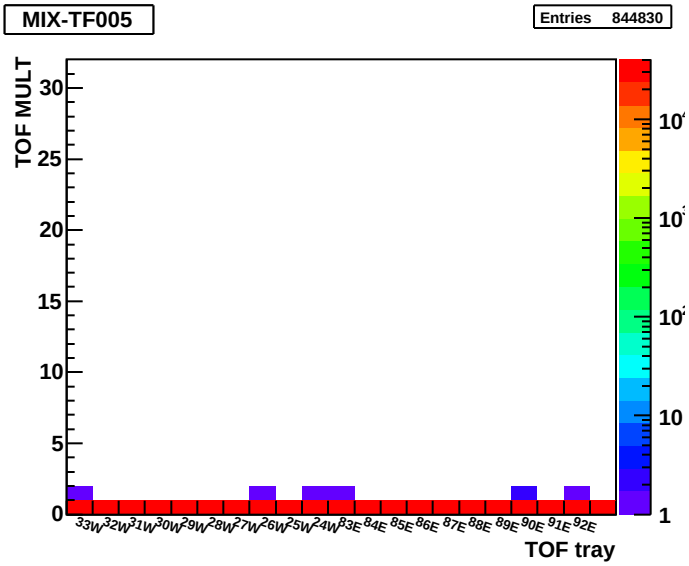

!Yellow-Fill
Entries 20115

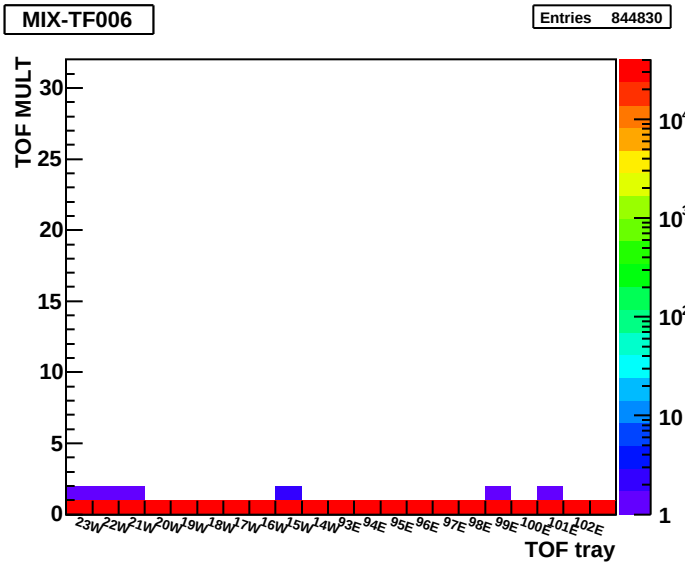

MIX-TF001
Entries 844830
TOF MULT
TOF tray

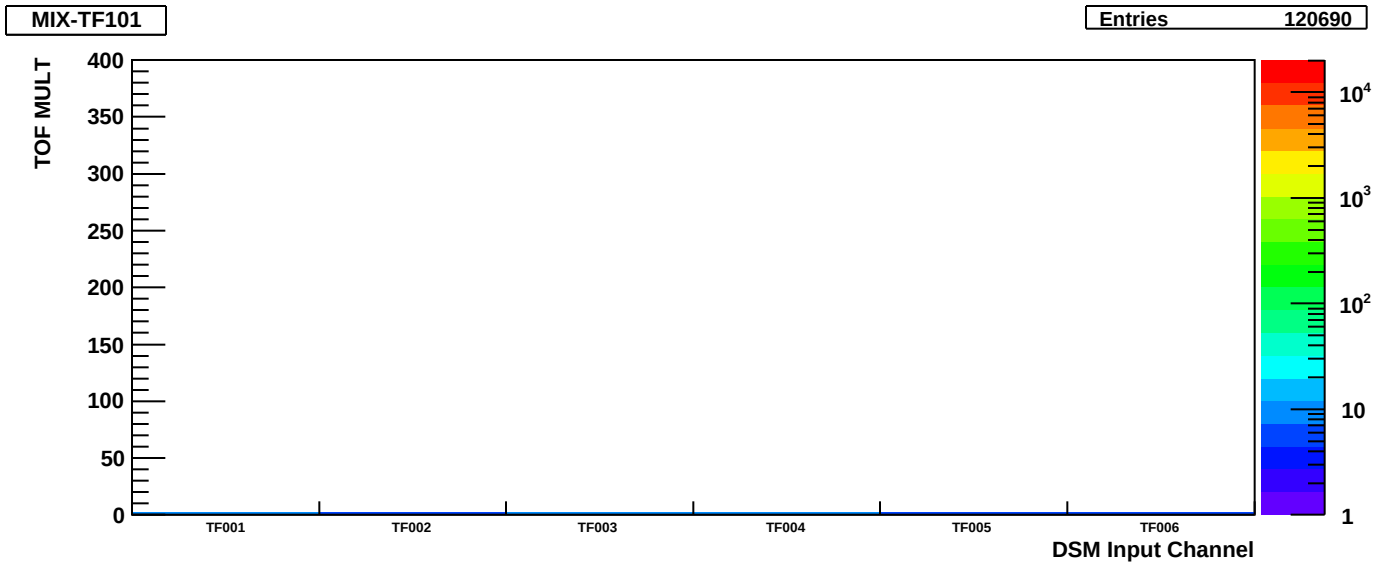

MIX-TF002
Entries 844830
TOF MULT
TOF tray

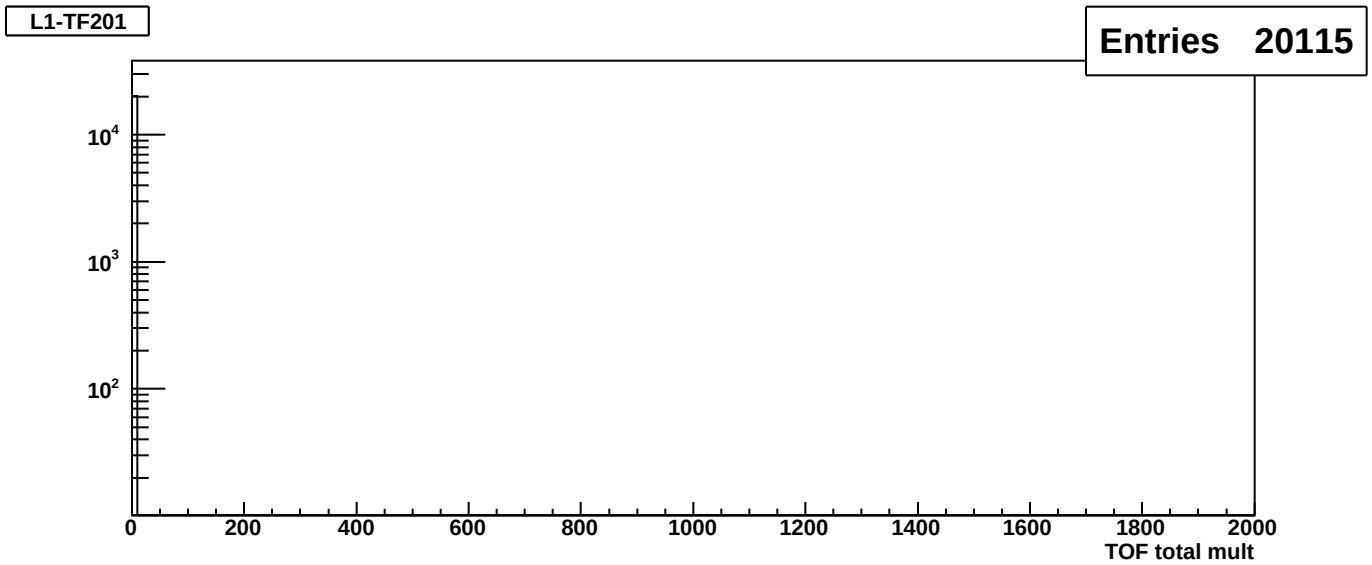

MIX-TF003
Entries 844830
TOF MULT
TOF tray

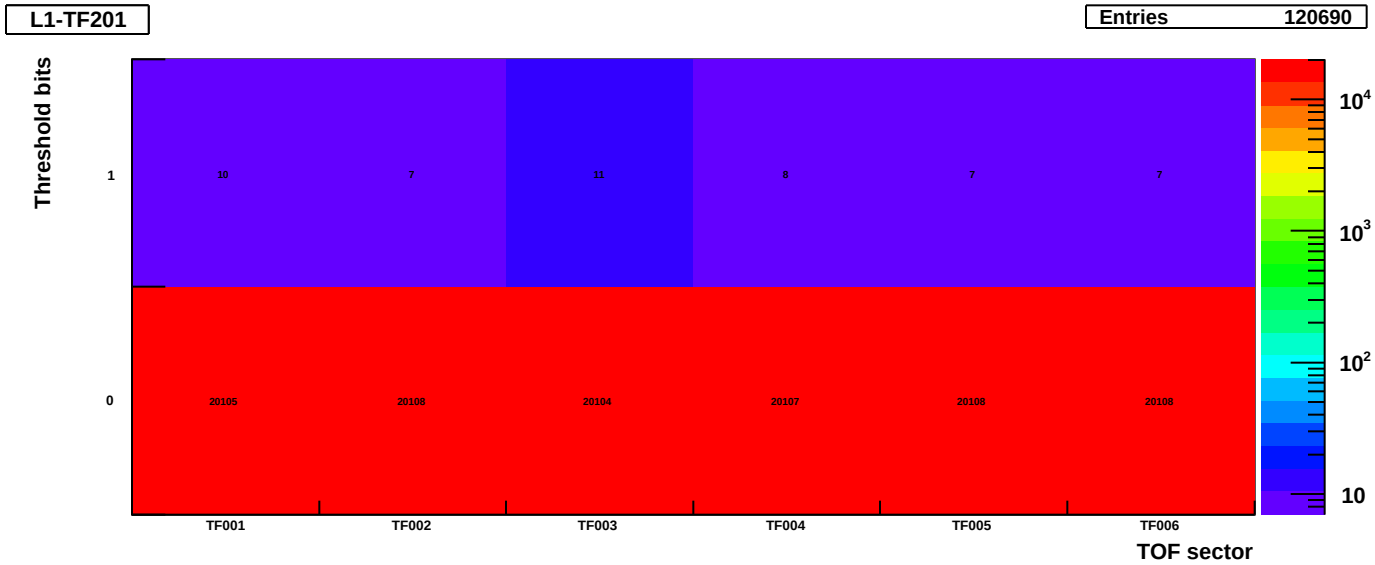

MIX-TF004
Entries 844830
TOF MULT
TOF tray

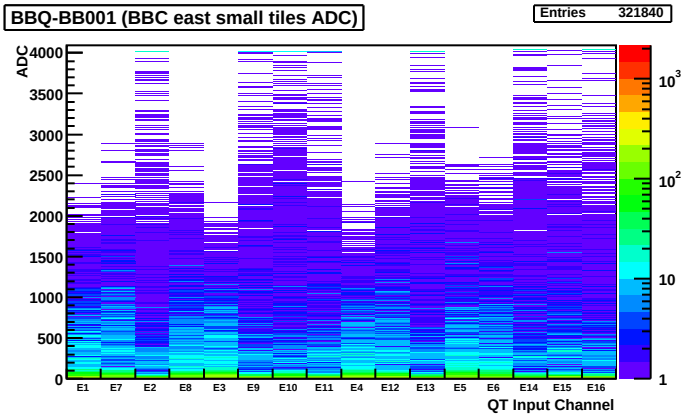

MIX-TF005
Entries 844830
TOF MULT
TOF tray

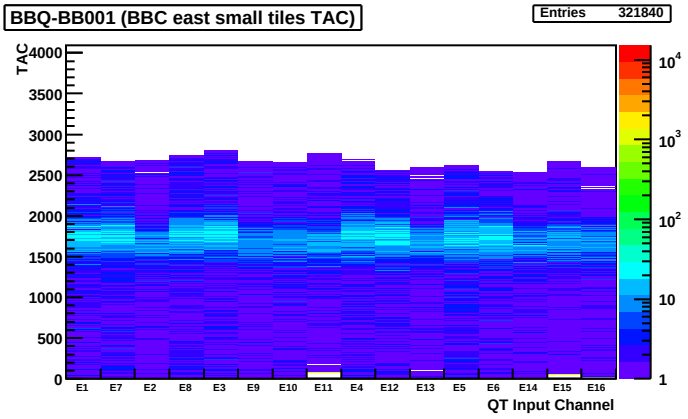

MIX-TF006
Entries 844830
TOF MULT
TOF tray

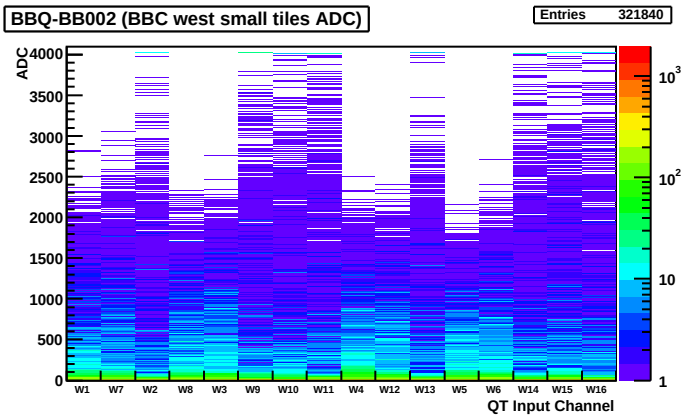

MIX-TF101
Entries 120690
TOF MULT
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^4$
$10^3$
$10^2$
10
1

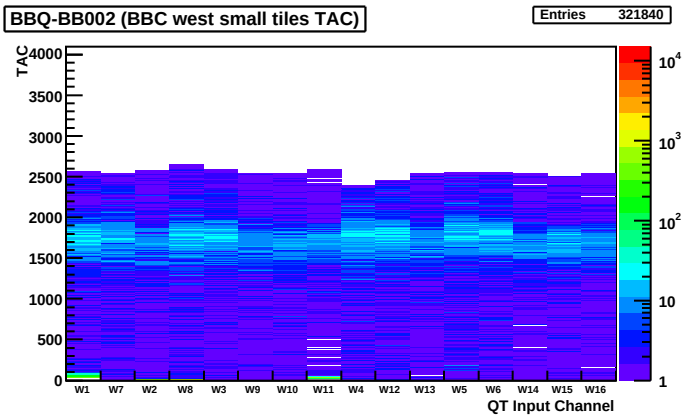

L1-TF201
Entries 20115
$10^4$
$10^3$
$10^2$
0
200
400
600
800
1000
1200
1400
1600
1800
2000
TOF total mult

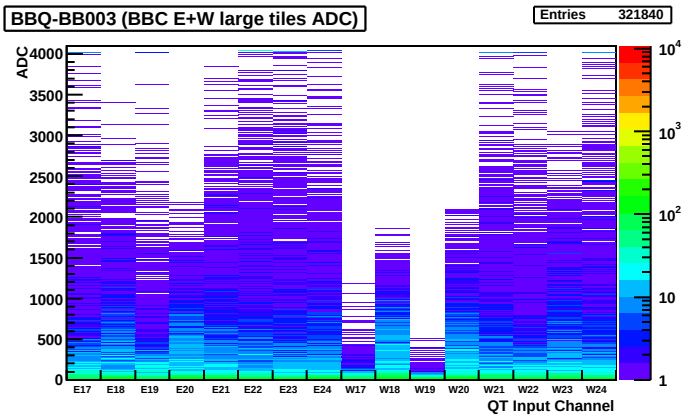

L1-TF201
Entries 120690
Threshold bits
1
0
10
7
11
8
7
7
20105
20108
20104
20107
20108
20108
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^4$
$10^3$
$10^2$
10

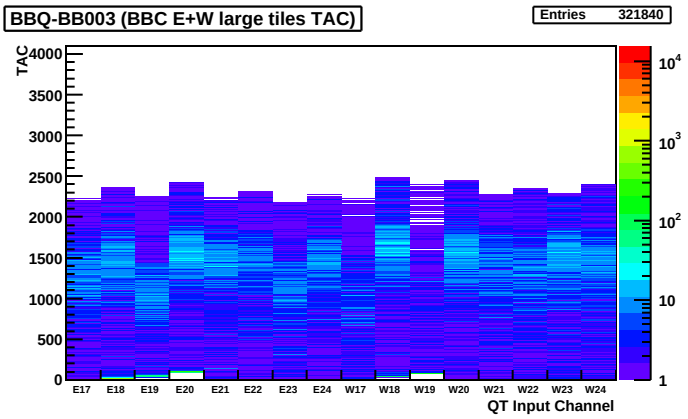

BBQ-BB001 (BBC east small tiles ADC)
Entries 321840
ADC
QT Input Channel

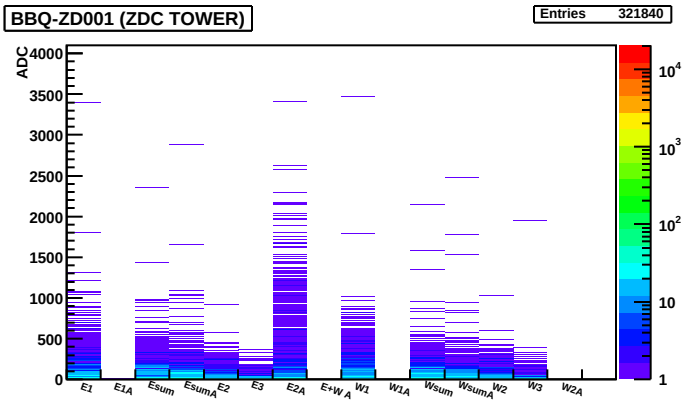

BBQ-BB001 (BBC east small tiles TAC)
Entries 321840
TAC
QT Input Channel

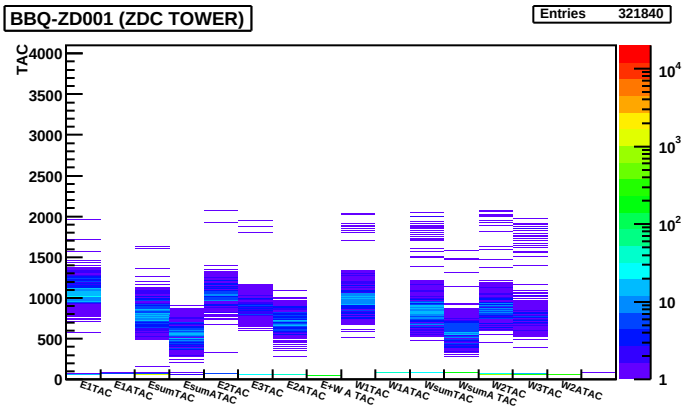

BBQ-BB002 (BBC west small tiles ADC)
Entries 321840
ADC
QT Input Channel

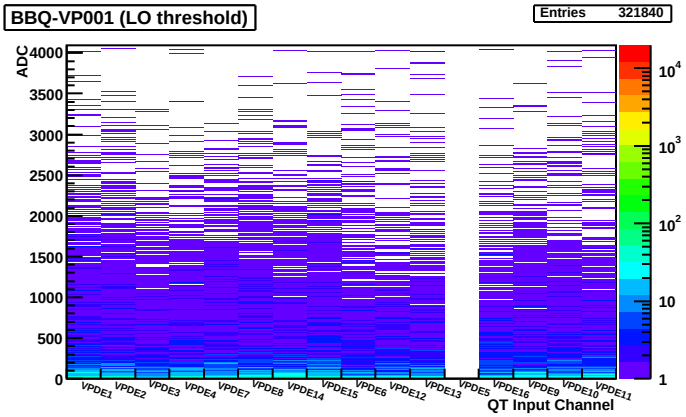

BBQ-BB002 (BBC west small tiles TAC)
Entries 321840
TAC
QT Input Channel

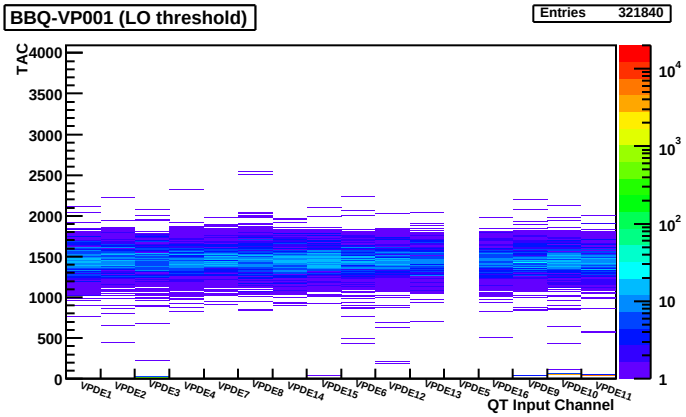

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 321840
ADC
QT Input Channel

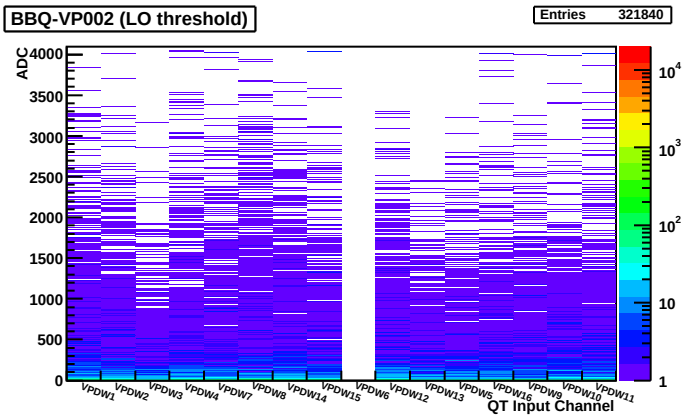

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 321840
TAC
QT Input Channel

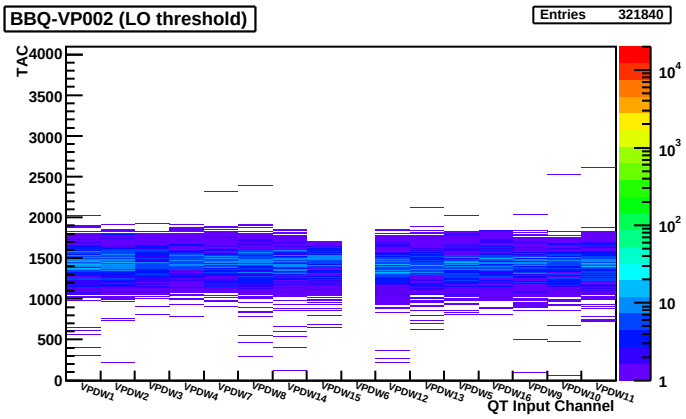

BBQ-ZD001 (ZDC TOWER)
Entries 321840
ADC

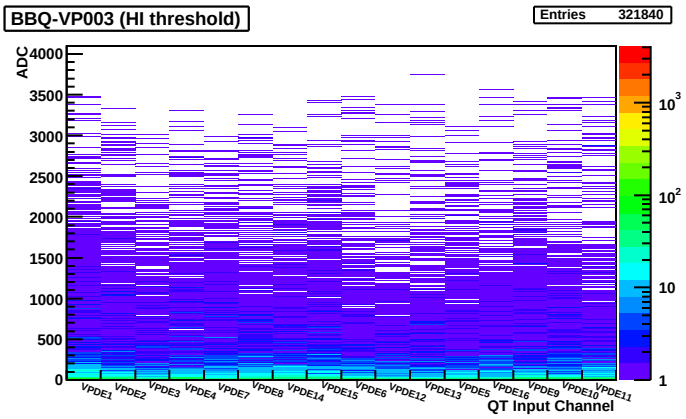

BBQ-ZD001 (ZDC TOWER)
Entries 321840
TAC

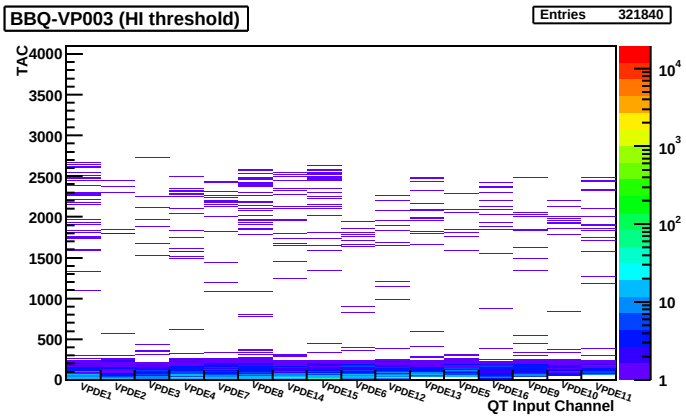

BBQ-VP001 (LO threshold)
Entries 321840
ADC
QT Input Channel

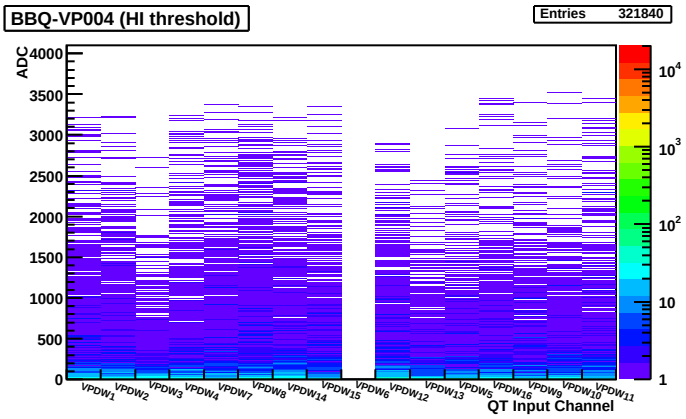

BBQ-VP001 (LO threshold)
Entries 321840
TAC
QT Input Channel

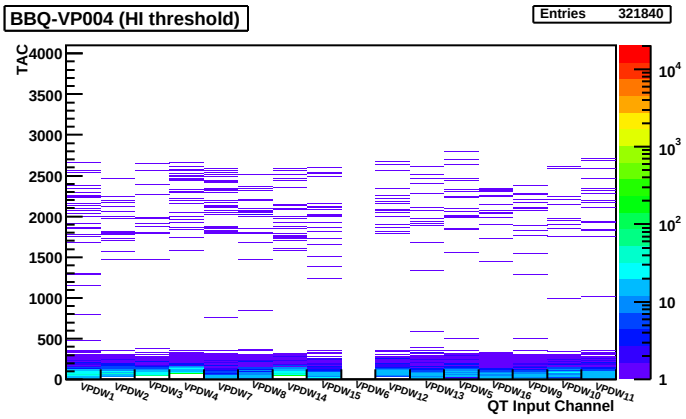

BBQ-VP002 (LO threshold)
Entries 321840
ADC
QT Input Channel

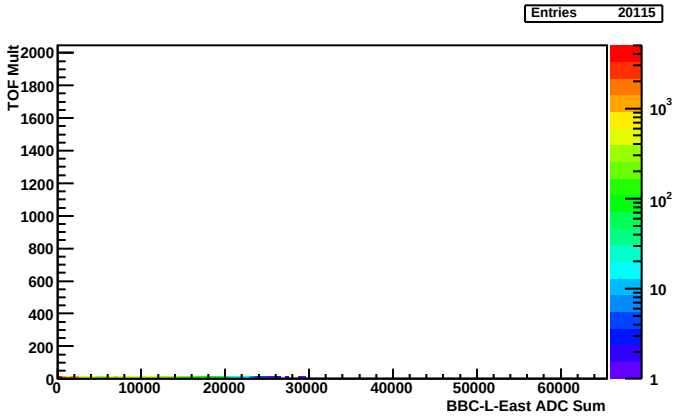

BBQ-VP002 (LO threshold)
Entries 321840
TAC
QT Input Channel

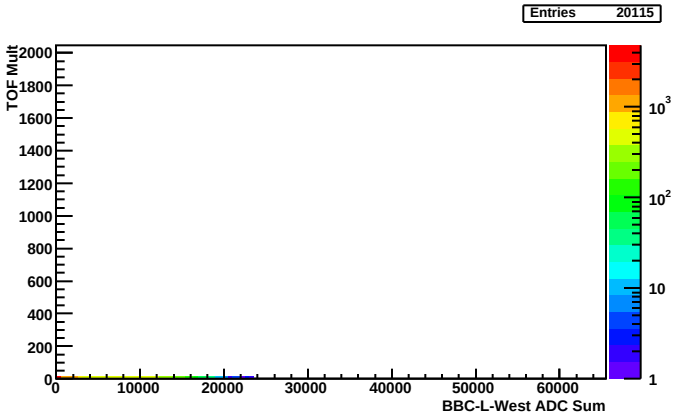

BBQ-VP003 (HI threshold)
Entries 321840
ADC
QT Input Channel

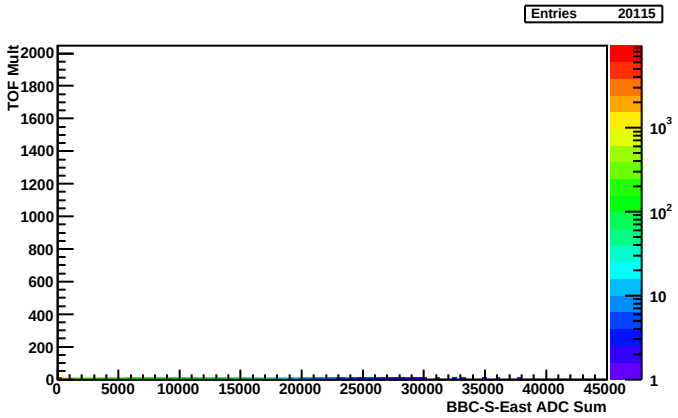

BBQ-VP003 (HI threshold)
Entries 321840
TAC
QT Input Channel

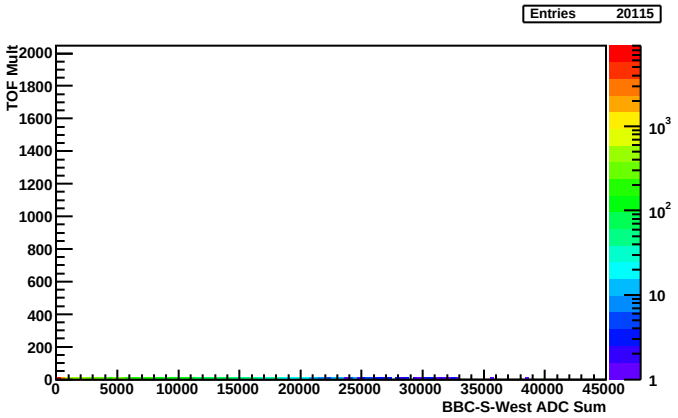

BBQ-VP004 (HI threshold)
Entries 321840
ADC
QT Input Channel

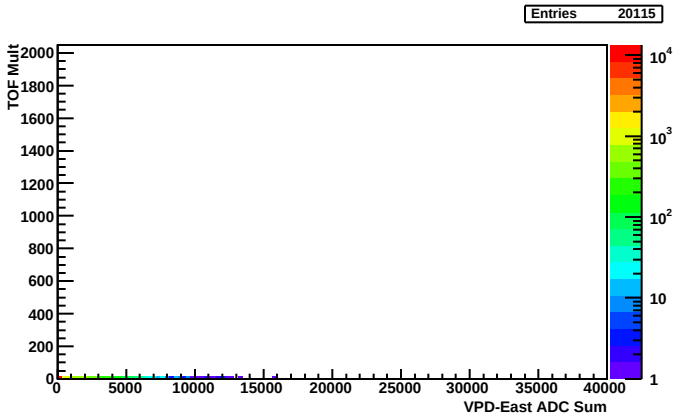

BBQ-VP004 (HI threshold)
Entries 321840
TAC
QT Input Channel

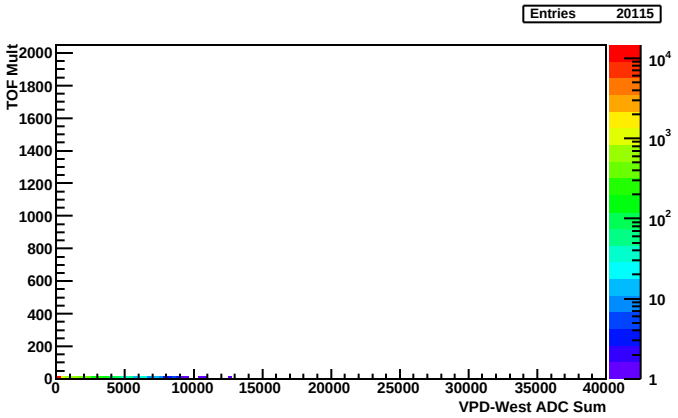

Entries 20115
TOF Mult
BBC-L-East ADC Sum

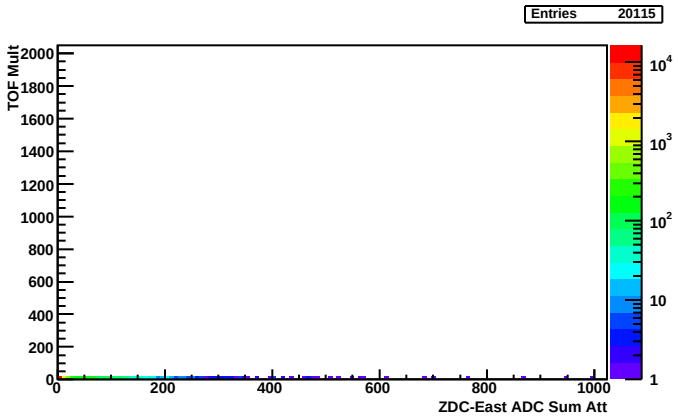

Entries 20115
TOF Mult
BBC-L-West ADC Sum

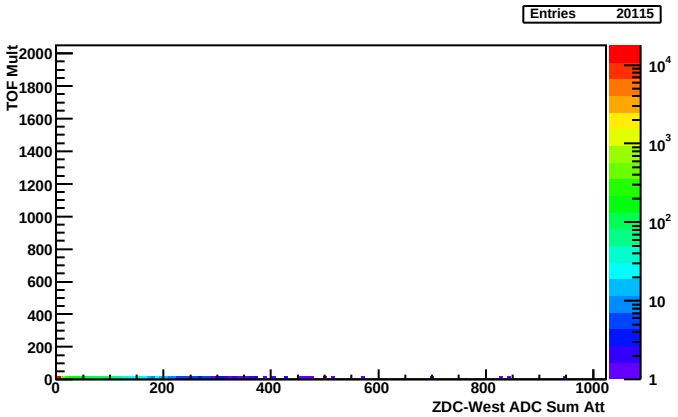

Entries 20115
TOF Mult
BBC-S-East ADC Sum

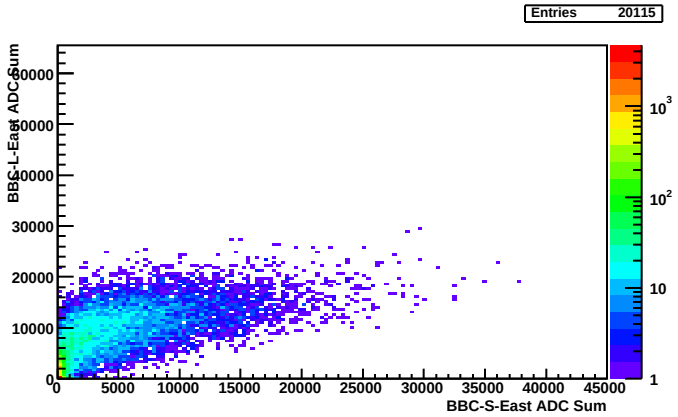

Entries 20115
TOF Mult
BBC-S-West ADC Sum

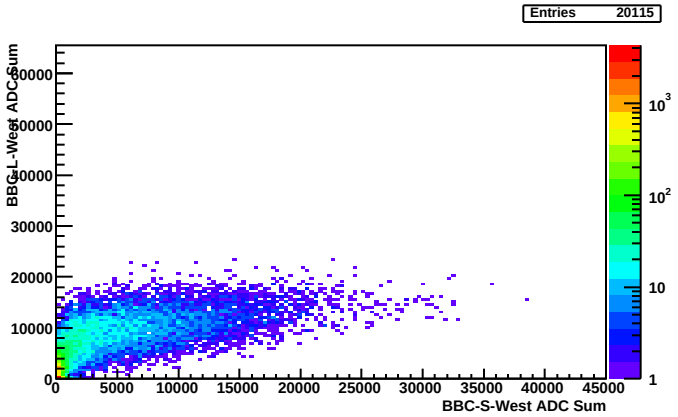

Entries 20115
TOF Mult
VPD-East ADC Sum

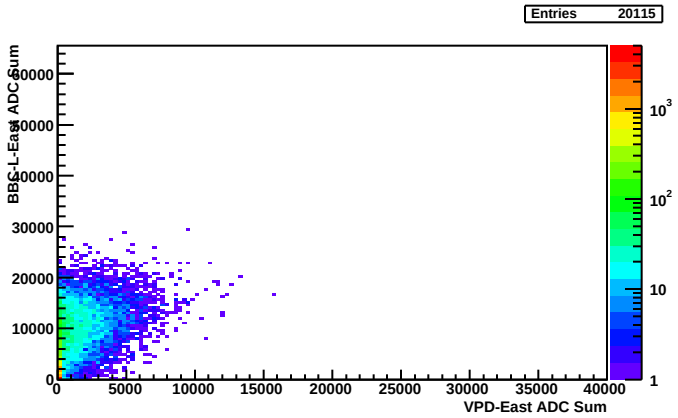

Entries 20115
TOF Mult
VPD-West ADC Sum

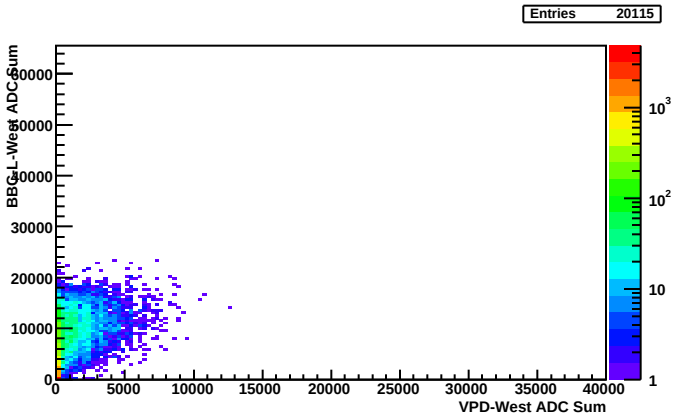

Entries 20115
TOF Mult
ZDC-East ADC Sum Att

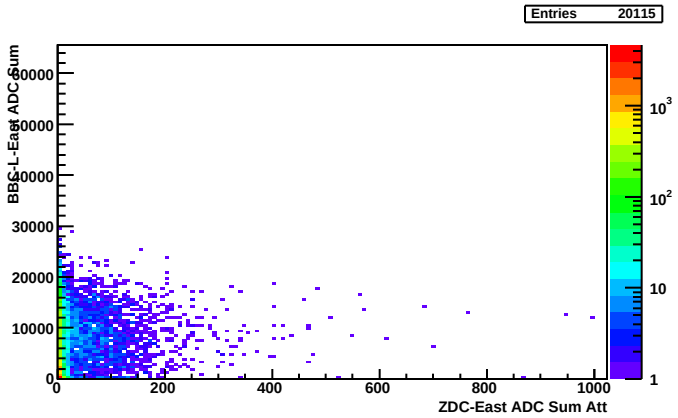

Entries 20115
TOF Mult
ZDC-West ADC Sum Att

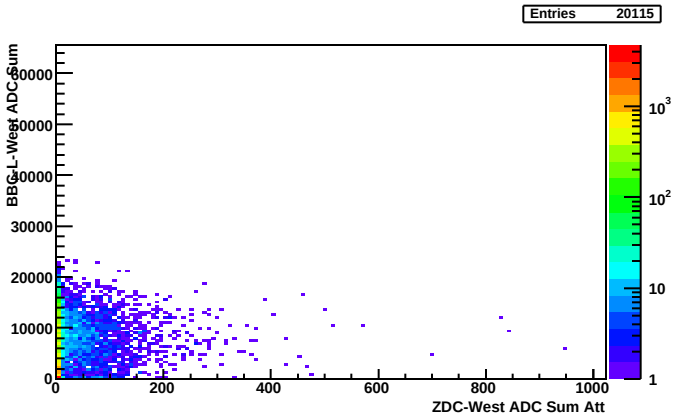

Entries 20115
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 20115
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 20115
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 20115
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 20115
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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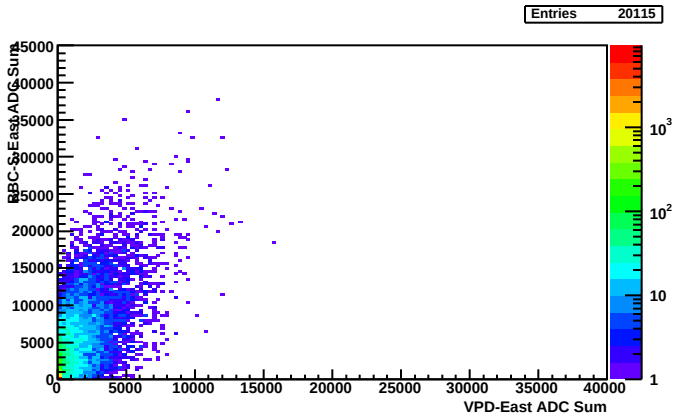
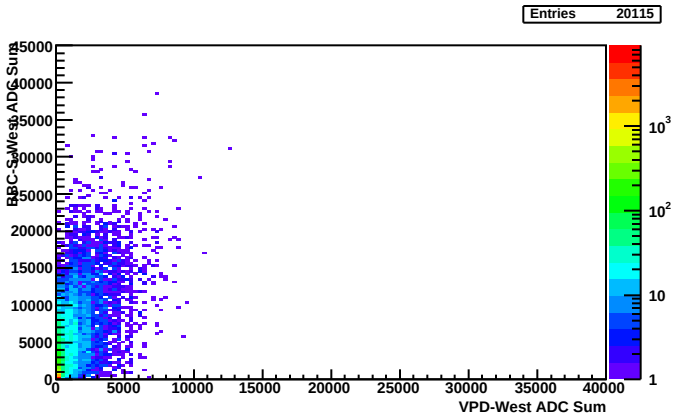
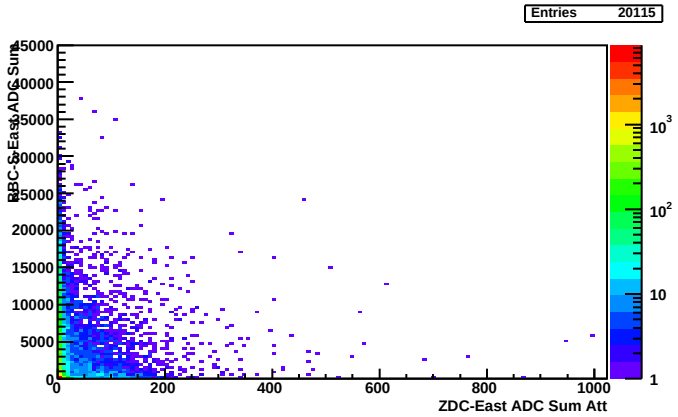
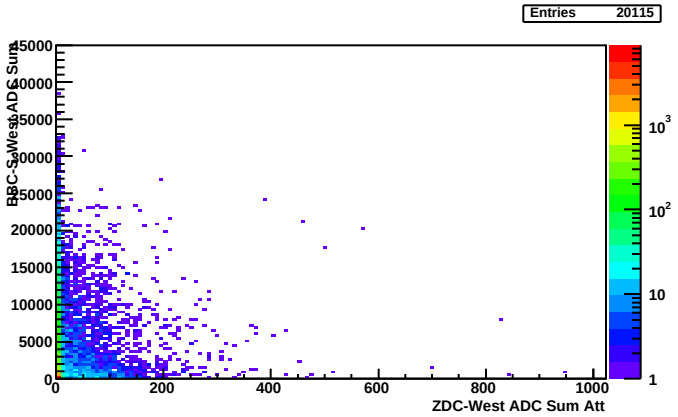
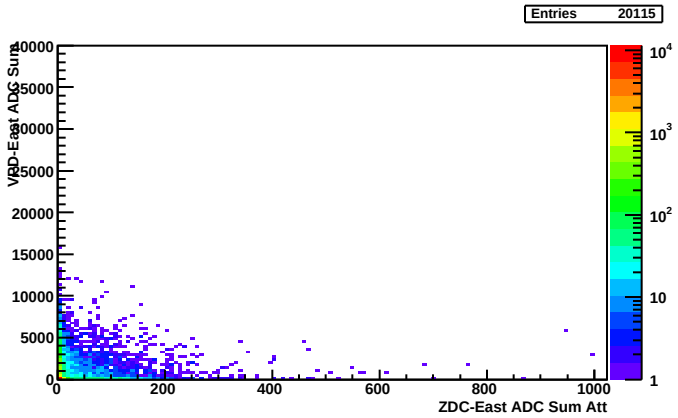
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 20115
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 20115
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 20115
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 20115
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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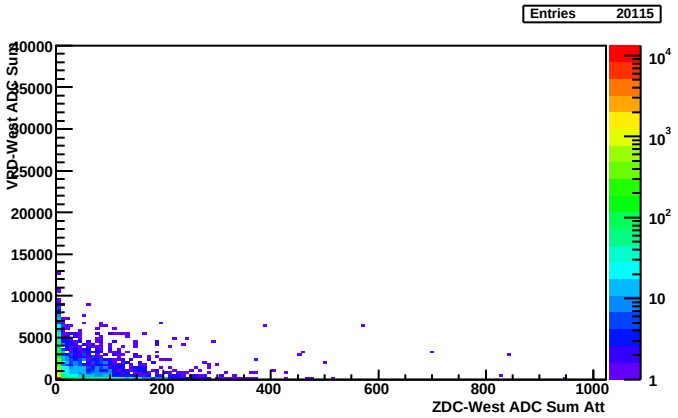
VPD-East ADC Sum
ZDC-East ADC Sum Att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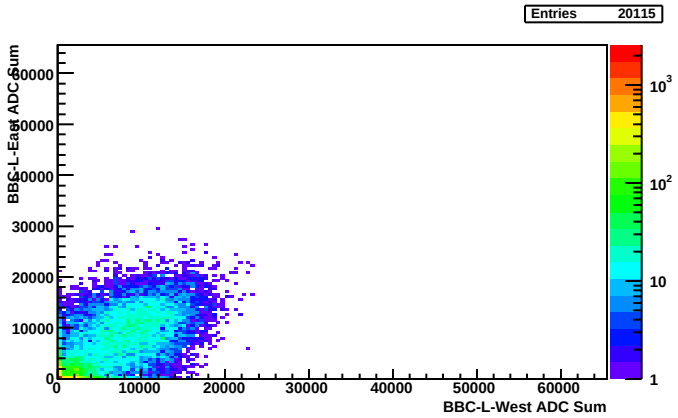
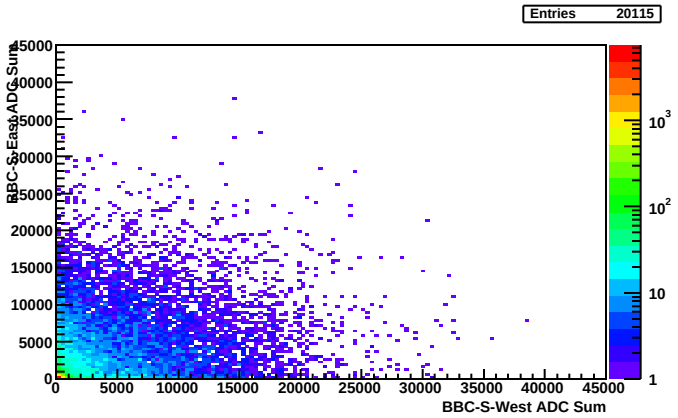
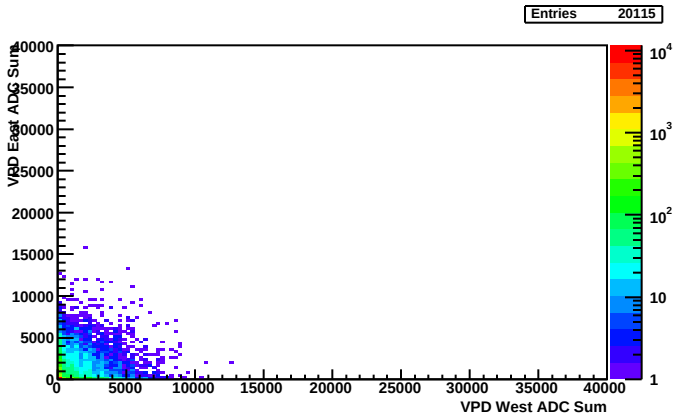
ZDC-West ADC Sum Att


Entries 20115
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 20115
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 20115
VPD East ADC Sum
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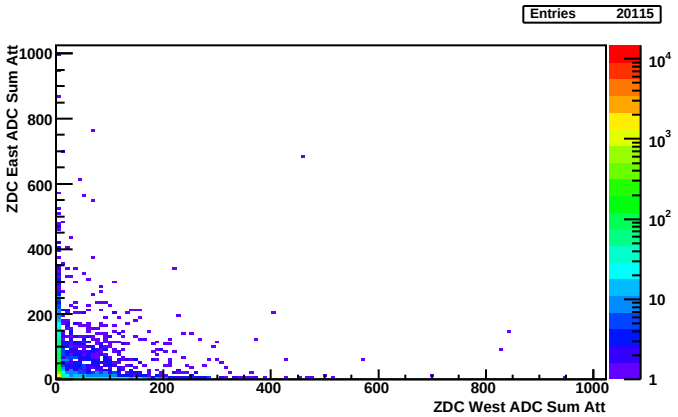
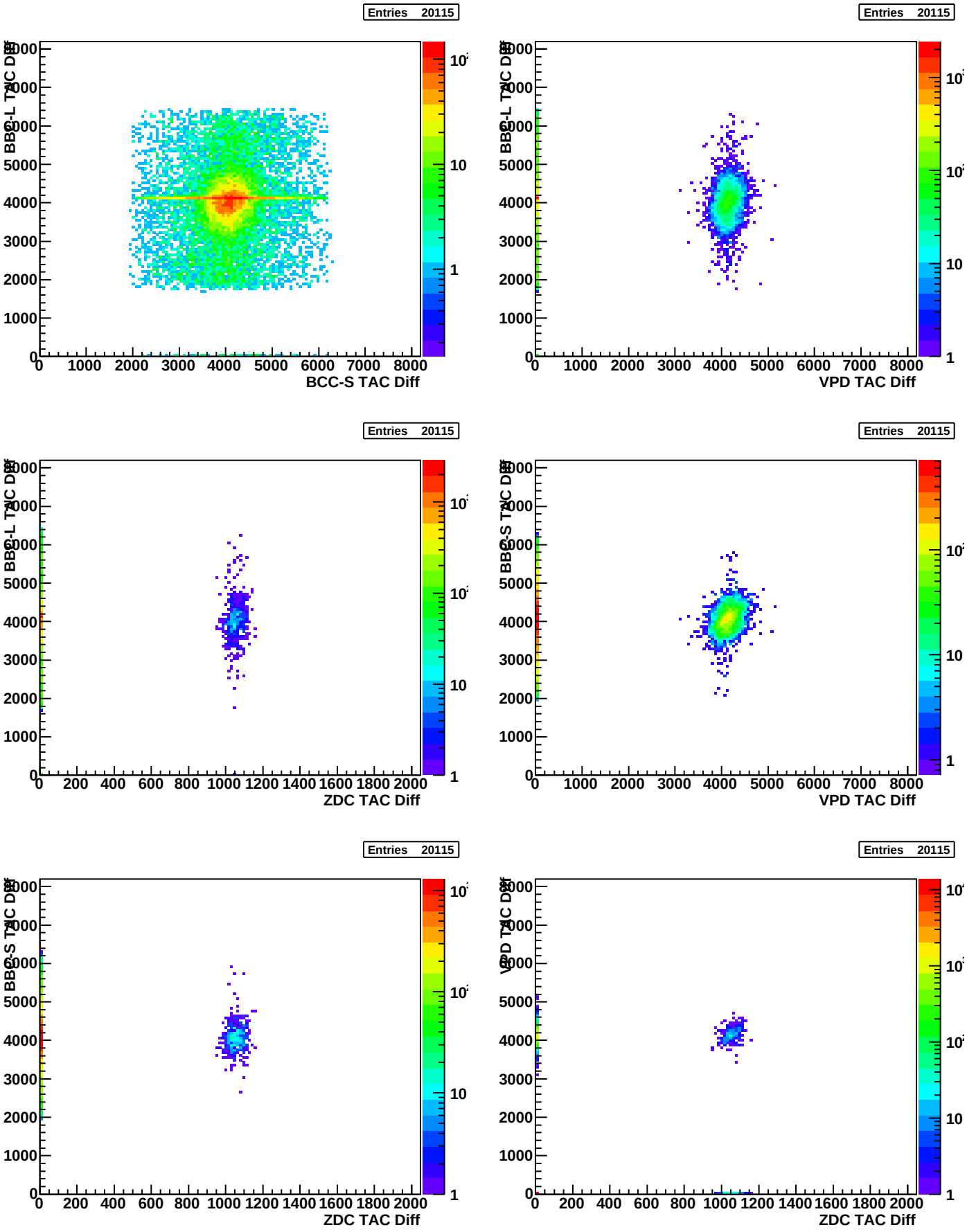
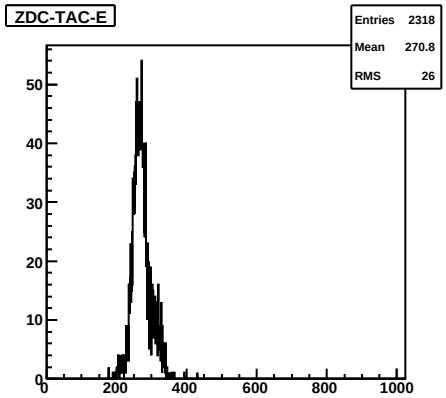
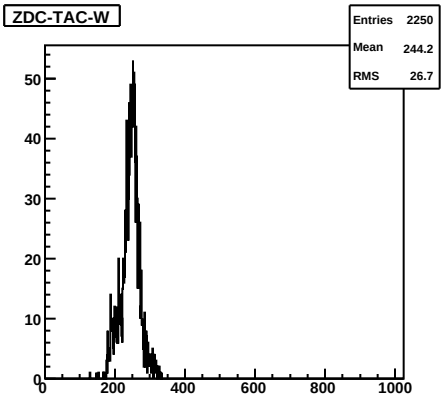
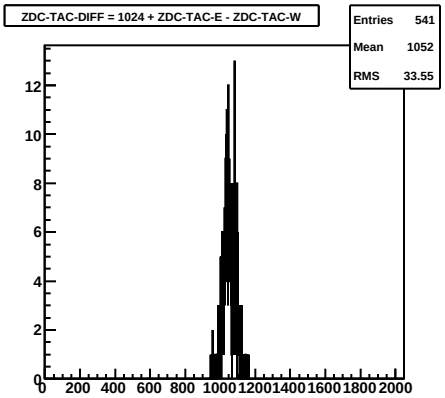
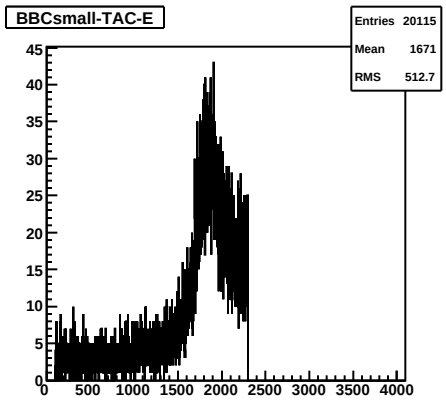

Entries 20115
ZDC East ADC Sum Att
ZDC West ADC Sum Att

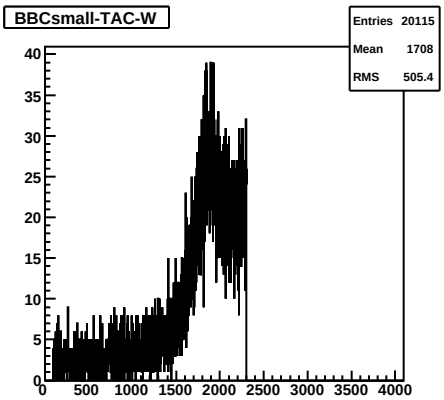

Entries 20115
BBC-L TAC Diff
BCC-S TAC Diff
Entries 20115
BBC-L TAC Diff
VPD TAC Diff
Entries 20115
BBC-L TAC Diff
ZDC TAC Diff
Entries 20115
BBC-S TAC Diff
VPD TAC Diff
Entries 20115
BBC-S TAC Diff
ZDC TAC Diff
Entries 20115
VPD TAC Diff
ZDC TAC Diff

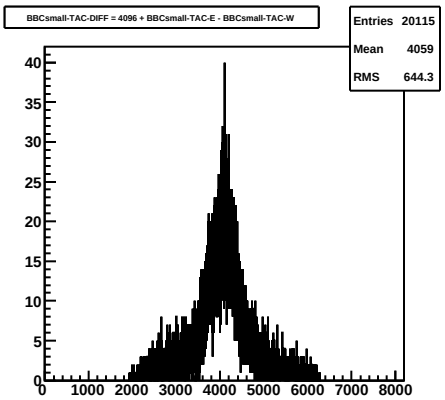

ZDC-TAC-E
Entries 2318
Mean 270.8
RMS 26

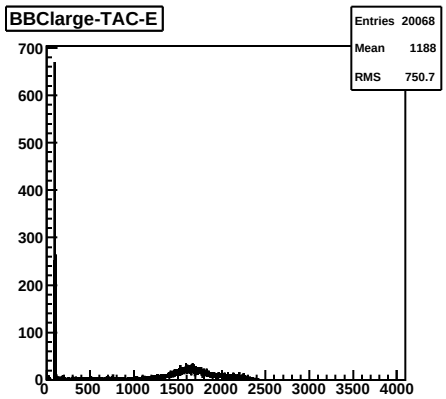

ZDC-TAC-W
Entries 2250
Mean 244.2
RMS 26.7

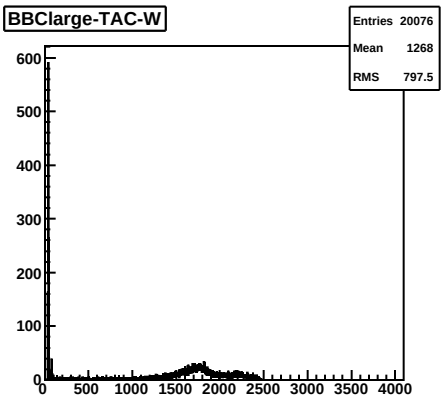

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 541
Mean 1052
RMS 33.55

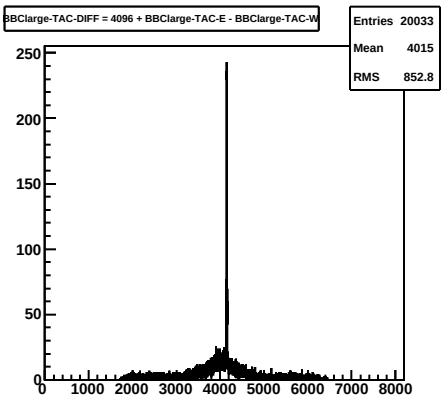

BBCsmall-TAC-E
Entries 20115
Mean 1671
RMS 512.7

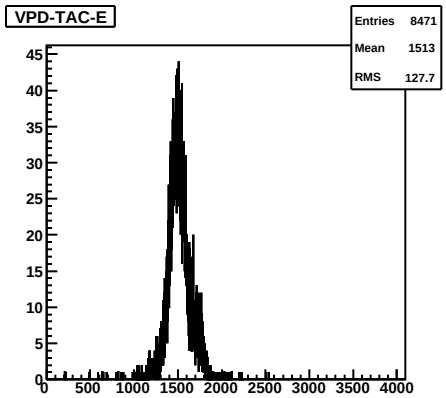

BBCsmall-TAC-W
Entries 20115
Mean 1708
RMS 505.4

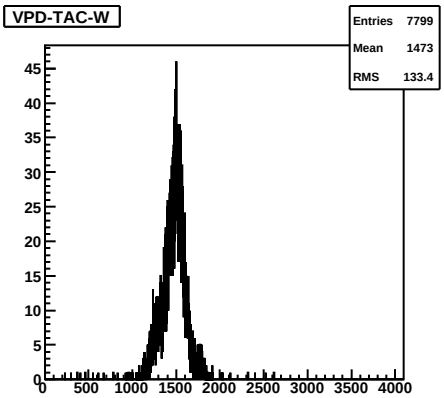

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 20115
Mean 4059
RMS 644.3

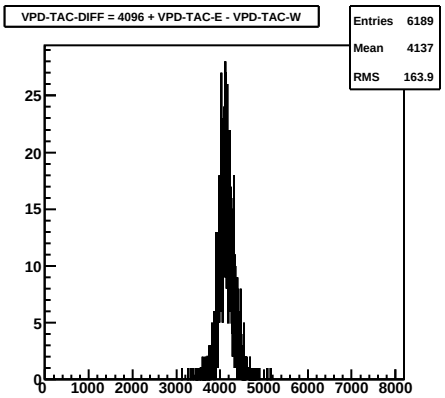

BBClarge-TAC-E
Entries 20068
Mean 1188
RMS 750.7

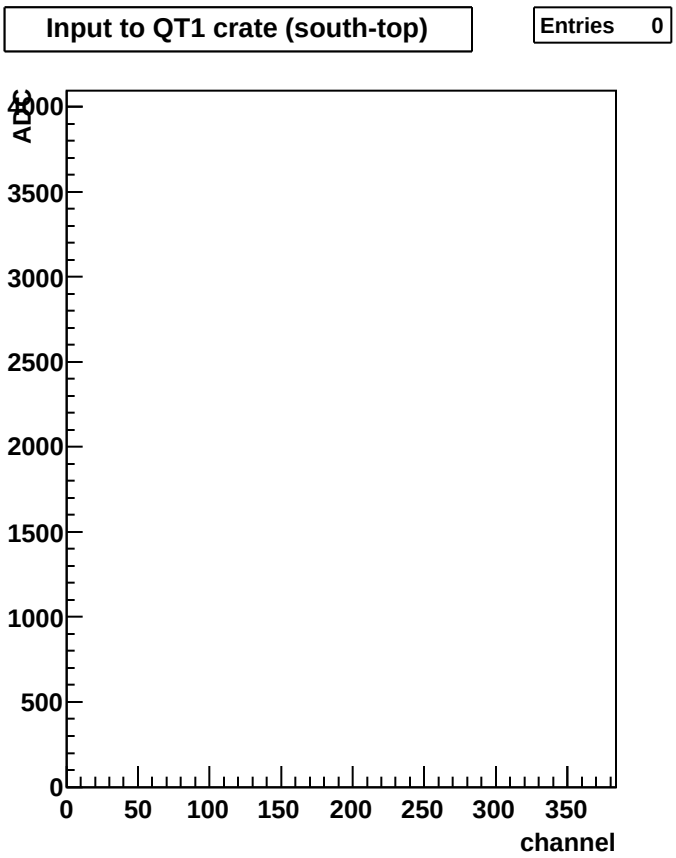

BBClarge-TAC-W
Entries 20076
Mean 1268
RMS 797.5

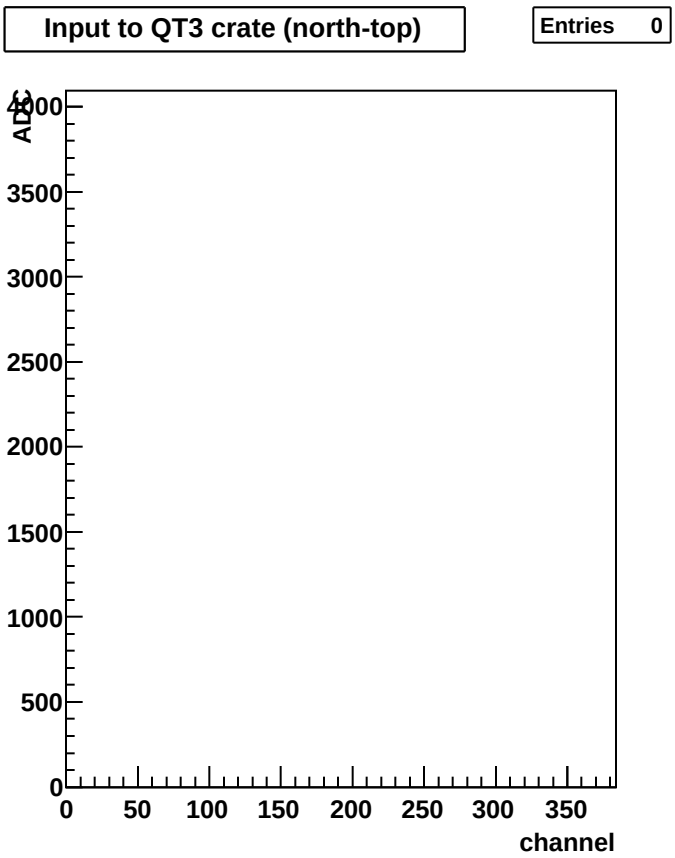

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 20033
Mean 4015
RMS 852.8

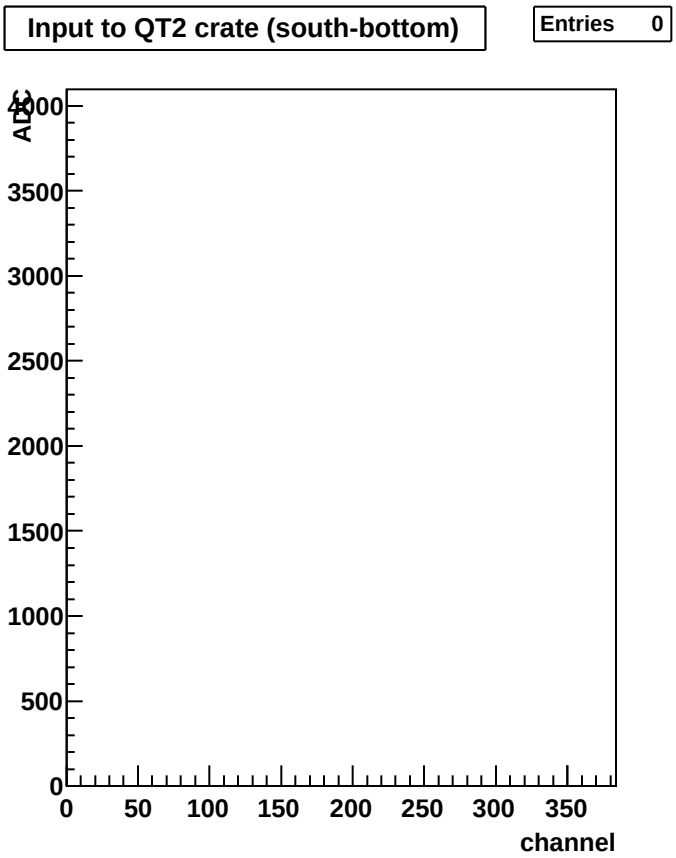

VPD-TAC-E
Entries 8471
Mean 1513
RMS 127.7

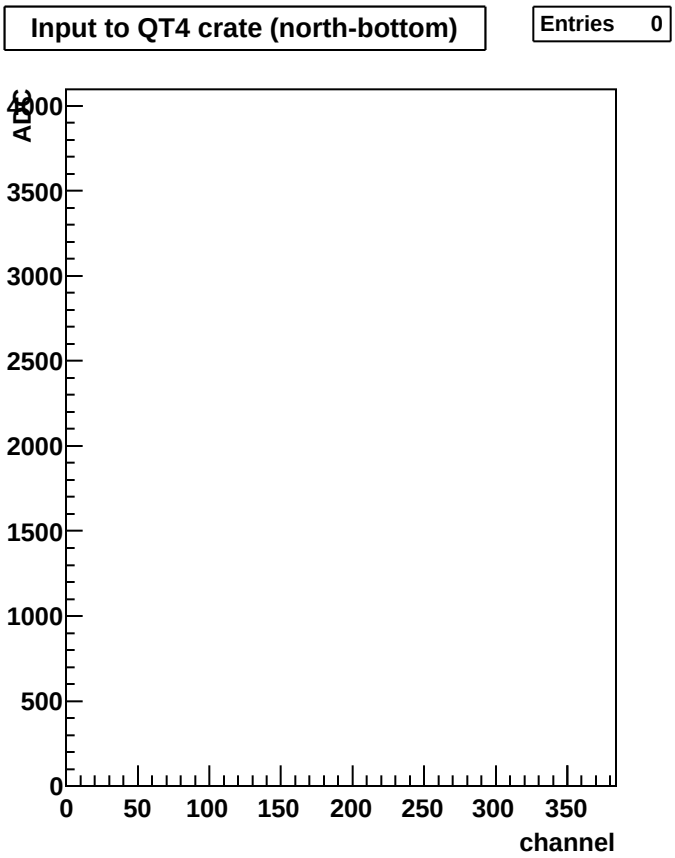

VPD-TAC-W
Entries 7799
Mean 1473
RMS 133.4

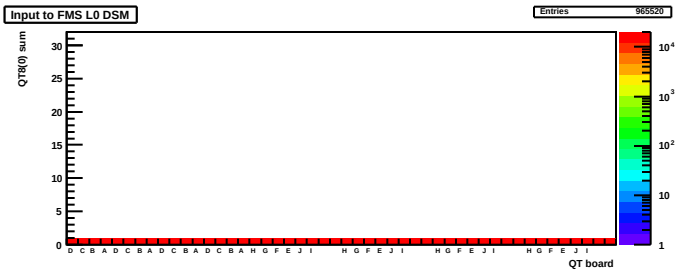

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 6189
Mean 4137
RMS 163.9

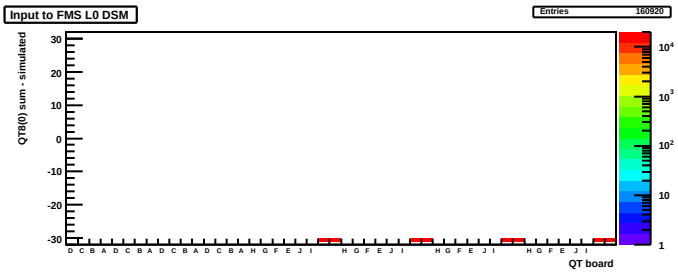

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

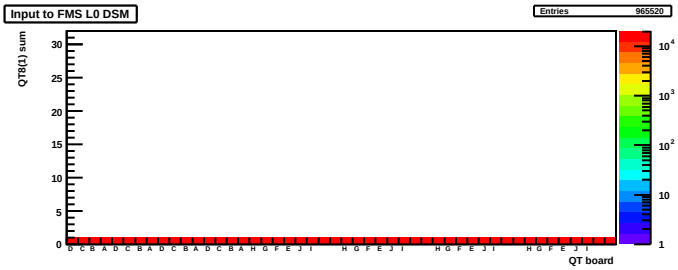

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

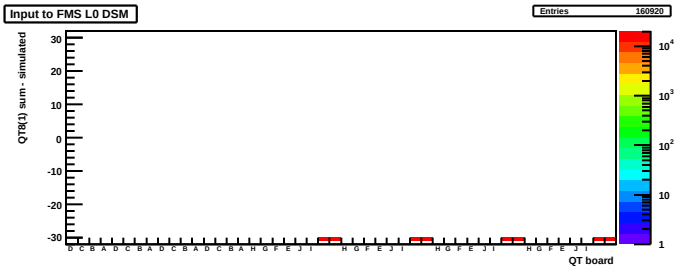

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

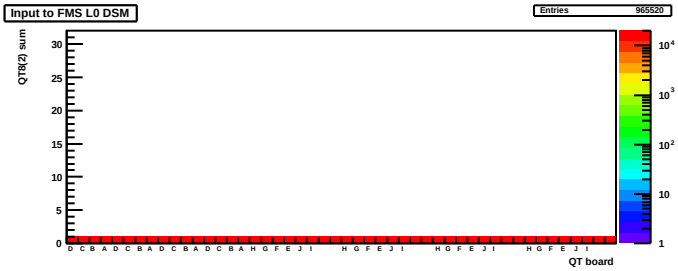

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

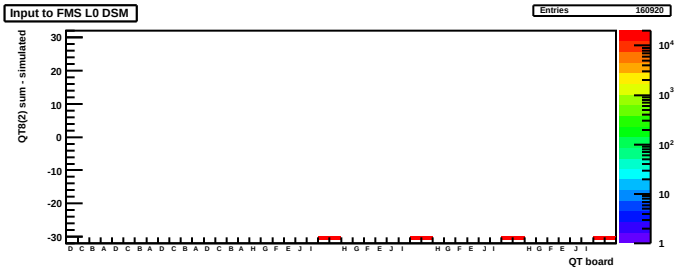

Input to FMS L0 DSM
Entries 965520
QT8(0) sum
QT board

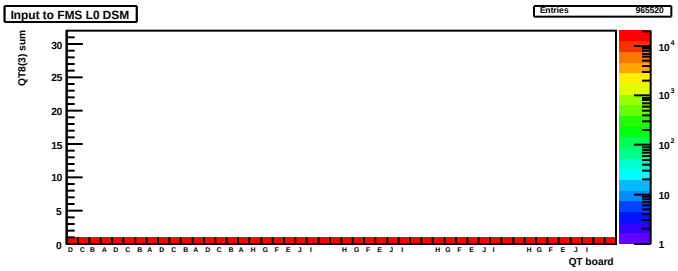

Input to FMS L0 DSM
Entries 160920
QT8(0) sum - simulated
QT board

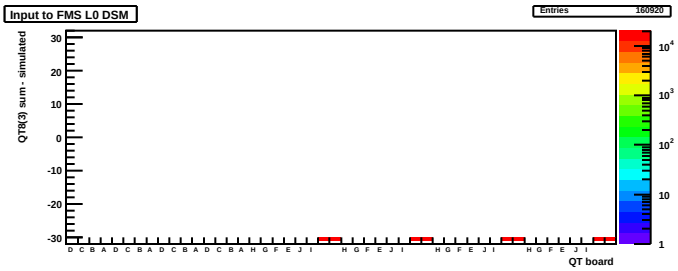

Input to FMS L0 DSM
Entries 965520
QT8(1) sum
QT board

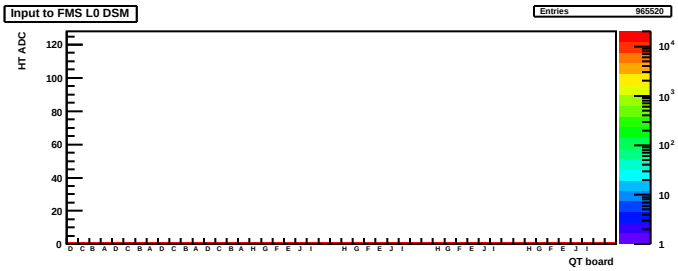

Input to FMS L0 DSM
Entries 160920
QT8(1) sum - simulated
QT board

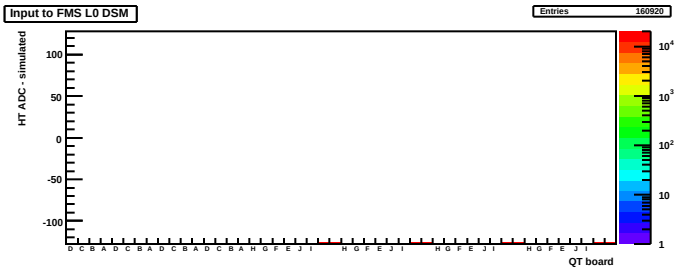

Input to FMS L0 DSM
Entries 965520
QT8(2) sum
QT board

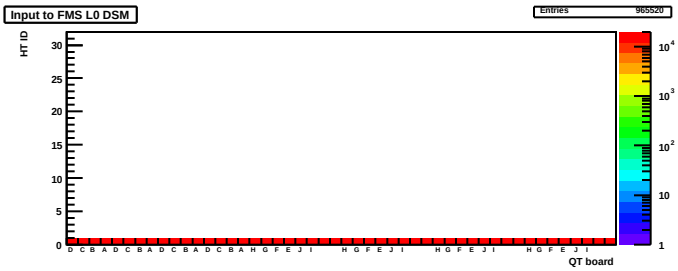

Input to FMS L0 DSM
Entries 160920
QT8(2) sum - simulated
QT board

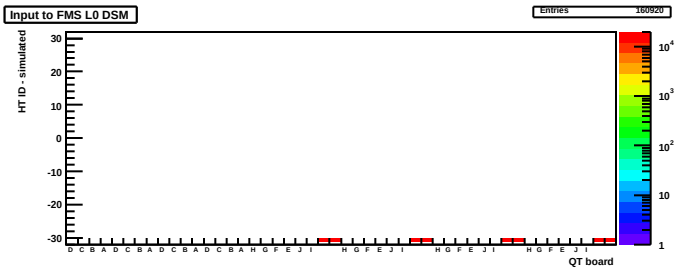

Input to FMS L0 DSM
Entries 965520
QT8(3) sum
QT board

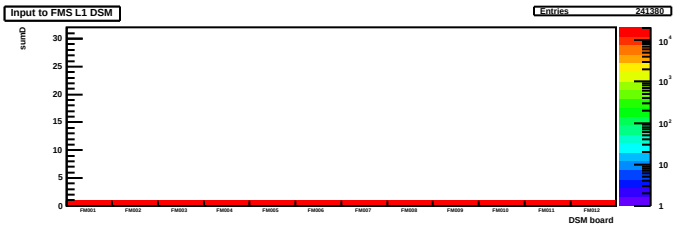

Input to FMS L0 DSM
Entries 160920
QT8(3) sum - simulated
QT board

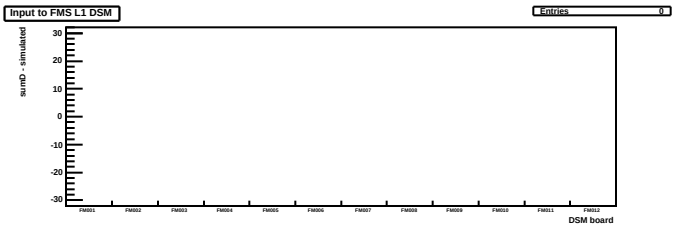

Input to FMS L0 DSM
Entries 965520
HT ADC
QT board

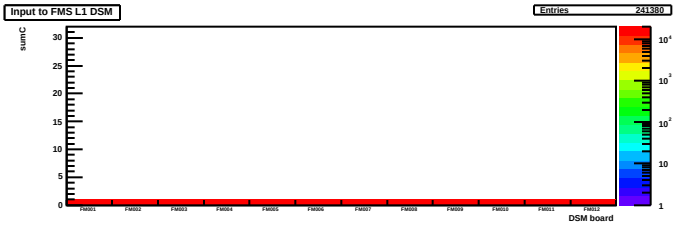

Input to FMS L0 DSM
Entries 160920
HT ADC - simulated
QT board

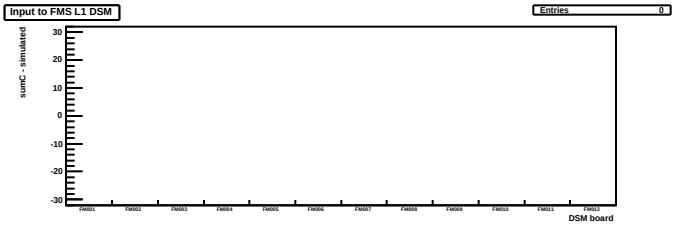

Input to FMS L0 DSM
Entries 965520
HT ID
QT board

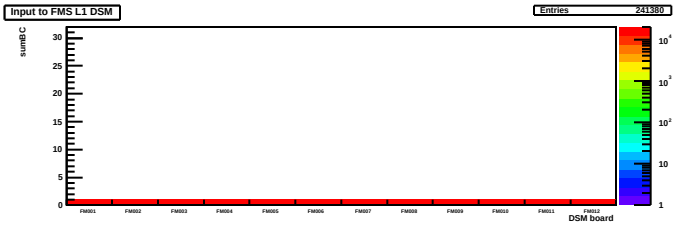

Input to FMS L0 DSM
Entries 160920
HT ID - simulated
QT board

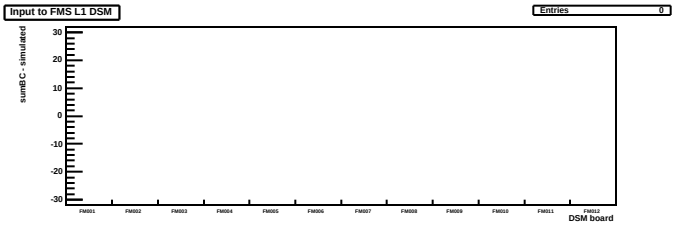

Input to FMS L1 DSM
Entries 241380
sumD
DSM board

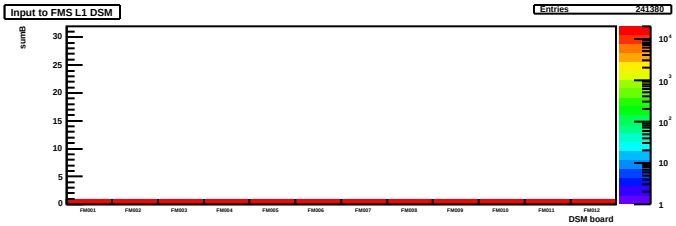

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

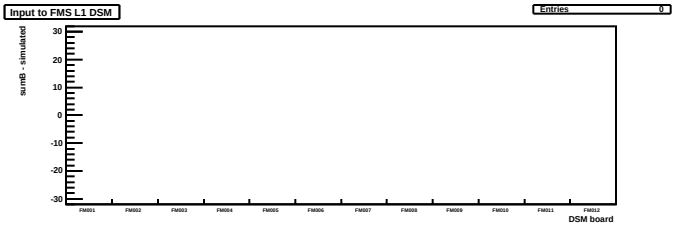

Input to FMS L1 DSM
Entries 241380
sumC
DSM board

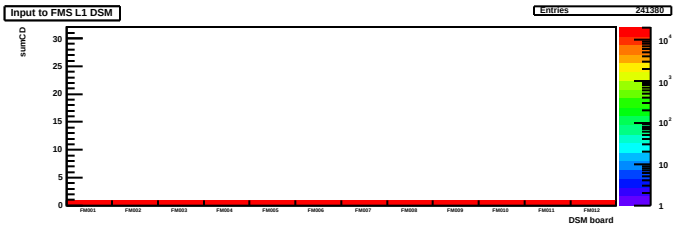

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

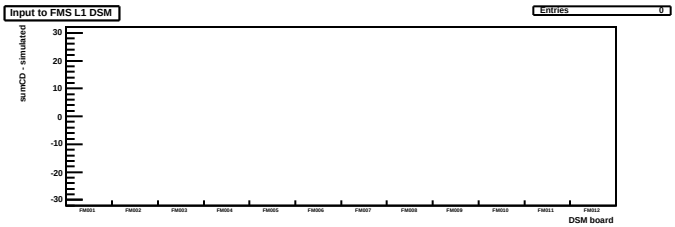

Input to FMS L1 DSM
Entries 241380
sumBC
DSM board

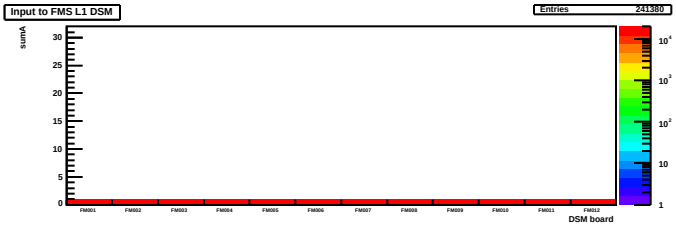

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

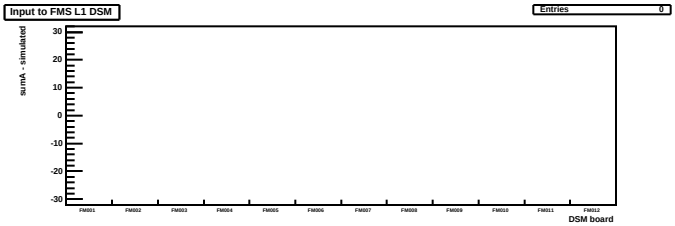

Input to FMS L1 DSM
Entries 241380
sumB
DSM board

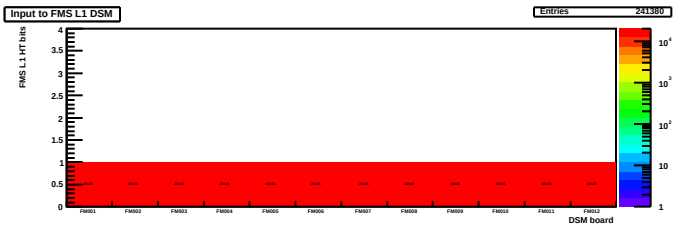

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

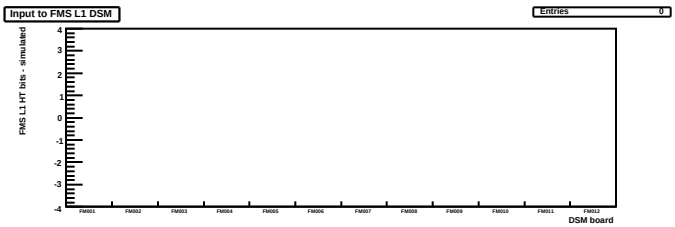

Input to FMS L1 DSM
Entries 241380
sumCD
DSM board

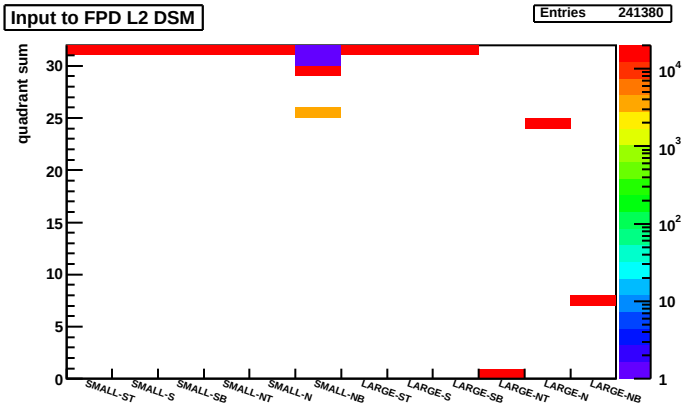

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

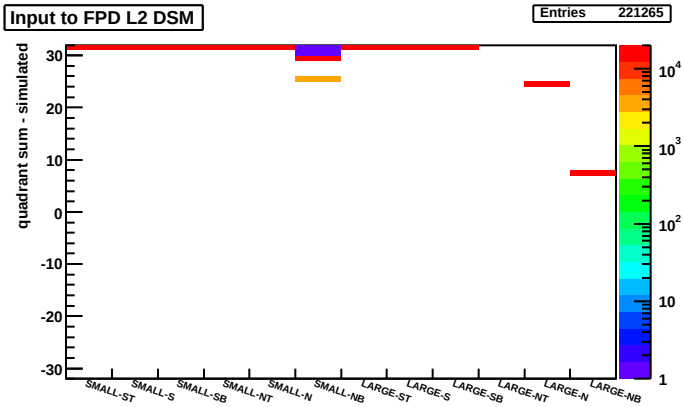

Input to FMS L1 DSM
Entries 241380
sumA
DSM board

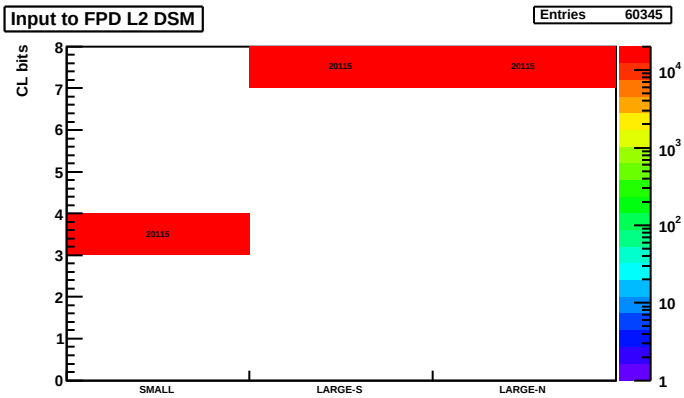

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

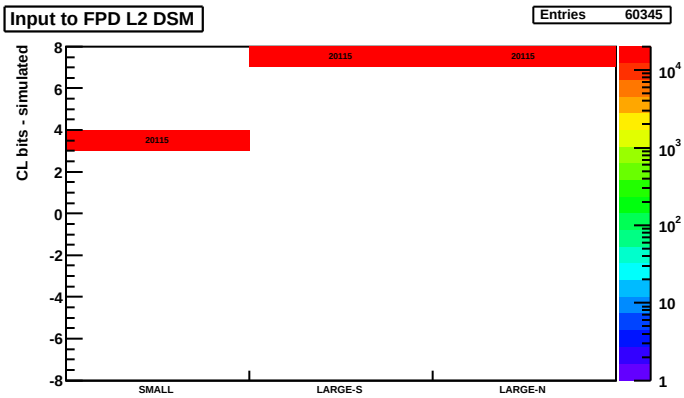

Input to FMS L1 DSM
Entries 241380
FMS L1 HT bits
DSM board

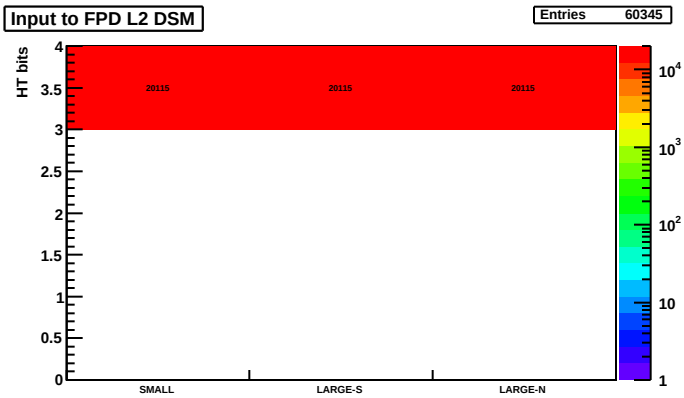

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

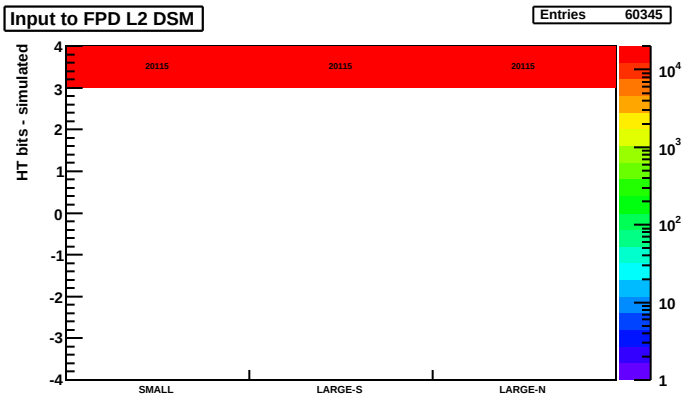

Input to FPD L2 DSM
Entries 241380
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

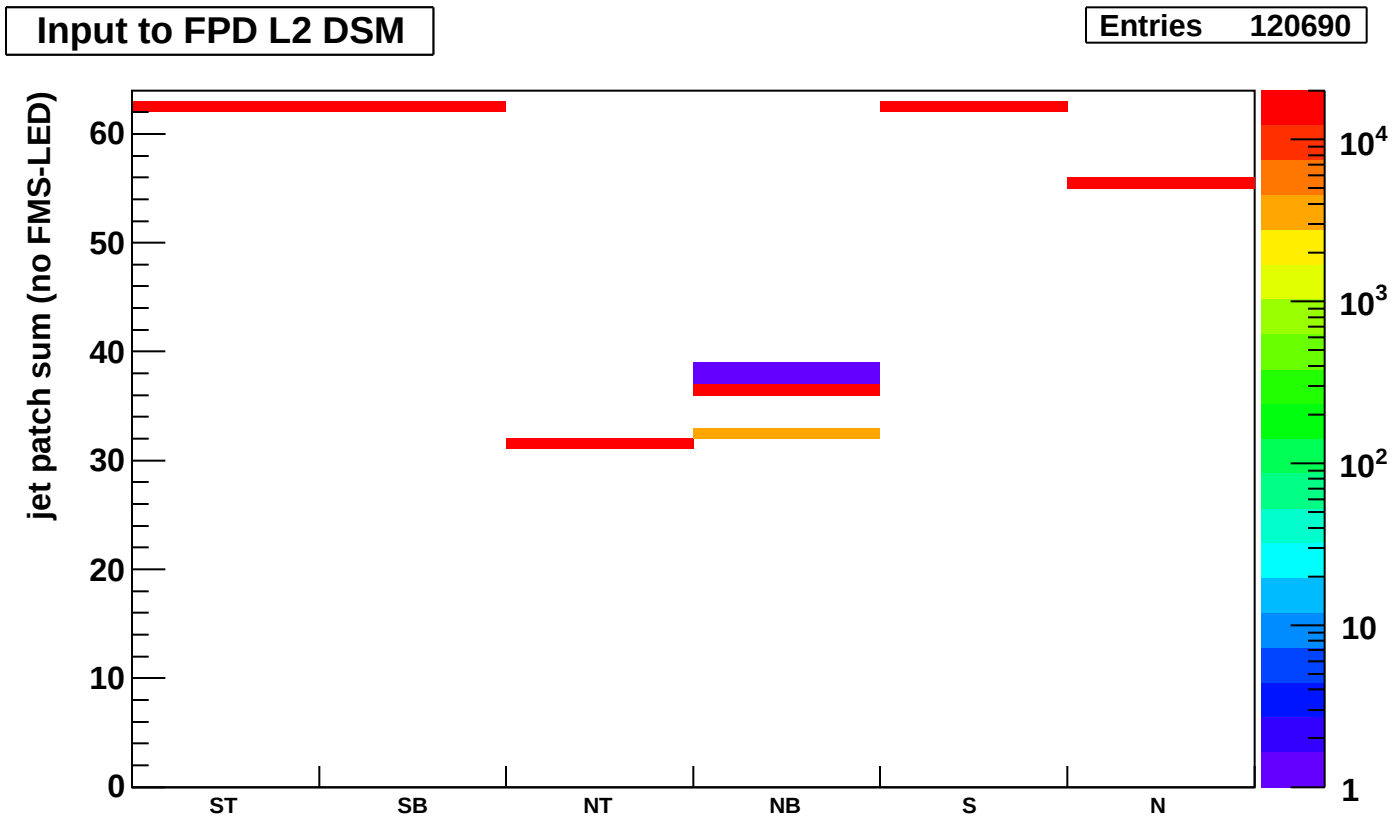

Input to FPD L2 DSM
Entries 221265
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

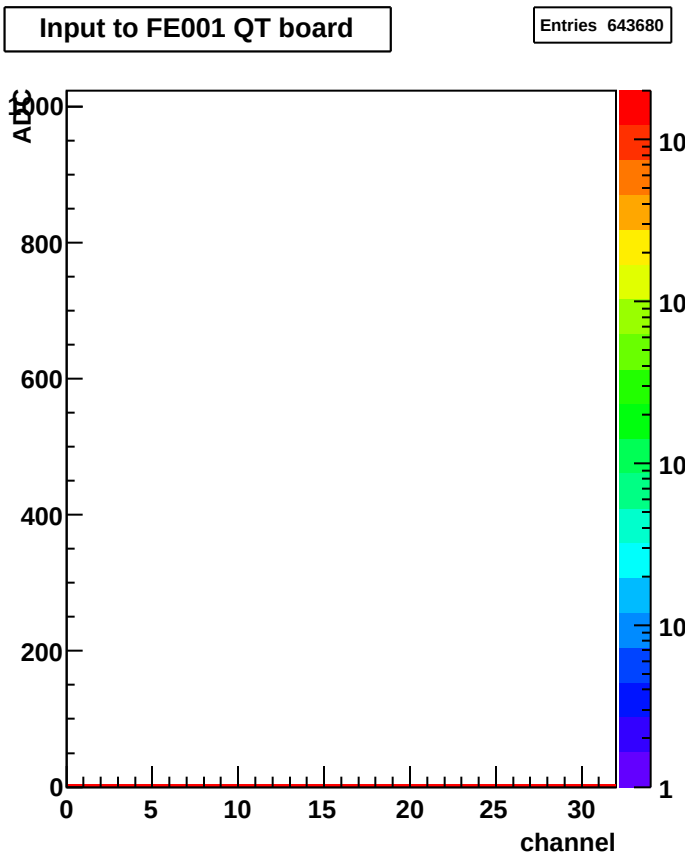

Input to FPD L2 DSM
Entries 60345
CL bits
20115
20115
20115
SMALL
LARGE-S
LARGE-N

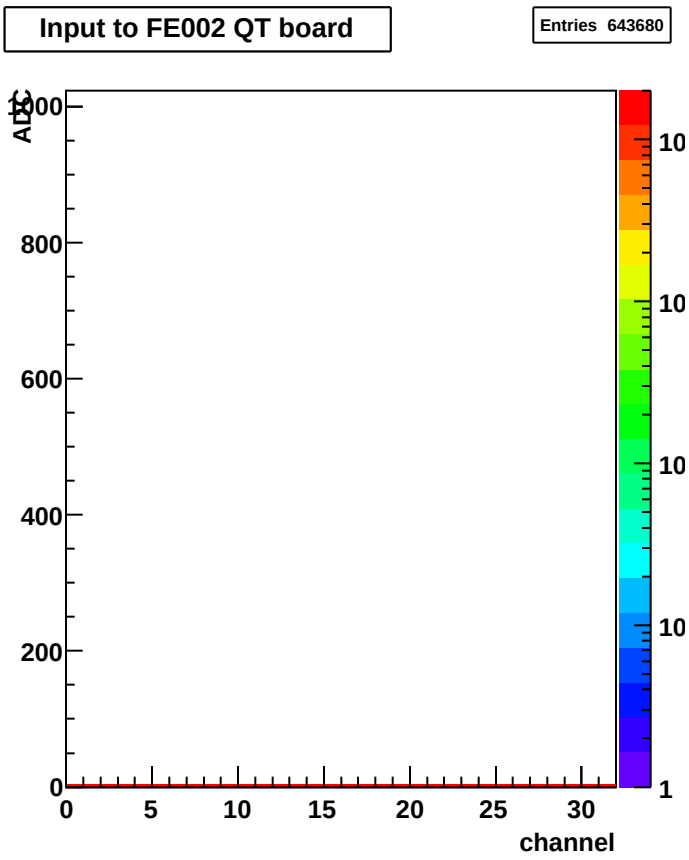

Input to FPD L2 DSM
Entries 60345
CL bits - simulated
20115
20115
20115
SMALL
LARGE-S
LARGE-N

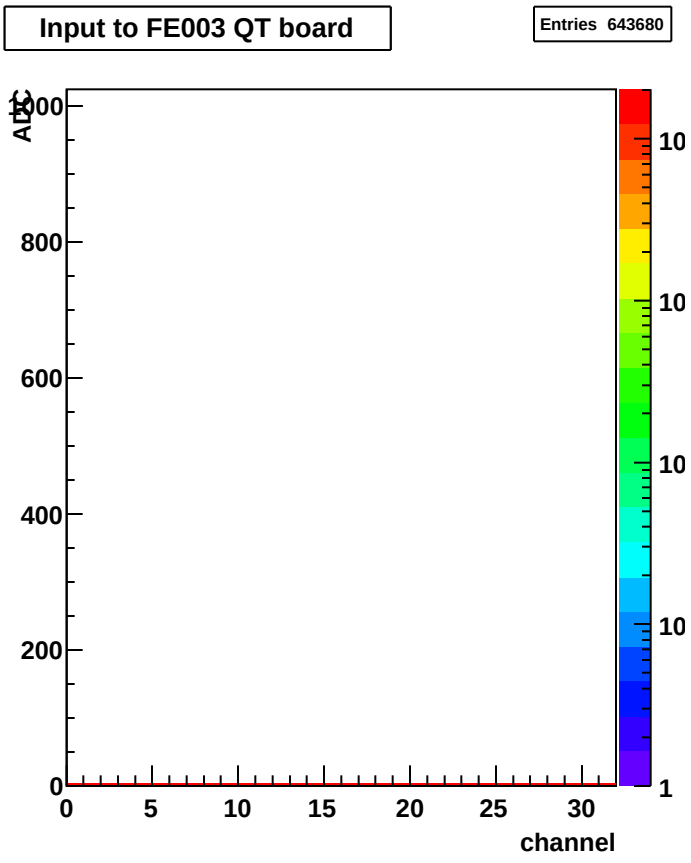

Input to FPD L2 DSM
Entries 60345
HT bits
20115
20115
20115
SMALL
LARGE-S
LARGE-N

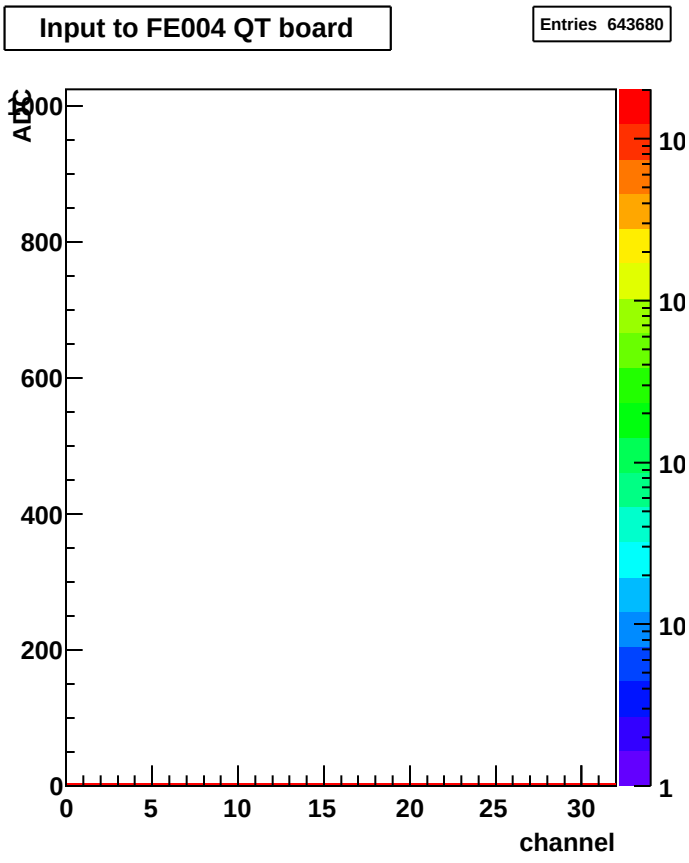

Input to FPD L2 DSM
Entries 60345
HT bits - simulated
20115
20115
20115
SMALL
LARGE-S
LARGE-N

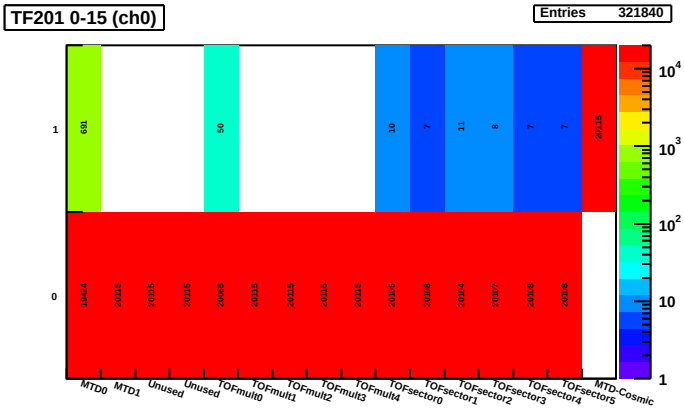

Input to FPD L2 DSM
Entries 120690
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
$10^2$
$10^3$
$10^4$

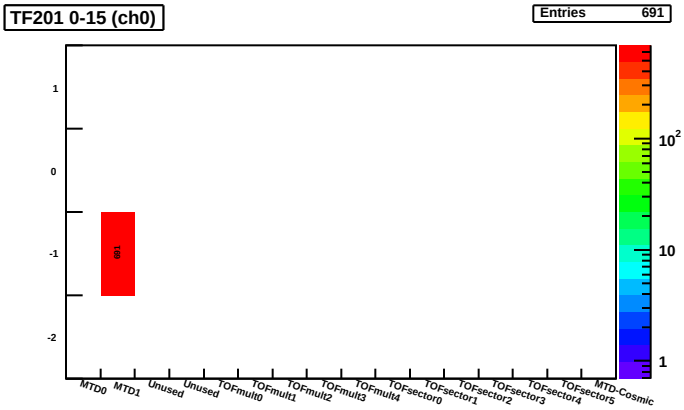

Input to FE001 QT board
Entries 643680
ADC
channel

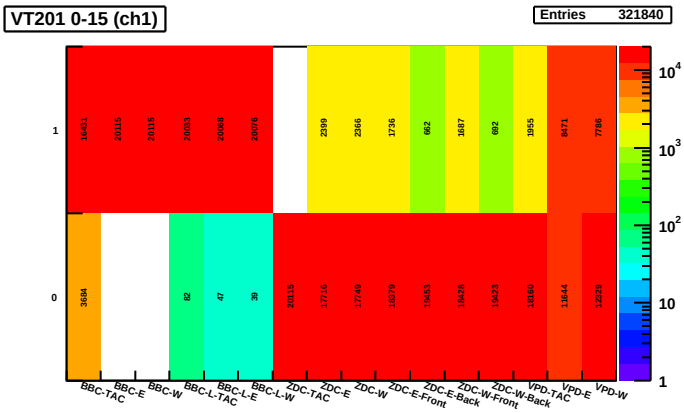

Input to FE002 QT board
Entries 643680
ADC
channel

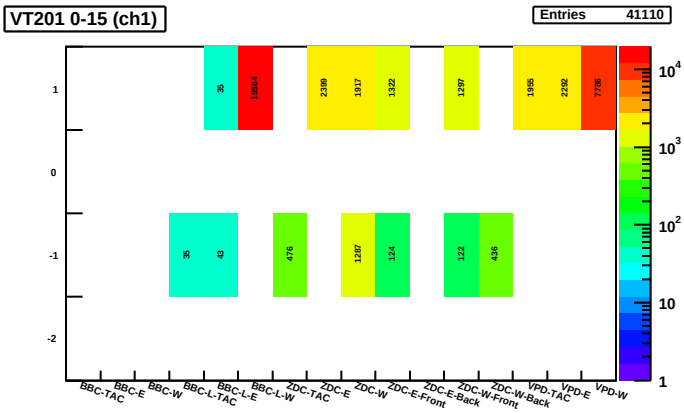

Input to FE003 QT board
Entries 643680
ADC
channel

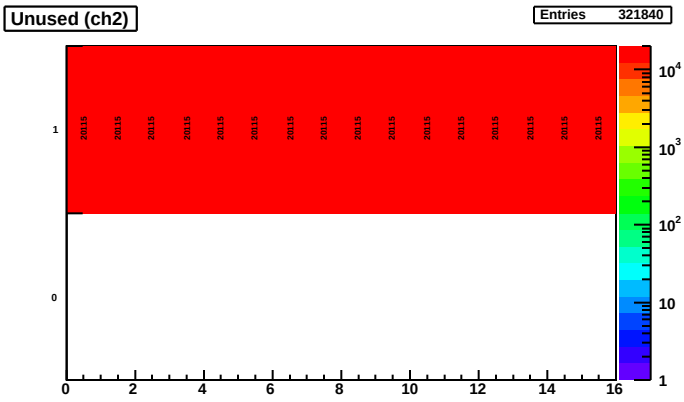

Input to FE004 QT board
Entries 643680
ADC
channel

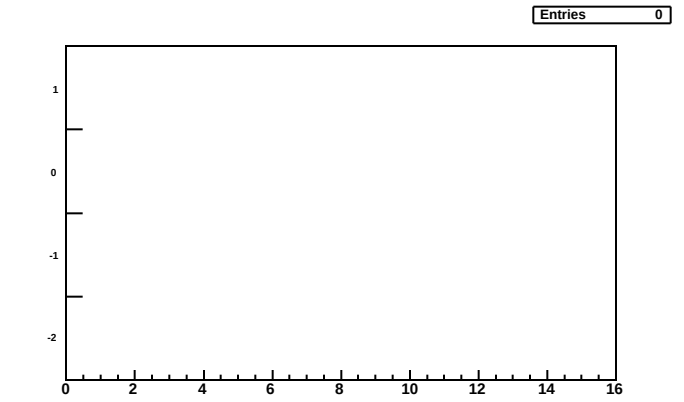

TF201 0-15 (ch0)
Entries 321840
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

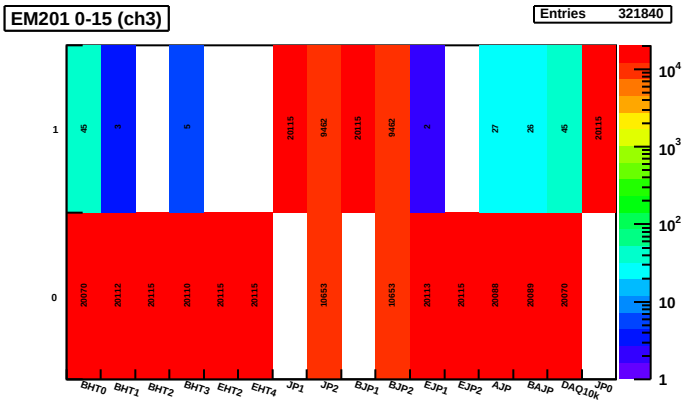

TF201 0-15 (ch0)
Entries 691
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

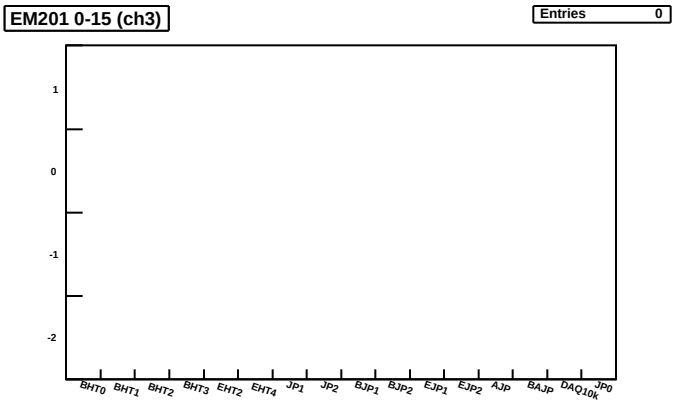

VT201 0-15 (ch1)
Entries 321840
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

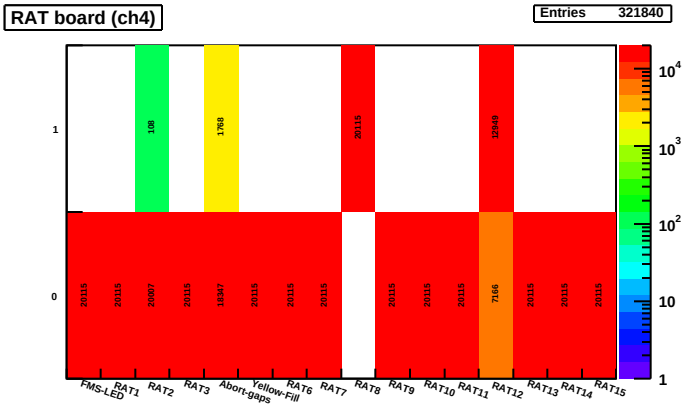

VT201 0-15 (ch1)
Entries 41110
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

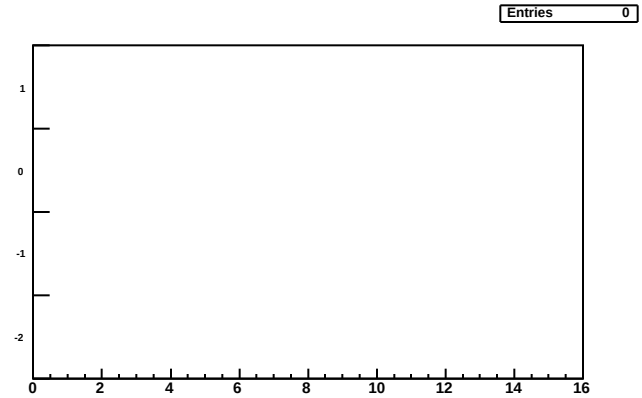

Unused (ch2)
Entries 321840

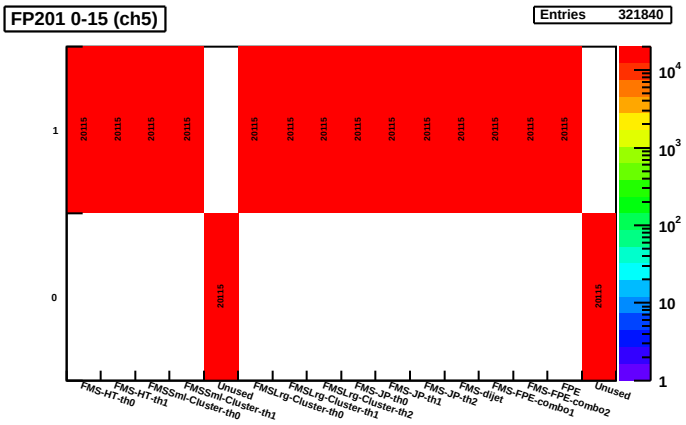

Entries 0

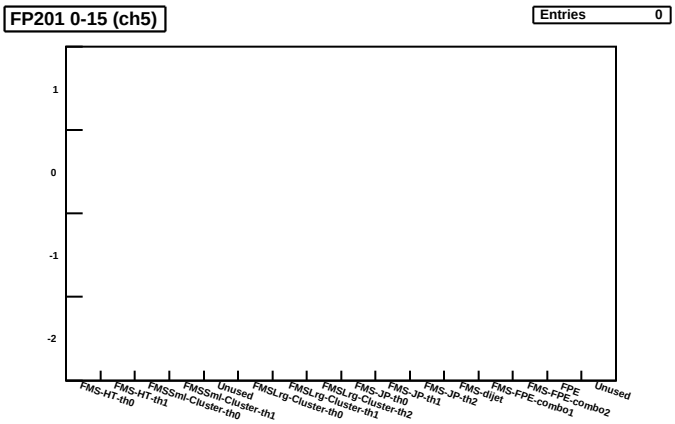

EM201 0-15 (ch3)
Entries 321840
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

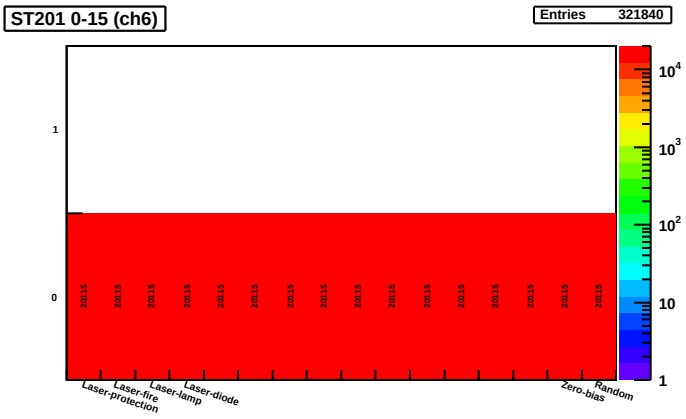

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

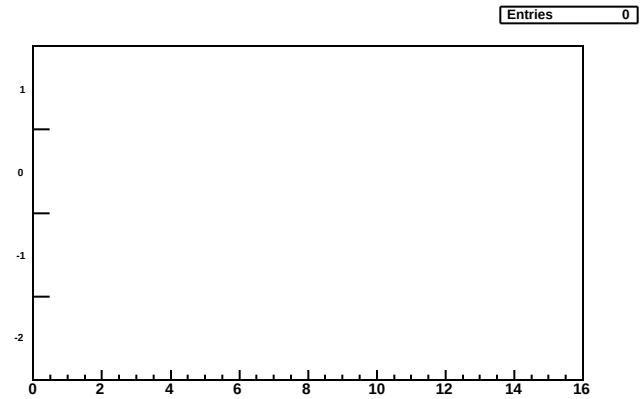

RAT board (ch4)
Entries 321840
FMS-LED
RAT1
RAT2
RAT3
Abort-gaps
Yellow-Fill
RAT6
RAT7
RAT8
RAT9
RAT10
RAT11
RAT12
RAT13
RAT14
RAT15

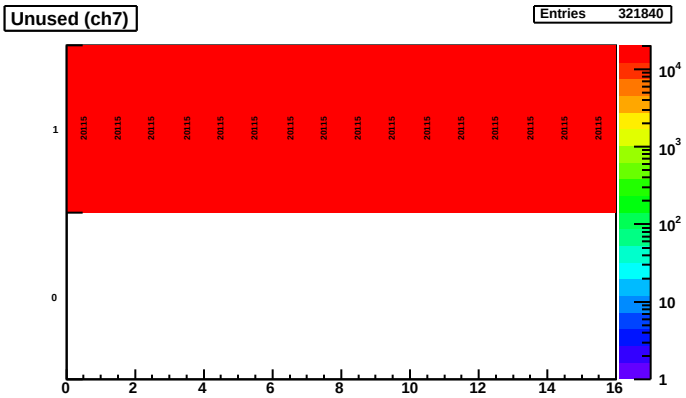

Entries 0

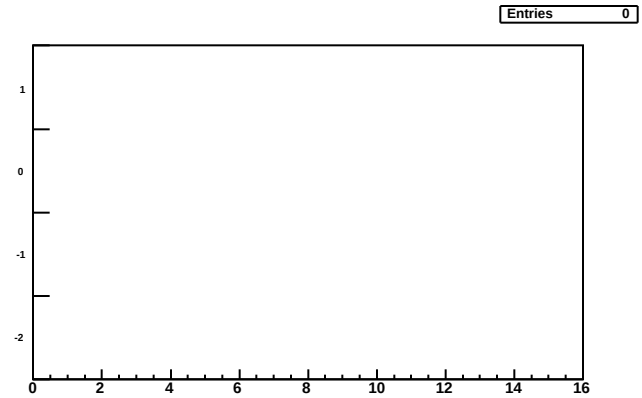

FP201 0-15 (ch5)
Entries 321840
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
Unused
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-dijet
FMS-FPE-combo1
FPE
FMS-FPE-combo2
Unused


FP201 0-15 (ch5)
Entries 0
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
Unused
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-dijet
FMS-FPE-combo1
FPE
FMS-FPE-combo2
Unused


ST201 0-15 (ch6)
Entries 321840
Laser-protection
Laser-fire
Laser-lamp
Laser-diode
Zero-bias
Random


Entries 0


Unused (ch7)
Entries 321840


Entries 0