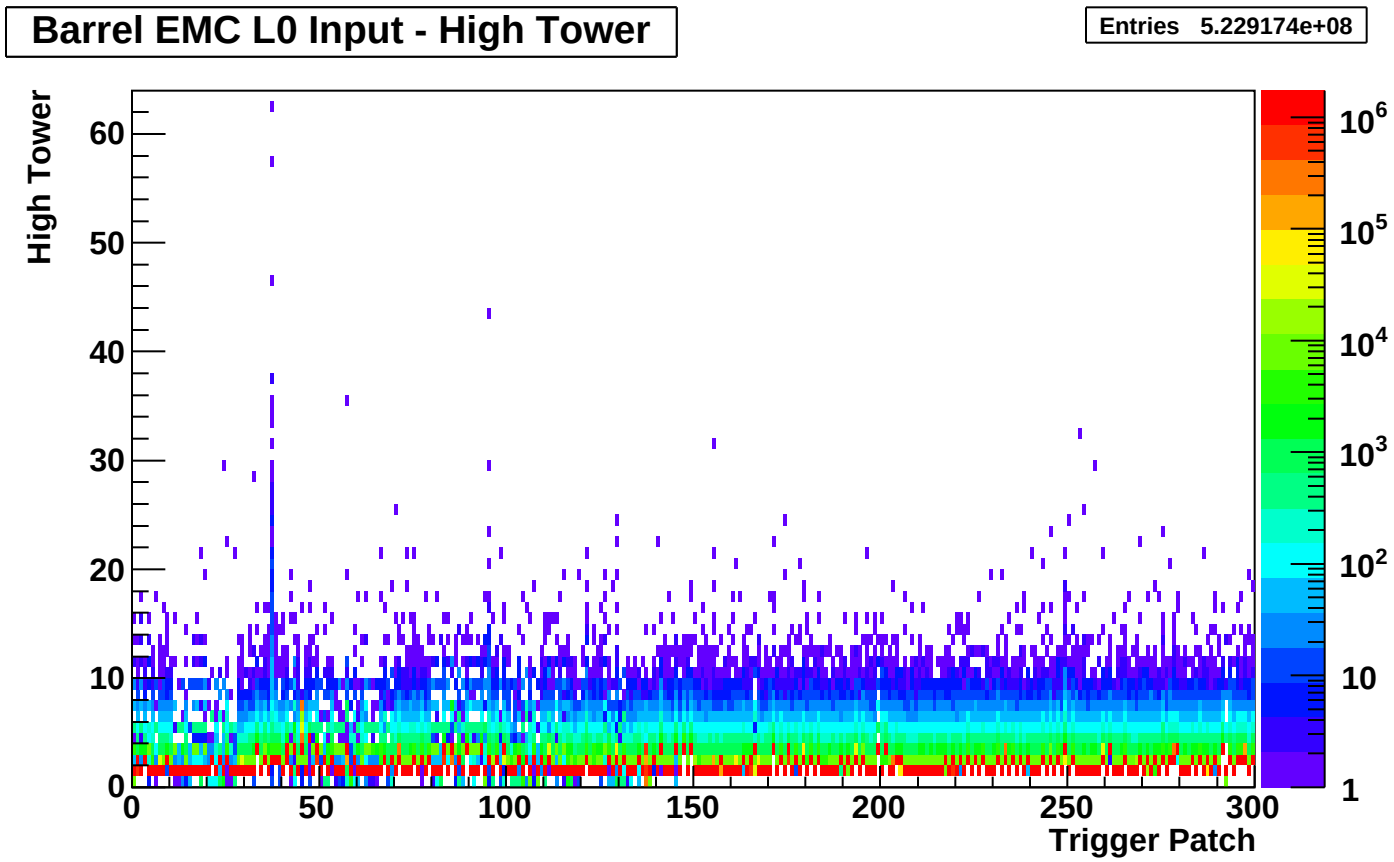
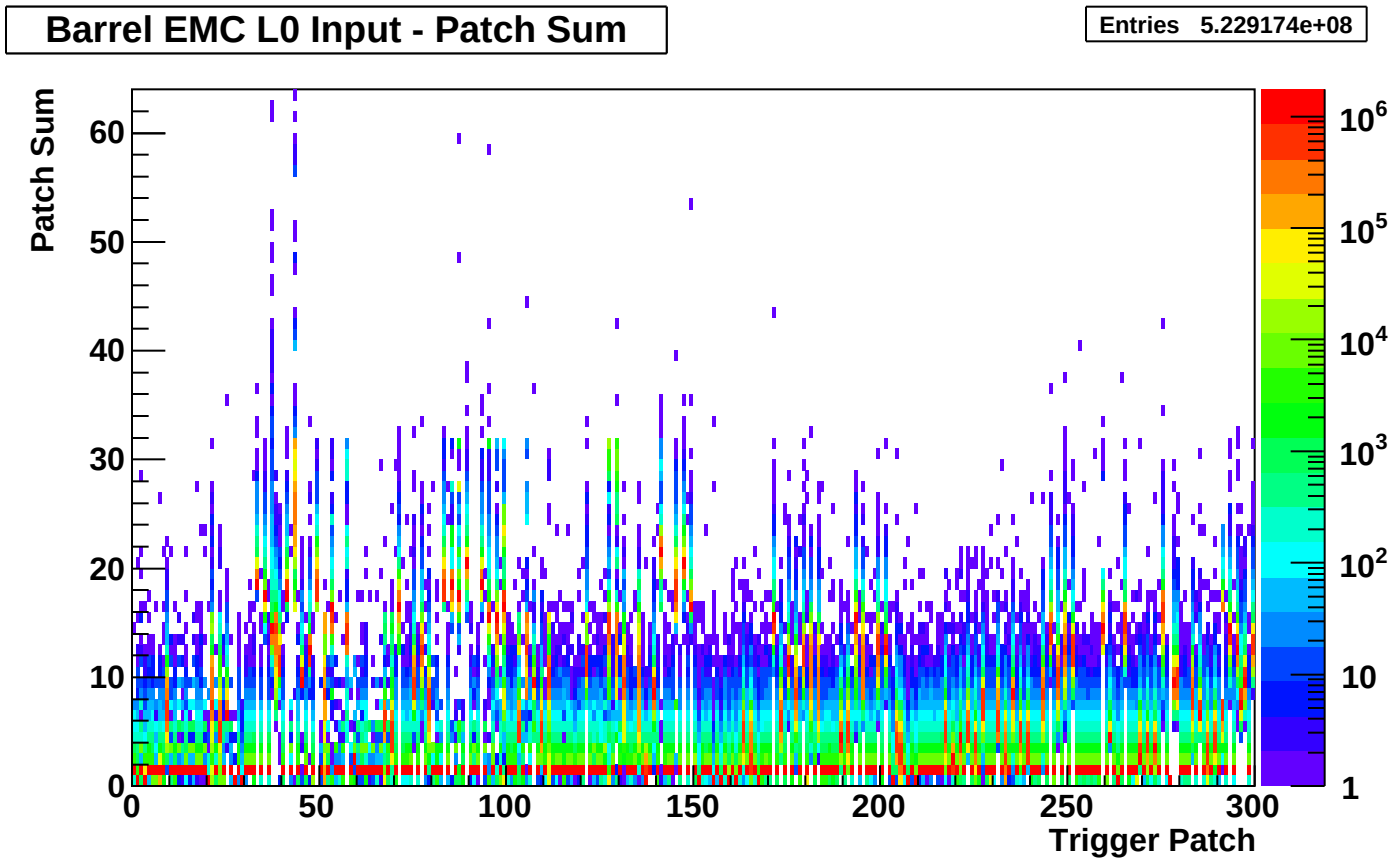
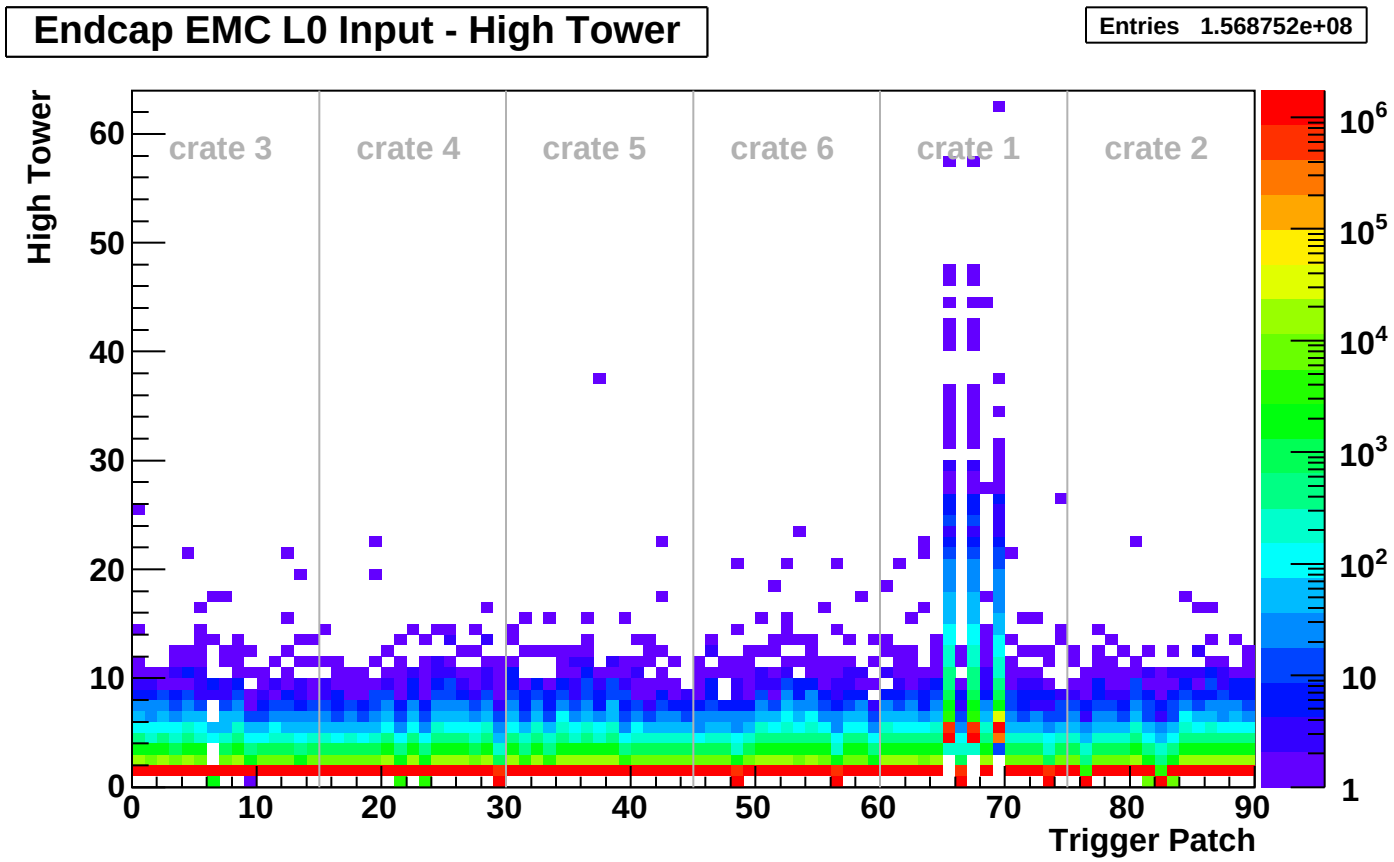
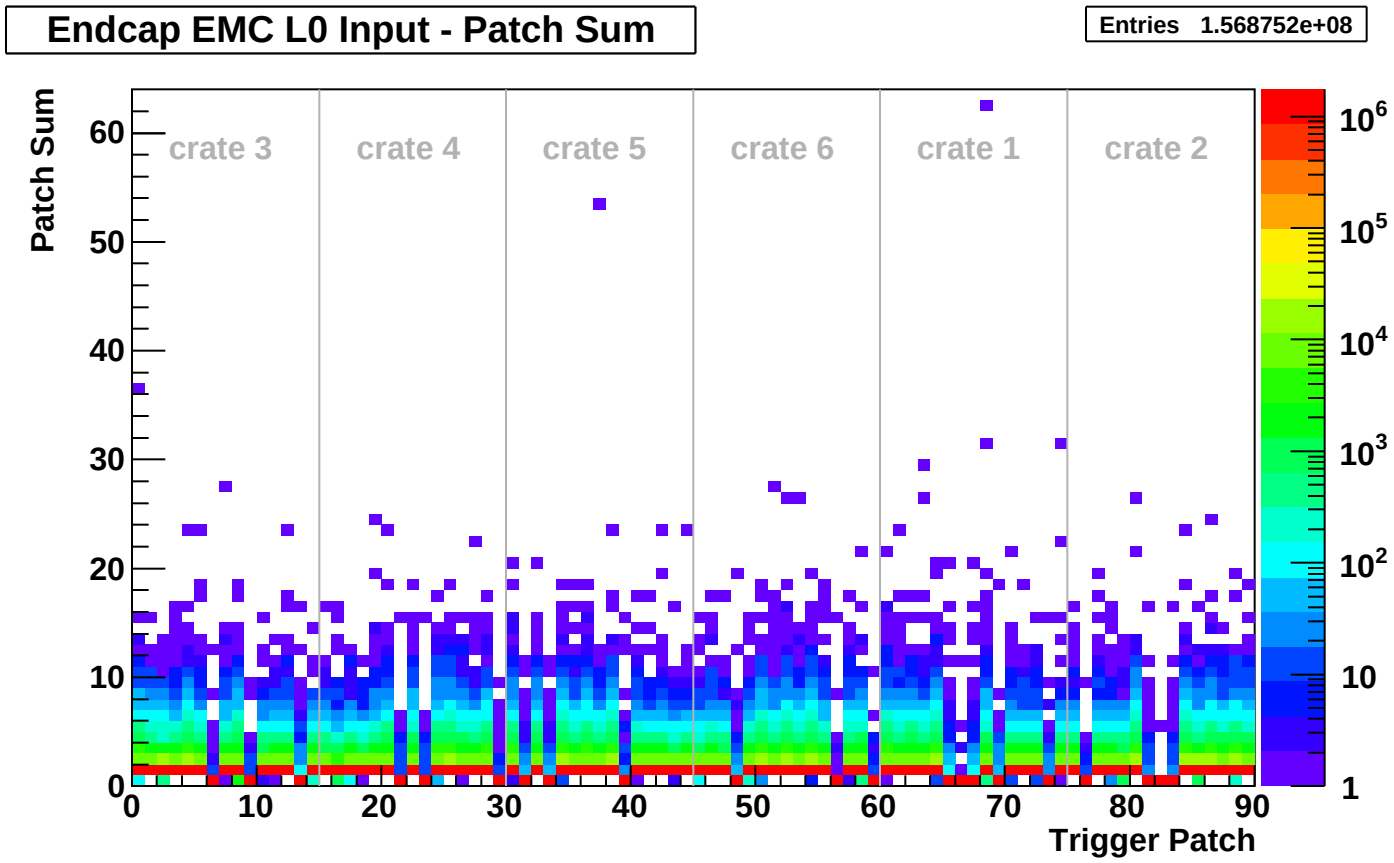
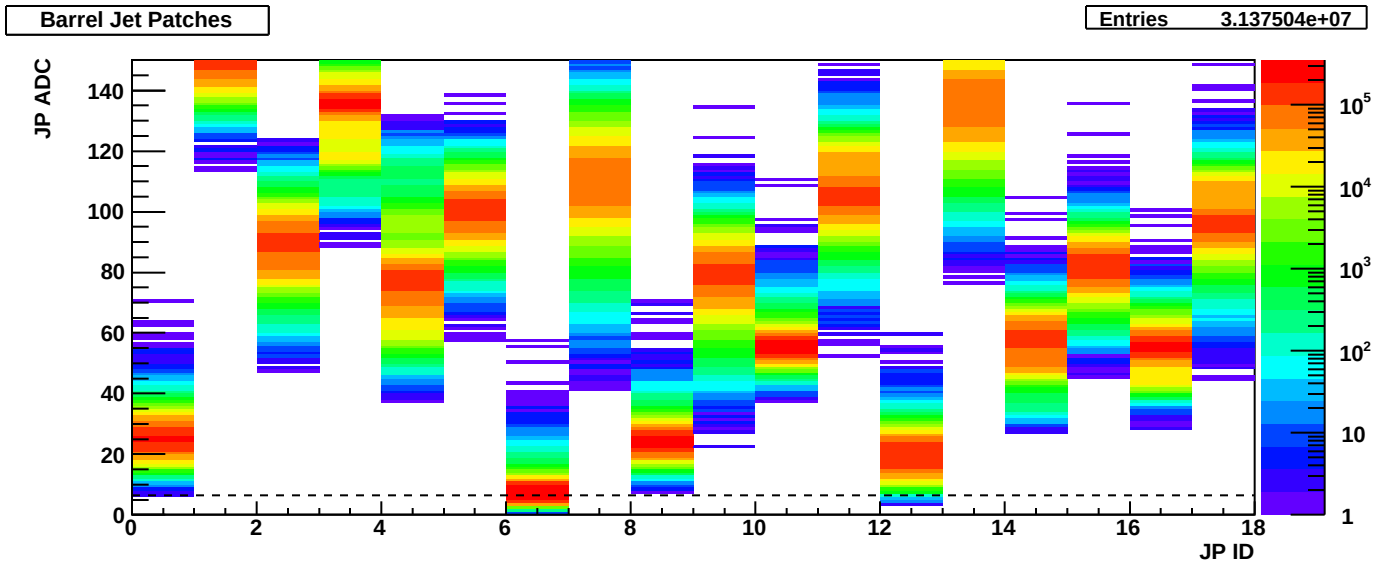
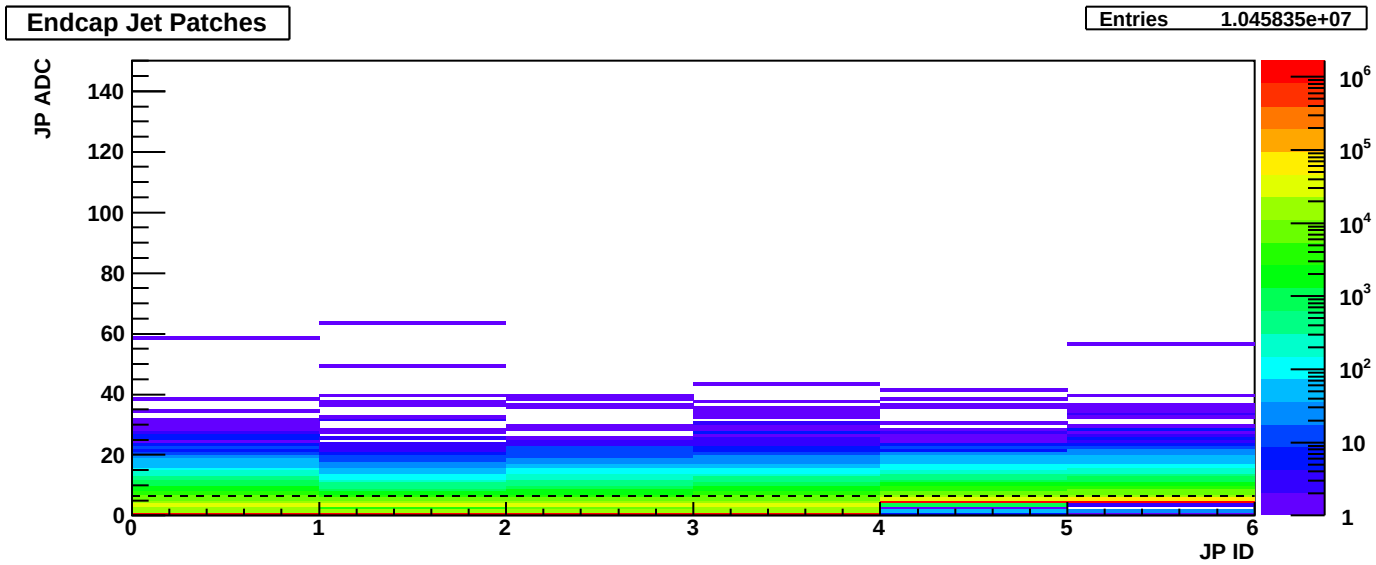

Barrel EMC L0 Input - High Tower
Entries 5.229174e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

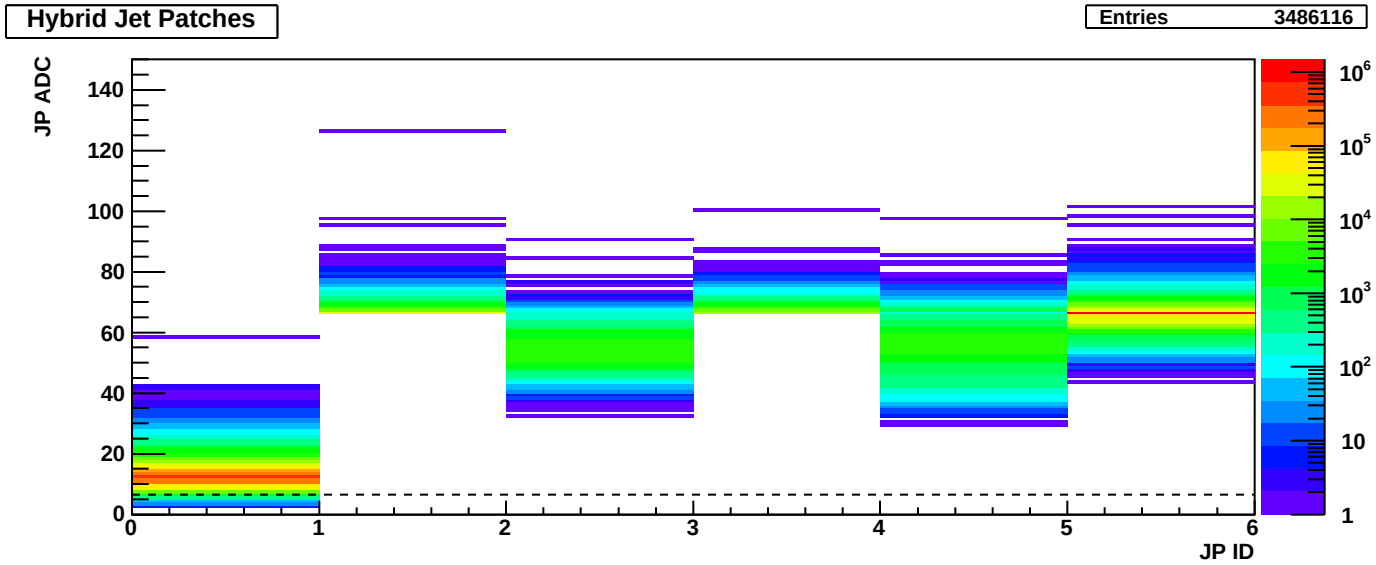

Barrel EMC L0 Input - Patch Sum
Entries 5.229174e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

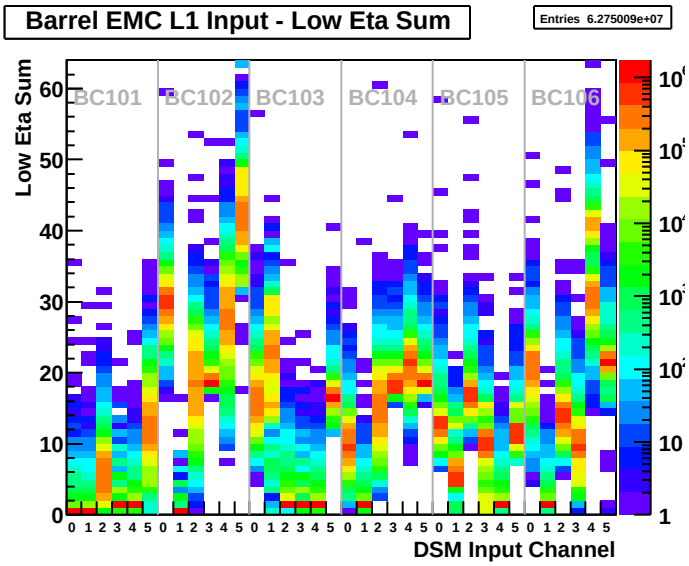

Endcap EMC L0 Input - High Tower
Entries 1.568752e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

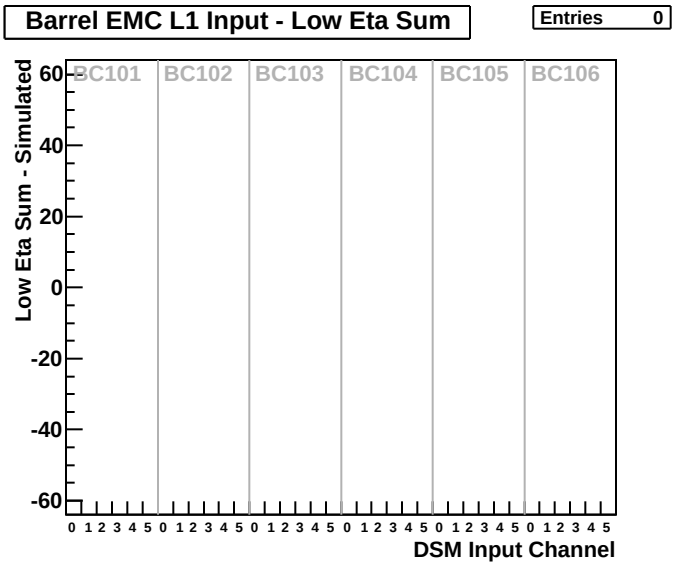

Endcap EMC L0 Input - Patch Sum
Entries 1.568752e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

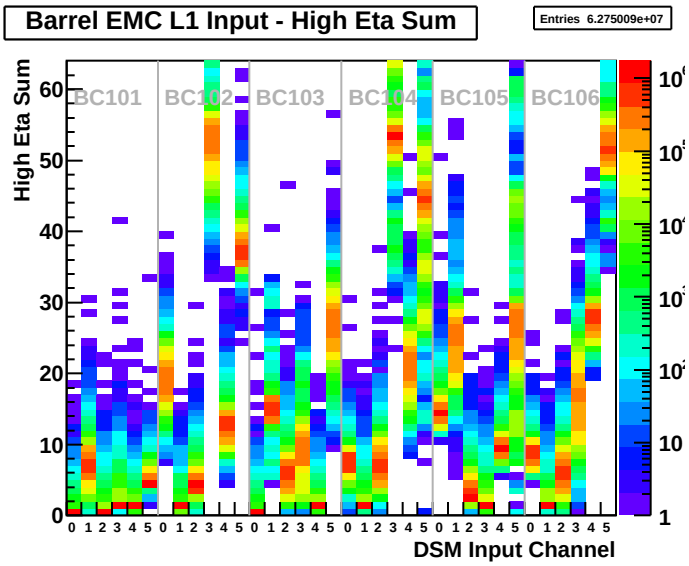

Barrel Jet Patches
Entries 3.137504e+07
JP ADC
JP ID

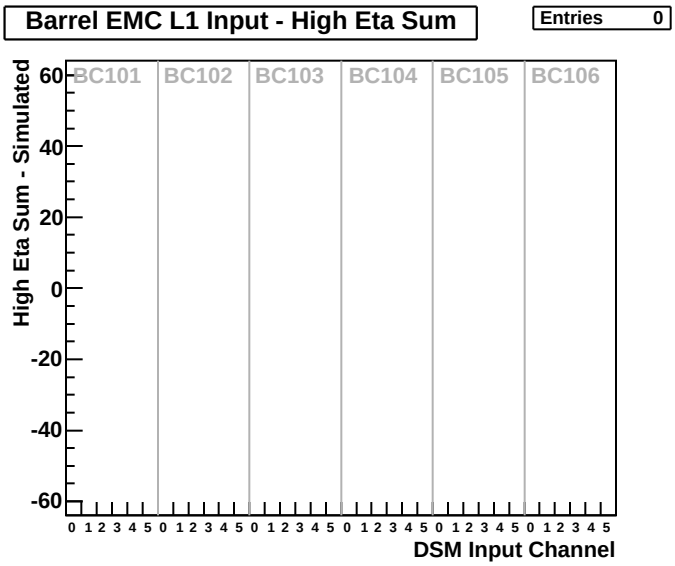

Endcap Jet Patches
Entries 1.045835e+07
JP ADC
JP ID

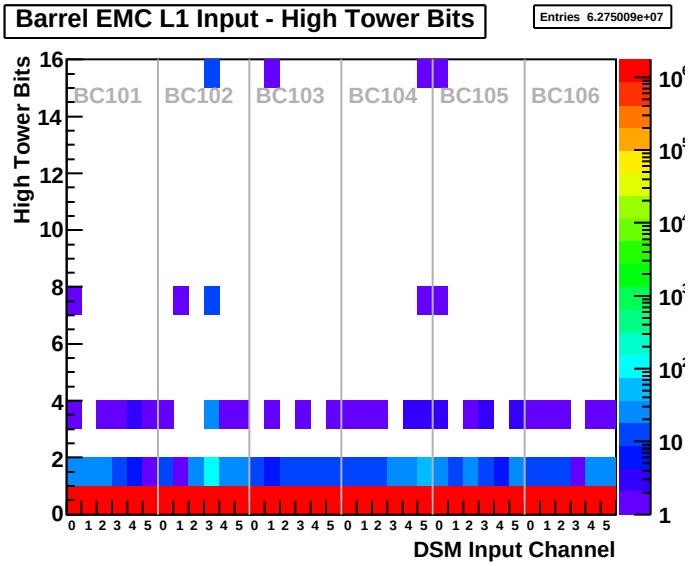

Hybrid Jet Patches
Entries 3486116
JP ADC
JP ID

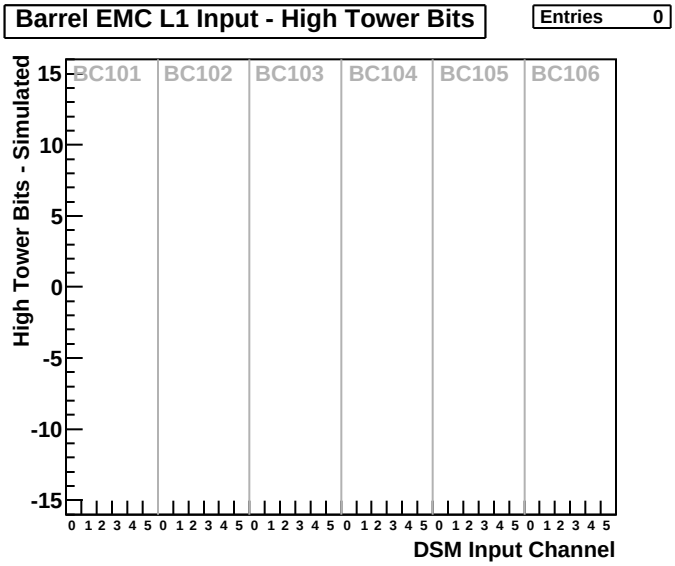

Barrel EMC L1 Input - Low Eta Sum
Entries 6.275009e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

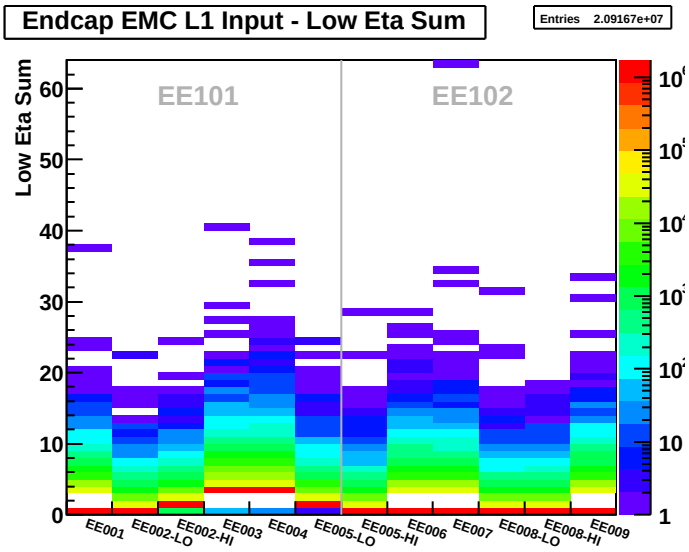

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

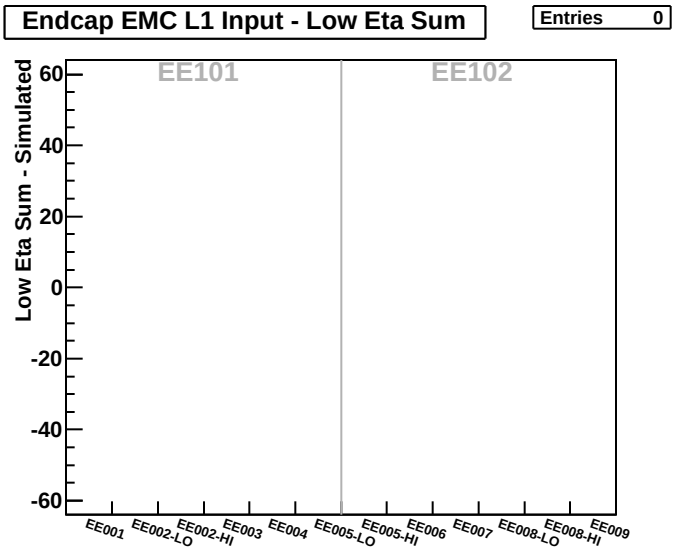

Barrel EMC L1 Input - High Eta Sum
Entries 6.275009e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

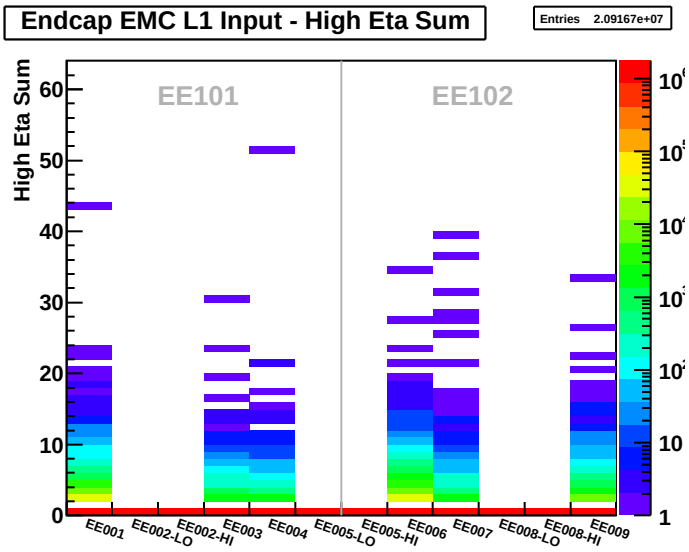

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

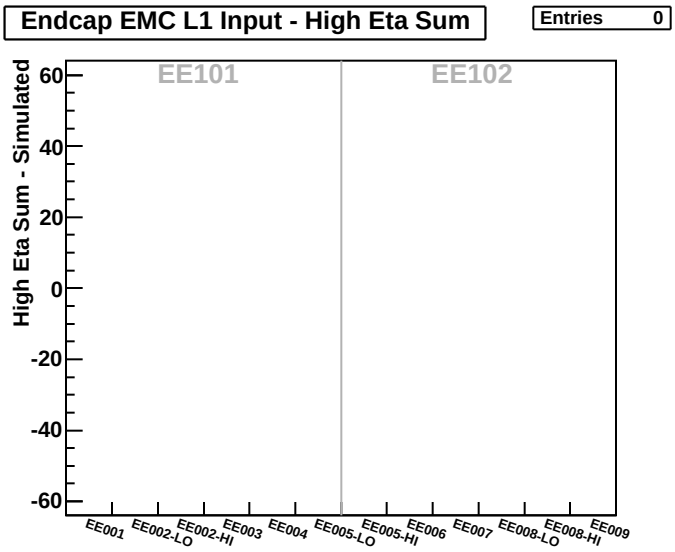

Barrel EMC L1 Input - High Tower Bits
Entries 6.275009e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

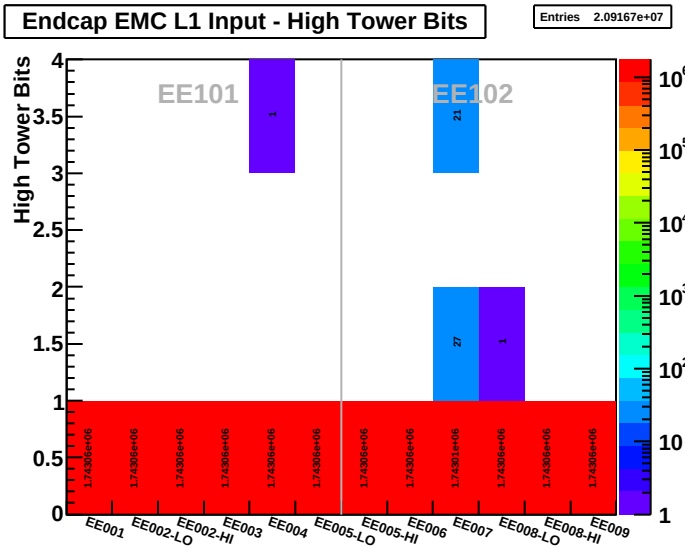

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

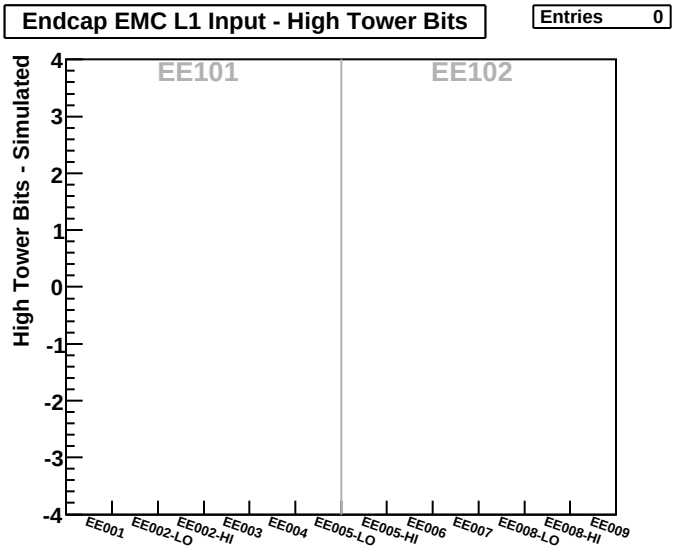

Endcap EMC L1 Input - Low Eta Sum
Entries 2.09167e+07
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

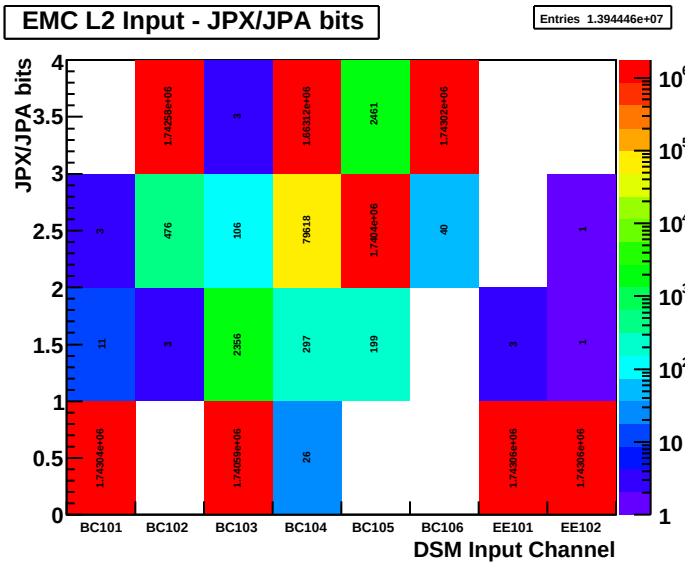

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

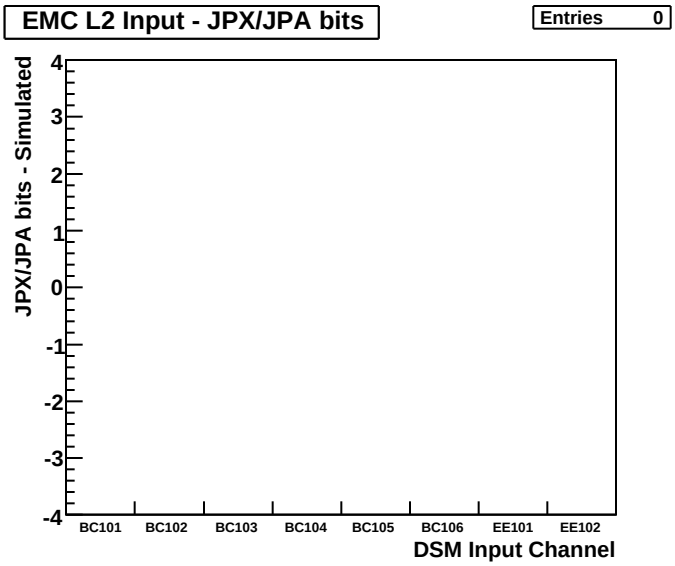

Endcap EMC L1 Input - High Eta Sum
Entries 2.09167e+07
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

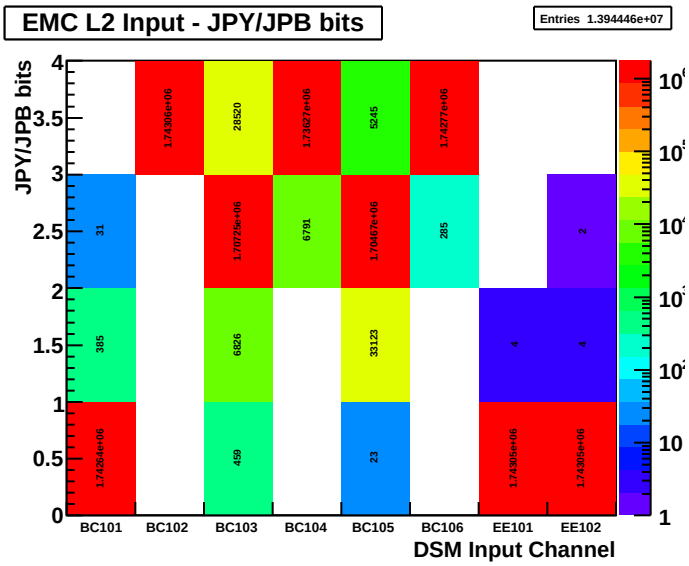

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

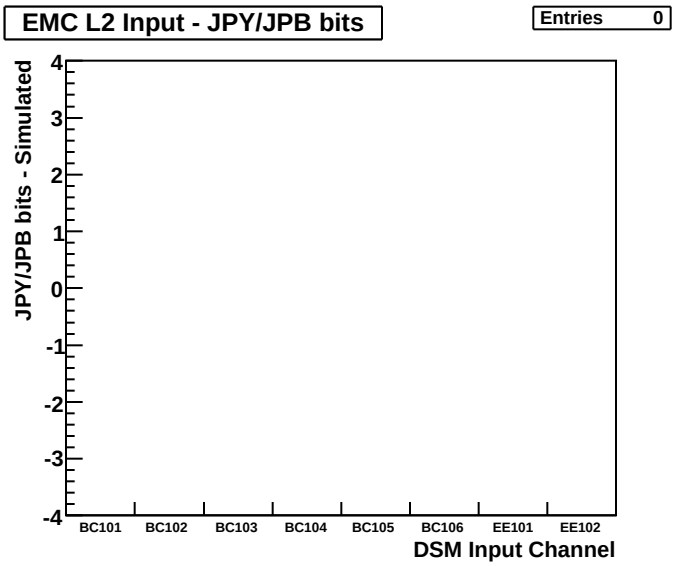

Endcap EMC L1 Input - High Tower Bits
Entries 2.09167e+07
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

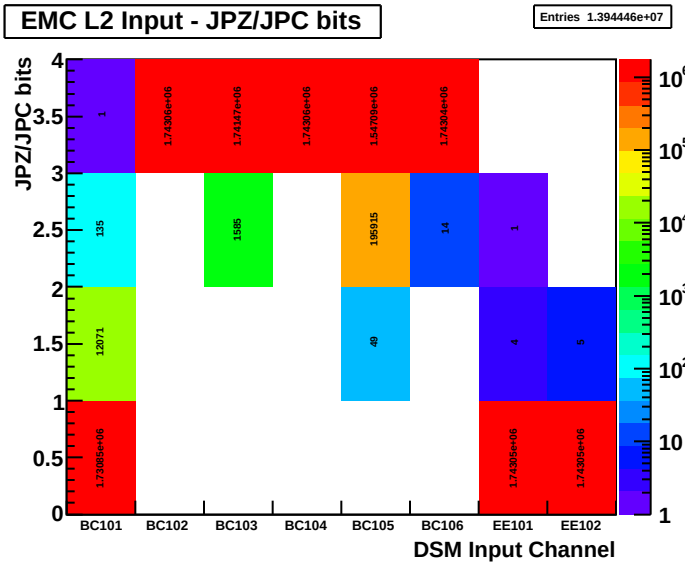

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

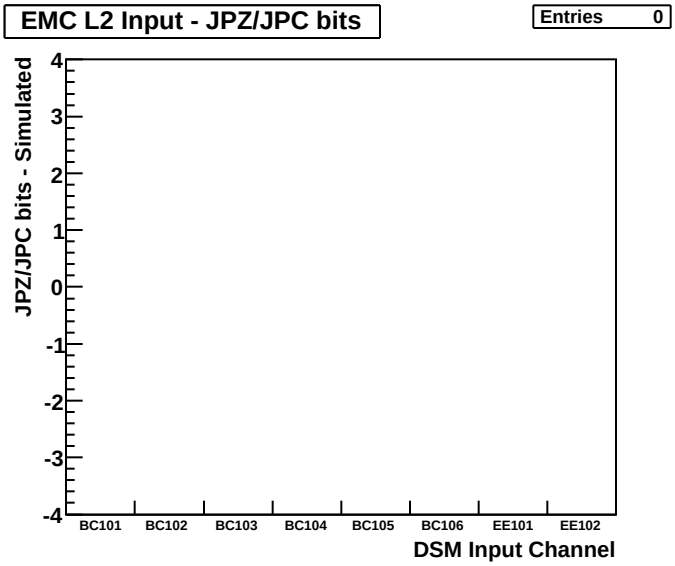

EMC L2 Input - JPX/JPA bits
Entries 1.394446e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

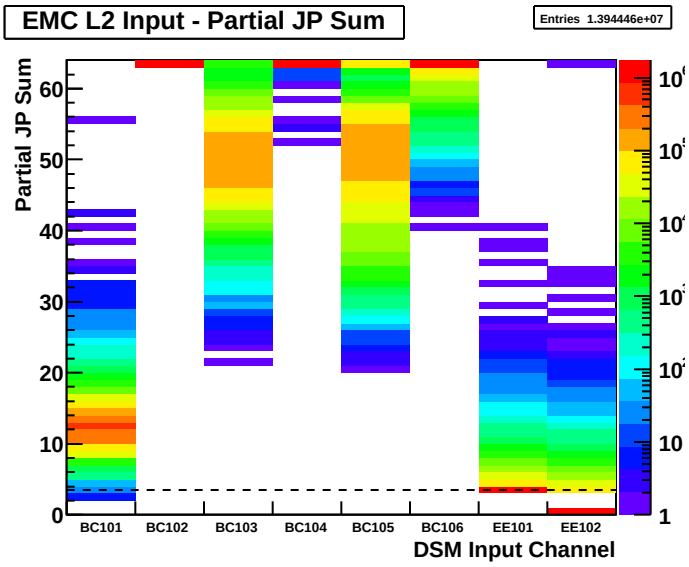

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

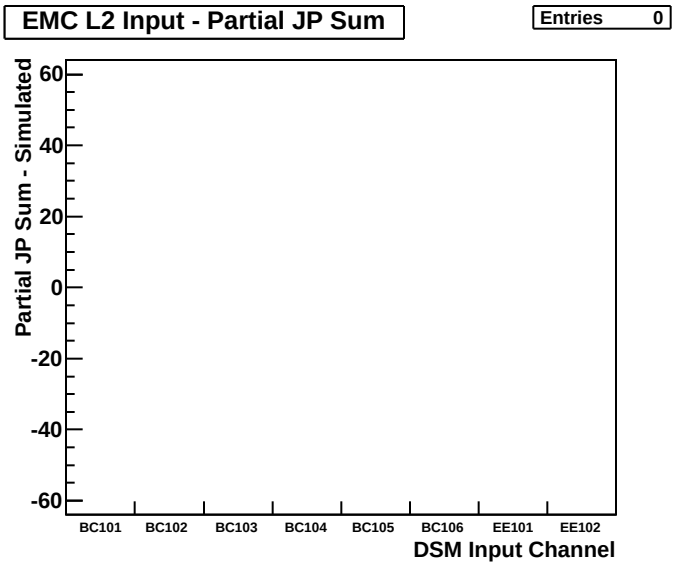

EMC L2 Input - JPY/JPB bits
Entries 1.394446e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

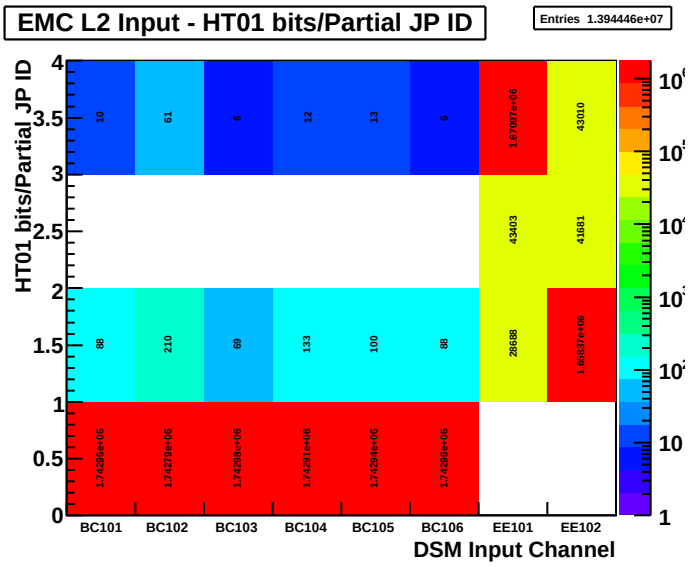

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

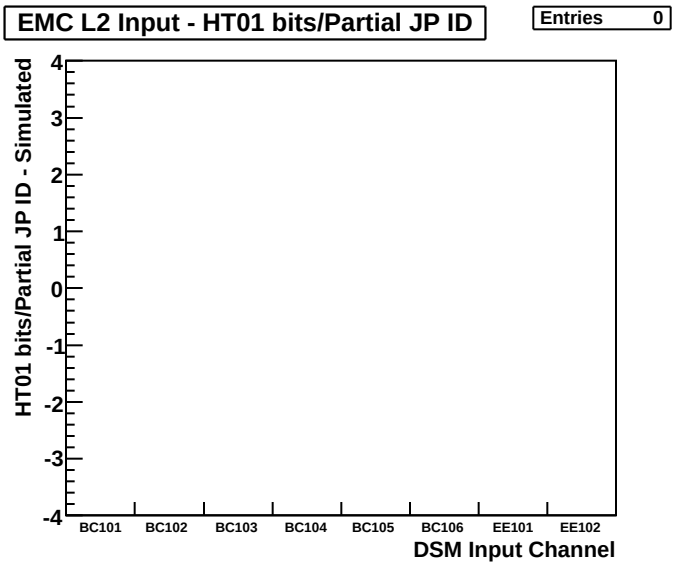

EMC L2 Input - JPZ/JPC bits
Entries 1.394446e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

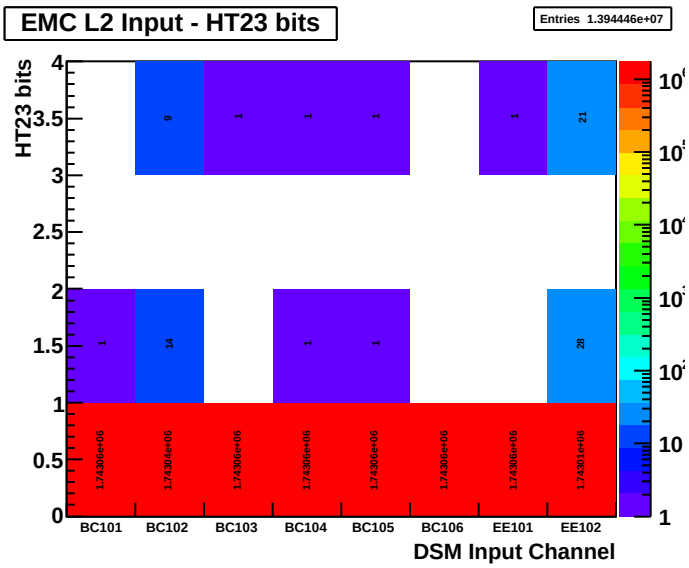

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

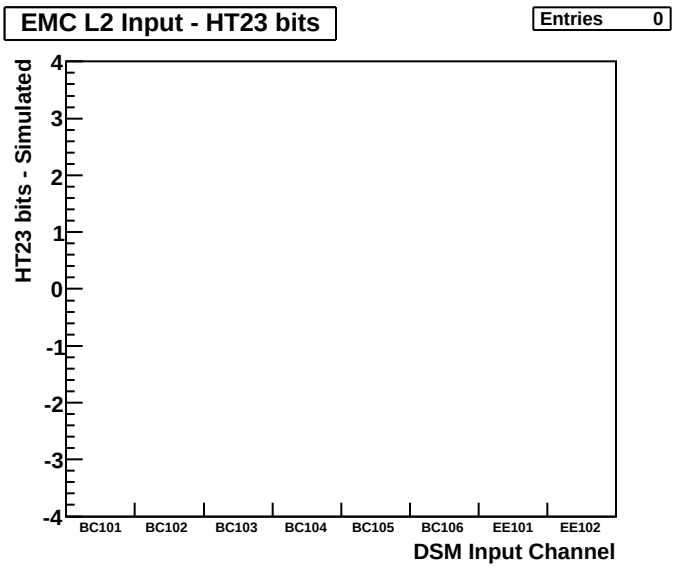

EMC L2 Input - Partial JP Sum
Entries 1.394446e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

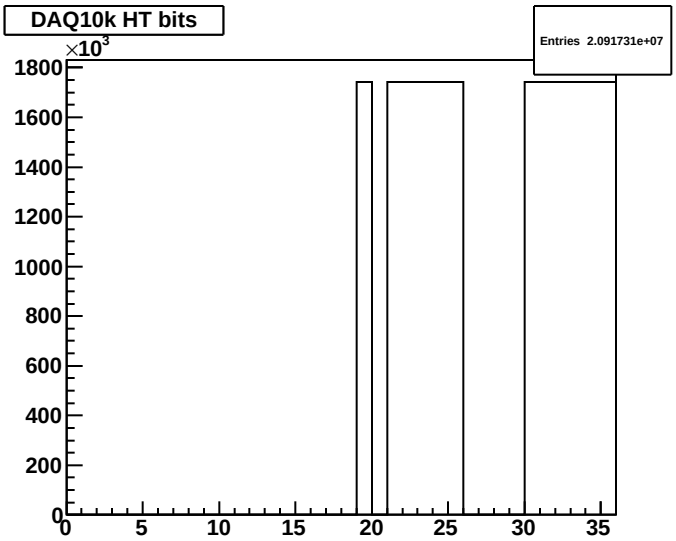

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

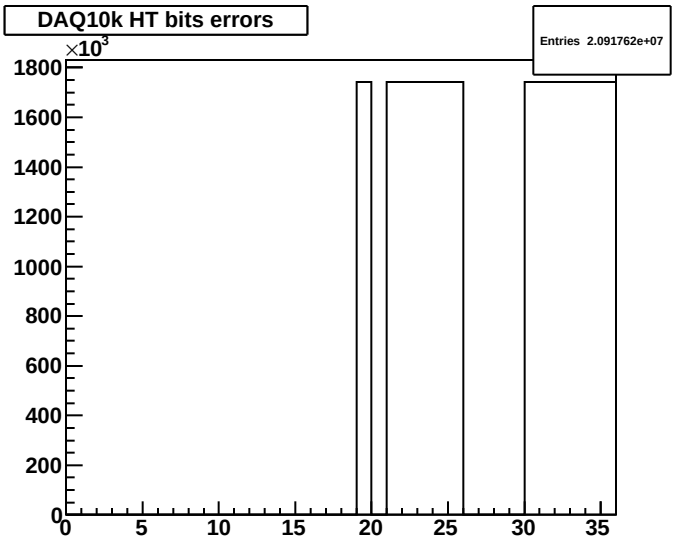

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.394446e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

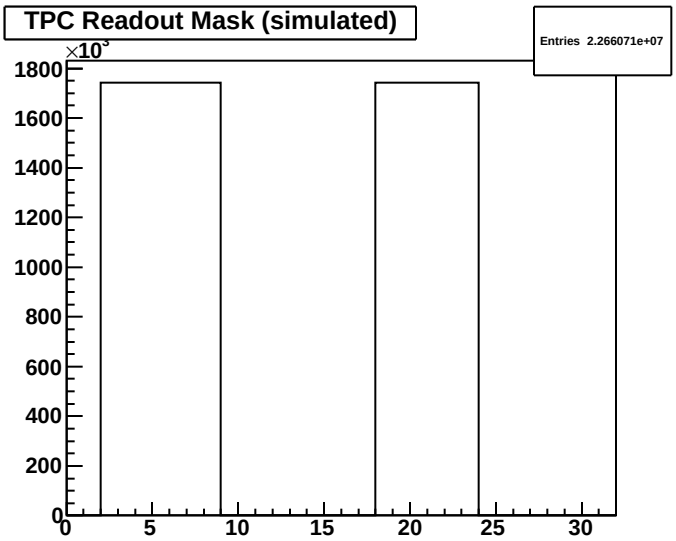

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

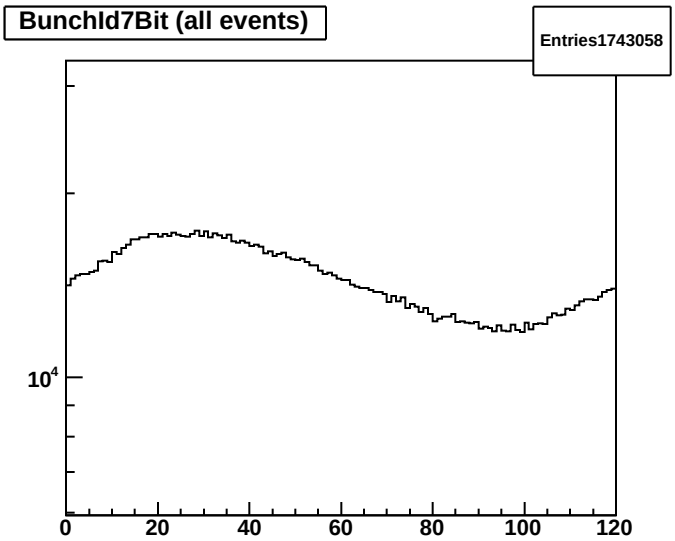

EMC L2 Input - HT23 bits
Entries 1.394446e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

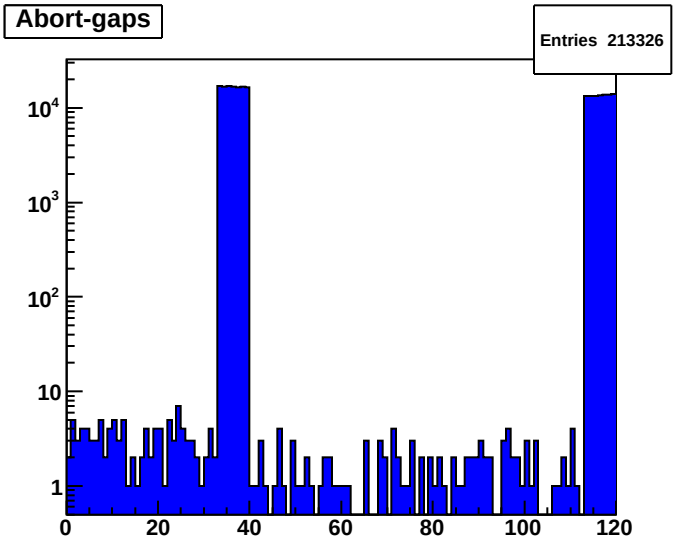

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

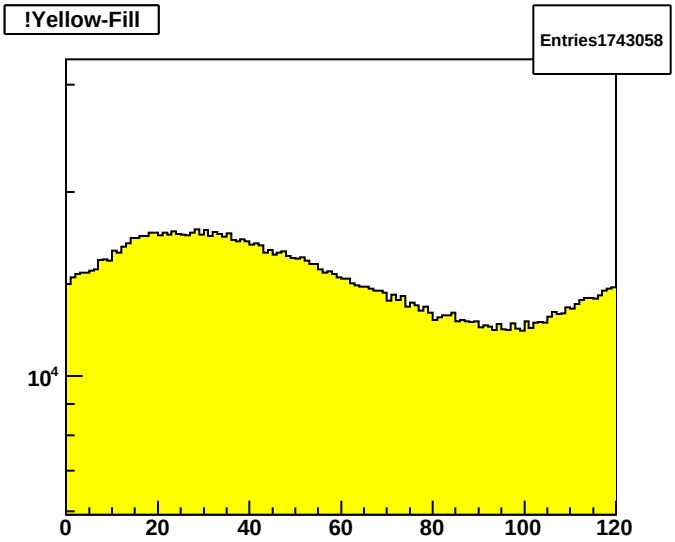

DAQ10k HT bits
Entries 2.091731e+07
×10^3
1800
1600
1400
1200
1000
800
600
400
200
0
0 5 10 15 20 25 30 35

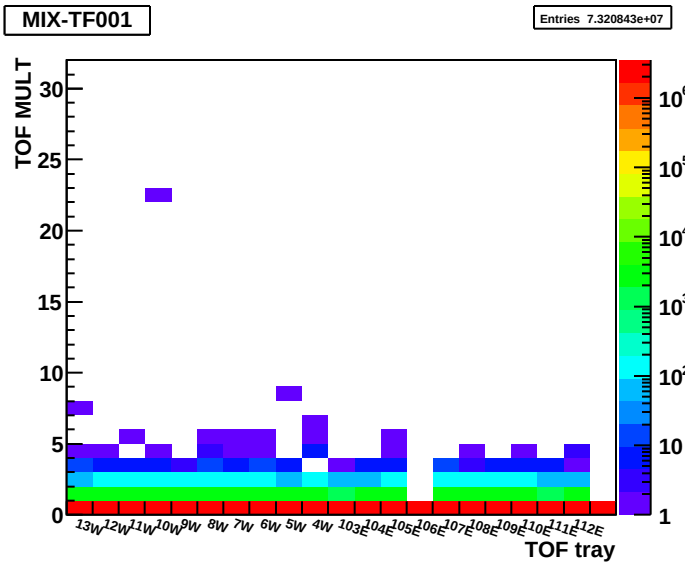

DAQ10k HT bits errors
Entries 2.091762e+07
×10^3
1800
1600
1400
1200
1000
800
600
400
200
0
0 5 10 15 20 25 30 35

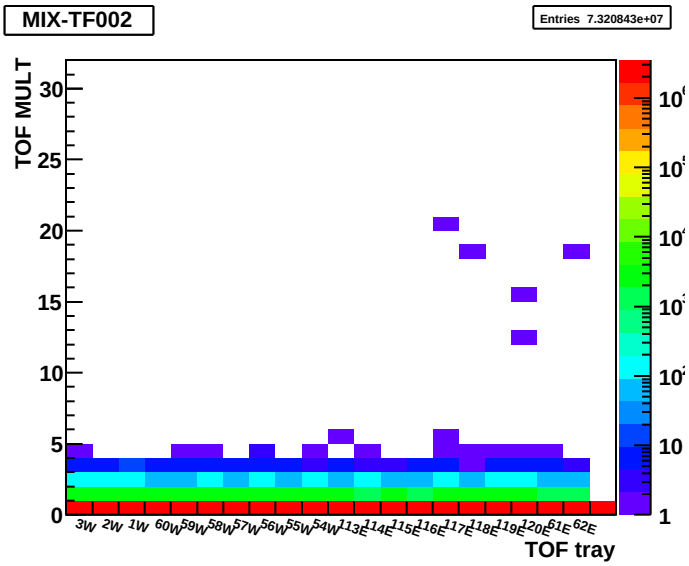

TPC Readout Mask (simulated)
Entries 2.266071e+07
×10^3
1800
1600
1400
1200
1000
800
600
400
200
0
0 5 10 15 20 25 30

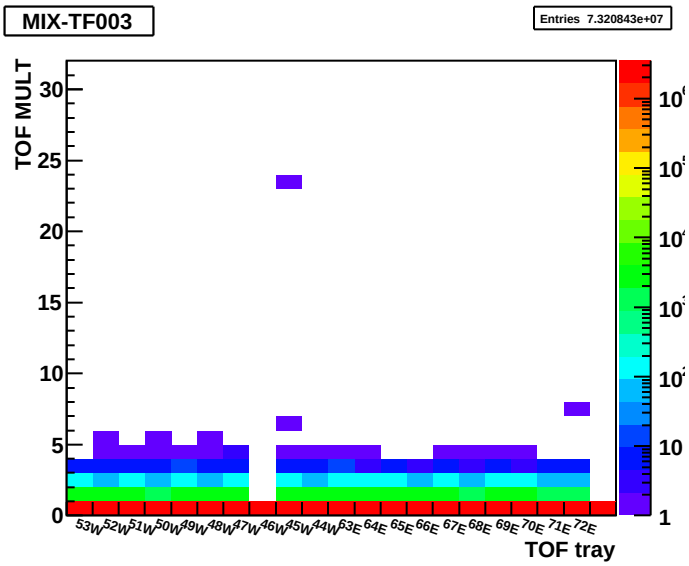

BunchId7Bit (all events)
Entries1743058
10^4
0 20 40 60 80 100 120

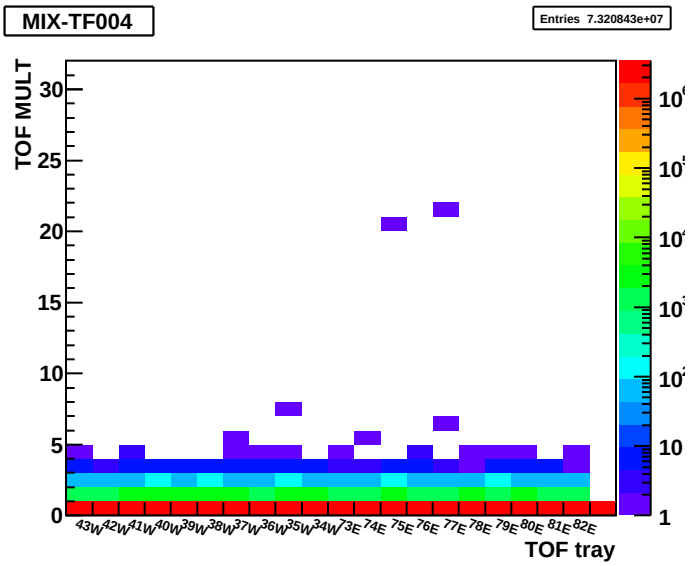

Abort-gaps
Entries 213326
10^4
10^3
10^2
10
1
0 20 40 60 80 100 120

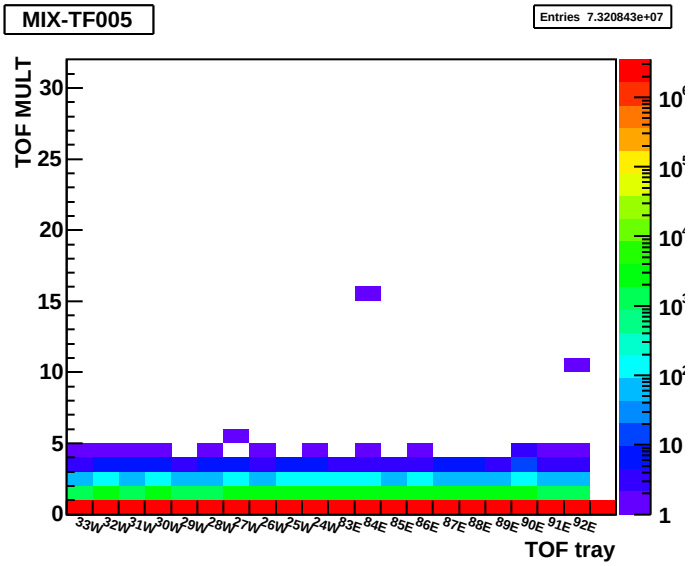

!Yellow-Fill
Entries1743058
10^4
0 20 40 60 80 100 120

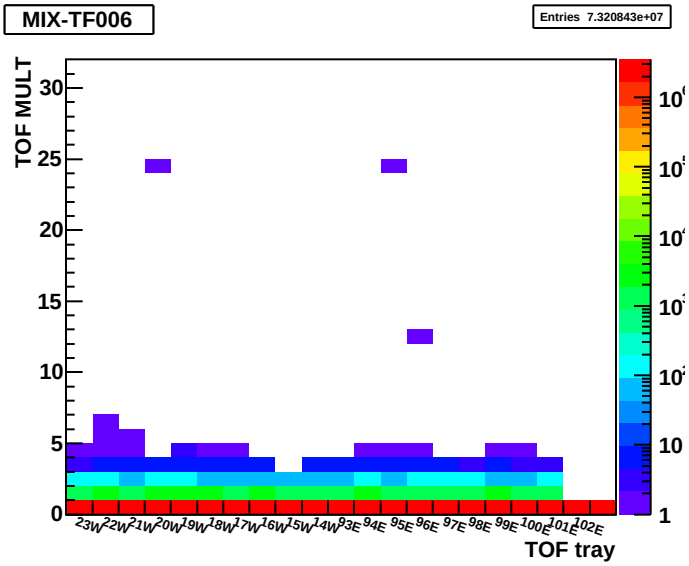

MIX-TF001
Entries 7.320843e+07
TOF MULT
TOF tray

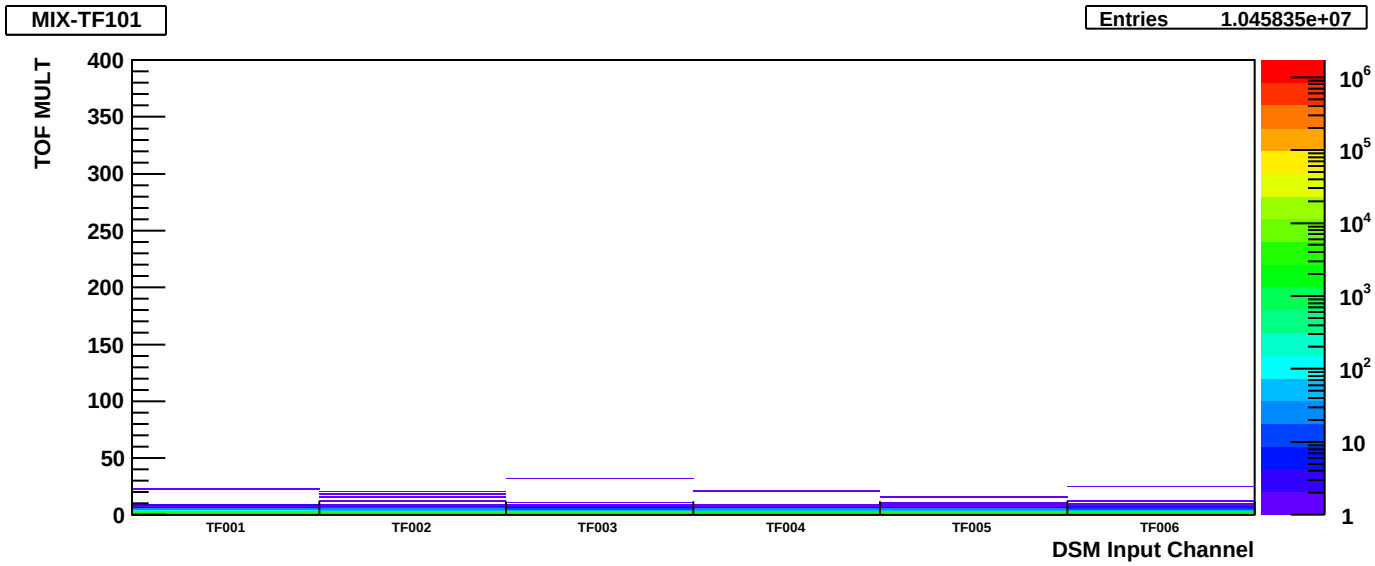

MIX-TF002
Entries 7.320843e+07
TOF MULT
TOF tray

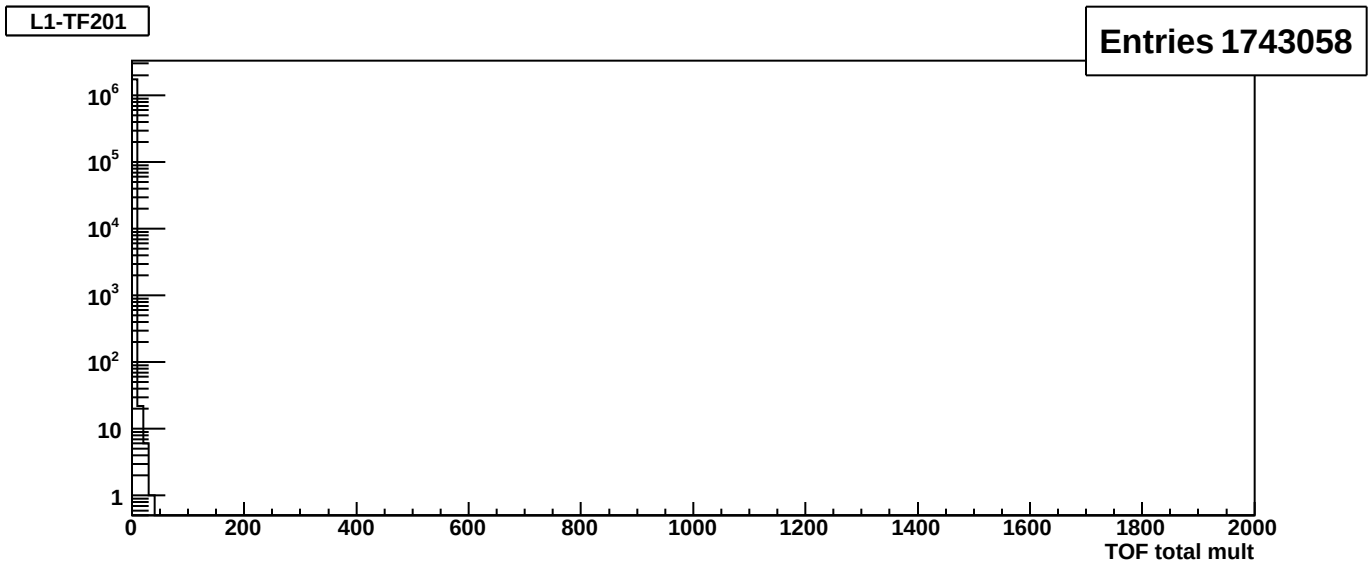

MIX-TF003
Entries 7.320843e+07
TOF MULT
TOF tray

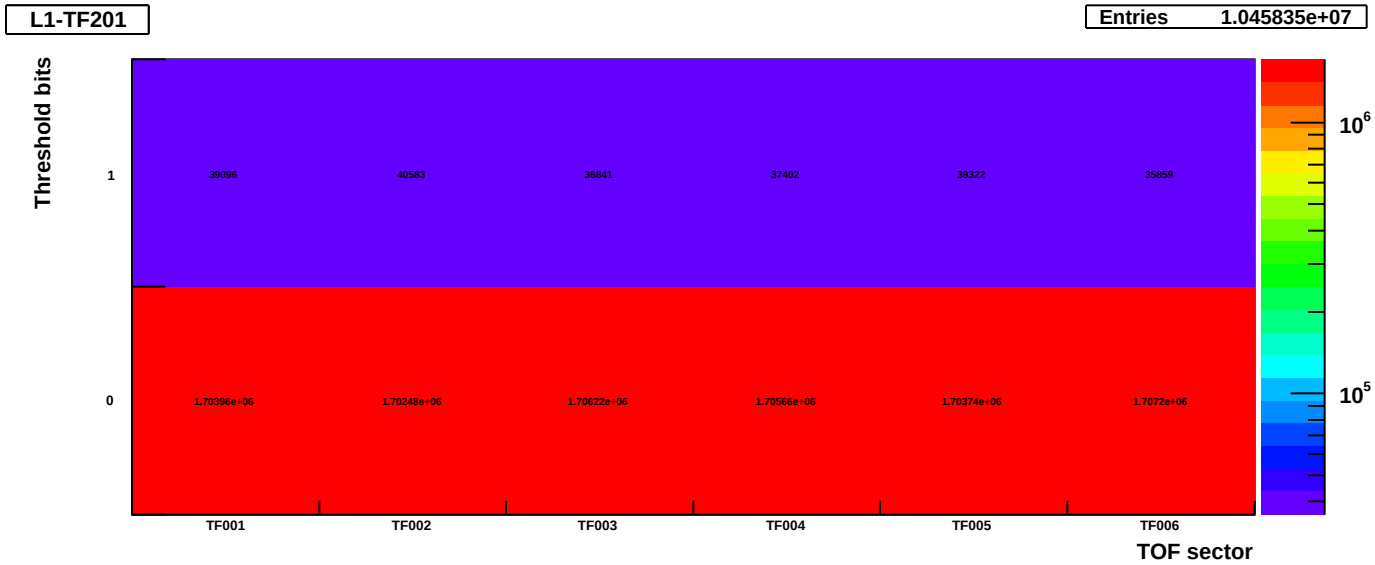

MIX-TF004
Entries 7.320843e+07
TOF MULT
TOF tray

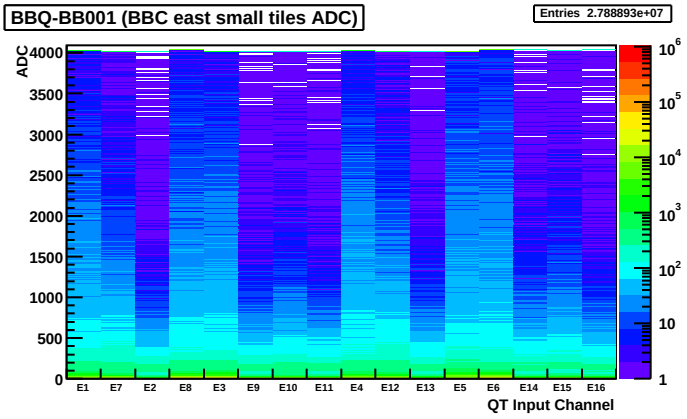

MIX-TF005
Entries 7.320843e+07
TOF MULT
TOF tray

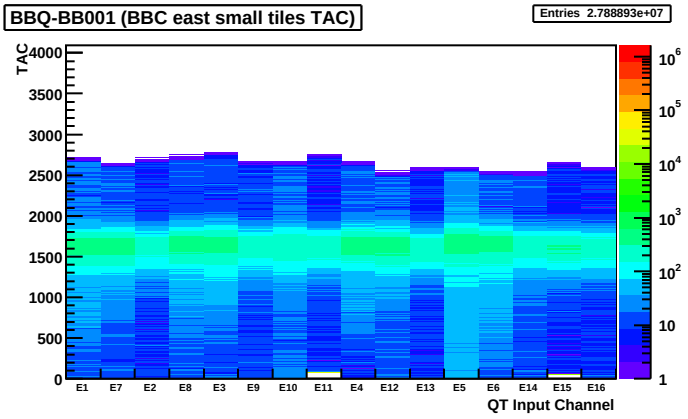

MIX-TF006
Entries 7.320843e+07
TOF MULT
TOF tray

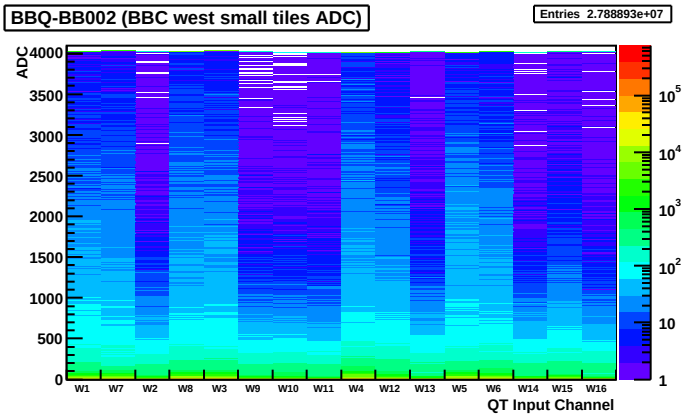

MIX-TF101
Entries 1.045835e+07
TOF MULT
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^6
10^5
10^4
10^3
10^2
10
1

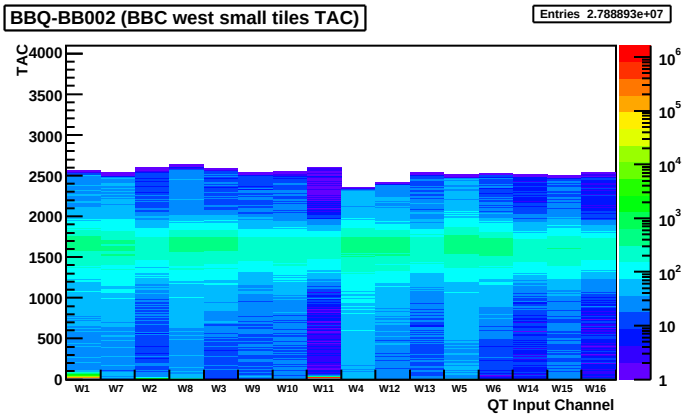

L1-TF201
Entries 1743058
10^6
10^5
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
1600
1800
2000
TOF total mult

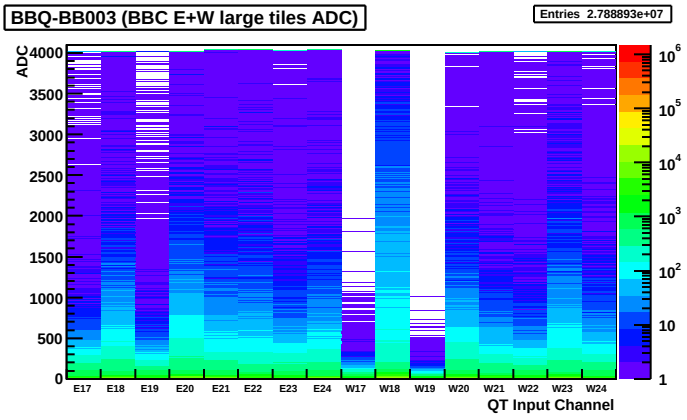

L1-TF201
Entries 1.045835e+07
Threshold bits
1
0
39096
40583
36841
37402
39322
35859
1.70396e+06
1.70248e+06
1.70622e+06
1.70566e+06
1.70374e+06
1.7072e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6
10^5

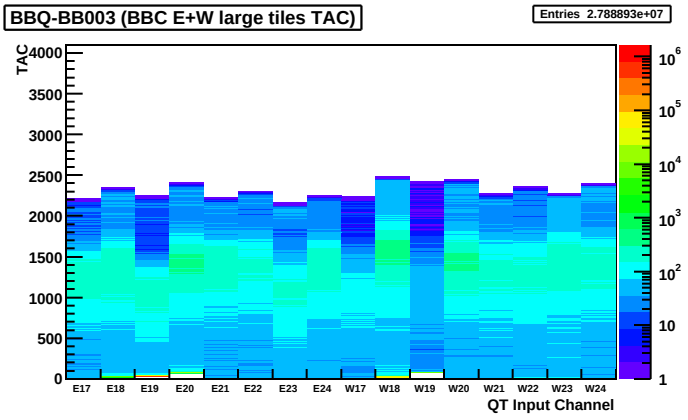

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.788893e+07
ADC
QT Input Channel

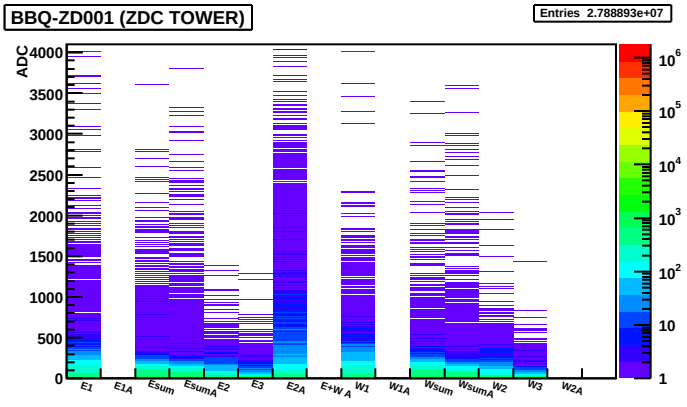

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.788893e+07
TAC
QT Input Channel

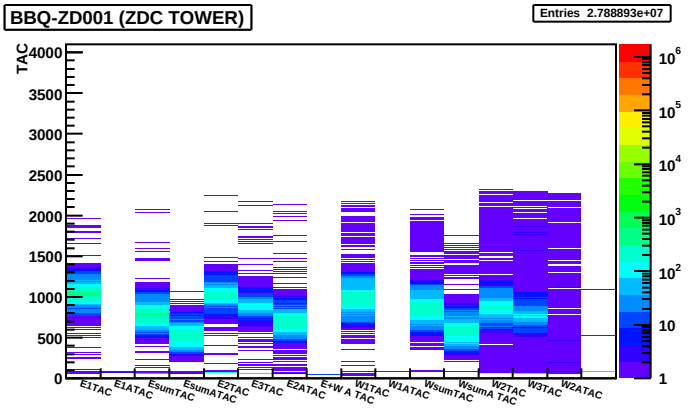

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.788893e+07
ADC
QT Input Channel

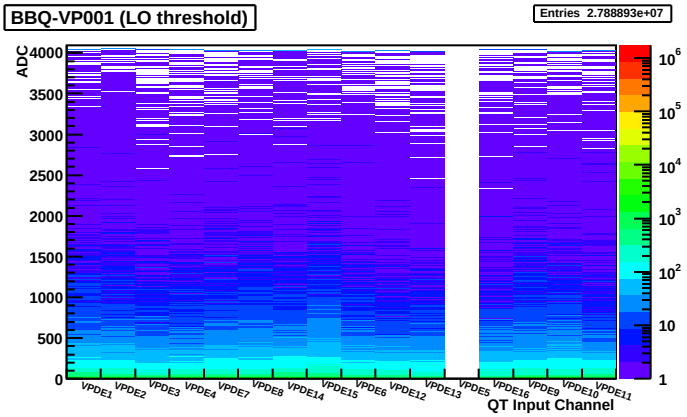

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.788893e+07
TAC
QT Input Channel

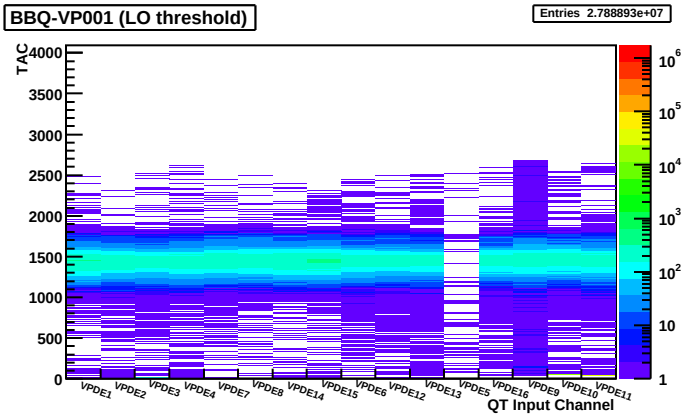

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.788893e+07
ADC
QT Input Channel

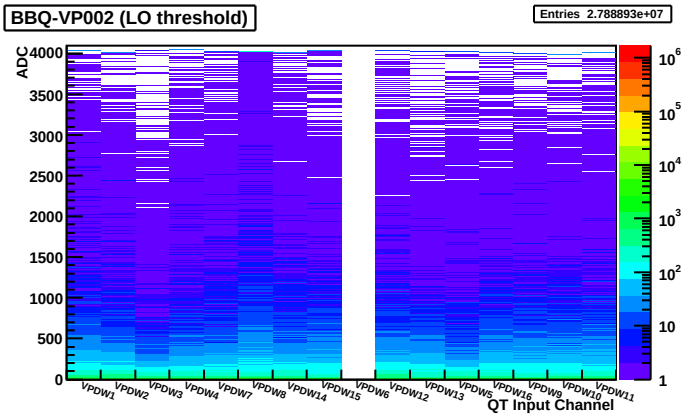

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.788893e+07
TAC
QT Input Channel

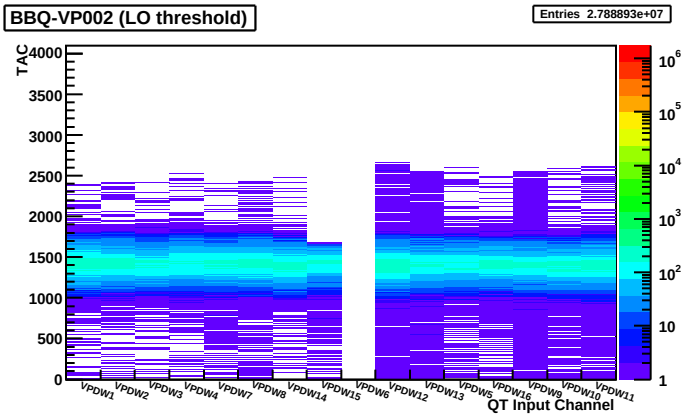

BBQ-ZD001 (ZDC TOWER)
Entries 2.788893e+07
ADC

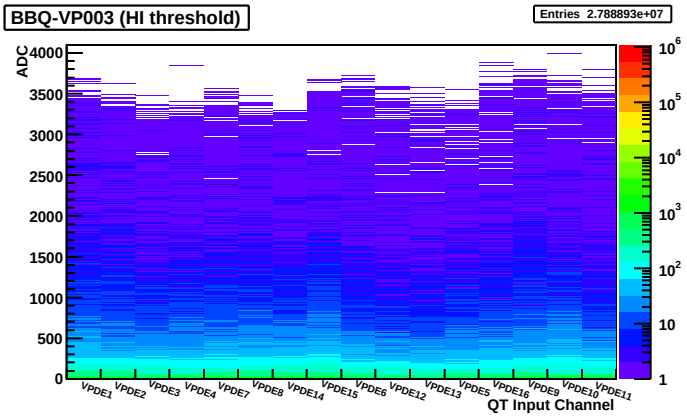

BBQ-ZD001 (ZDC TOWER)
Entries 2.788893e+07
TAC

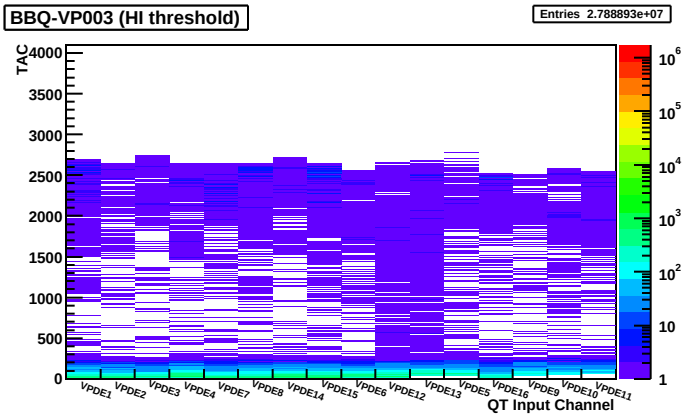

BBQ-VP001 (LO threshold)
Entries 2.788893e+07
ADC
QT Input Channel

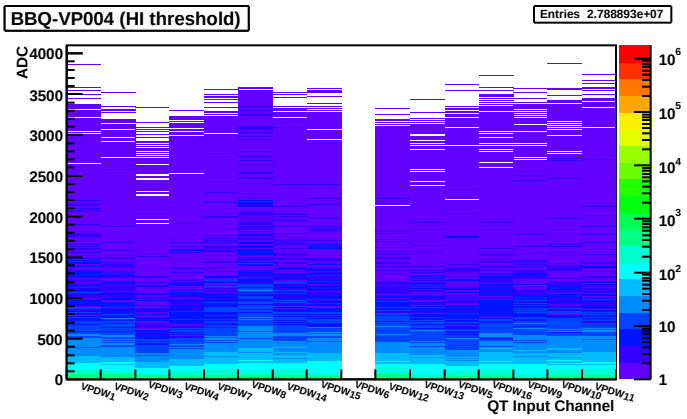

BBQ-VP001 (LO threshold)
Entries 2.788893e+07
TAC
QT Input Channel

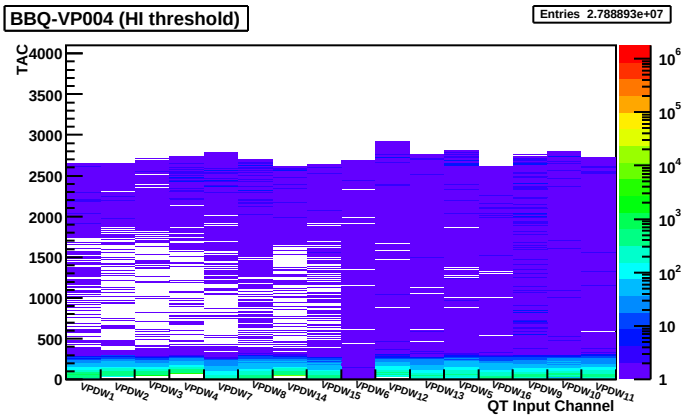

BBQ-VP002 (LO threshold)
Entries 2.788893e+07
ADC
QT Input Channel

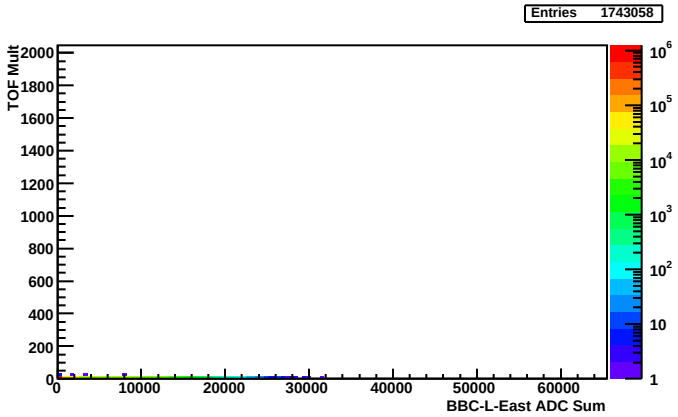

BBQ-VP002 (LO threshold)
Entries 2.788893e+07
TAC
QT Input Channel

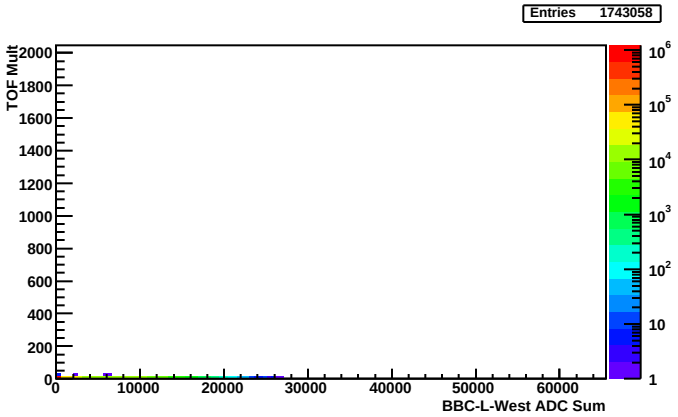

BBQ-VP003 (HI threshold)
Entries 2.788893e+07
ADC
QT Input Channel

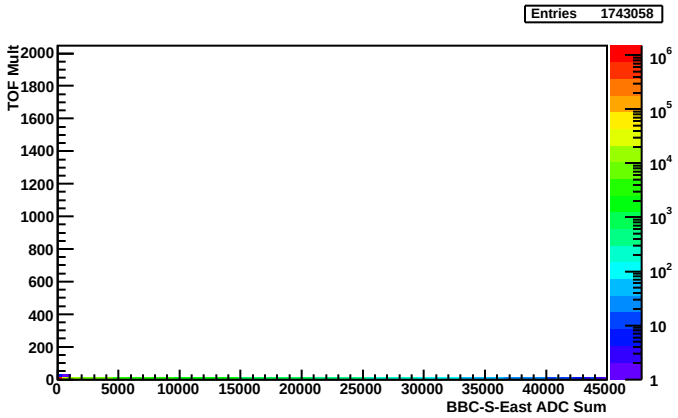

BBQ-VP003 (HI threshold)
Entries 2.788893e+07
TAC
QT Input Channel

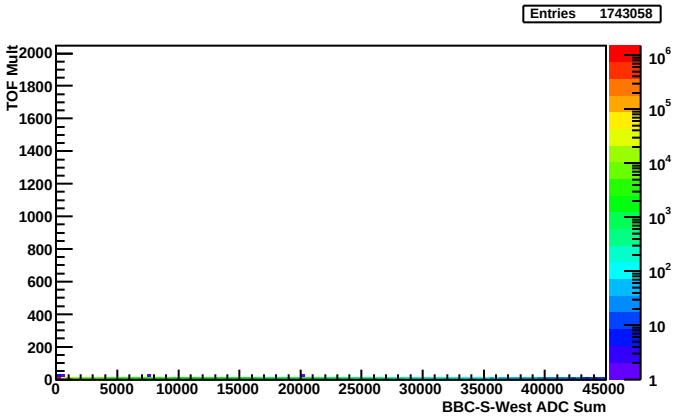

BBQ-VP004 (HI threshold)
Entries 2.788893e+07
ADC
QT Input Channel

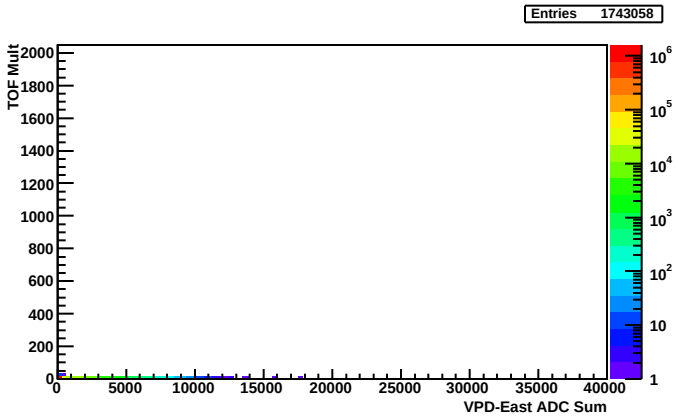

BBQ-VP004 (HI threshold)
Entries 2.788893e+07
TAC
QT Input Channel

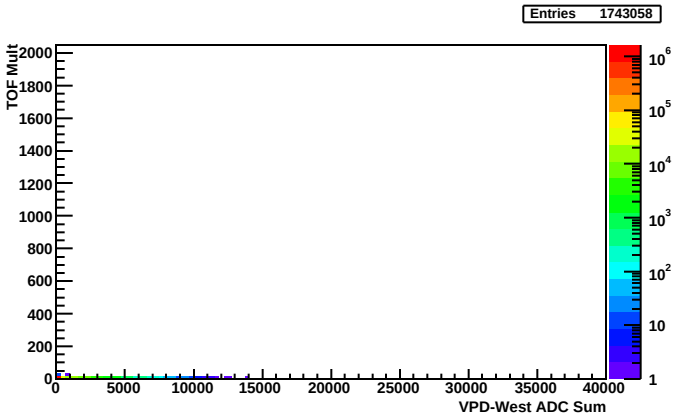

Entries 1743058
TOF Mult
BBC-L-East ADC Sum

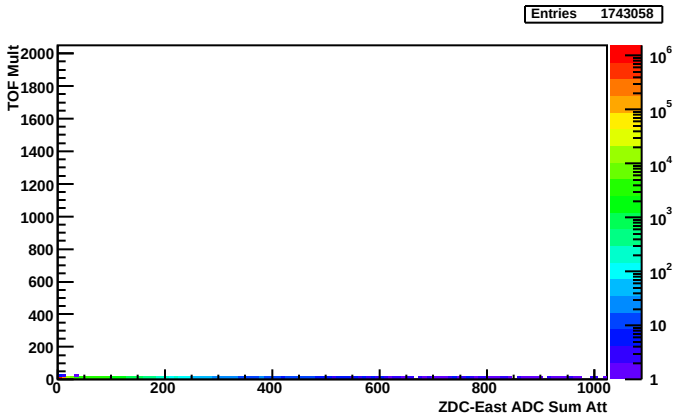

Entries 1743058
TOF Mult
BBC-L-West ADC Sum

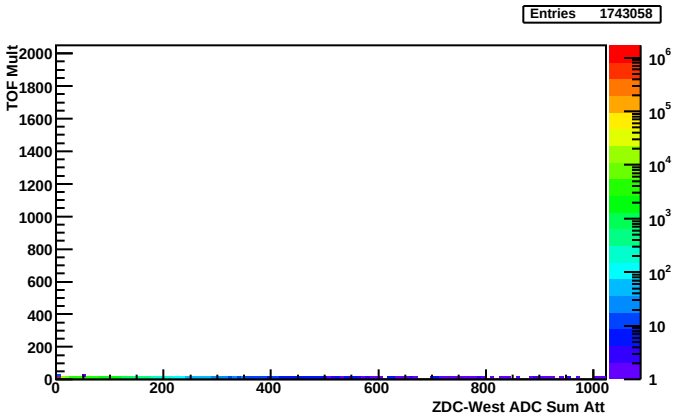

Entries 1743058
TOF Mult
BBC-S-East ADC Sum

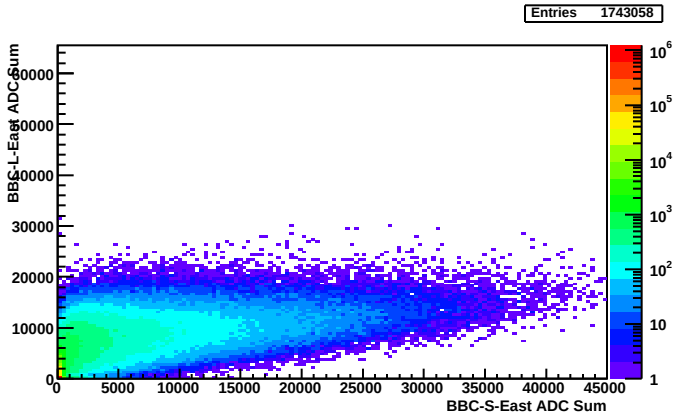

Entries 1743058
TOF Mult
BBC-S-West ADC Sum

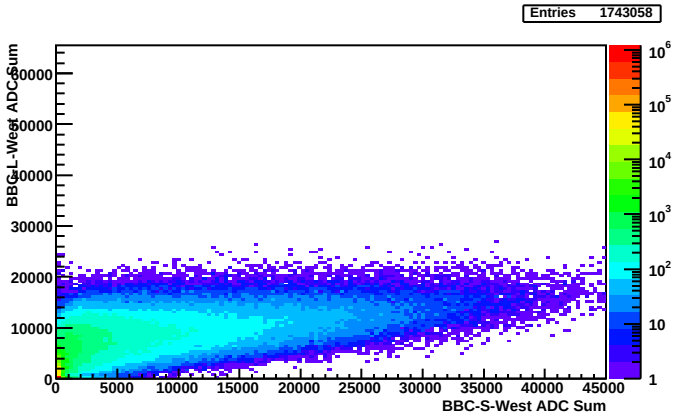

Entries 1743058
TOF Mult
VPD-East ADC Sum

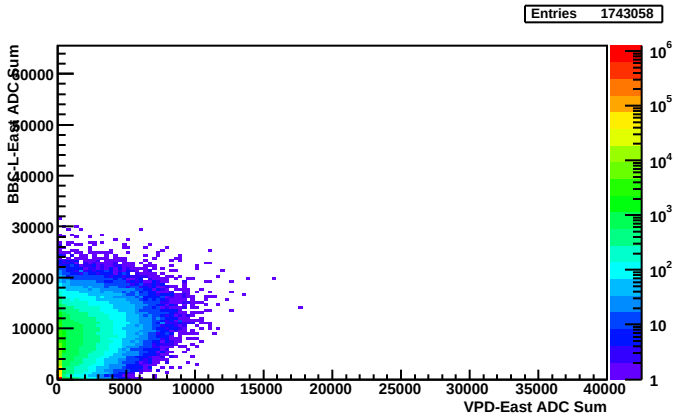

Entries 1743058
TOF Mult
VPD-West ADC Sum

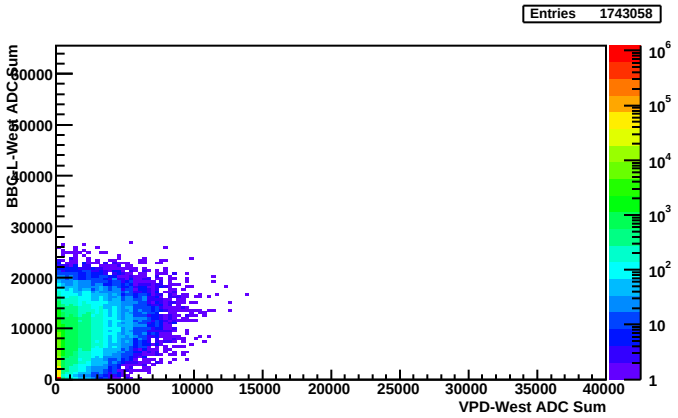

Entries 1743058
TOF Mult
ZDC-East ADC Sum Att

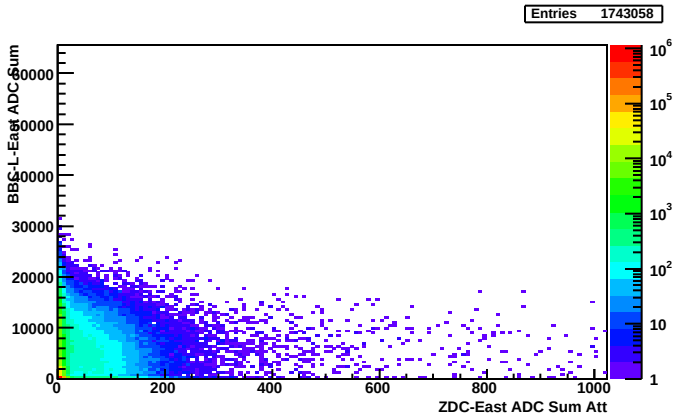

Entries 1743058
TOF Mult
ZDC-West ADC Sum Att

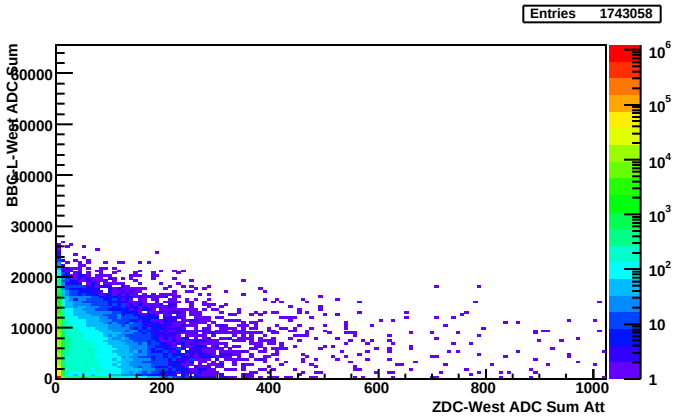

Entries 1743058
BBC-L-East ADC Sum
BBC-S-East ADC Sum

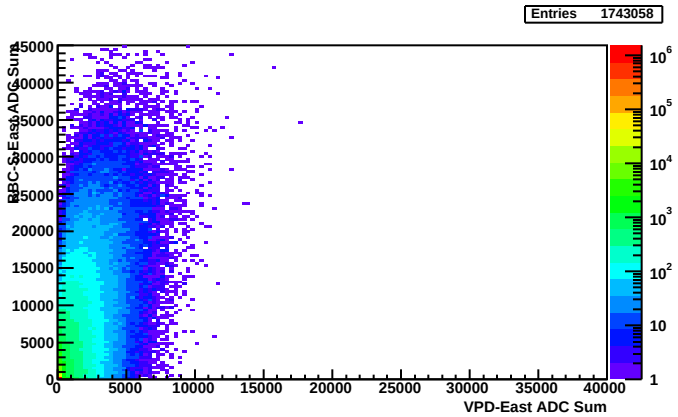

Entries 1743058
BBC-L-West ADC Sum
BBC-S-West ADC Sum

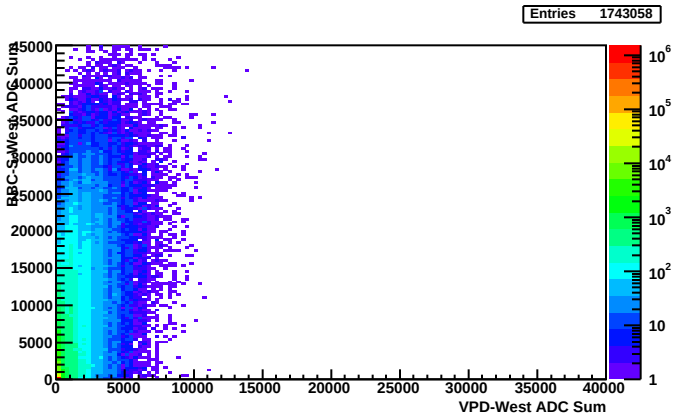

Entries 1743058
BBC-L-East ADC Sum
VPD-East ADC Sum

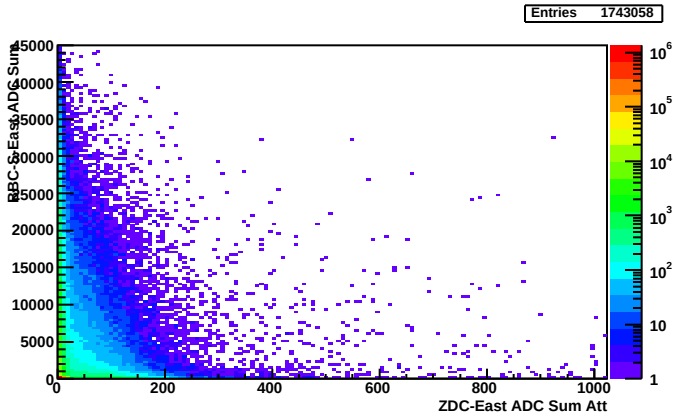

Entries 1743058
BBC-L-West ADC Sum
VPD-West ADC Sum

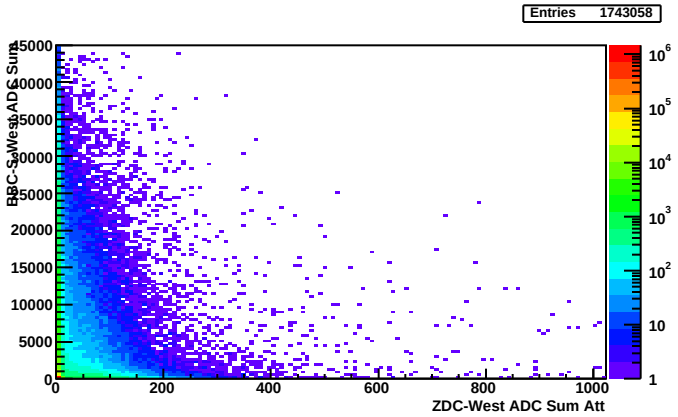

Entries 1743058
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

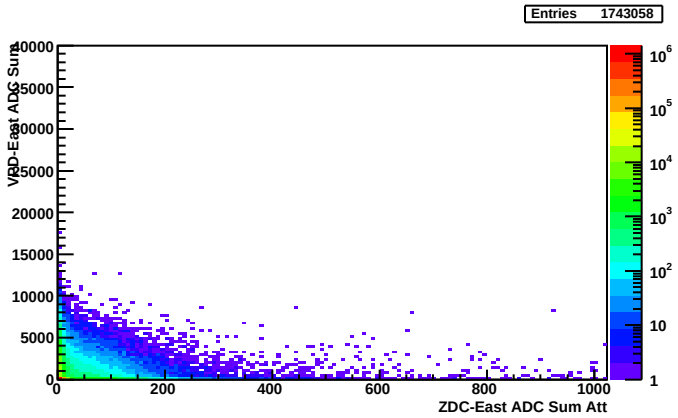

Entries 1743058
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

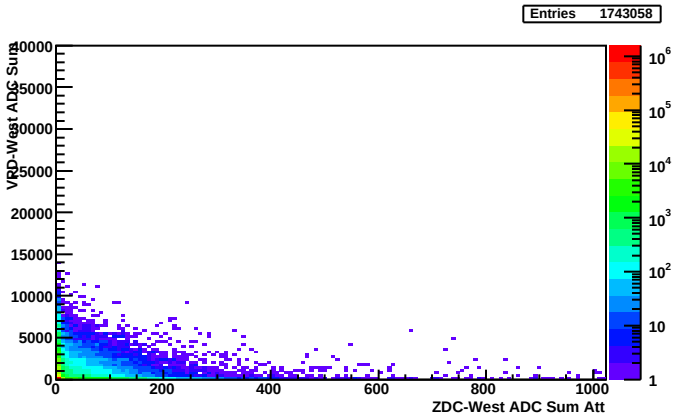

Entries 1743058
BBC-S-East ADC Sum
VPD-East ADC Sum

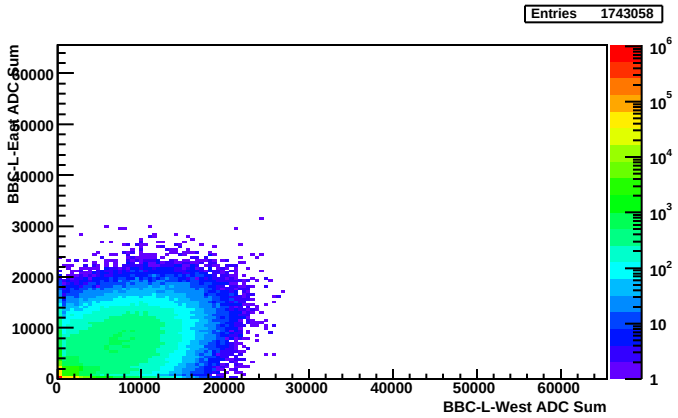

Entries 1743058
BBC-S-West ADC Sum
VPD-West ADC Sum

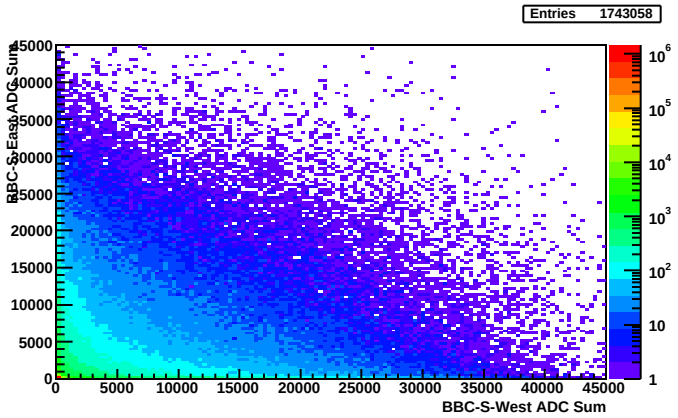

Entries 1743058
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

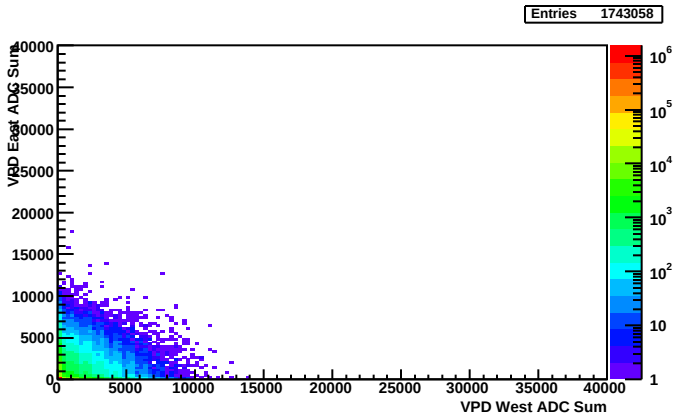

Entries 1743058
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

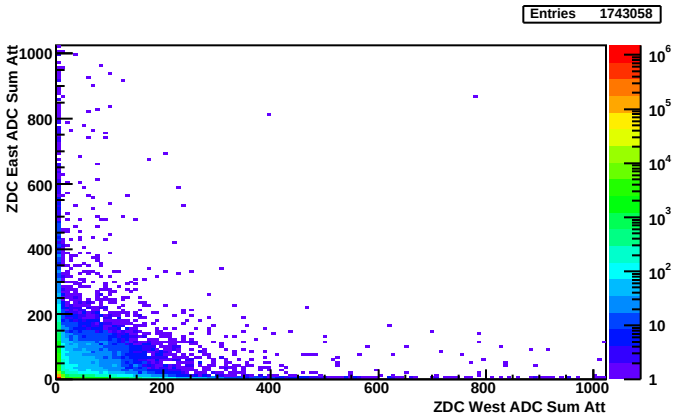

Entries 1743058
VPD-East ADC Sum
ZDC-East ADC Sum Att

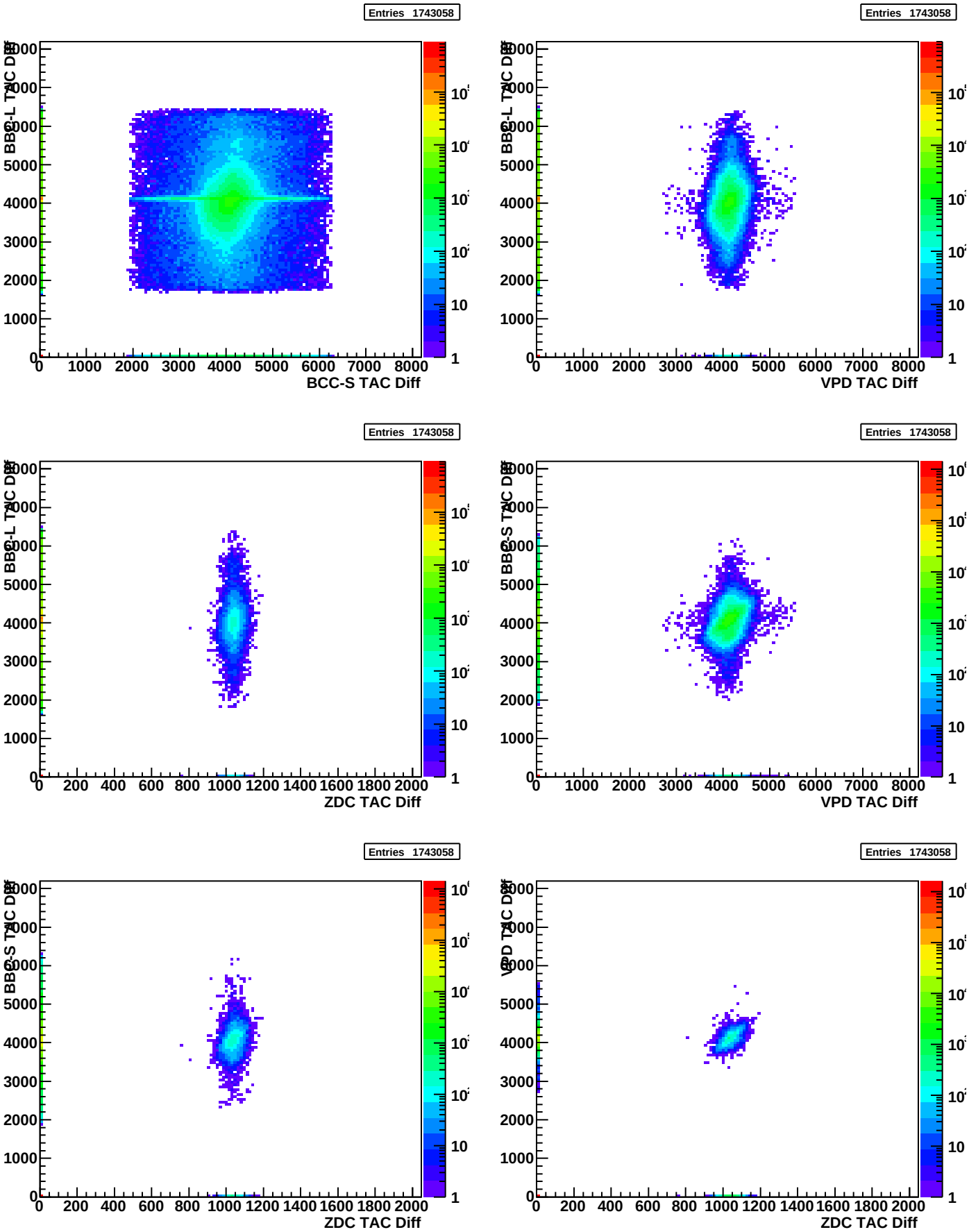

Entries 1743058
VPD-West ADC Sum
ZDC-West ADC Sum Att

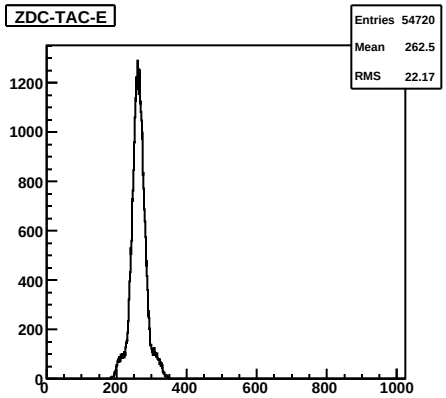

Entries 1743058
BBC-L-East ADC Sum
BBC-L-West ADC Sum

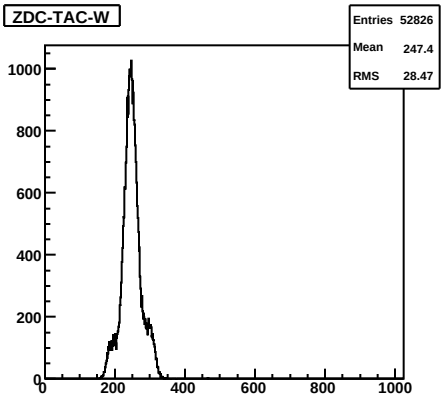

Entries 1743058
BBC-S-East ADC Sum
BBC-S-West ADC Sum

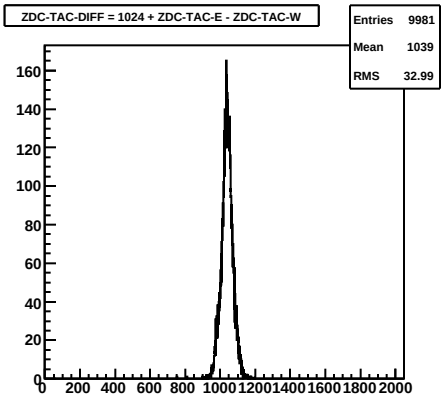

Entries 1743058
VPD East ADC Sum
VPD West ADC Sum

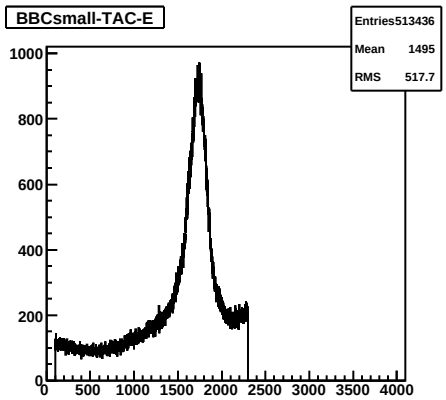

Entries 1743058
ZDC East ADC Sum Att
ZDC West ADC Sum Att

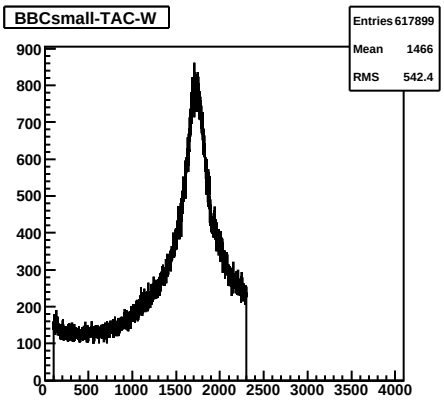

Entries 1743058
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1743058
BBC-L TAC Diff
VPD TAC Diff
Entries 1743058
BBC-L TAC Diff
ZDC TAC Diff
Entries 1743058
BBC-S TAC Diff
VPD TAC Diff
Entries 1743058
BBC-S TAC Diff
ZDC TAC Diff
Entries 1743058
VPD TAC Diff
ZDC TAC Diff

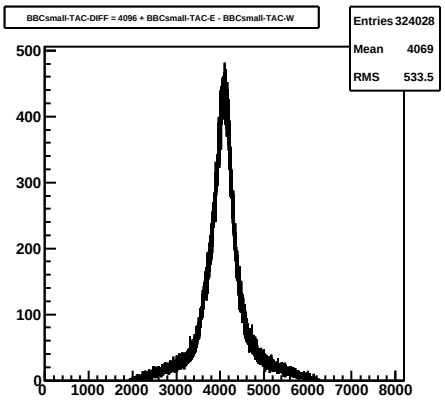

ZDC-TAC-E
Entries 54720
Mean 262.5
RMS 22.17

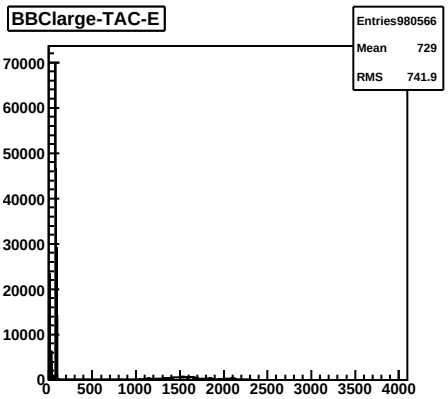

ZDC-TAC-W
Entries 52826
Mean 247.4
RMS 28.47

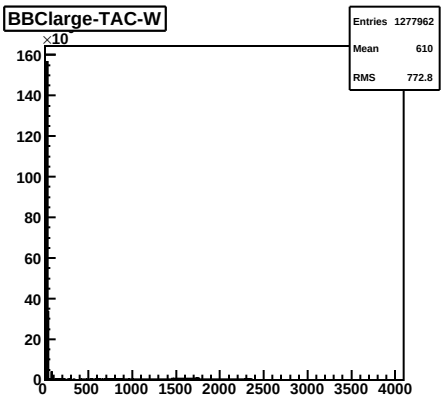

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 9981
Mean 1039
RMS 32.99

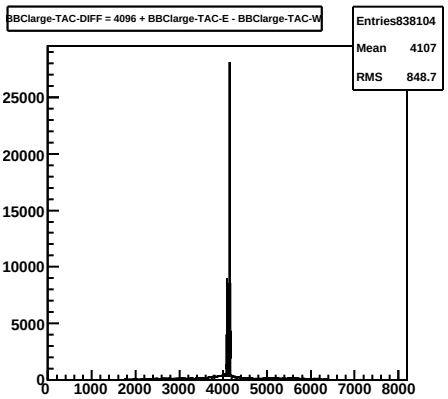

BBCsmall-TAC-E
Entries513436
Mean 1495
RMS 517.7

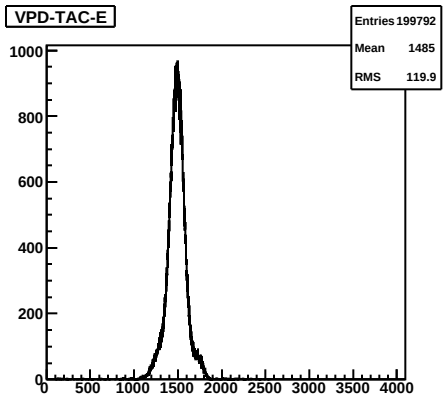

BBCsmall-TAC-W
Entries 617899
Mean 1466
RMS 542.4

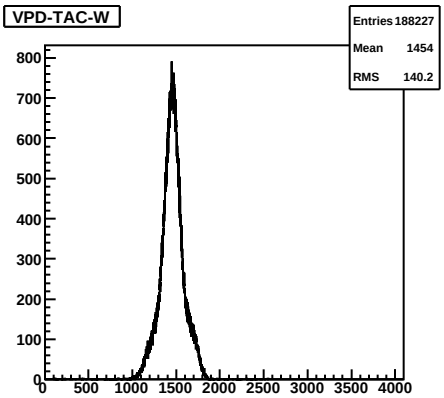

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 324028
Mean 4069
RMS 533.5

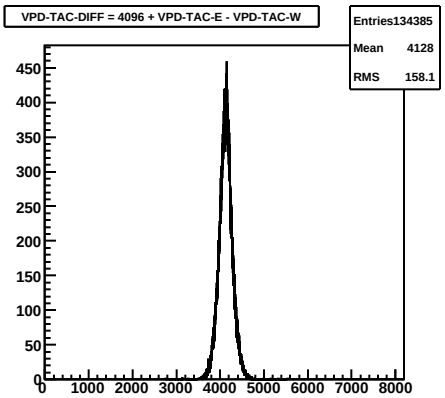

BBClarge-TAC-E
Entries980566
Mean 729
RMS 741.9

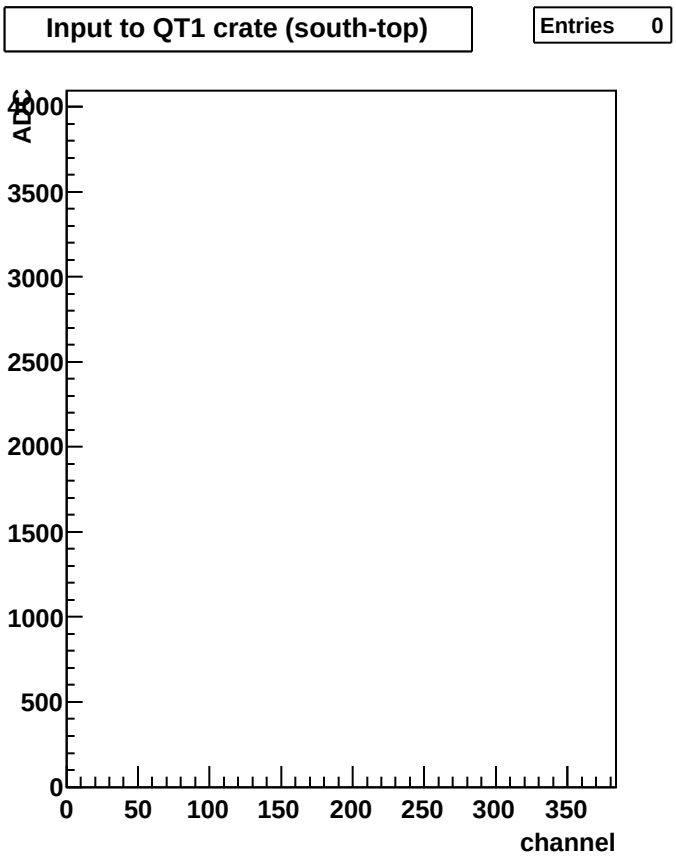

BBClarge-TAC-W
Entries 1277962
Mean 610
RMS 772.8

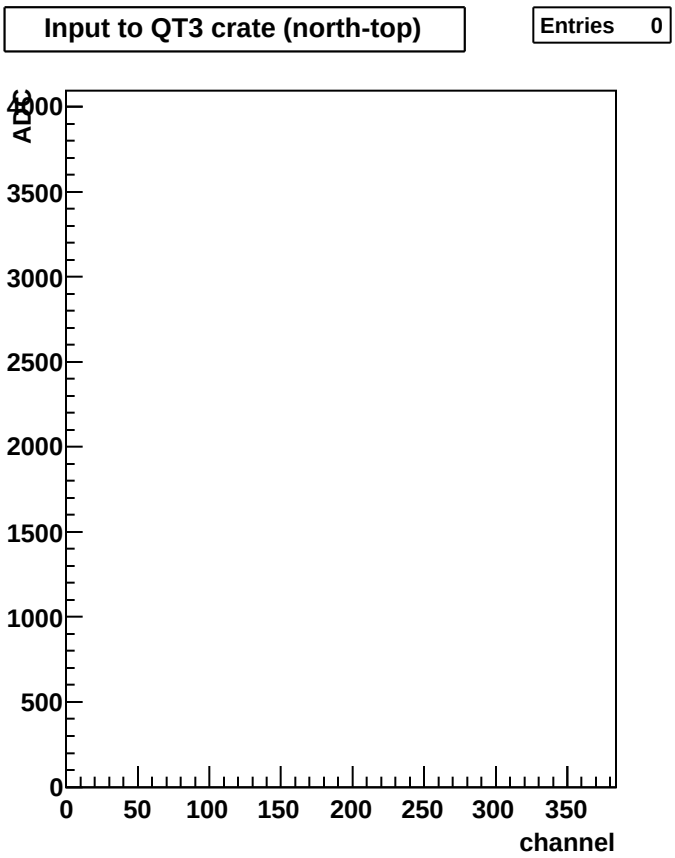

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries838104
Mean 4107
RMS 848.7

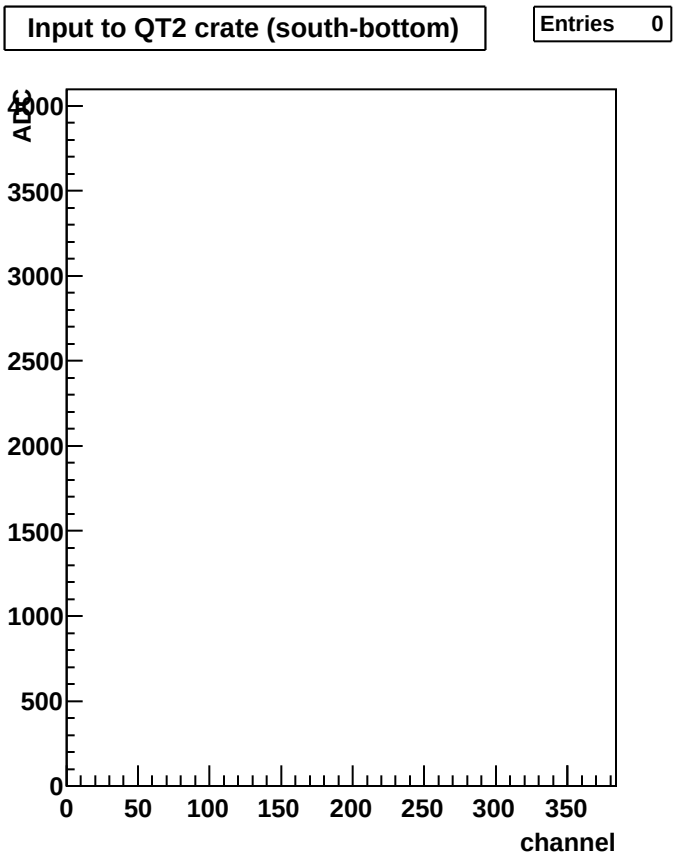

VPD-TAC-E
Entries 199792
Mean 1485
RMS 119.9

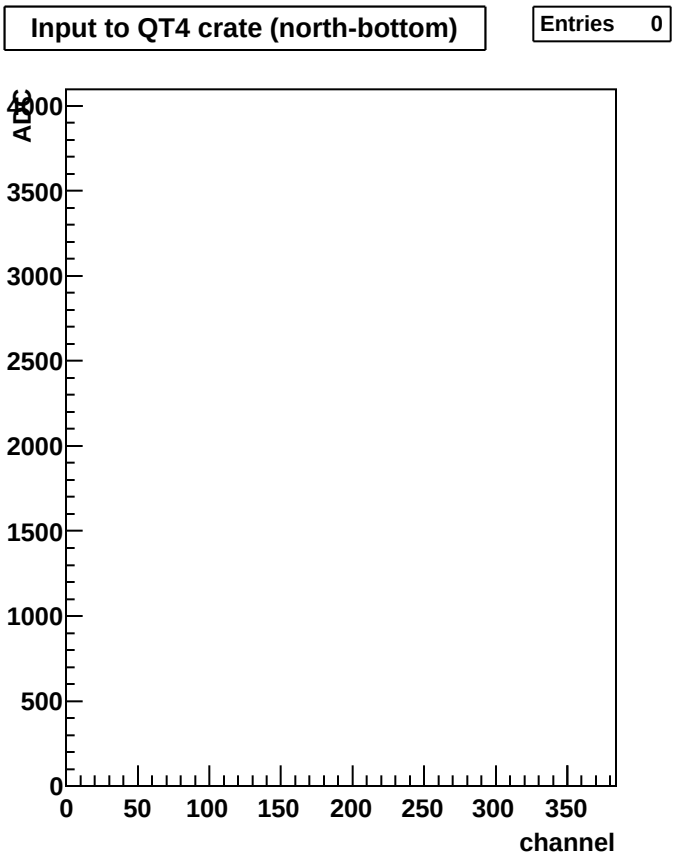

VPD-TAC-W
Entries 188227
Mean 1454
RMS 140.2

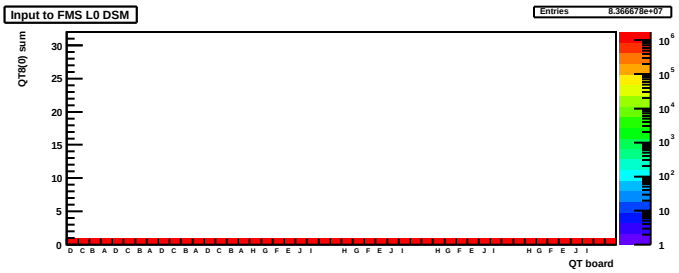

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries134385
Mean 4128
RMS 158.1

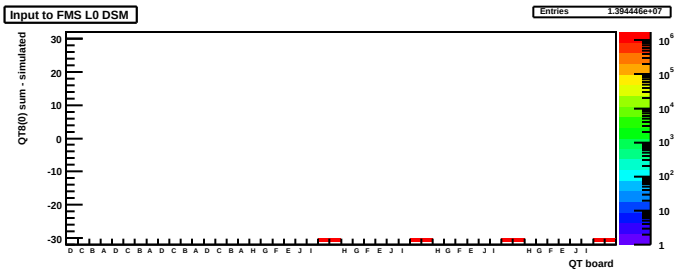

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

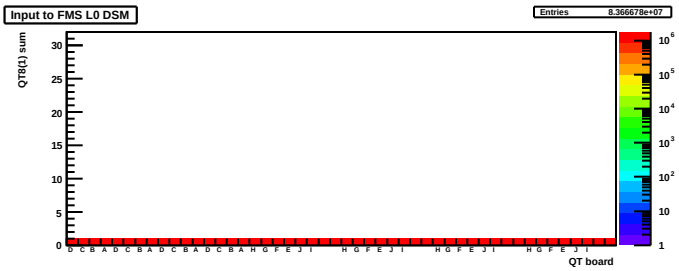

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

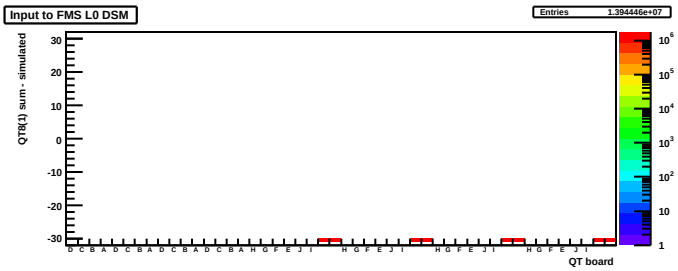

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

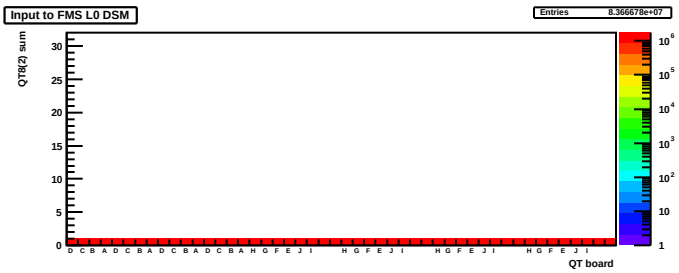

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

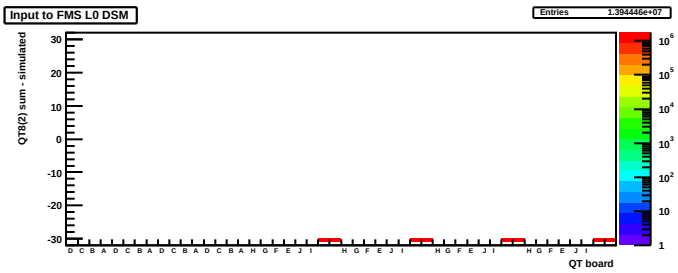

Input to FMS L0 DSM
Entries 8.366678e+07
QT8(0) sum
QT board

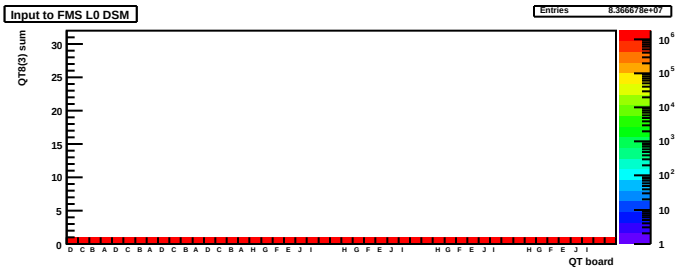

Input to FMS L0 DSM
Entries 1.394446e+07
QT8(0) sum - simulated
QT board

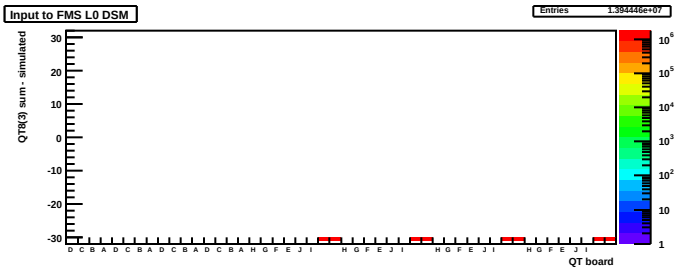

Input to FMS L0 DSM
Entries 8.366678e+07
QT8(1) sum
QT board

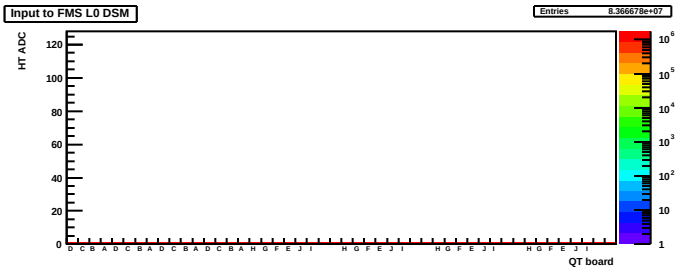

Input to FMS L0 DSM
Entries 1.394446e+07
QT8(1) sum - simulated
QT board

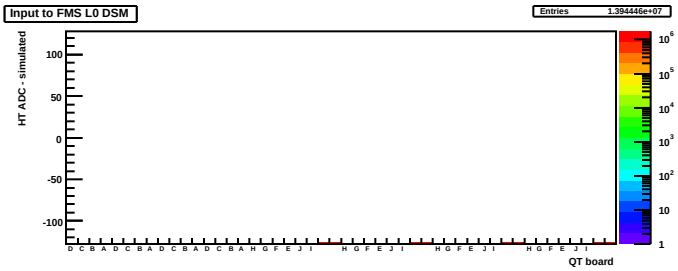

Input to FMS L0 DSM
Entries 8.366678e+07
QT8(2) sum
QT board

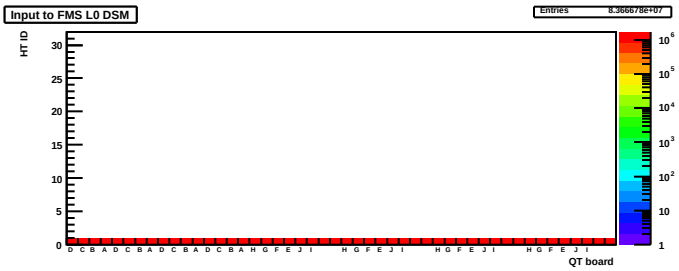

Input to FMS L0 DSM
Entries 1.394446e+07
QT8(2) sum - simulated
QT board

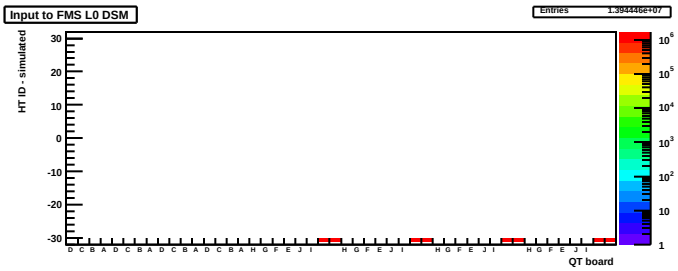

Input to FMS L0 DSM
Entries 8.366678e+07
QT8(3) sum
QT board

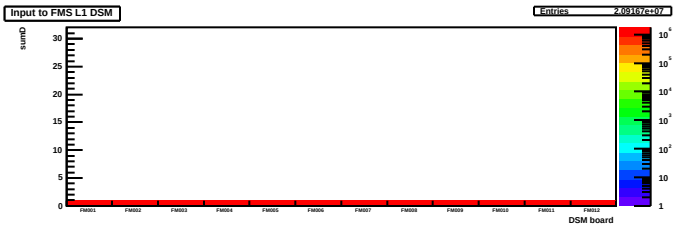

Input to FMS L0 DSM
Entries 1.394446e+07
QT8(3) sum - simulated
QT board

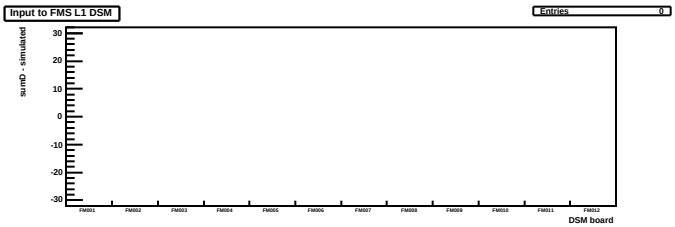

Input to FMS L0 DSM
Entries 8.366678e+07
HT ADC
QT board

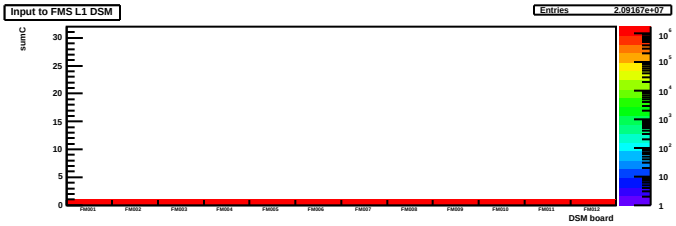

Input to FMS L0 DSM
Entries 1.394446e+07
HT ADC - simulated
QT board

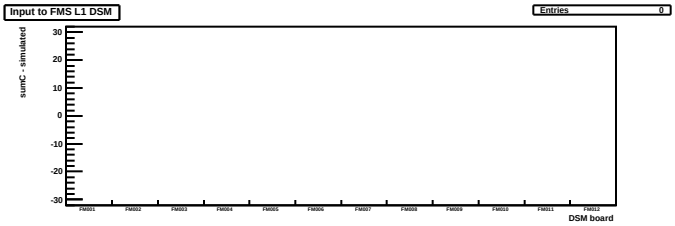

Input to FMS L0 DSM
Entries 8.366678e+07
HT ID
QT board

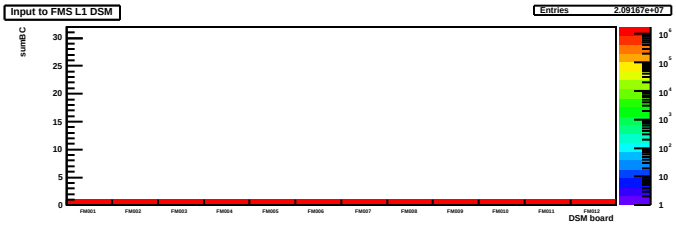

Input to FMS L0 DSM
Entries 1.394446e+07
HT ID - simulated
QT board

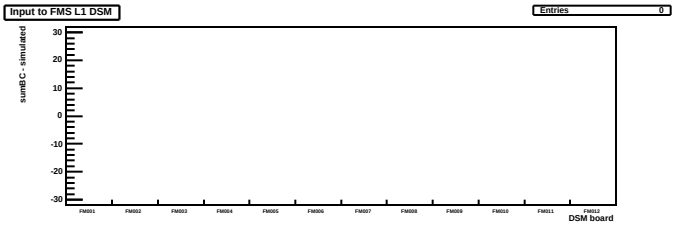

Input to FMS L1 DSM
Entries 2.09167e+07
sumD
DSM board

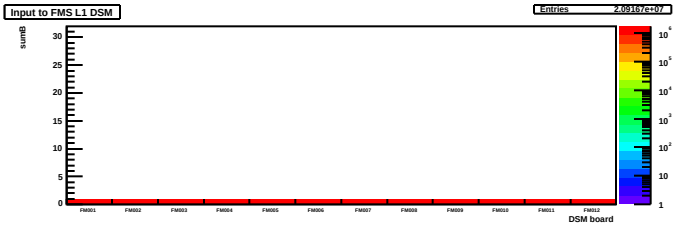

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

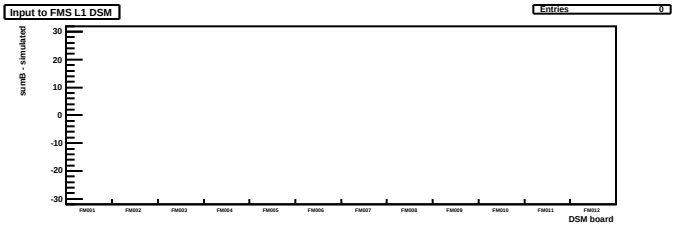

Input to FMS L1 DSM
Entries 2.09167e+07
sumC
DSM board

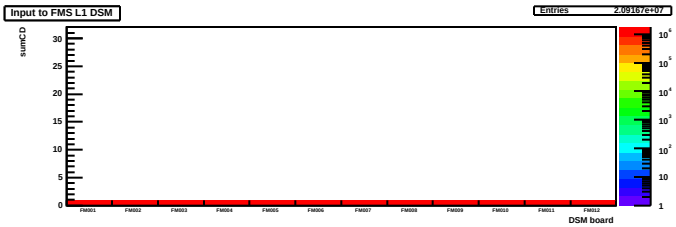

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

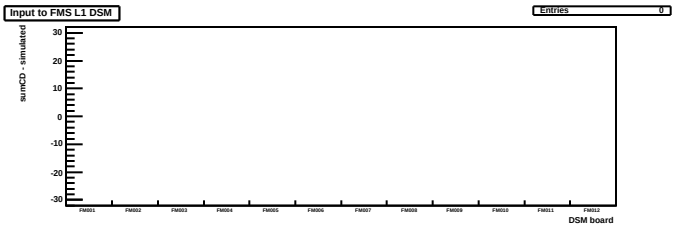

Input to FMS L1 DSM
Entries 2.09167e+07
sumBC
DSM board

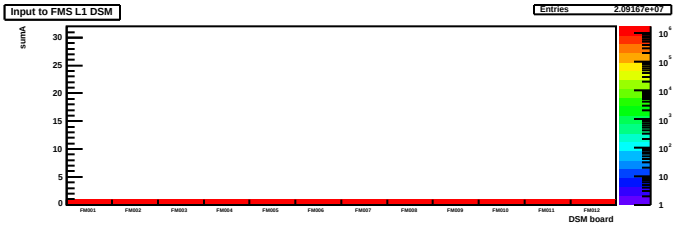

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

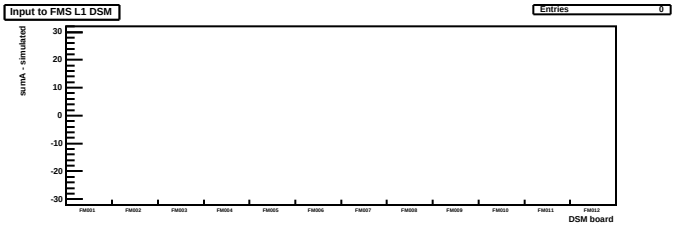

Input to FMS L1 DSM
Entries 2.09167e+07
sumB
DSM board

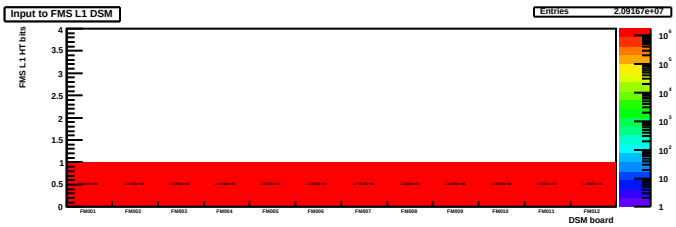

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

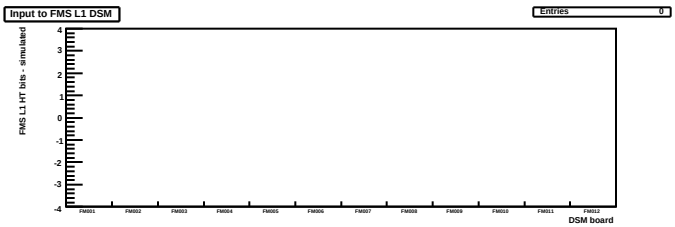

Input to FMS L1 DSM
Entries 2.09167e+07
sumCD
DSM board

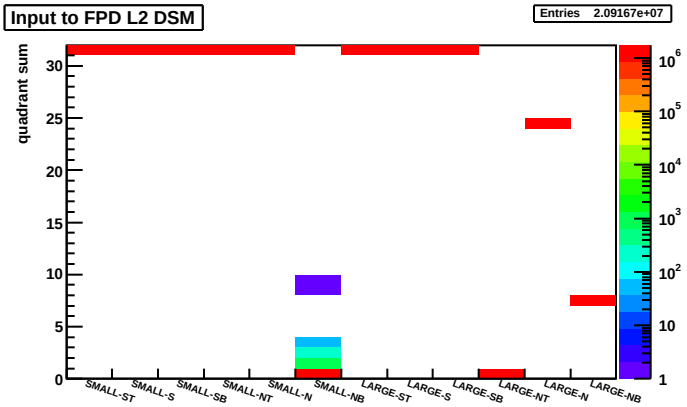

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

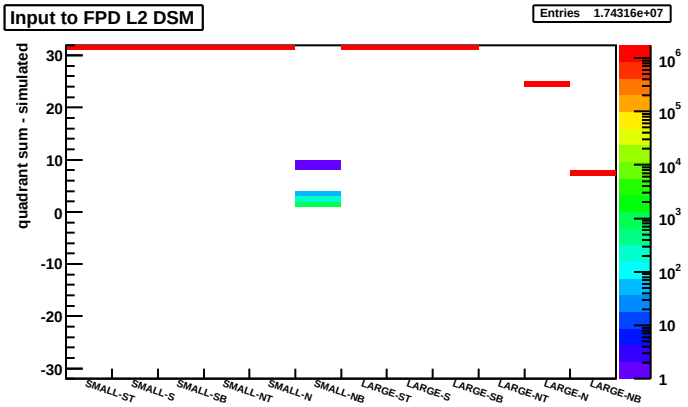

Input to FMS L1 DSM
Entries 2.09167e+07
sumA
DSM board

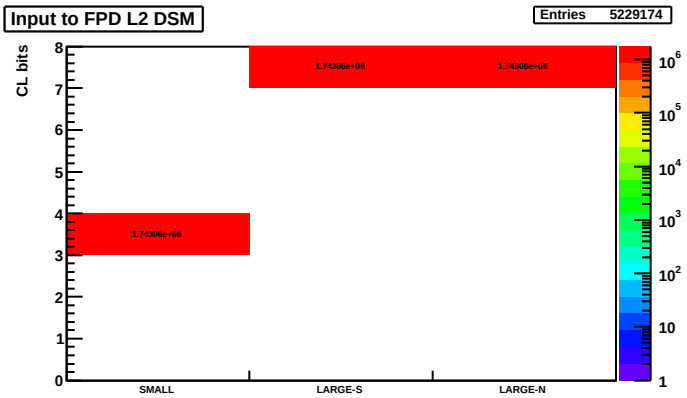

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

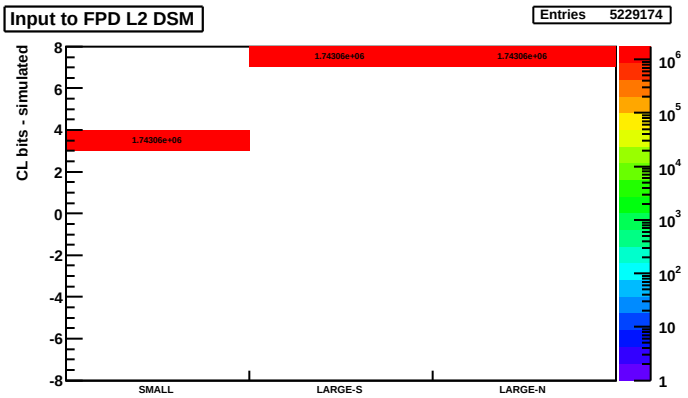

Input to FMS L1 DSM
Entries 2.09167e+07
FMS L1 HT bits
DSM board

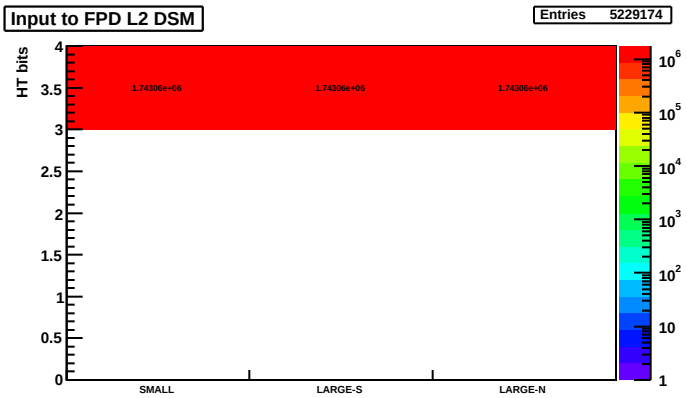

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

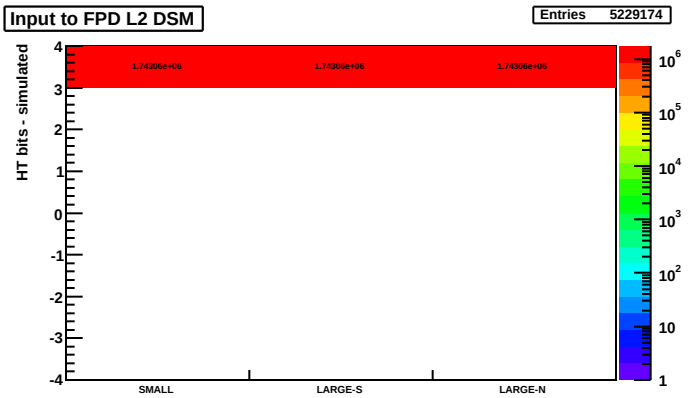

Input to FPD L2 DSM
Entries 2.09167e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

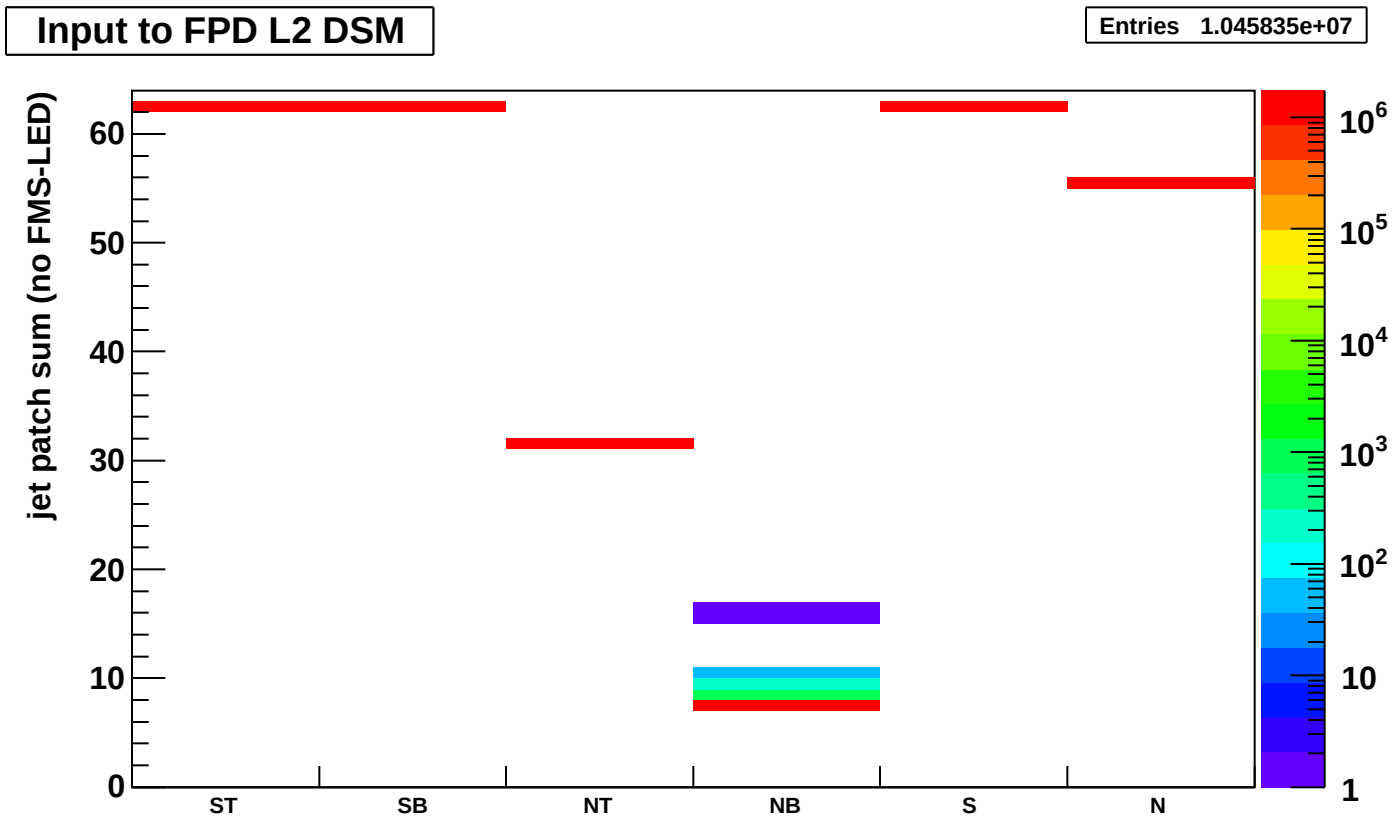

Input to FPD L2 DSM
Entries 1.74316e+07
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

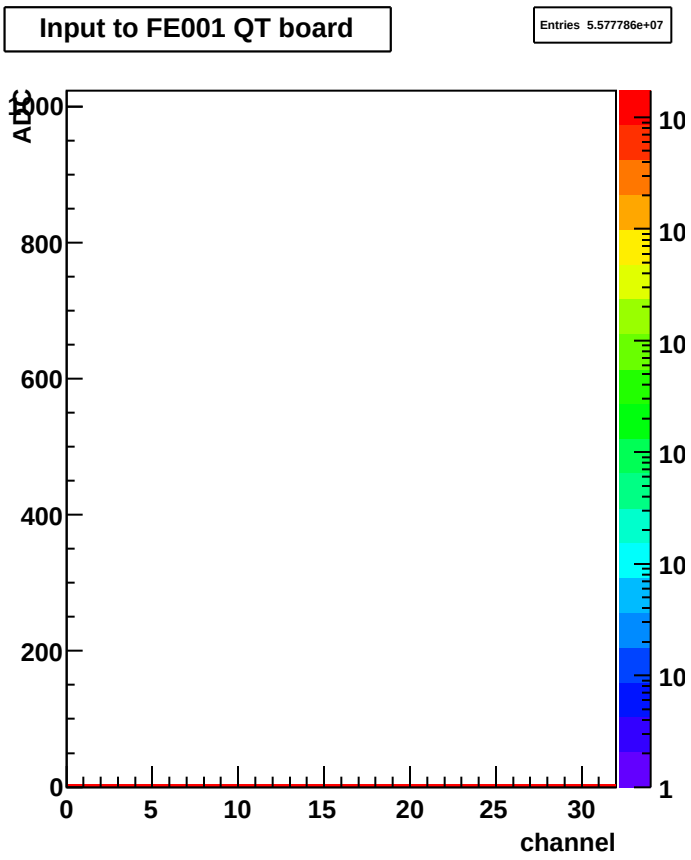

Input to FPD L2 DSM
Entries 5229174
CL bits
1.74306e+06
1.74306e+06
1.74306e+06
SMALL
LARGE-S
LARGE-N

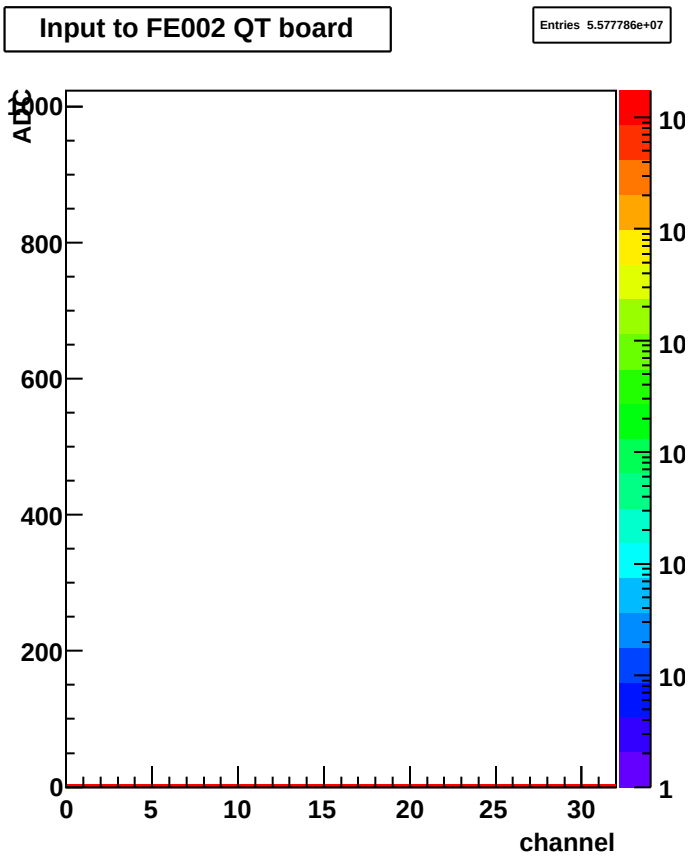

Input to FPD L2 DSM
Entries 5229174
CL bits - simulated
1.74306e+06
1.74306e+06
1.74306e+06
SMALL
LARGE-S
LARGE-N

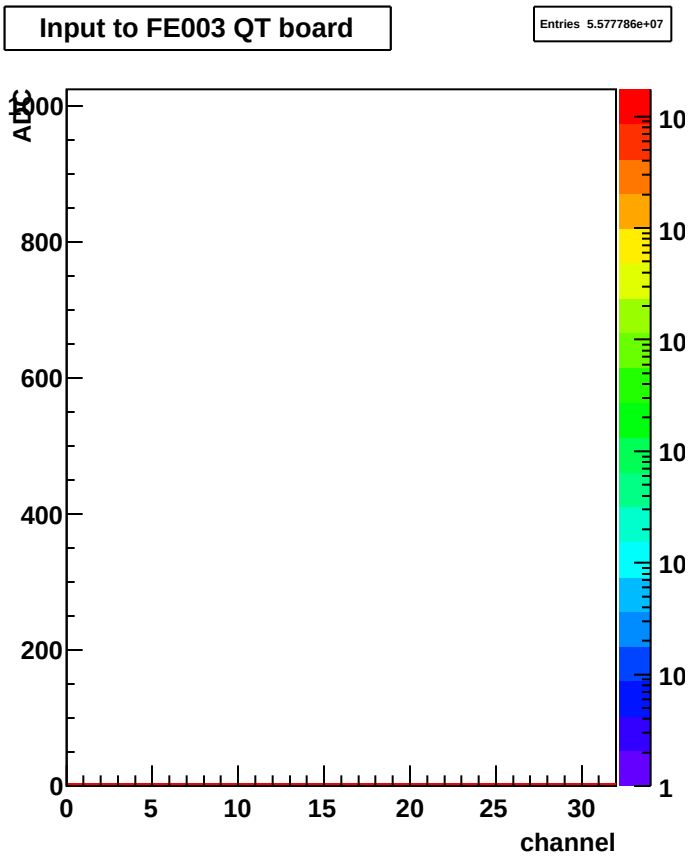

Input to FPD L2 DSM
Entries 5229174
HT bits
1.74306e+06
1.74306e+06
1.74306e+06
SMALL
LARGE-S
LARGE-N

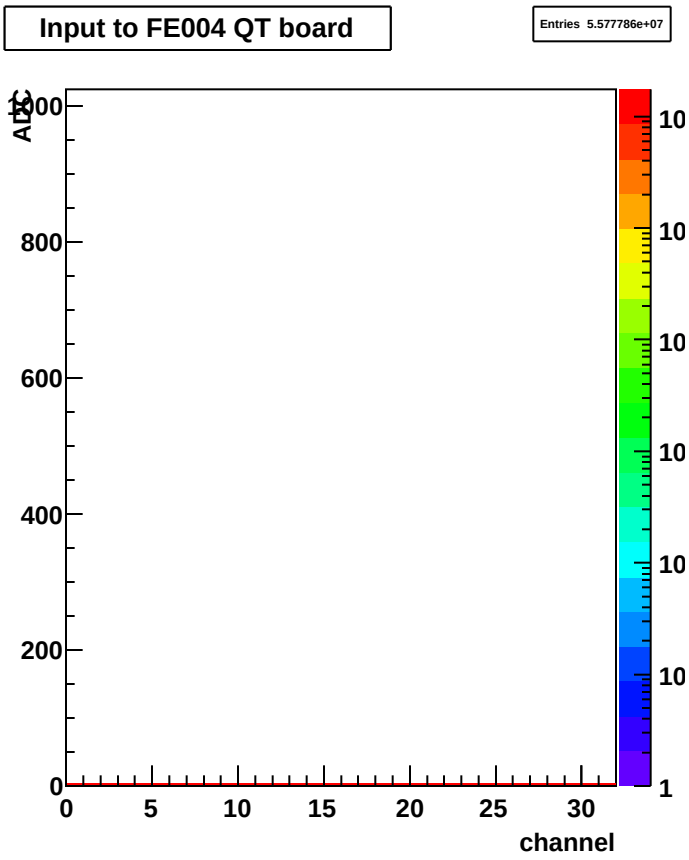

Input to FPD L2 DSM
Entries 5229174
HT bits - simulated
1.74306e+06
1.74306e+06
1.74306e+06
SMALL
LARGE-S
LARGE-N

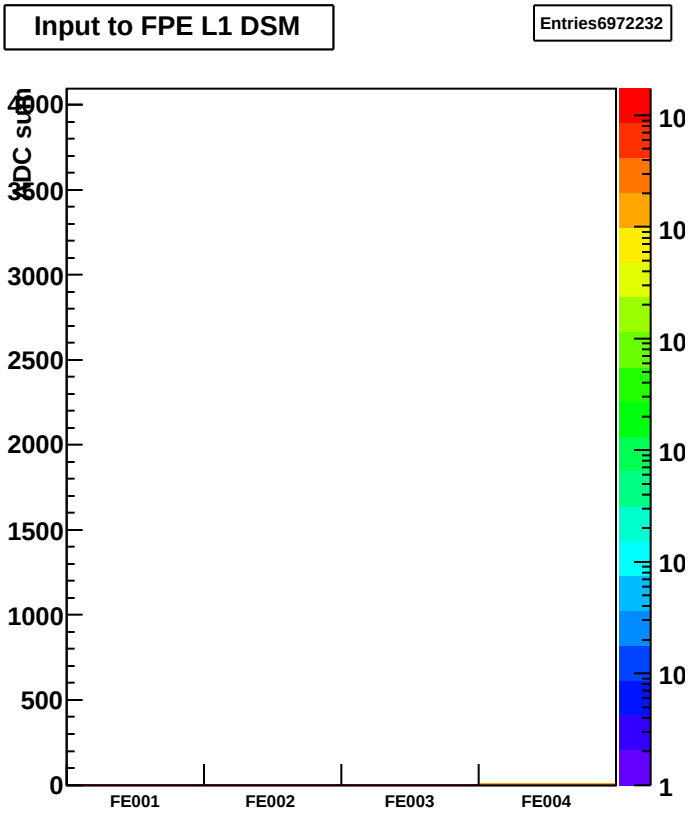

Input to FPD L2 DSM
Entries 1.045835e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

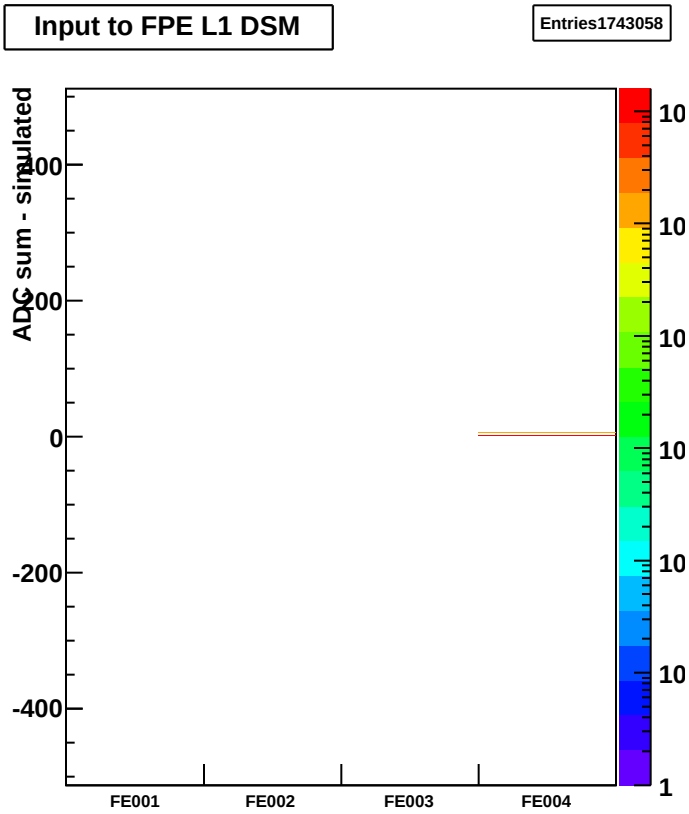

Input to FE001 QT board
Entries 5.577786e+07
ADC
channel


Input to FE002 QT board
Entries 5.577786e+07
ADC
channel


Input to FE003 QT board
Entries 5.577786e+07
ADC
channel


Input to FE004 QT board
Entries 5.577786e+07
ADC
channel


Input to FPE L1 DSM
Entries6972232
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries1743058
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
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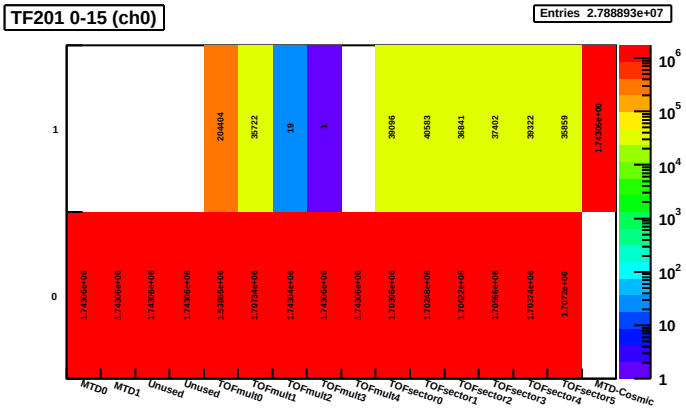
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Entries 2.788893e+07
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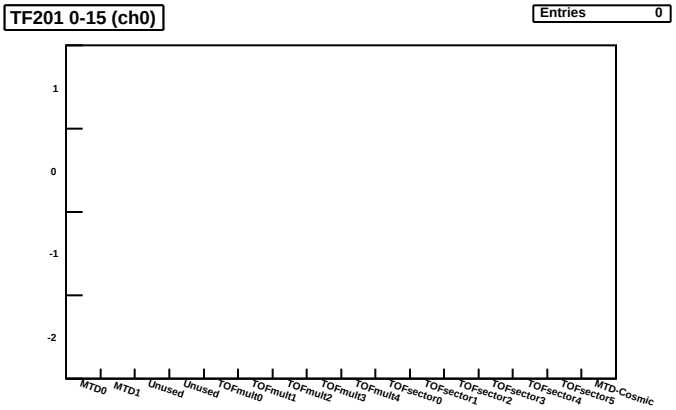
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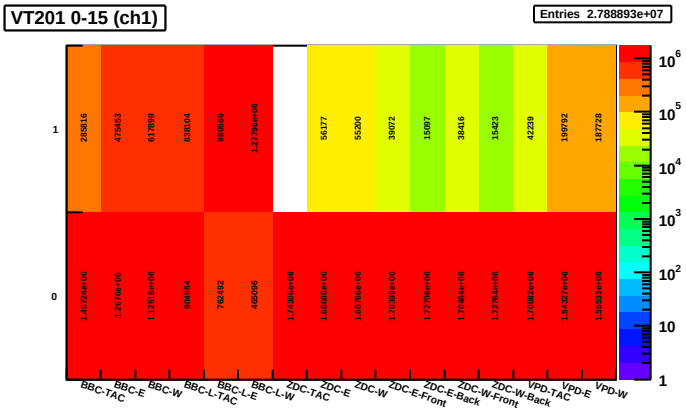
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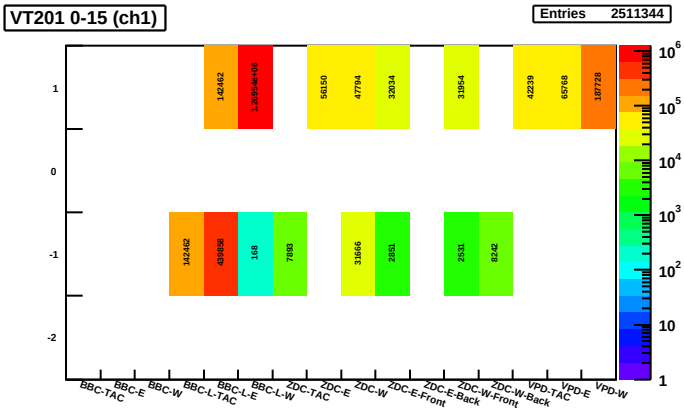
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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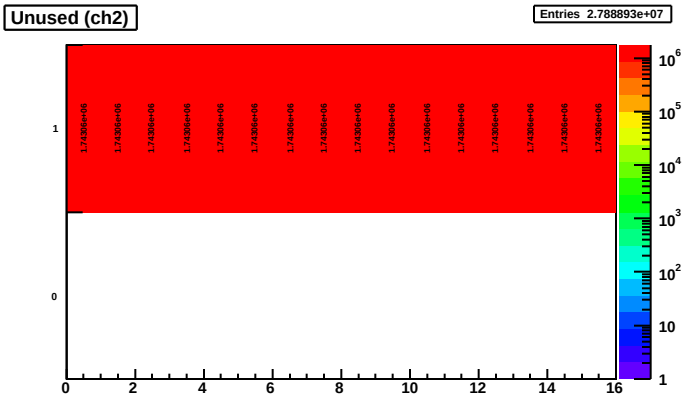
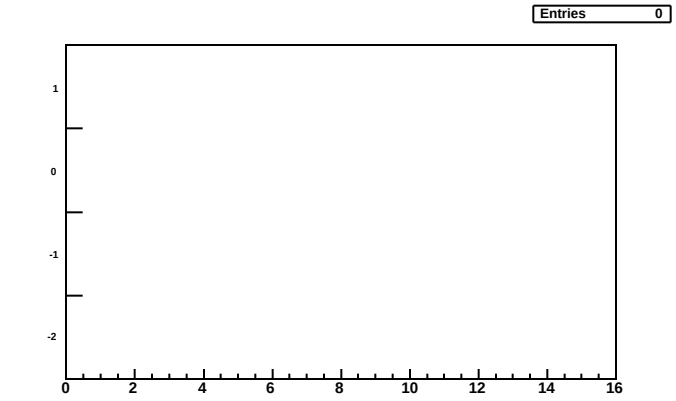

Unused (ch2)
Entries 2.788893e+07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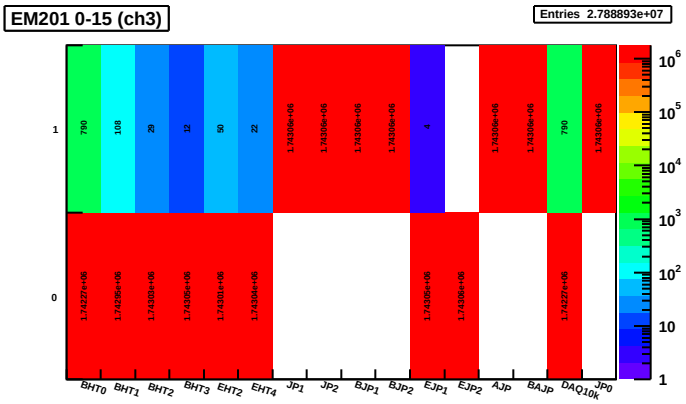
EM201 0-15 (ch3)
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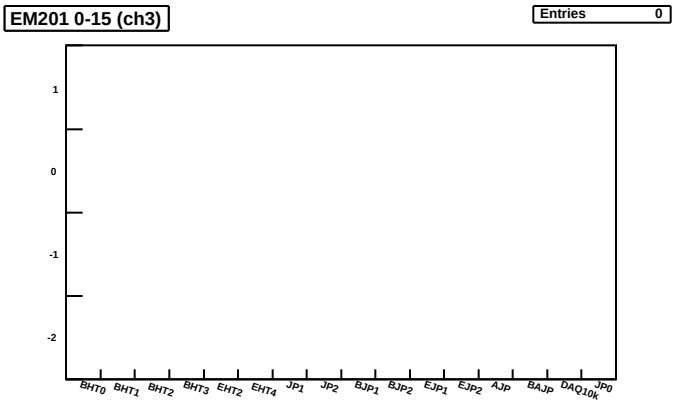
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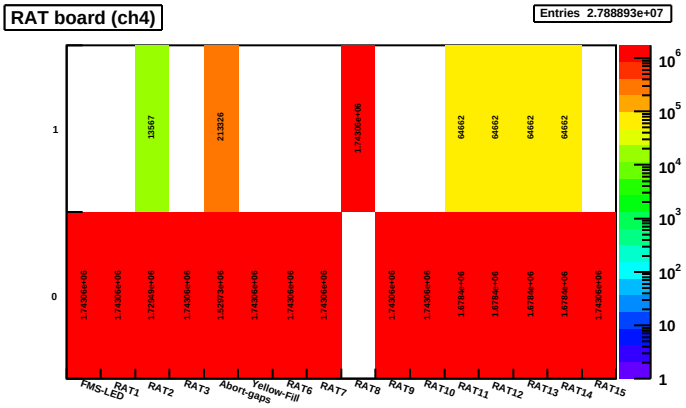
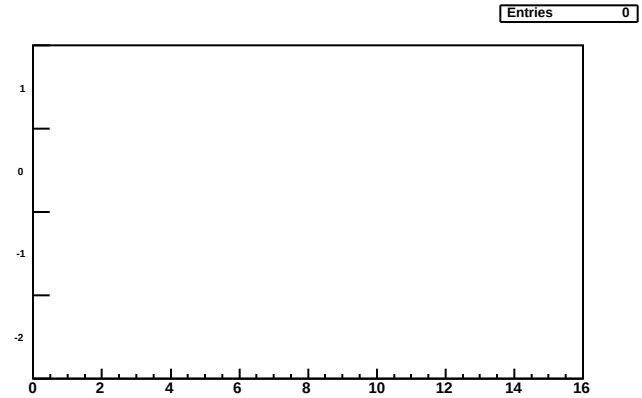
RAT board (ch4)





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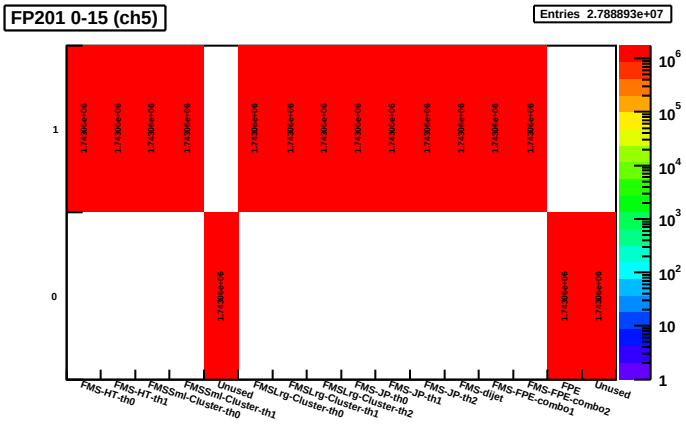

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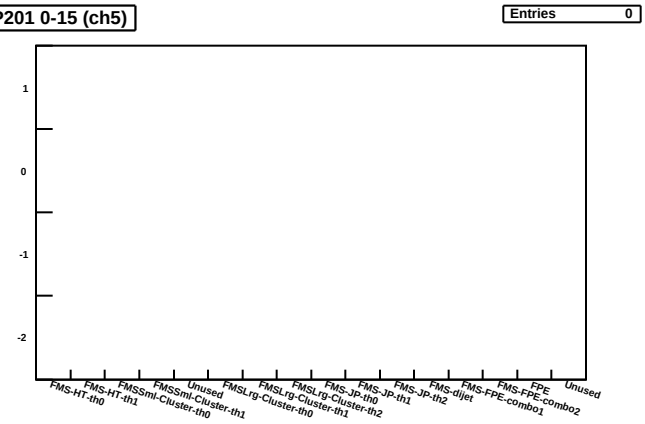
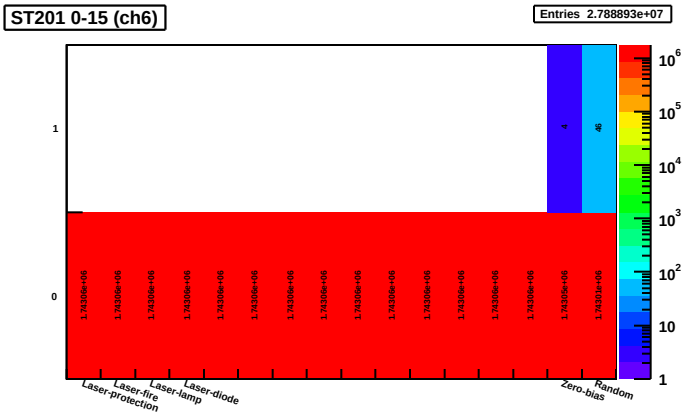
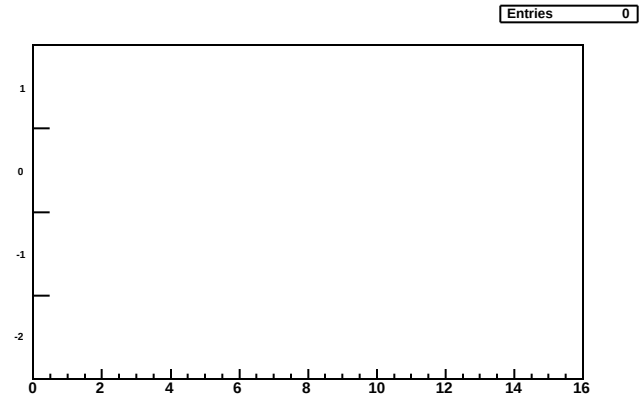
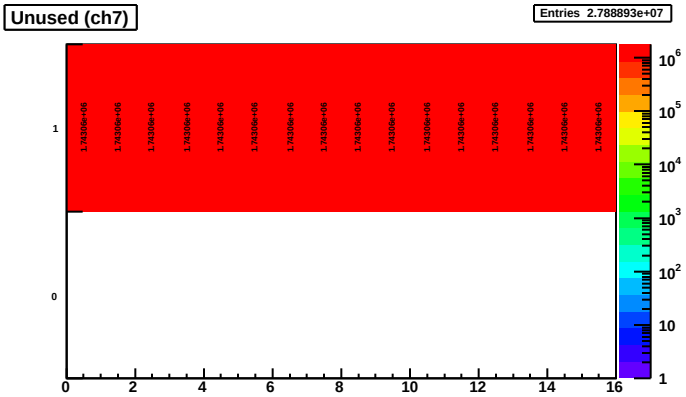
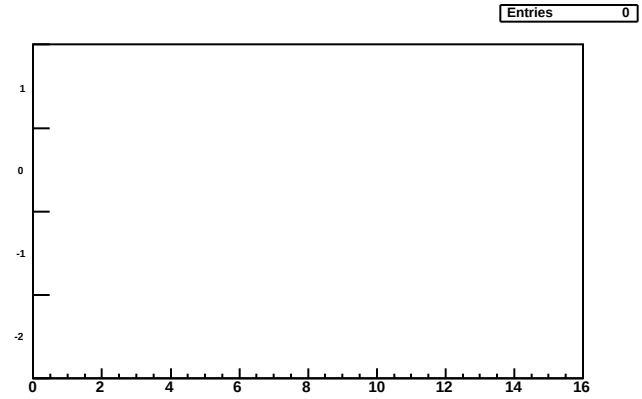


ST201 0-15 (ch6)





Unused (ch7)