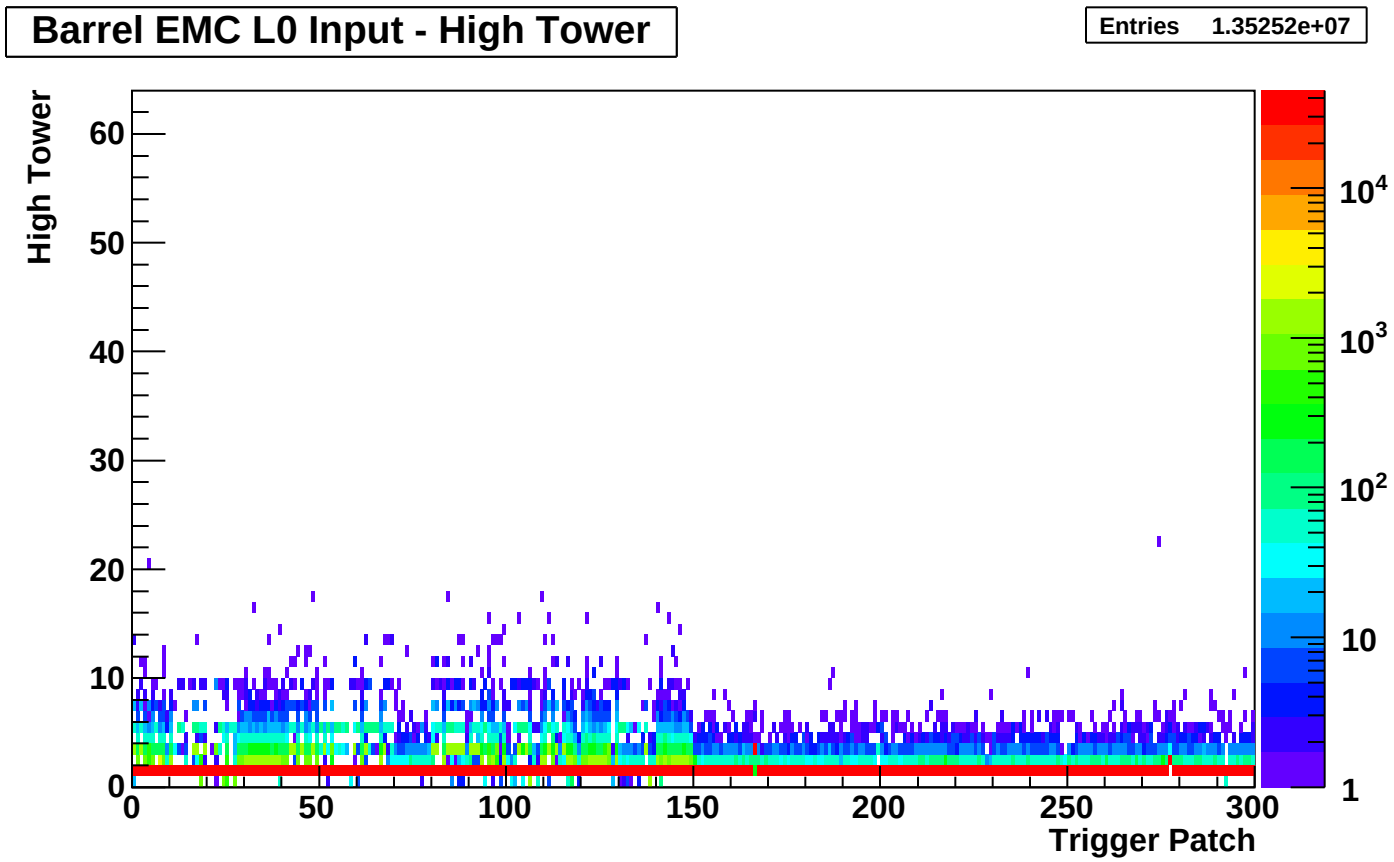
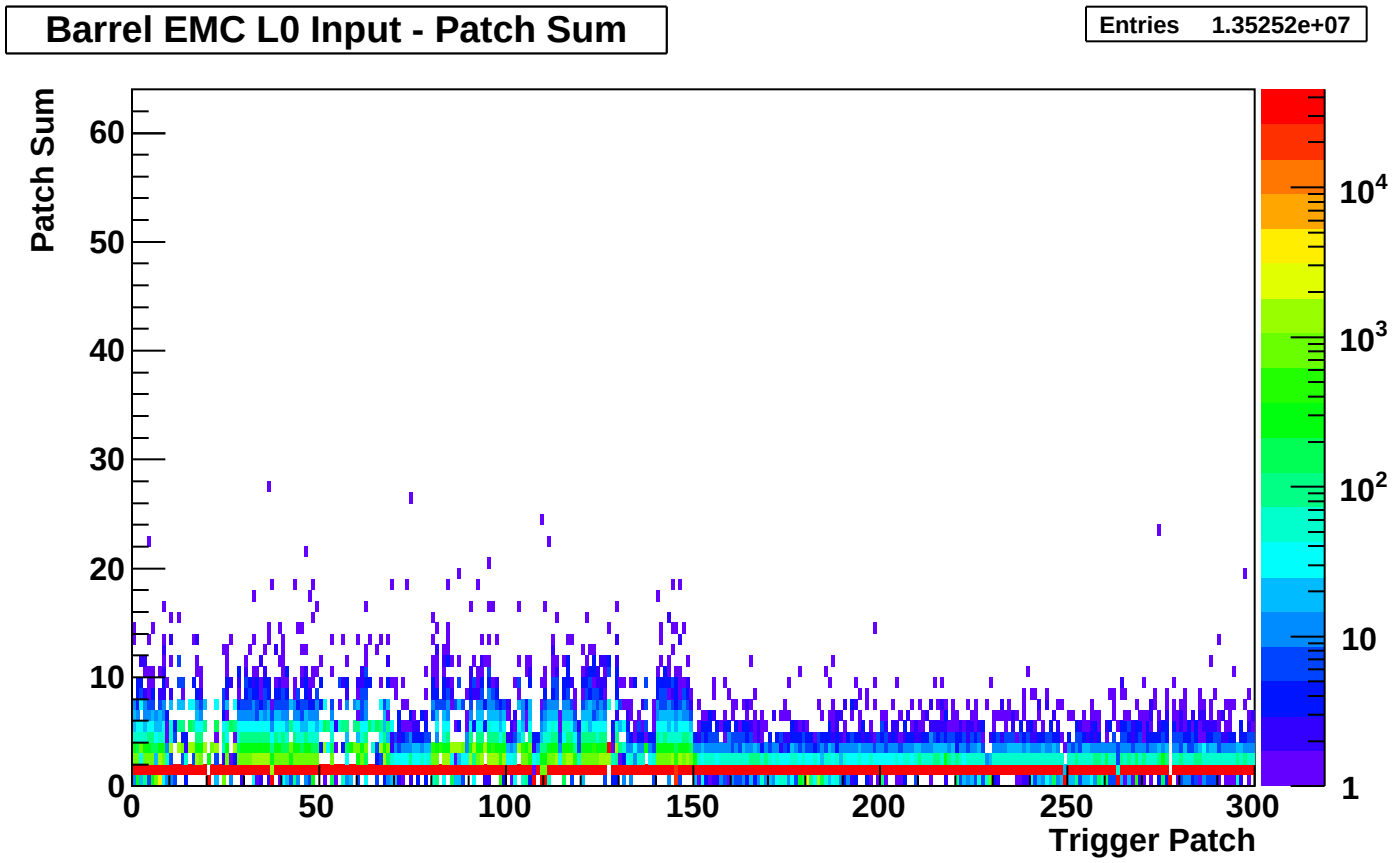
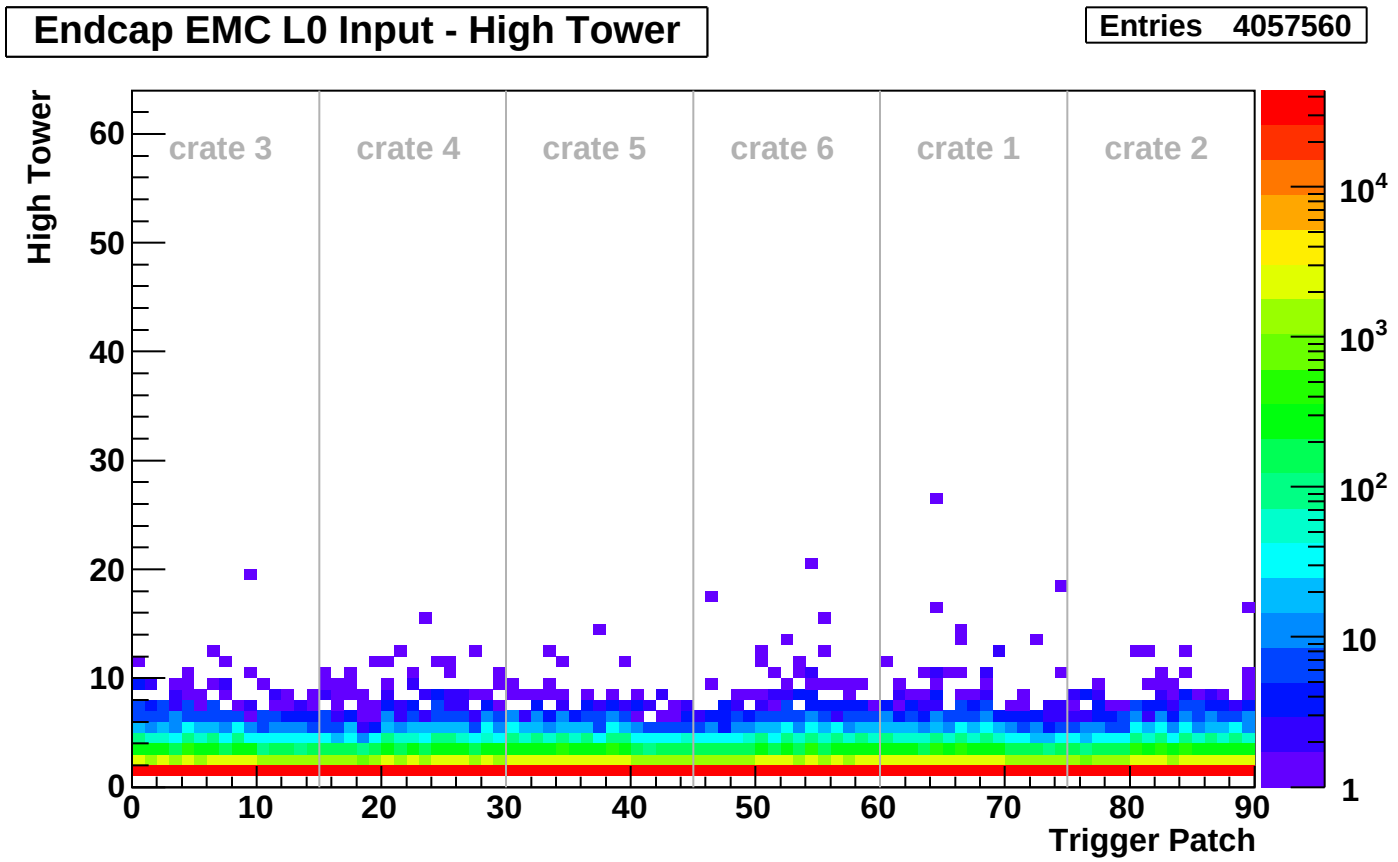
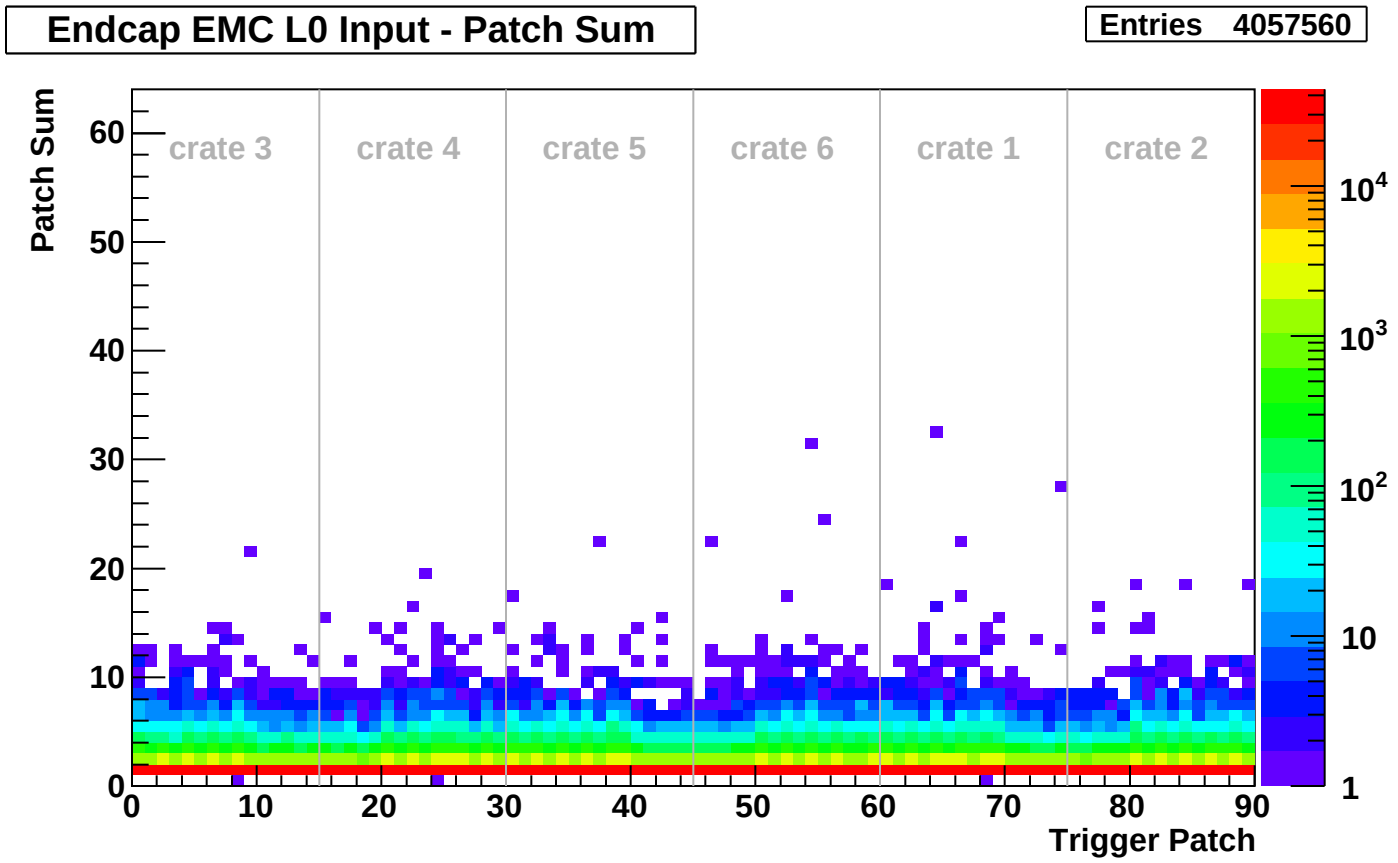
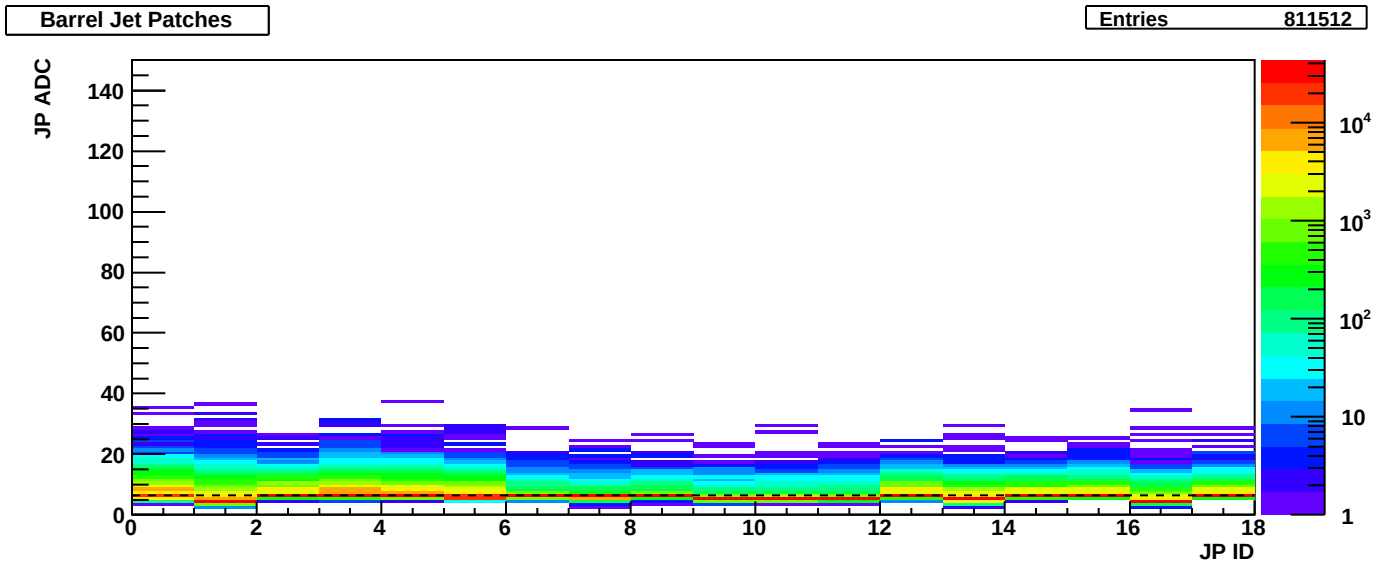
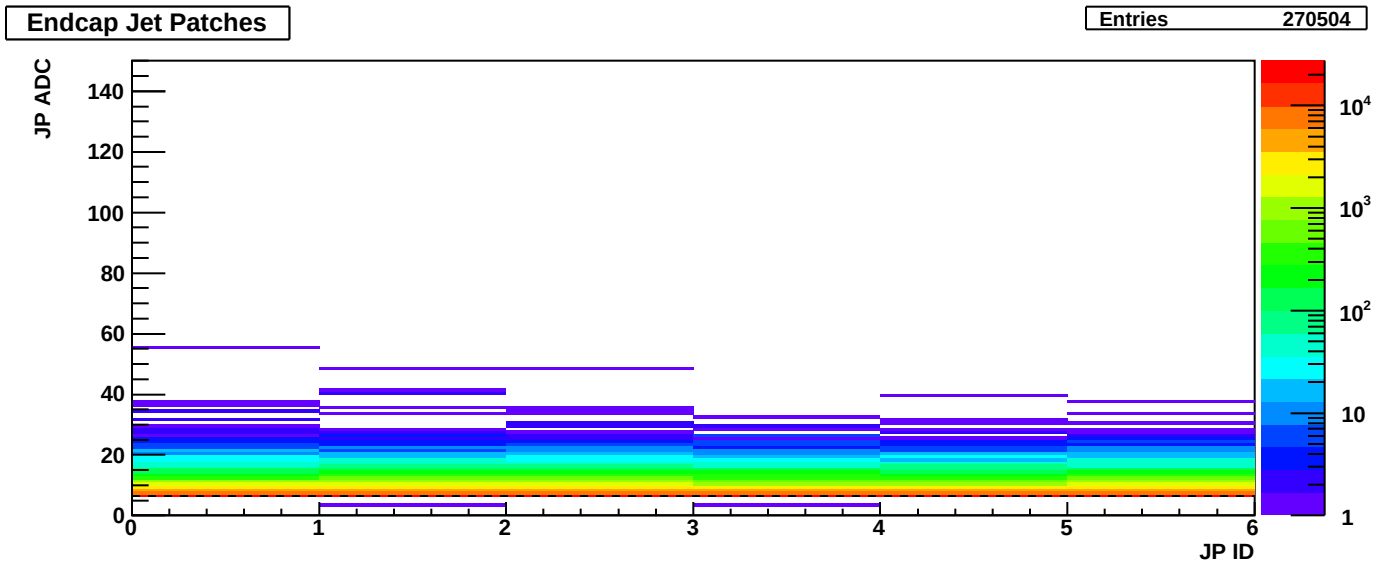

Barrel EMC L0 Input - High Tower
Entries 1.35252e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

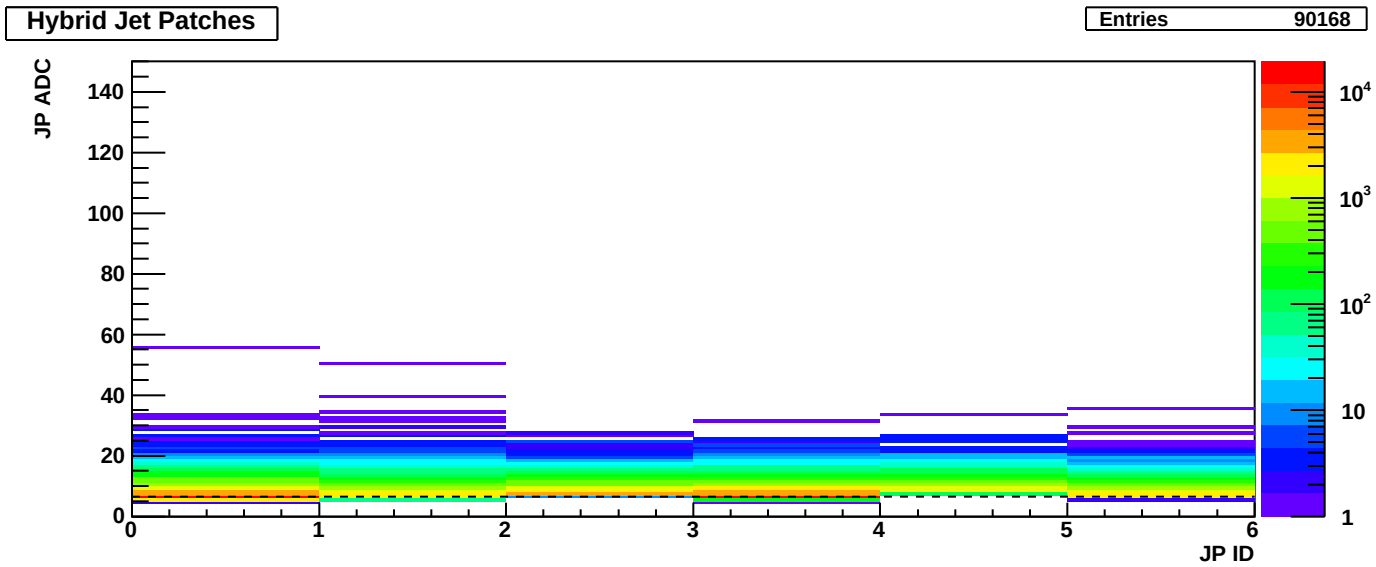

Barrel EMC L0 Input - Patch Sum
Entries 1.35252e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

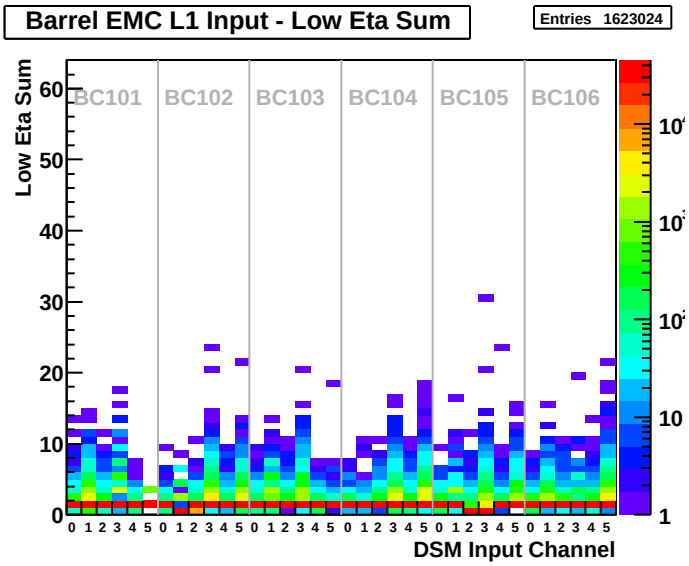

Endcap EMC L0 Input - High Tower
Entries 4057560
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

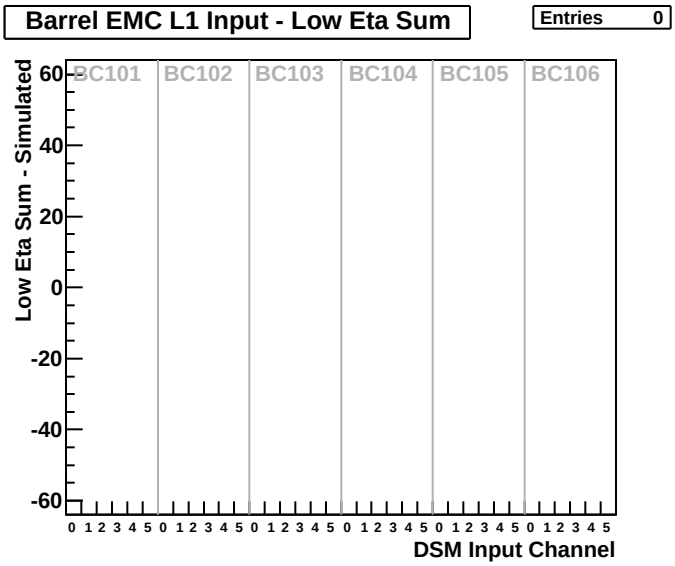

Endcap EMC L0 Input - Patch Sum
Entries 4057560
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

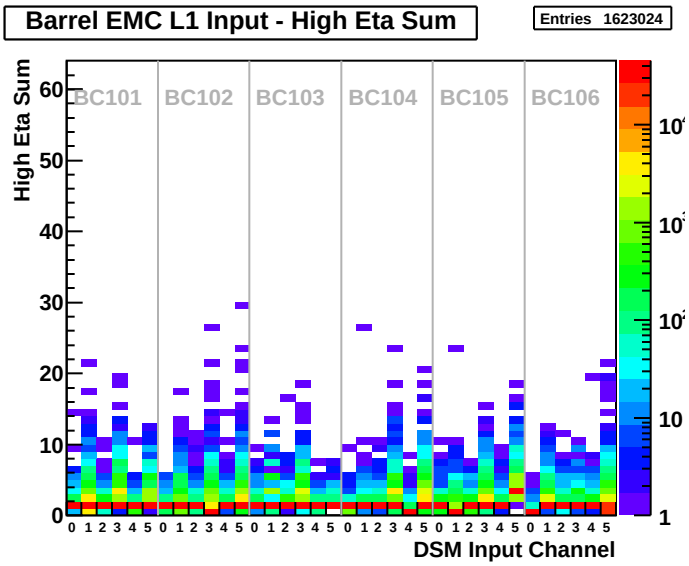

Barrel Jet Patches
Entries 811512
JP ADC
JP ID

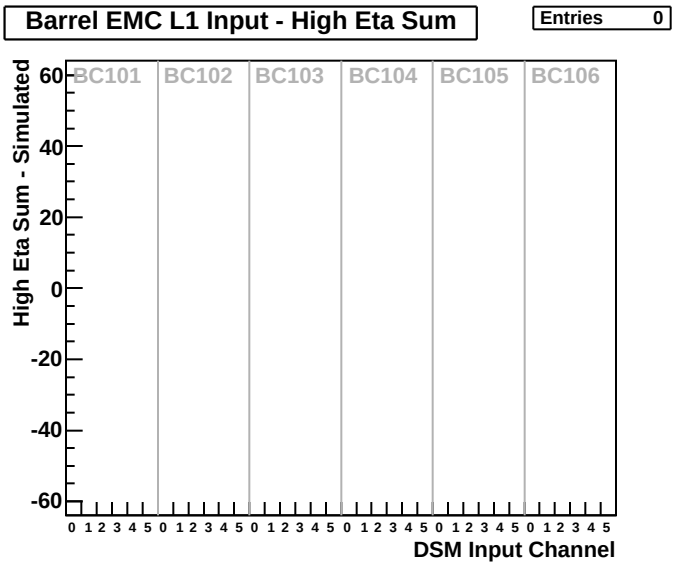

Endcap Jet Patches
Entries 270504
JP ADC
JP ID

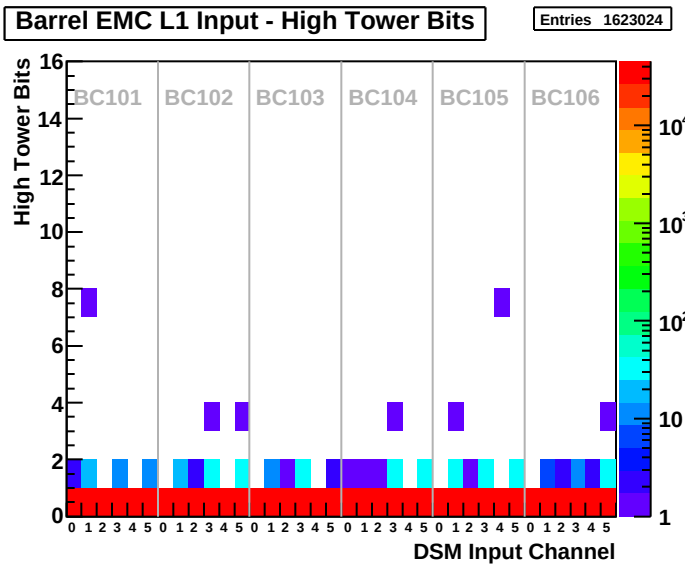

Hybrid Jet Patches
Entries 90168
JP ADC
JP ID

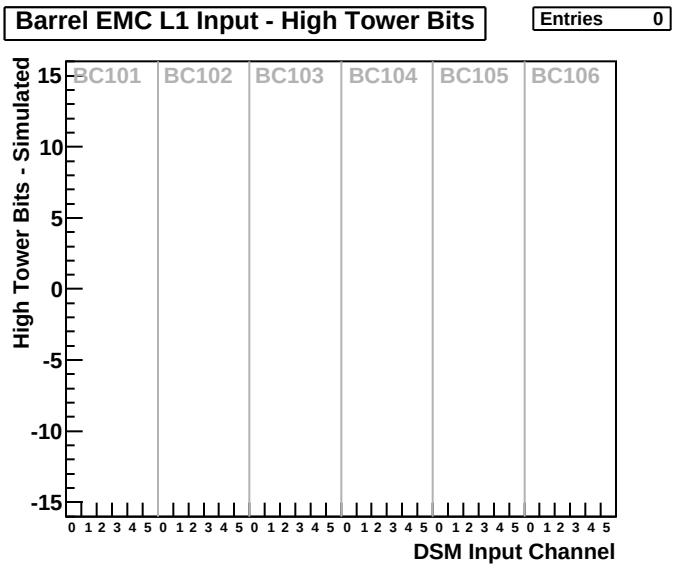

Barrel EMC L1 Input - Low Eta Sum
Entries 1623024
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

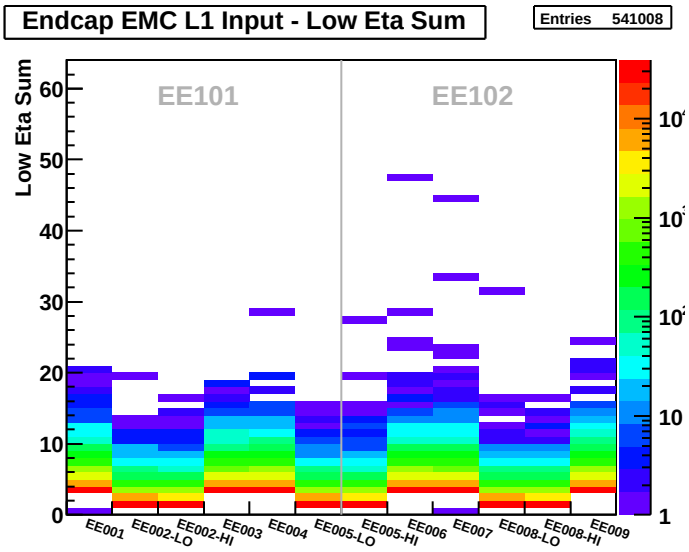

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

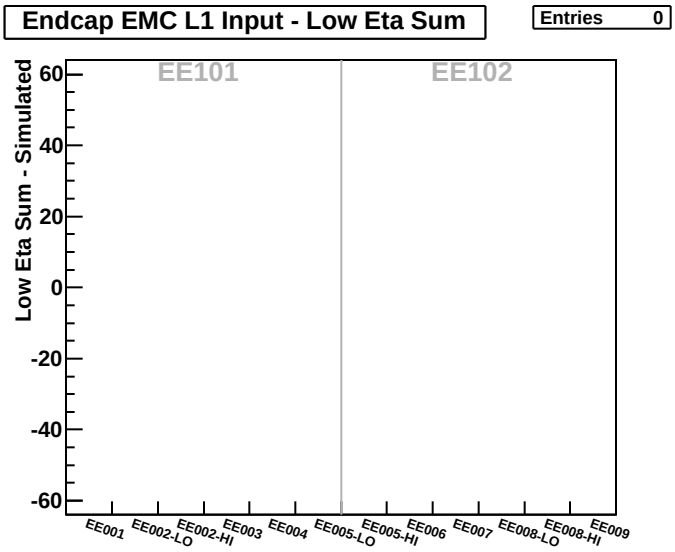

Barrel EMC L1 Input - High Eta Sum
Entries 1623024
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

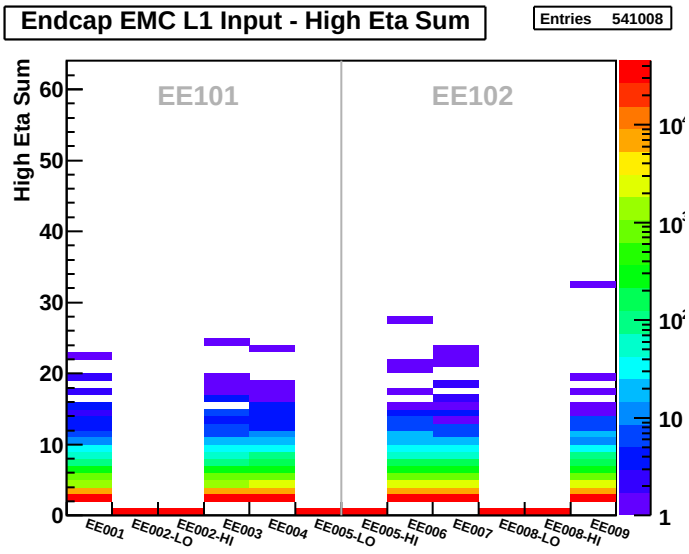

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

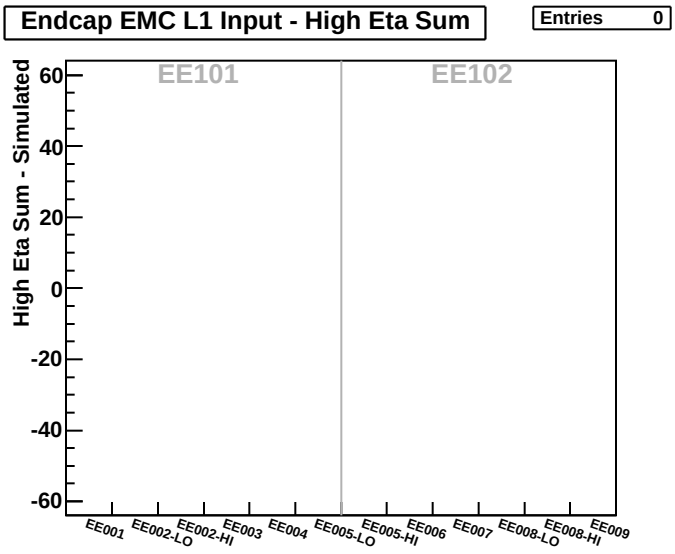

Barrel EMC L1 Input - High Tower Bits
Entries 1623024
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

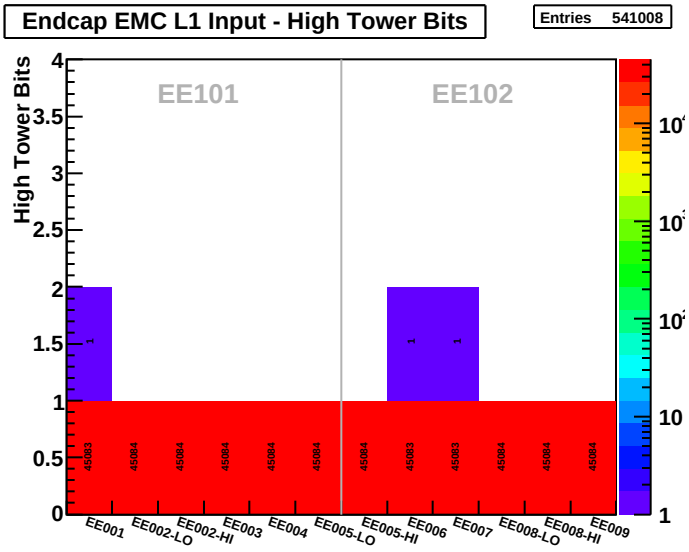

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

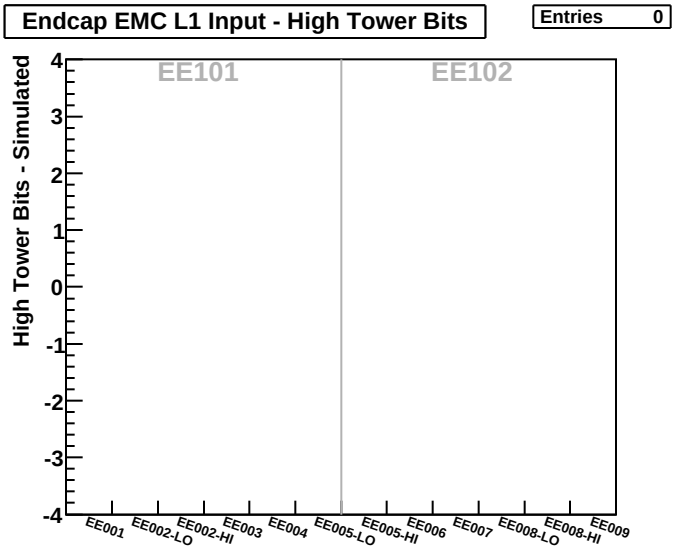

Endcap EMC L1 Input - Low Eta Sum
Entries 541008
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

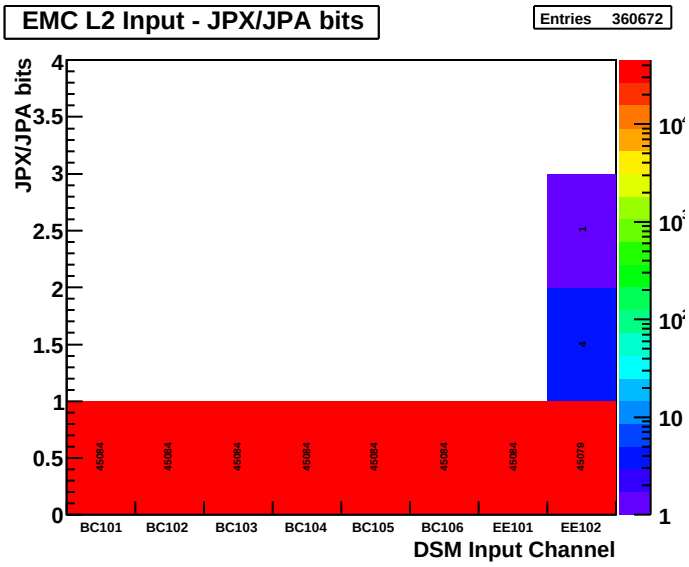

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

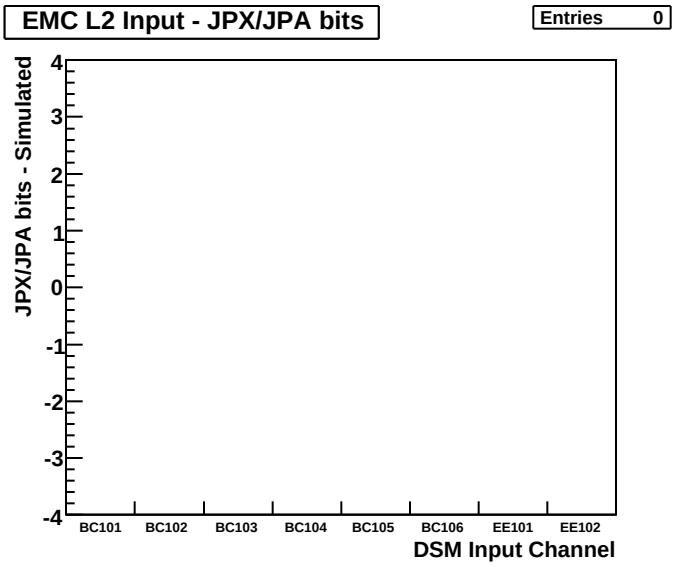

Endcap EMC L1 Input - High Eta Sum
Entries 541008
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

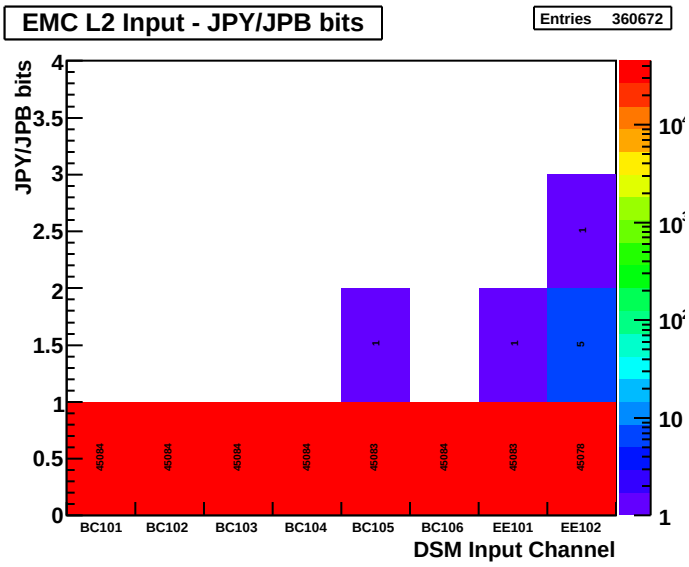

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

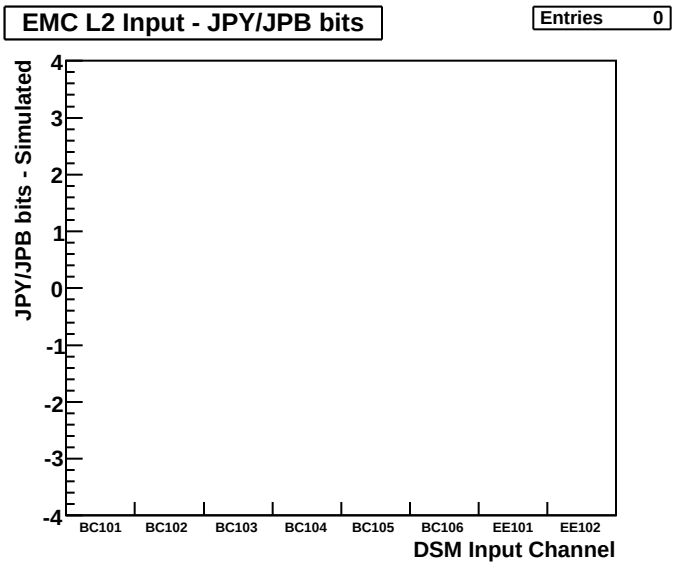

Endcap EMC L1 Input - High Tower Bits
Entries 541008
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

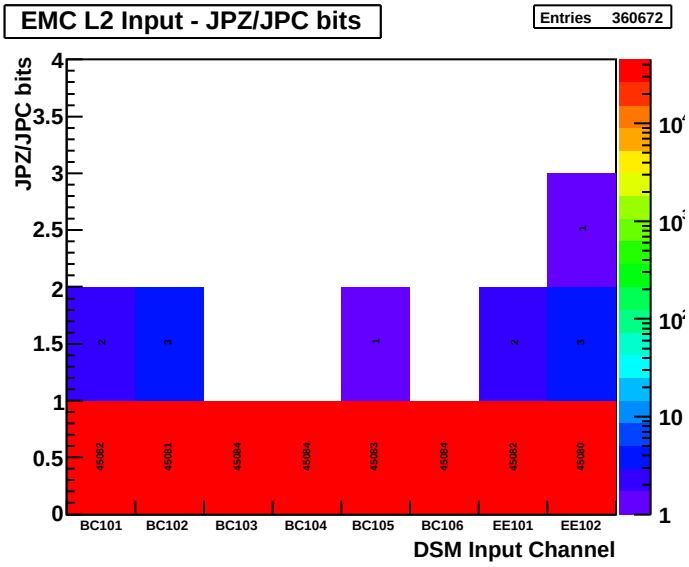

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

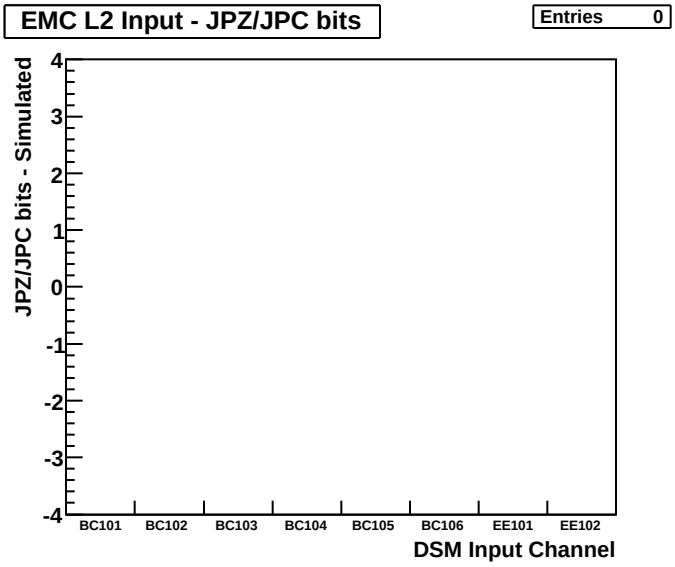

EMC L2 Input - JPX/JPA bits
Entries 360672
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

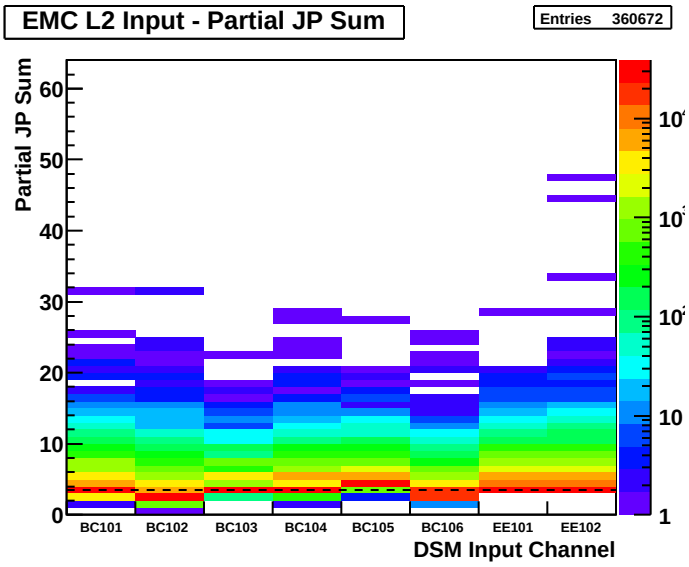

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

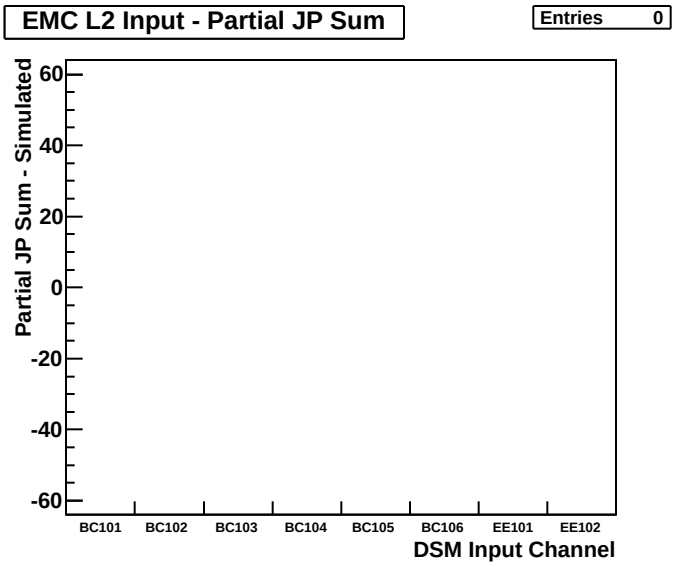

EMC L2 Input - JPY/JPB bits
Entries 360672
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

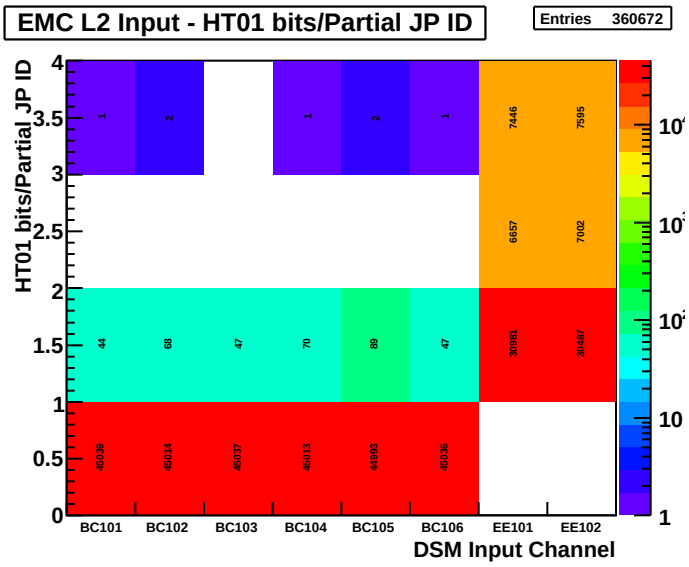

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

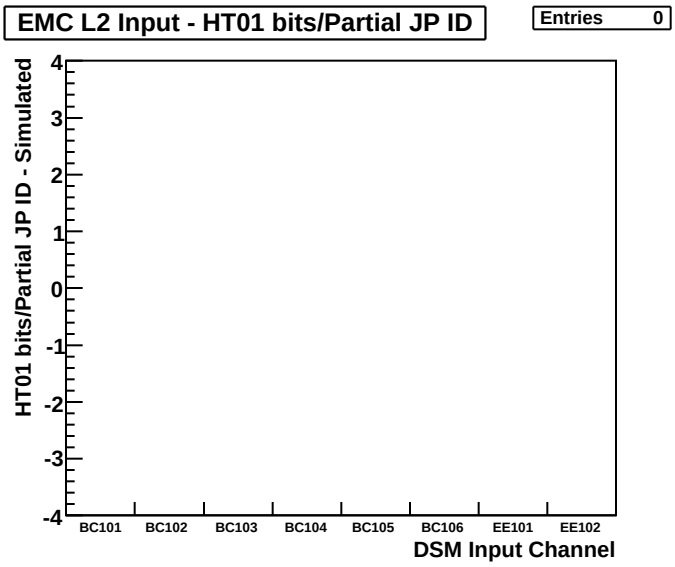

EMC L2 Input - JPZ/JPC bits
Entries 360672
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

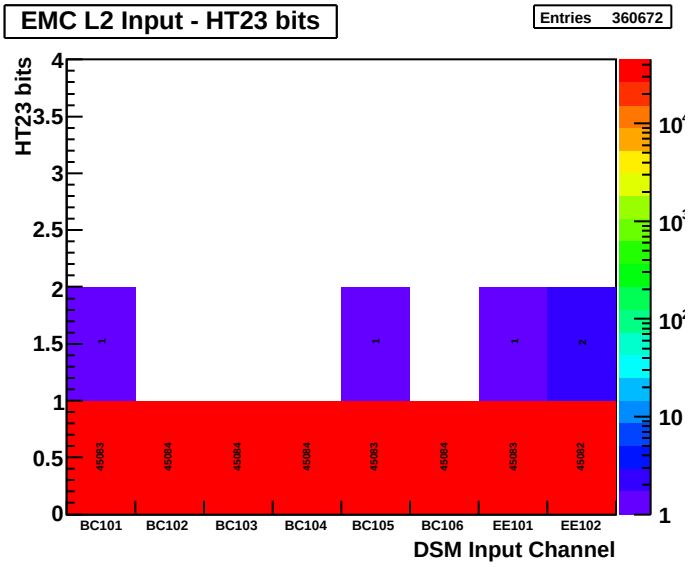

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

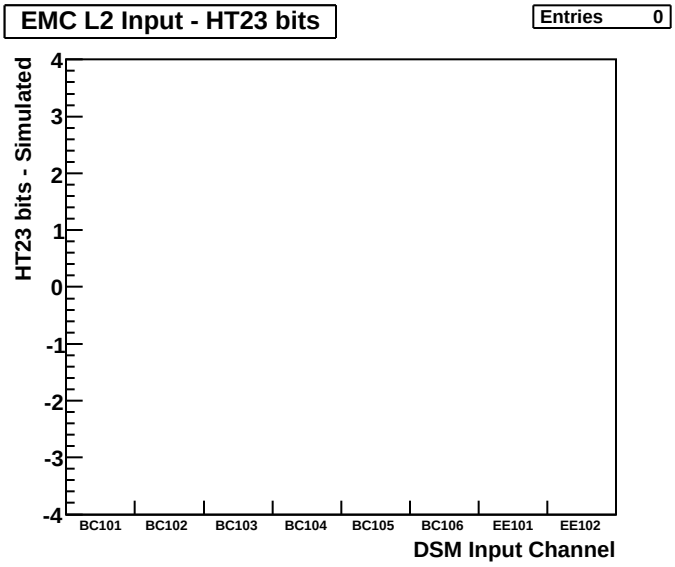

EMC L2 Input - Partial JP Sum
Entries 360672
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

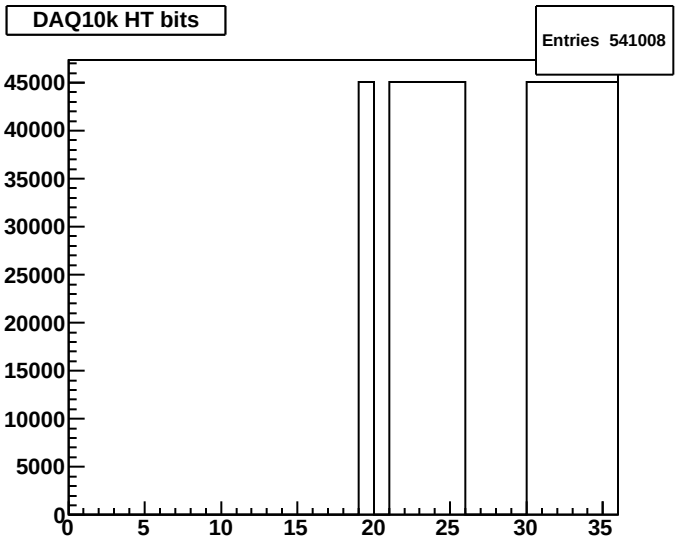

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

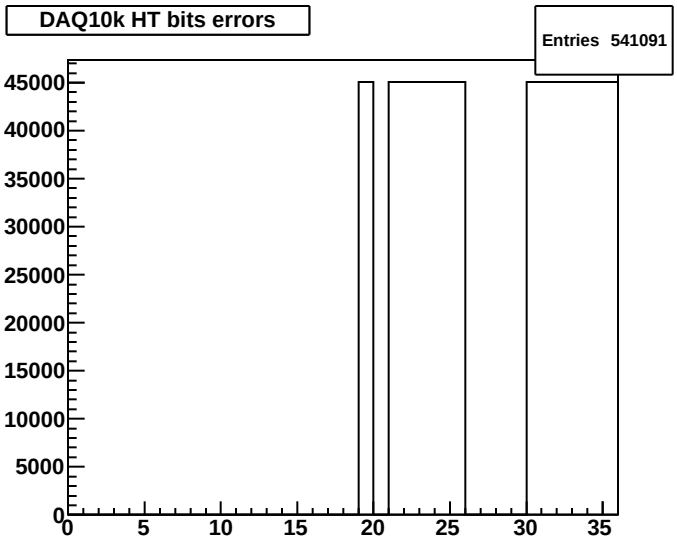

EMC L2 Input - HT01 bits/Partial JP ID
Entries 360672
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

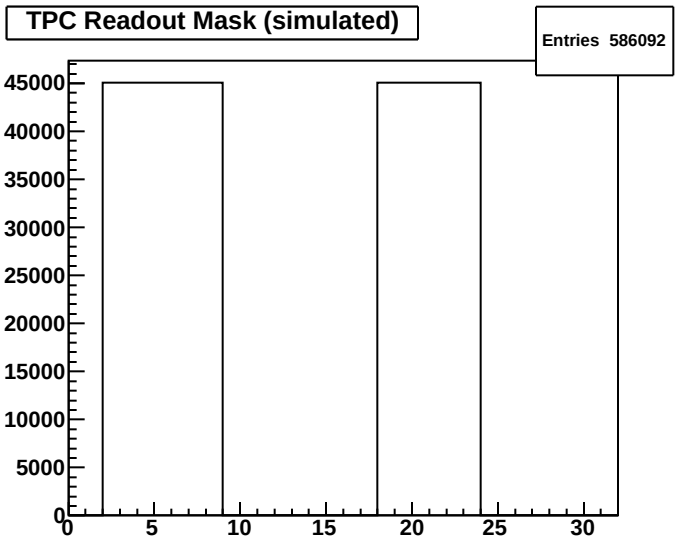

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

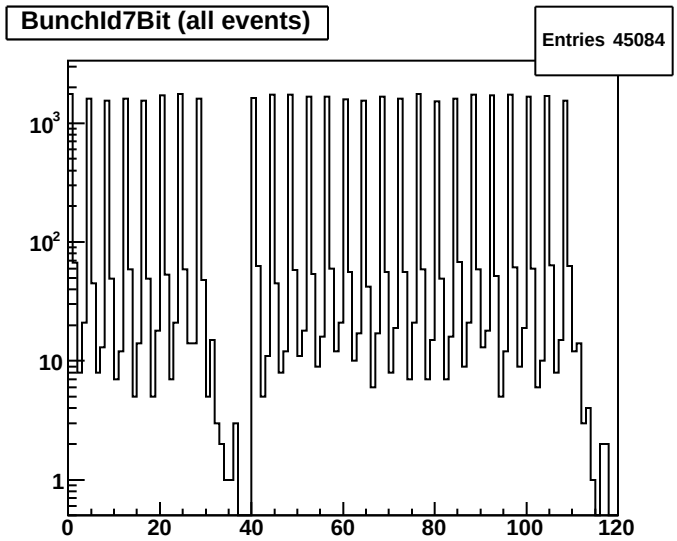

EMC L2 Input - HT23 bits
Entries 360672
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

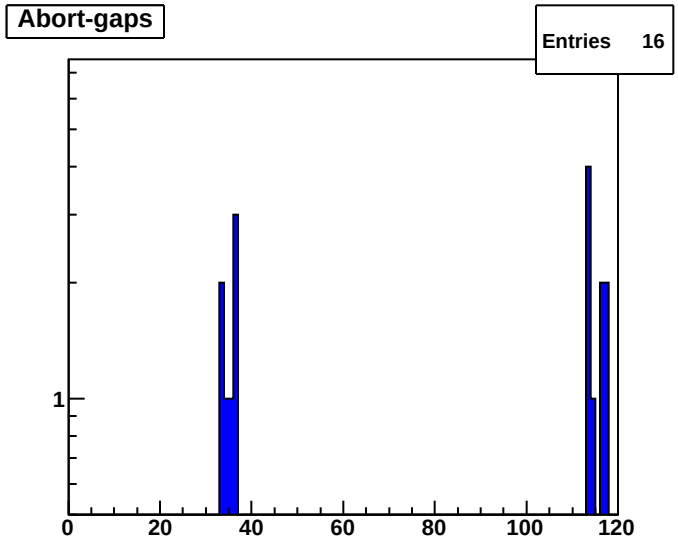

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

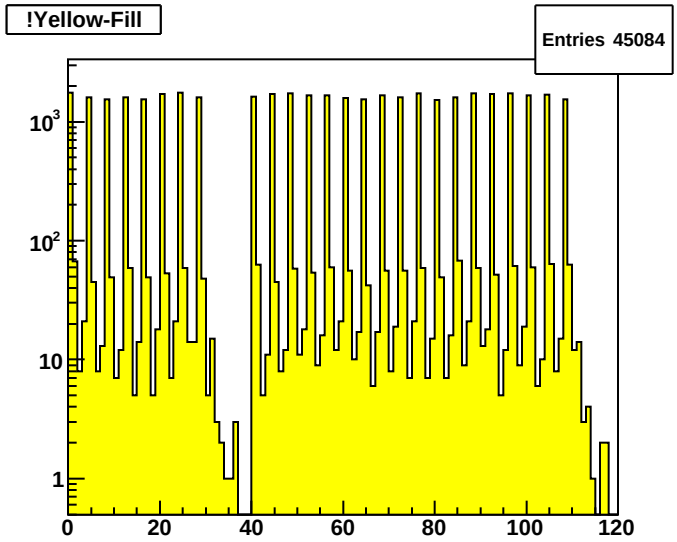

DAQ10k HT bits
Entries 541008

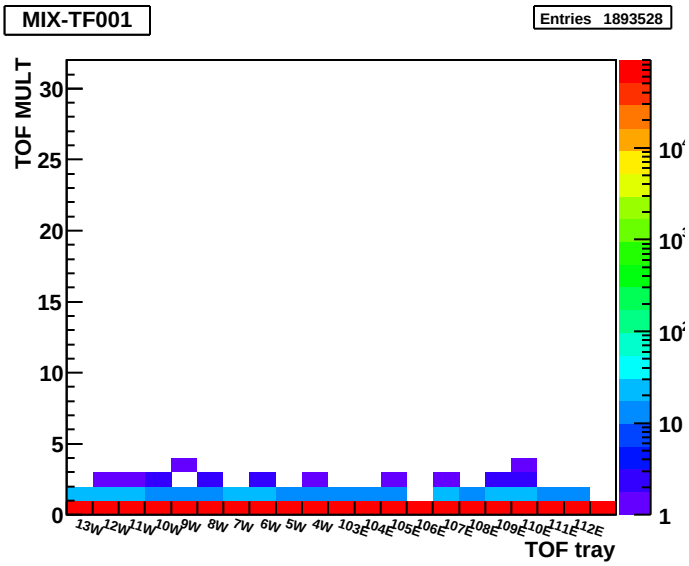

DAQ10k HT bits errors
Entries 541091

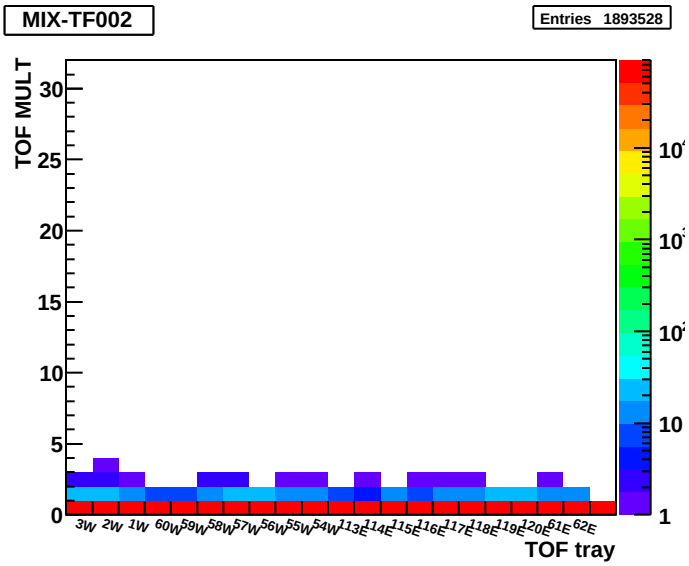

TPC Readout Mask (simulated)
Entries 586092

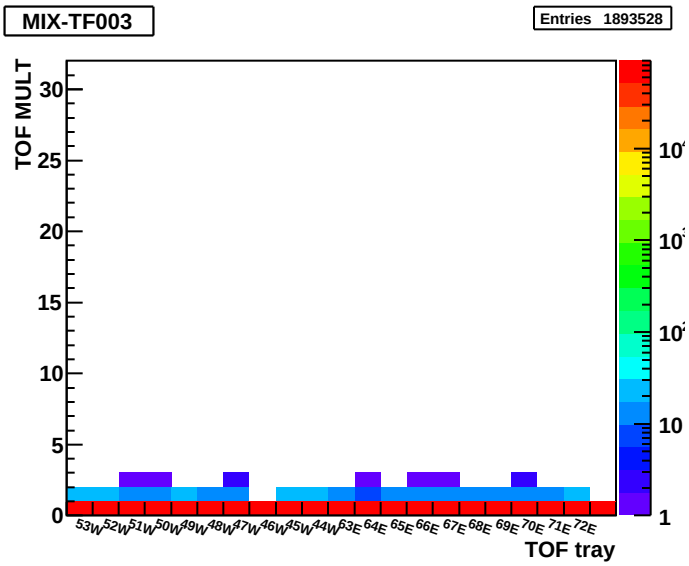

BunchId7Bit (all events)
Entries 45084

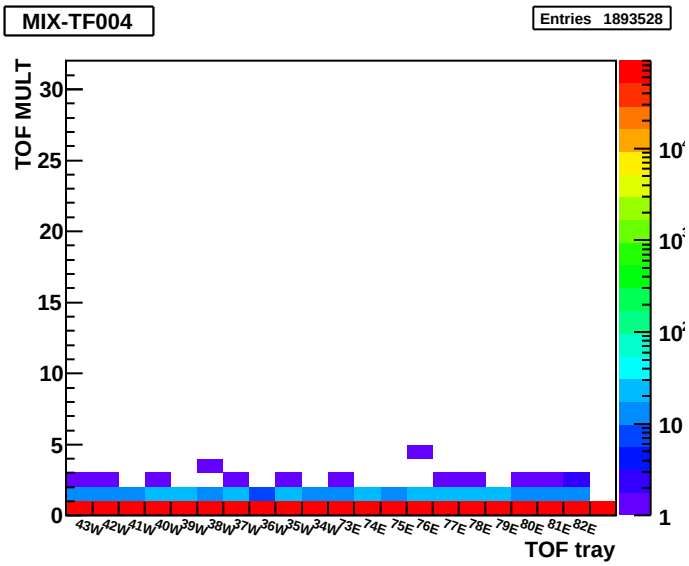

Abort-gaps
Entries 16

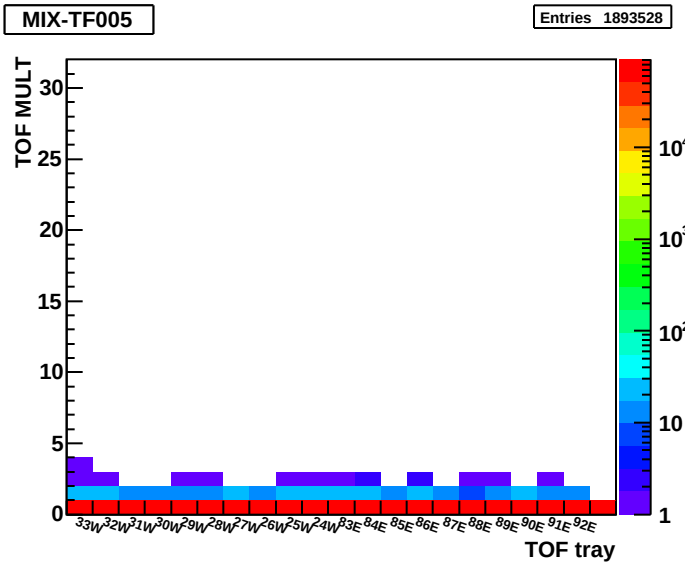

!Yellow-Fill
Entries 45084

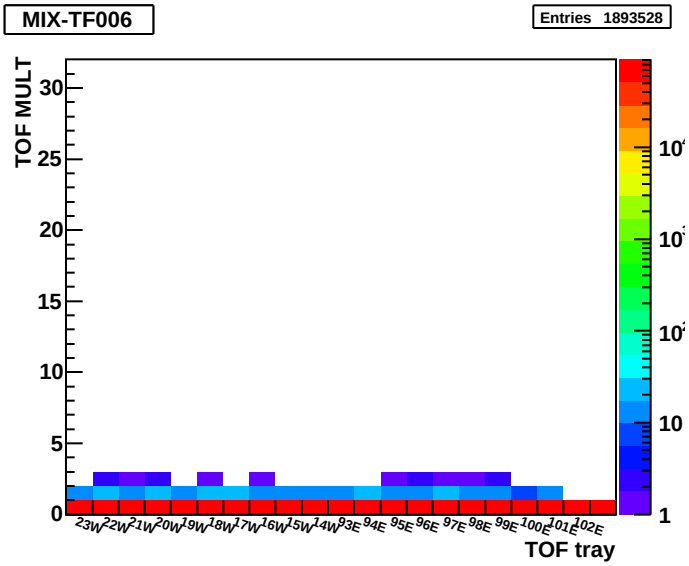

MIX-TF001
Entries 1893528
TOF MULT
TOF tray

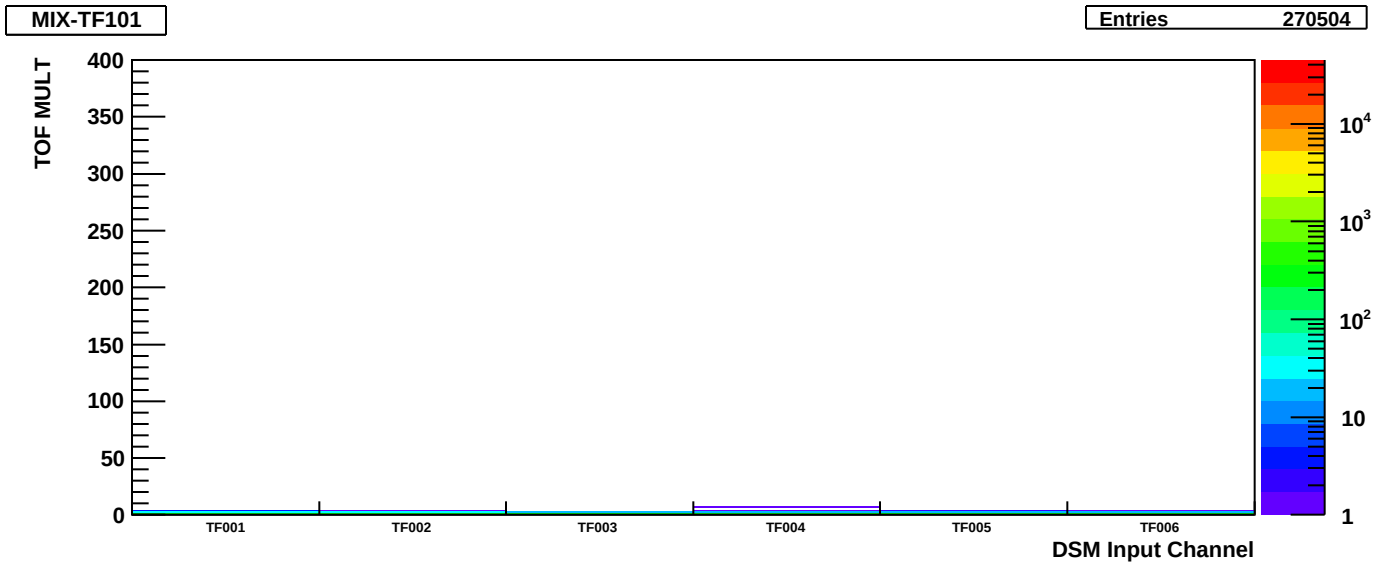

MIX-TF002
Entries 1893528
TOF MULT
TOF tray

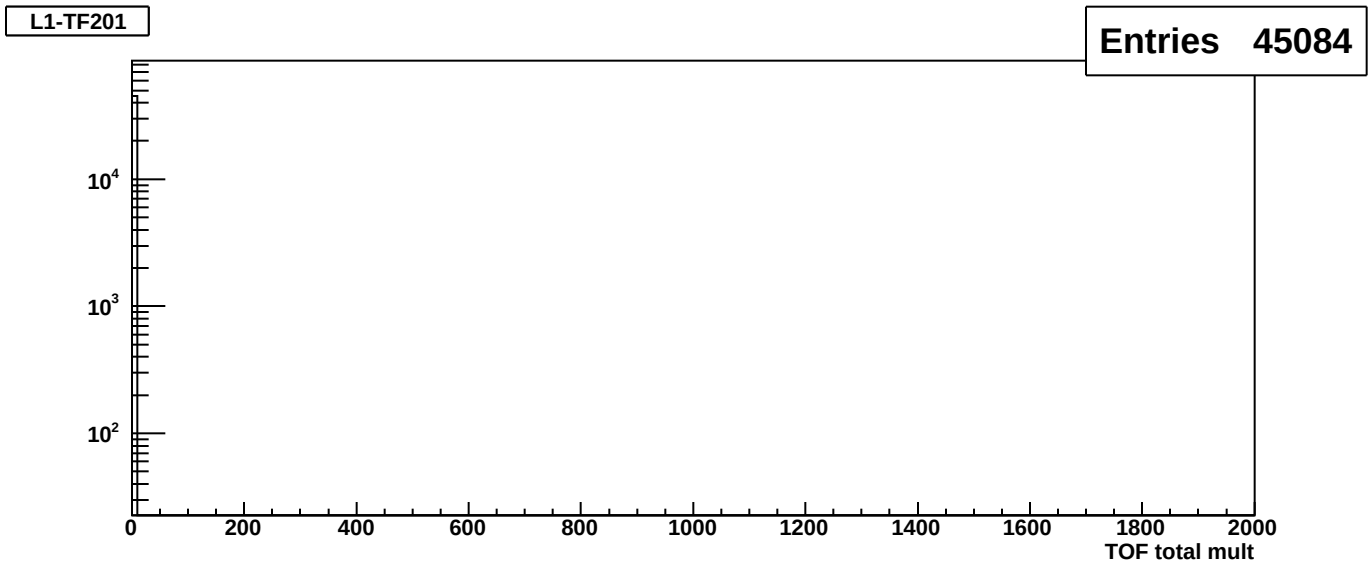

MIX-TF003
Entries 1893528
TOF MULT
TOF tray

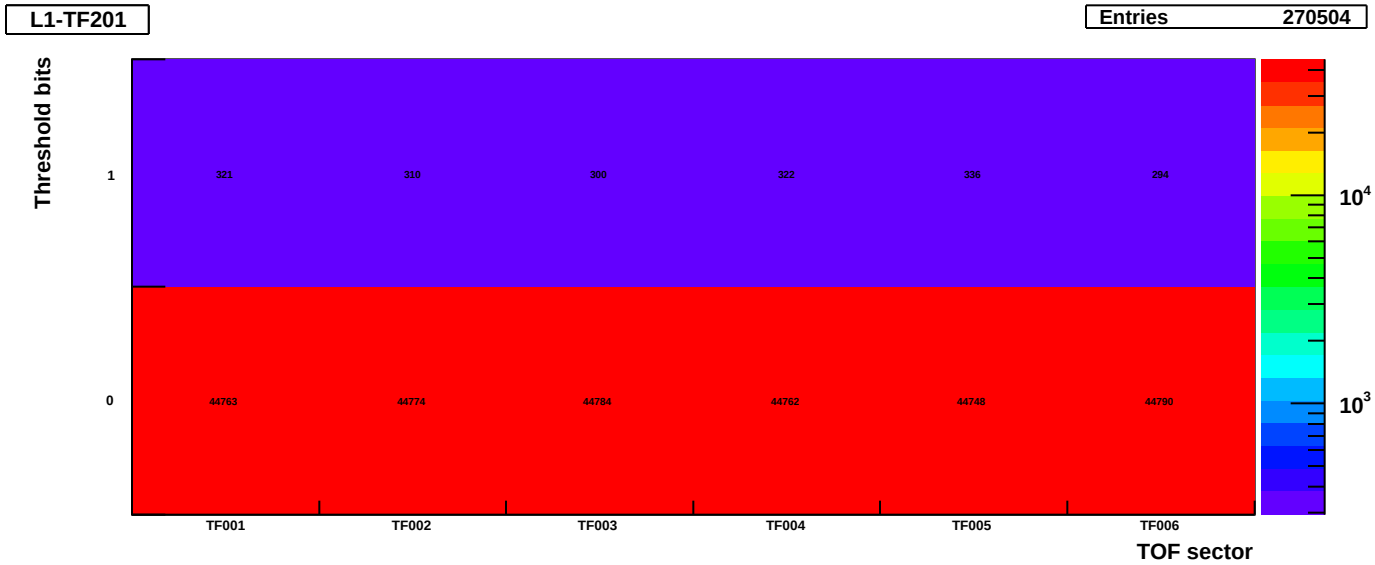

MIX-TF004
Entries 1893528
TOF MULT
TOF tray

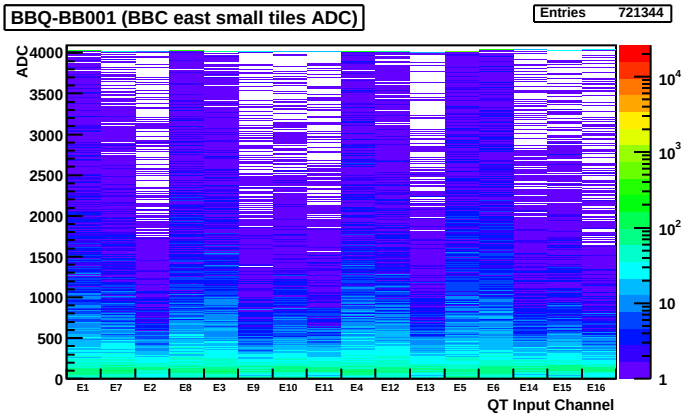

MIX-TF005
Entries 1893528
TOF MULT
TOF tray

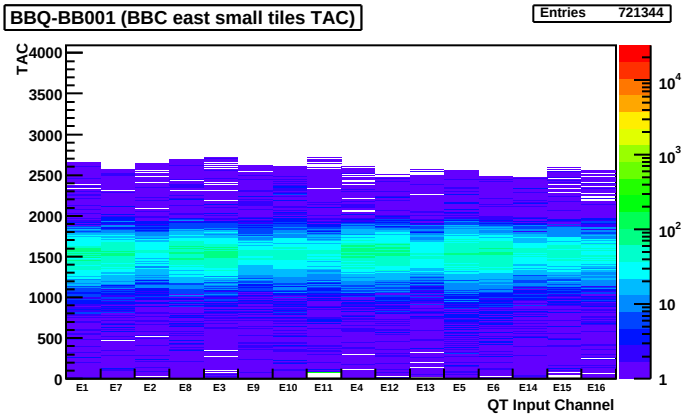

MIX-TF006
Entries 1893528
TOF MULT
TOF tray

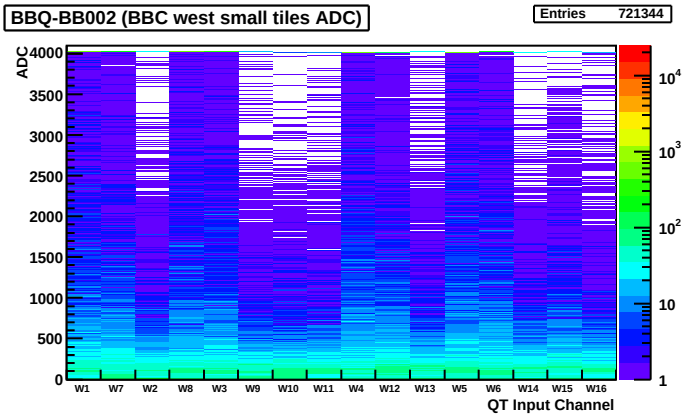

MIX-TF101
Entries 270504
TOF MULT
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

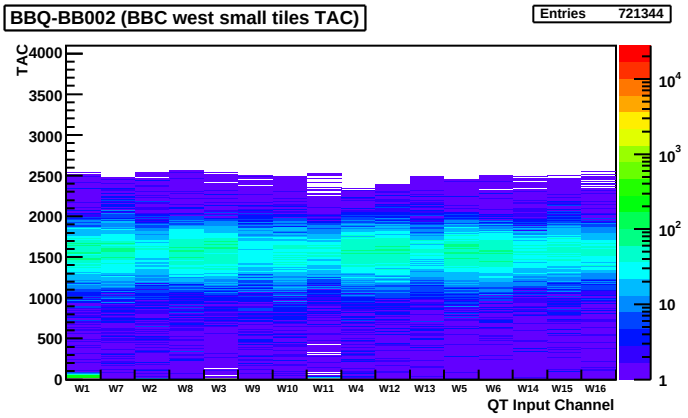

L1-TF201
Entries 45084
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
1600
1800
2000
TOF total mult

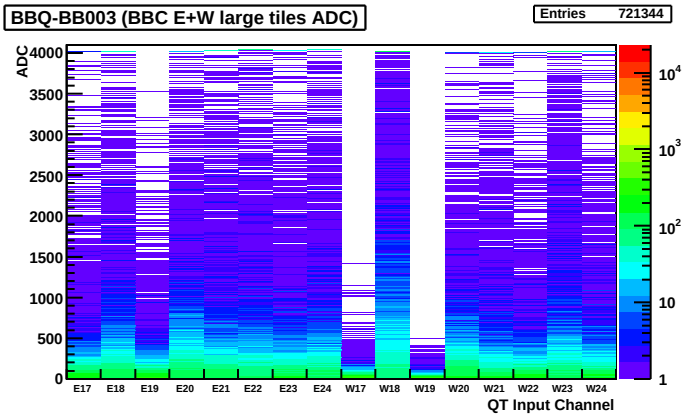

L1-TF201
Entries 270504
Threshold bits
1
0
321
310
300
322
336
294
44763
44774
44784
44762
44748
44790
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3

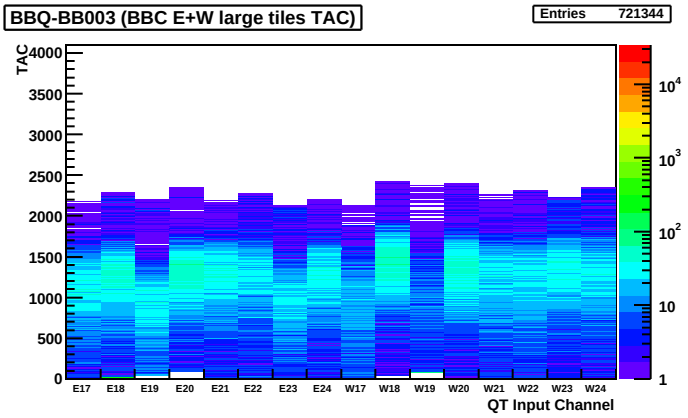

BBQ-BB001 (BBC east small tiles ADC)
Entries 721344
ADC
QT Input Channel

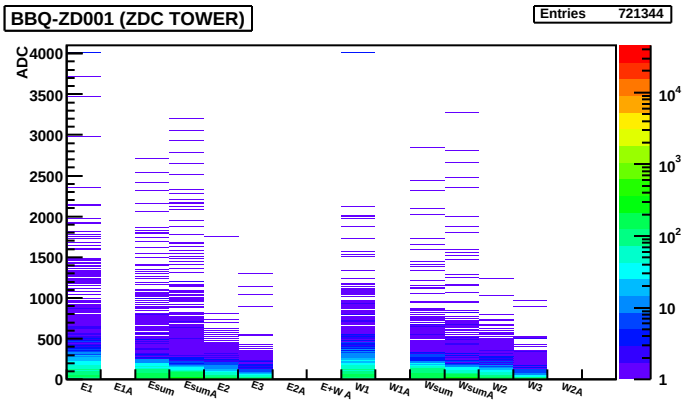

BBQ-BB001 (BBC east small tiles TAC)
Entries 721344
TAC
QT Input Channel

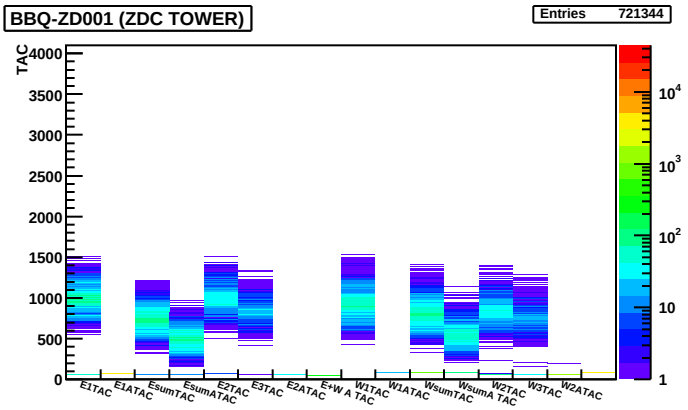

BBQ-BB002 (BBC west small tiles ADC)
Entries 721344
ADC
QT Input Channel

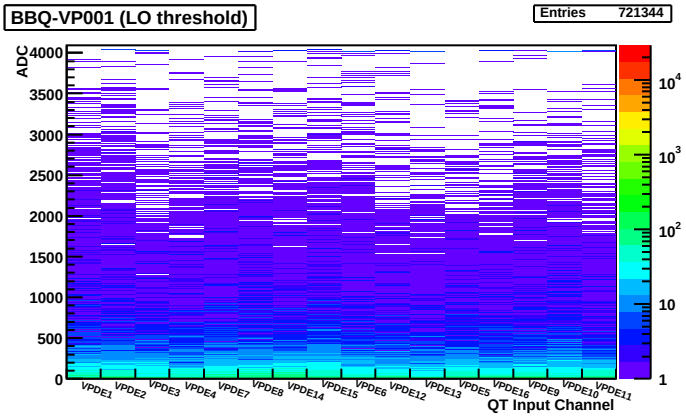

BBQ-BB002 (BBC west small tiles TAC)
Entries 721344
TAC
QT Input Channel

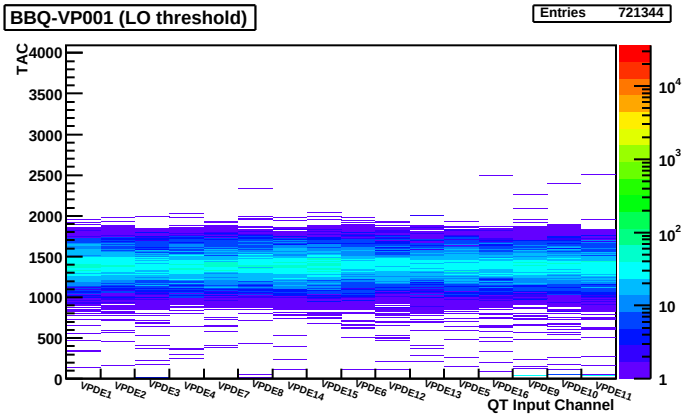

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 721344
ADC
QT Input Channel

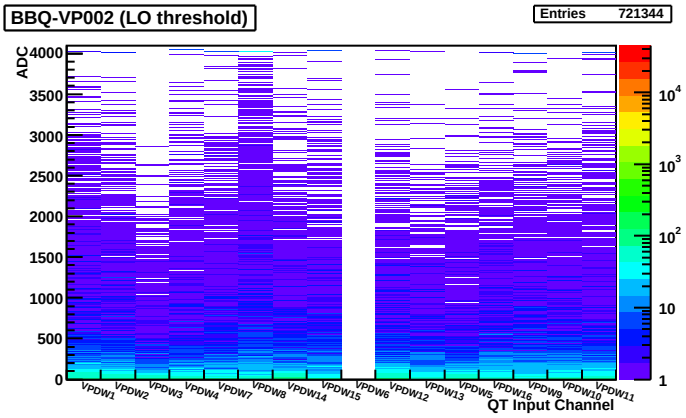

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 721344
TAC
QT Input Channel

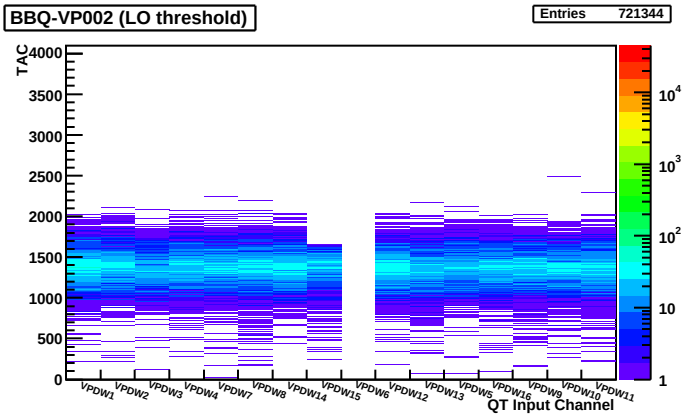

BBQ-ZD001 (ZDC TOWER)
Entries 721344
ADC

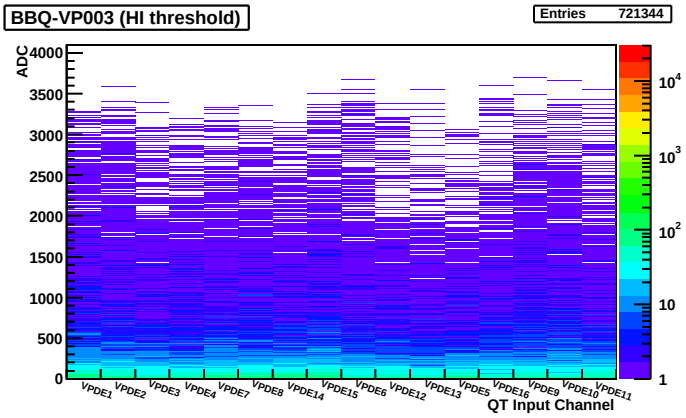

BBQ-ZD001 (ZDC TOWER)
Entries 721344
TAC

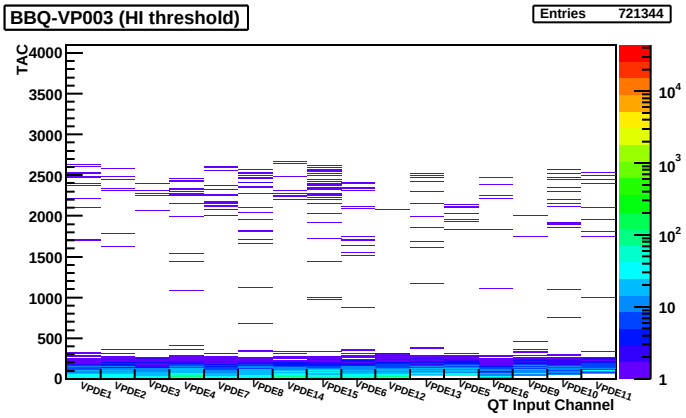

BBQ-VP001 (LO threshold)
Entries 721344
ADC
QT Input Channel

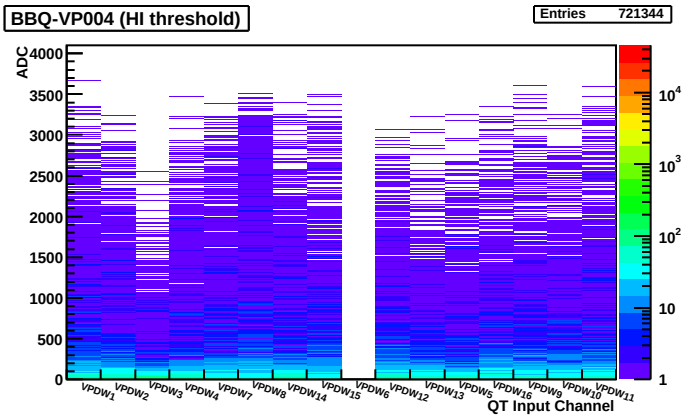

BBQ-VP001 (LO threshold)
Entries 721344
TAC
QT Input Channel

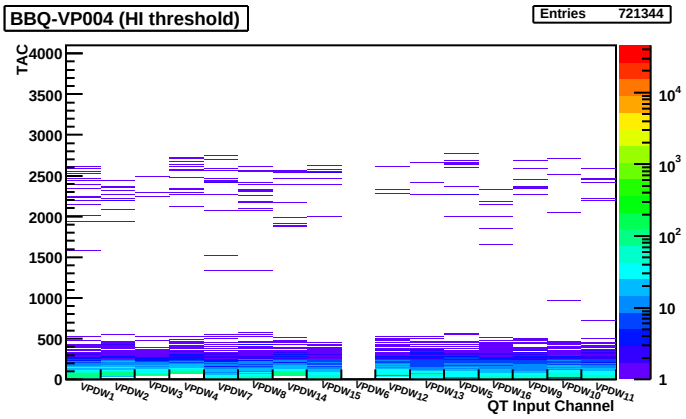

BBQ-VP002 (LO threshold)
Entries 721344
ADC
QT Input Channel

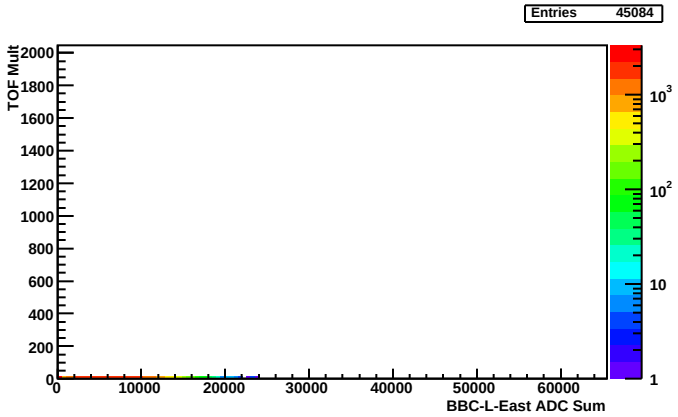

BBQ-VP002 (LO threshold)
Entries 721344
TAC
QT Input Channel

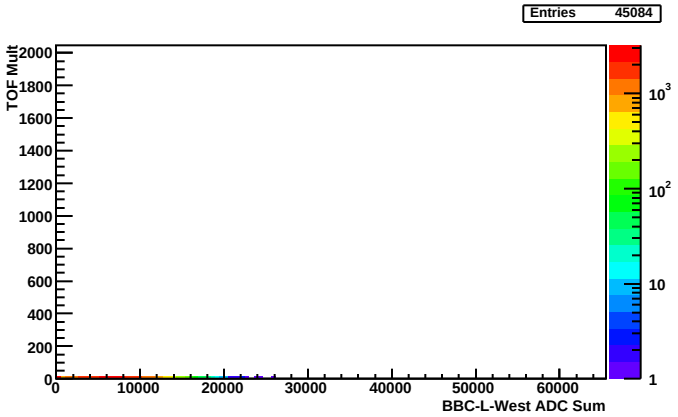

BBQ-VP003 (HI threshold)
Entries 721344
ADC
QT Input Channel

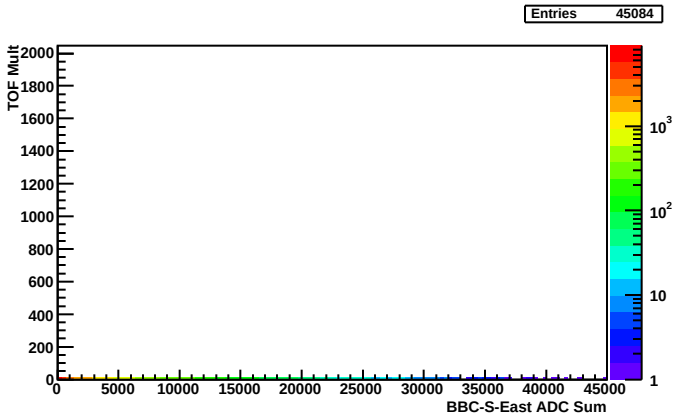

BBQ-VP003 (HI threshold)
Entries 721344
TAC
QT Input Channel

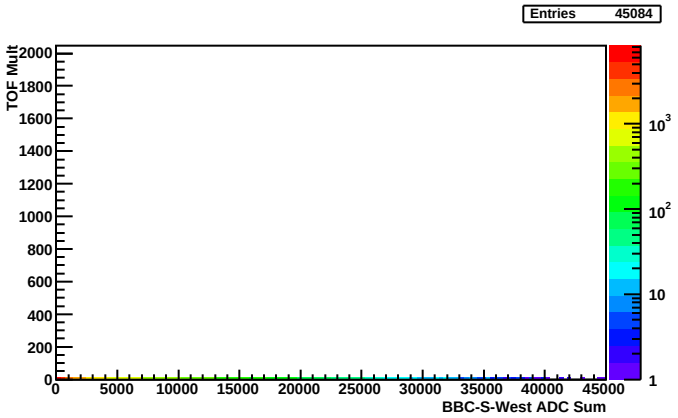

BBQ-VP004 (HI threshold)
Entries 721344
ADC
QT Input Channel

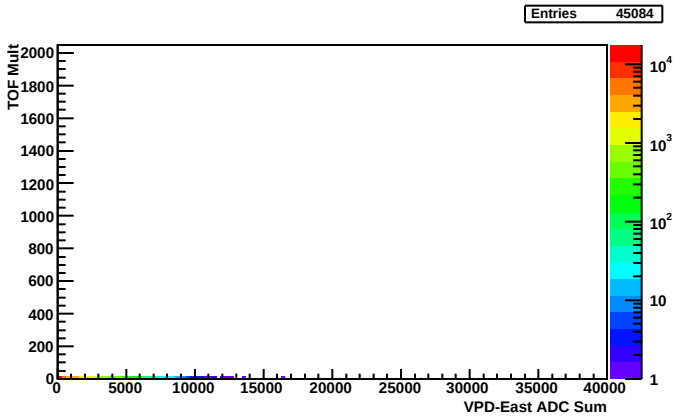

BBQ-VP004 (HI threshold)
Entries 721344
TAC
QT Input Channel

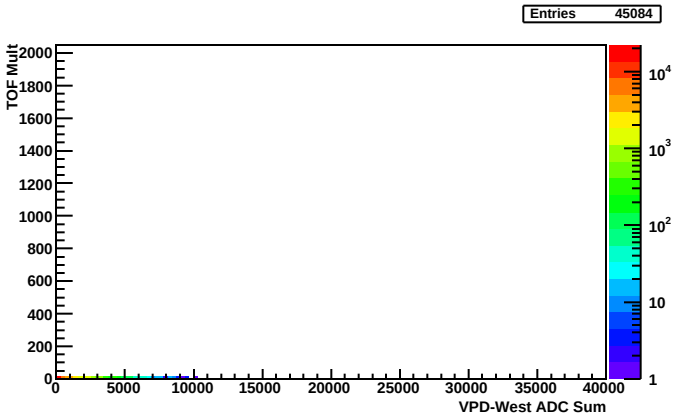

Entries 45084
TOF Mult
BBC-L-East ADC Sum

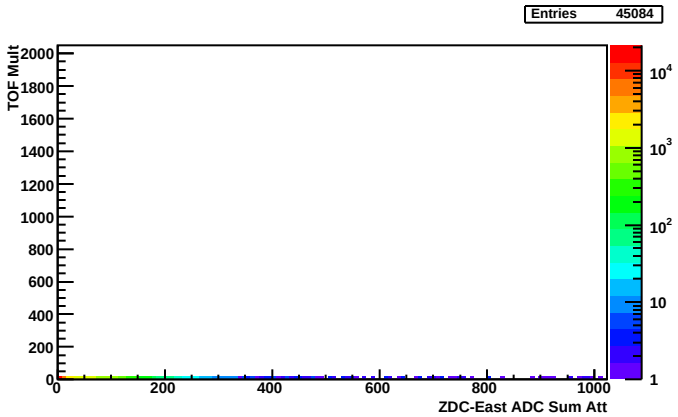

Entries 45084
TOF Mult
BBC-L-West ADC Sum

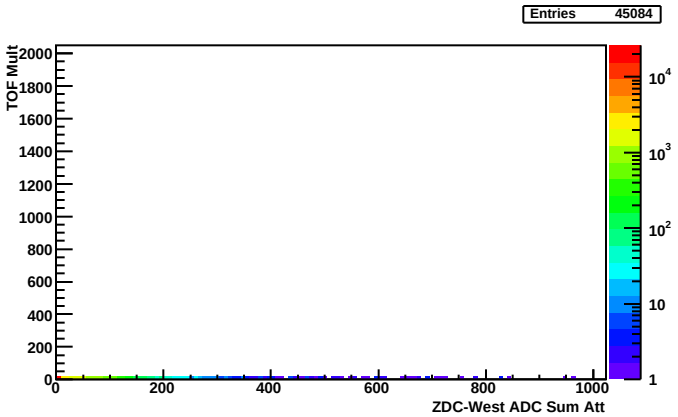

Entries 45084
TOF Mult
BBC-S-East ADC Sum

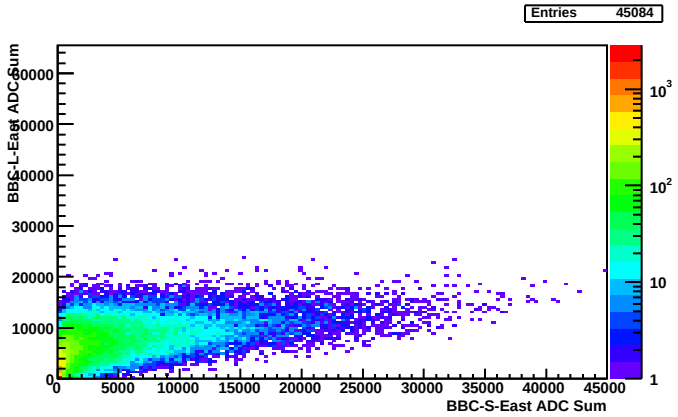

Entries 45084
TOF Mult
BBC-S-West ADC Sum

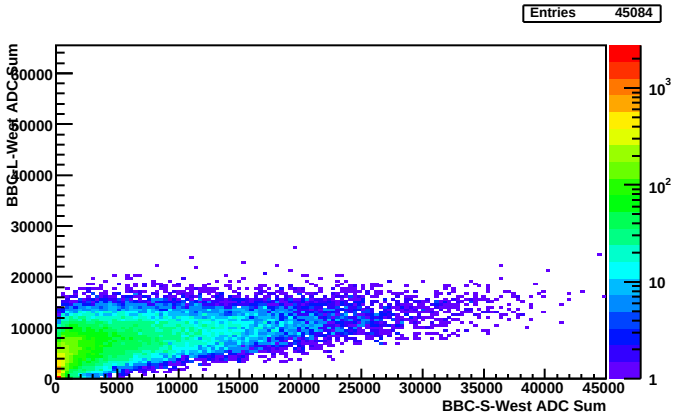

Entries 45084
TOF Mult
VPD-East ADC Sum

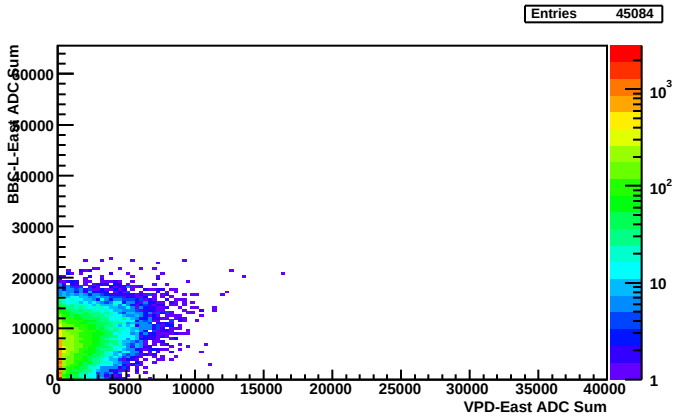

Entries 45084
TOF Mult
VPD-West ADC Sum

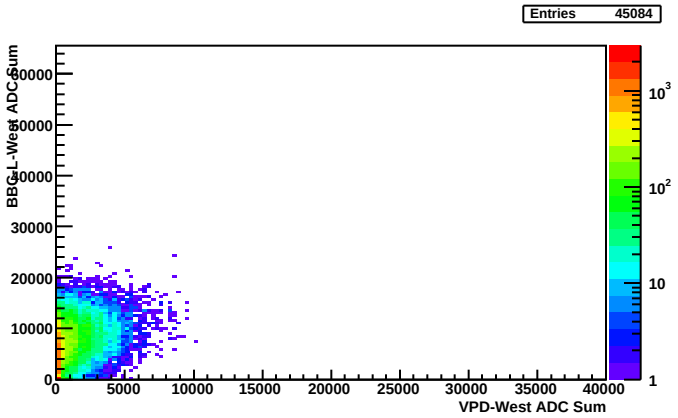

Entries 45084
TOF Mult
ZDC-East ADC Sum Att

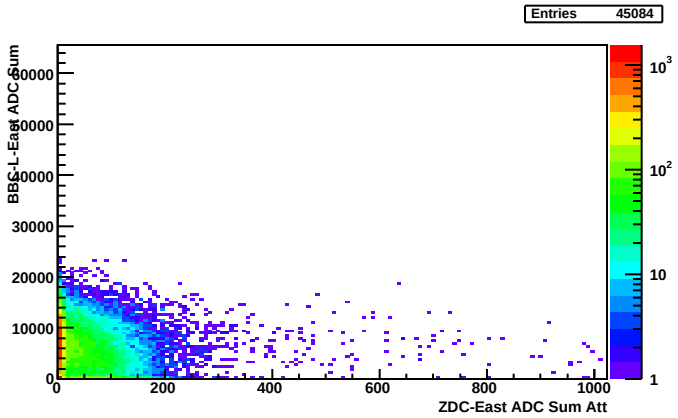

Entries 45084
TOF Mult
ZDC-West ADC Sum Att


Entries 45084
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 45084
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 45084
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 45084
BBC-L-West ADC Sum
VPD-West ADC Sum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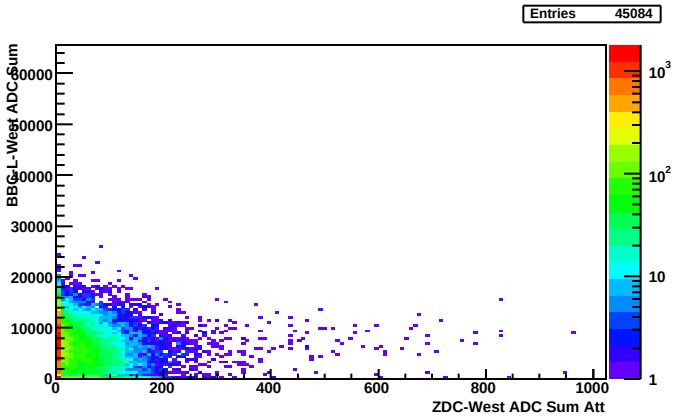
ZDC-East ADC Sum Att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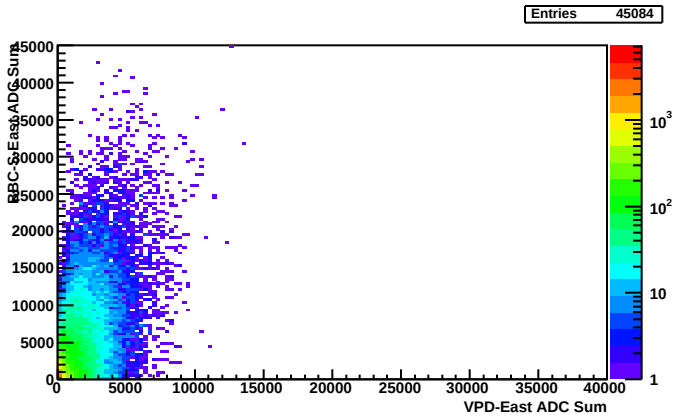
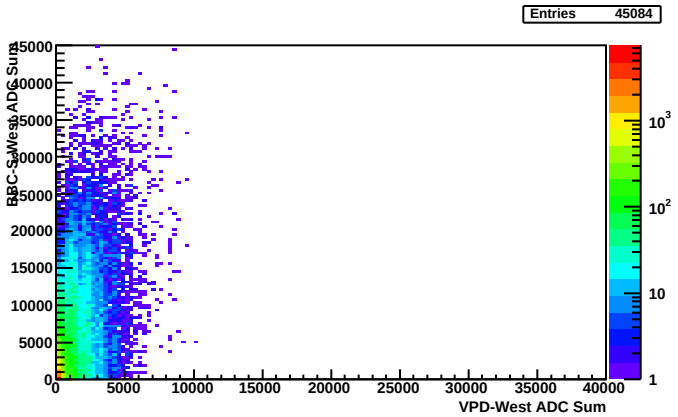
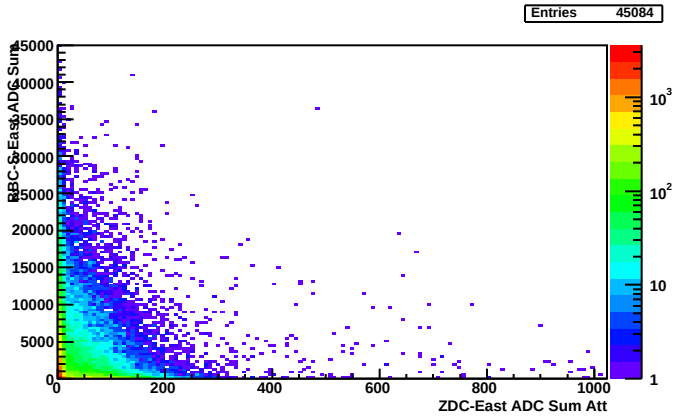
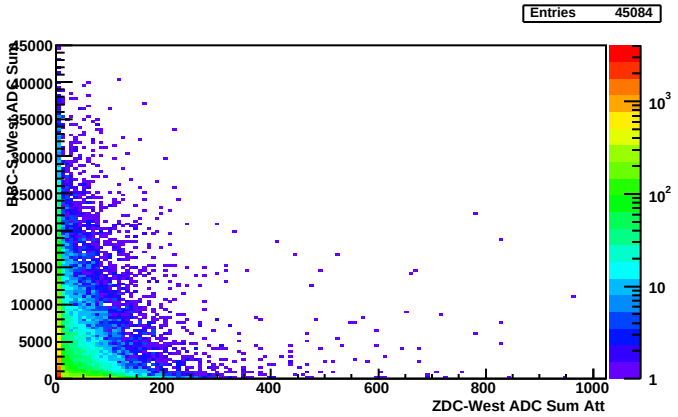
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 45084
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 45084
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 45084
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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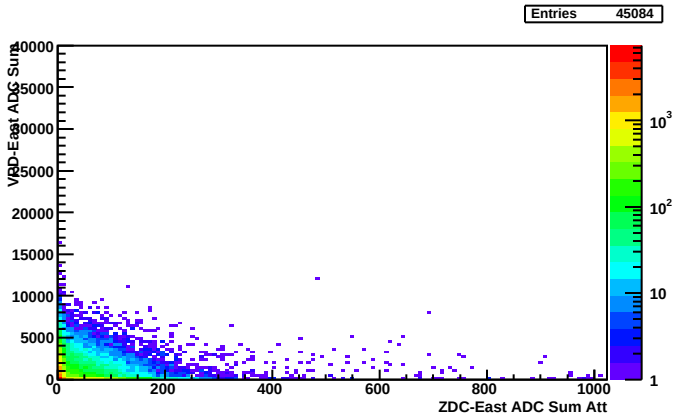
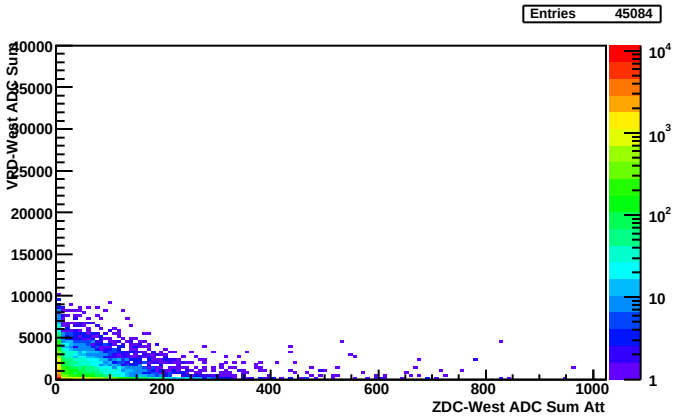
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 45084
VPD-East ADC Sum
ZDC-East ADC Sum Att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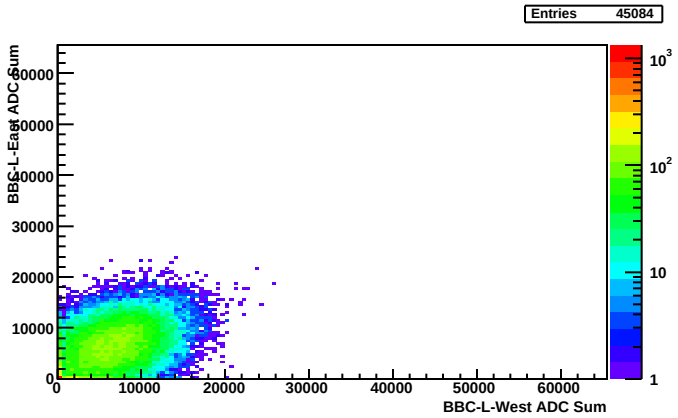
VPD-West ADC Sum
ZDC-West ADC Sum Att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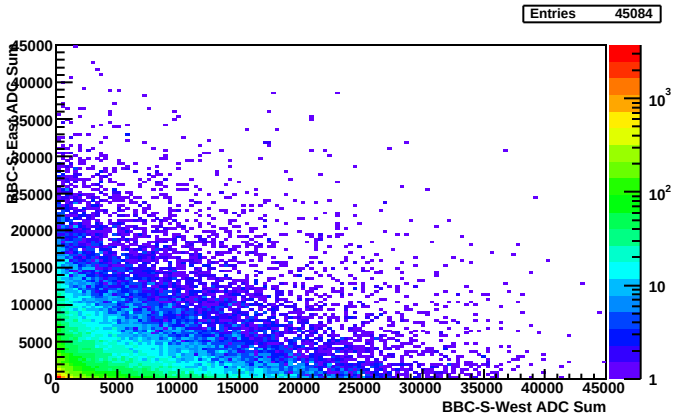
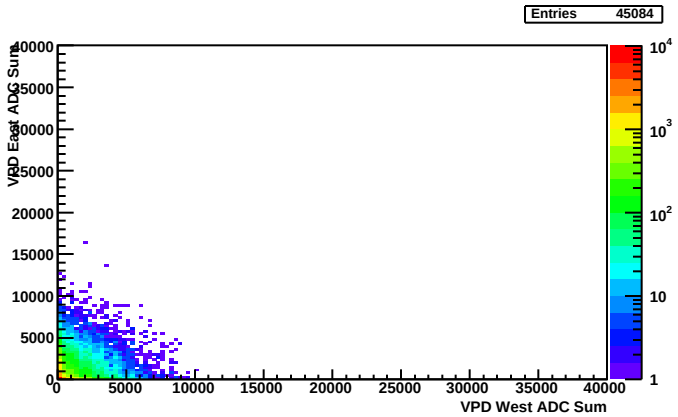
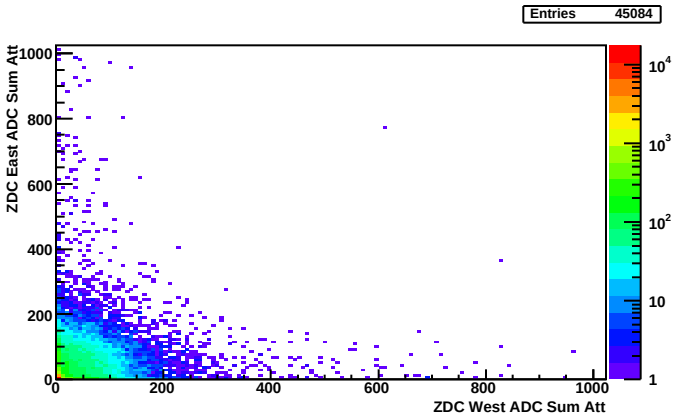
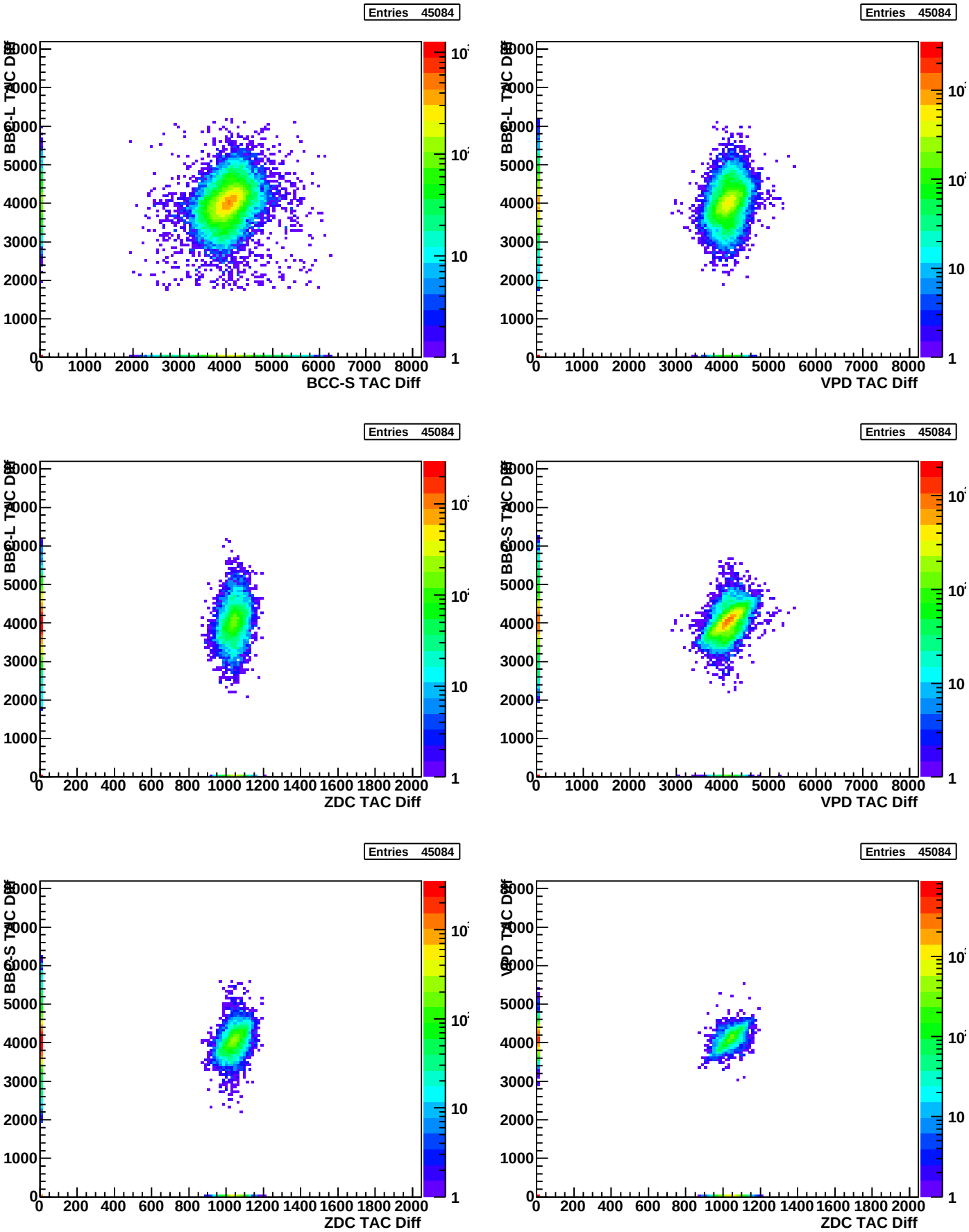
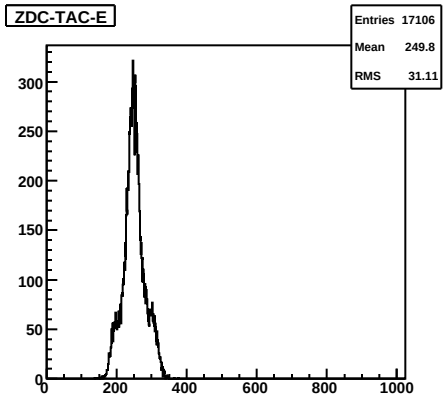
BBC-L-East ADC Sum
BBC-L-West ADC Sum

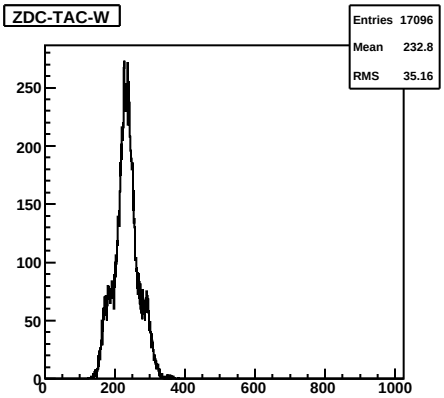

Entries 45084
BBC-S-East ADC Sum
BBC-S-West ADC Sum

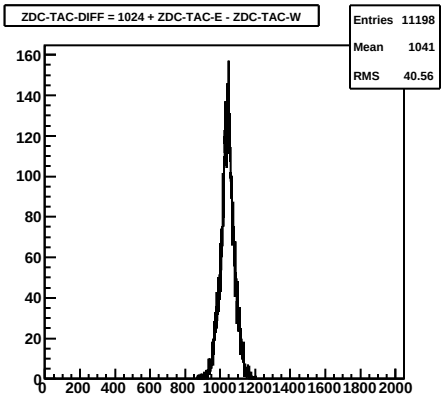

Entries 45084
VPD East ADC Sum
VPD West ADC Sum

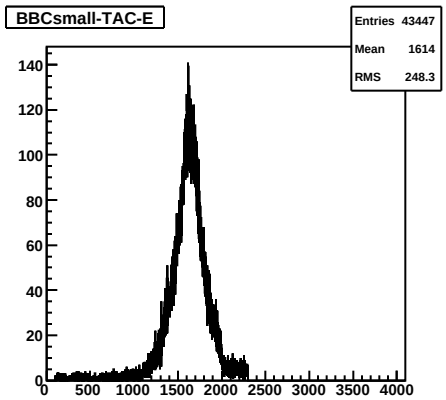

Entries 45084
ZDC East ADC Sum Att
ZDC West ADC Sum Att

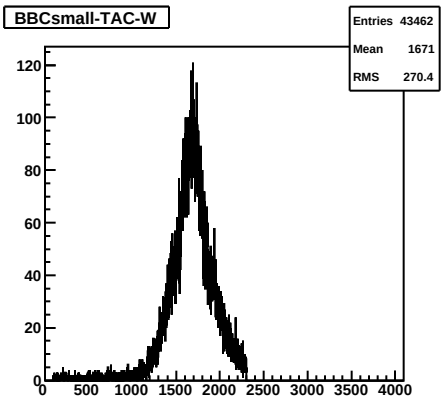

Entries 45084
BBC-L TAC Diff
BCC-S TAC Diff
Entries 45084
BBC-L TAC Diff
VPD TAC Diff
Entries 45084
BBC-L TAC Diff
ZDC TAC Diff
Entries 45084
BBC-S TAC Diff
VPD TAC Diff
Entries 45084
BBC-S TAC Diff
ZDC TAC Diff
Entries 45084
VPD TAC Diff
ZDC TAC Diff

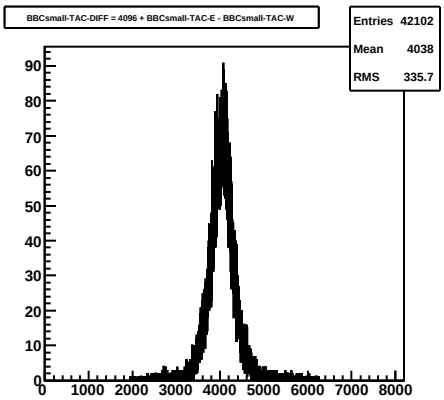

ZDC-TAC-E
Entries 17106
Mean 249.8
RMS 31.11

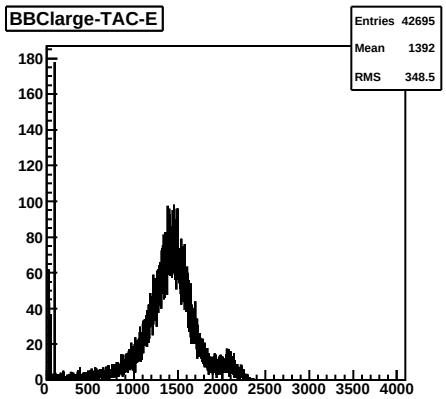

ZDC-TAC-W
Entries 17096
Mean 232.8
RMS 35.16

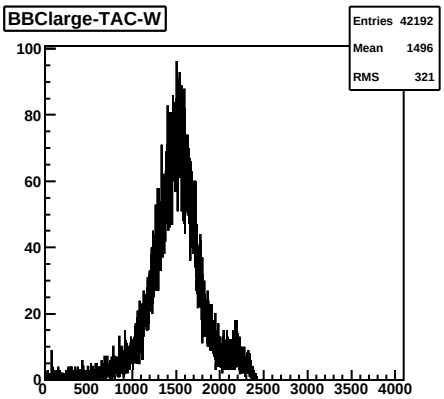

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 11198
Mean 1041
RMS 40.56

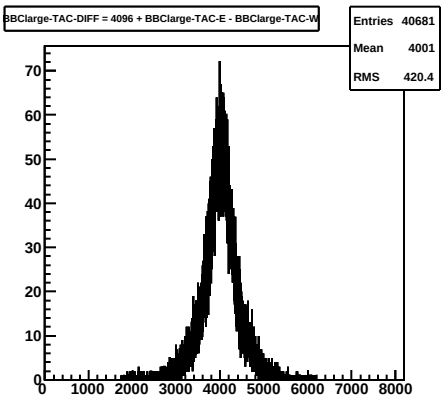

BBCsmall-TAC-E
Entries 43447
Mean 1614
RMS 248.3

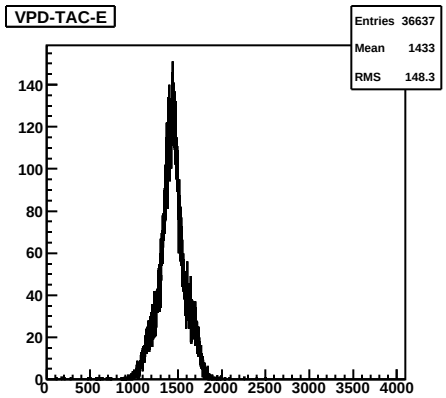

BBCsmall-TAC-W
Entries 43462
Mean 1671
RMS 270.4

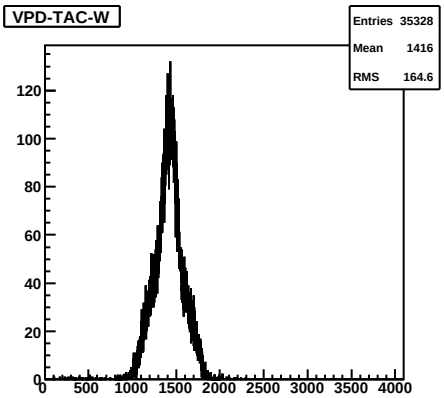

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 42102
Mean 4038
RMS 335.7

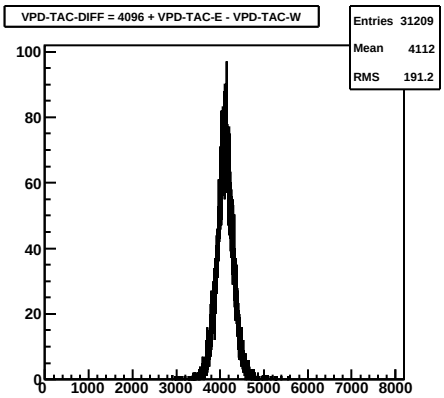

BBClarge-TAC-E
Entries 42695
Mean 1392
RMS 348.5

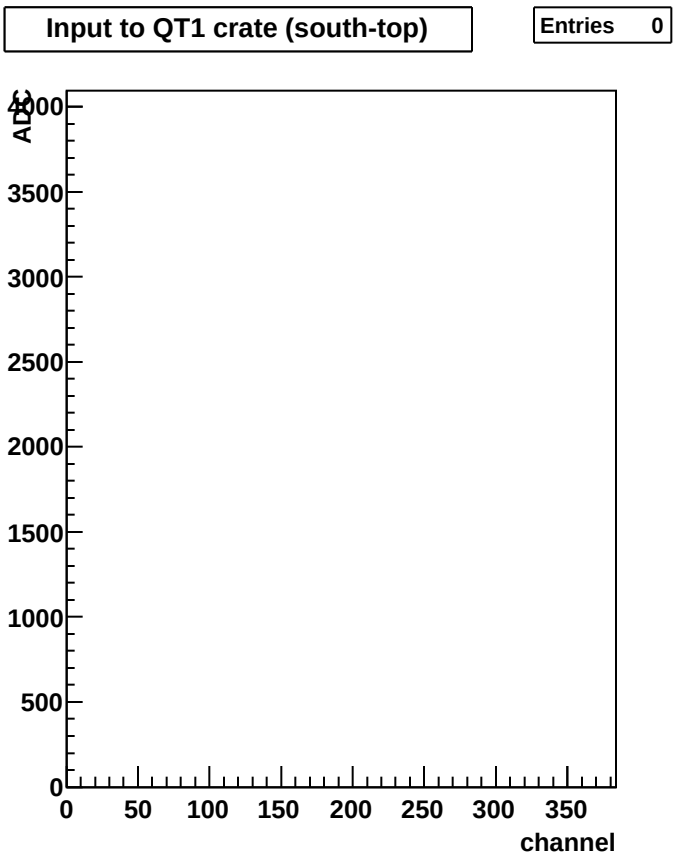

BBClarge-TAC-W
Entries 42192
Mean 1496
RMS 321

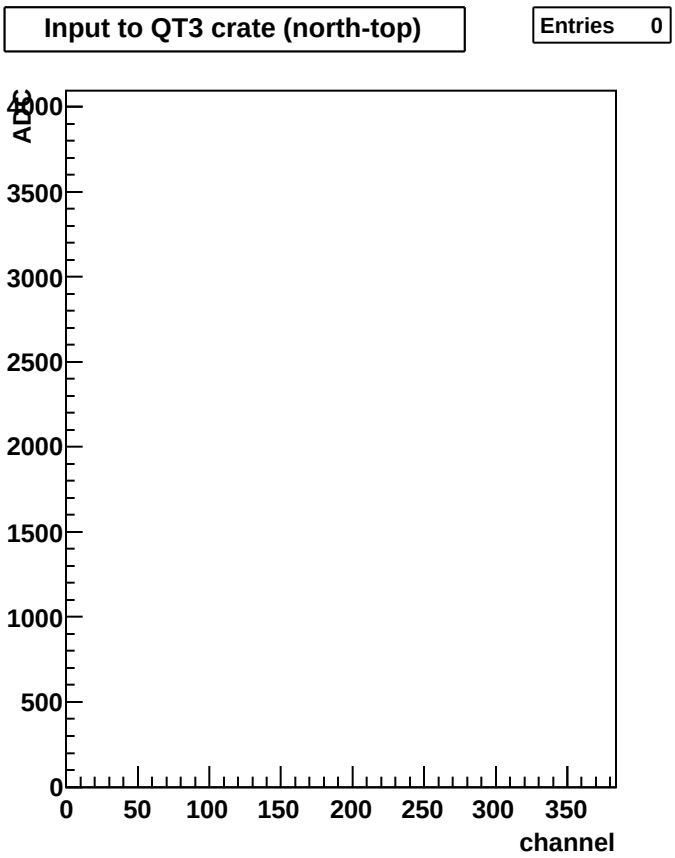

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 40681
Mean 4001
RMS 420.4

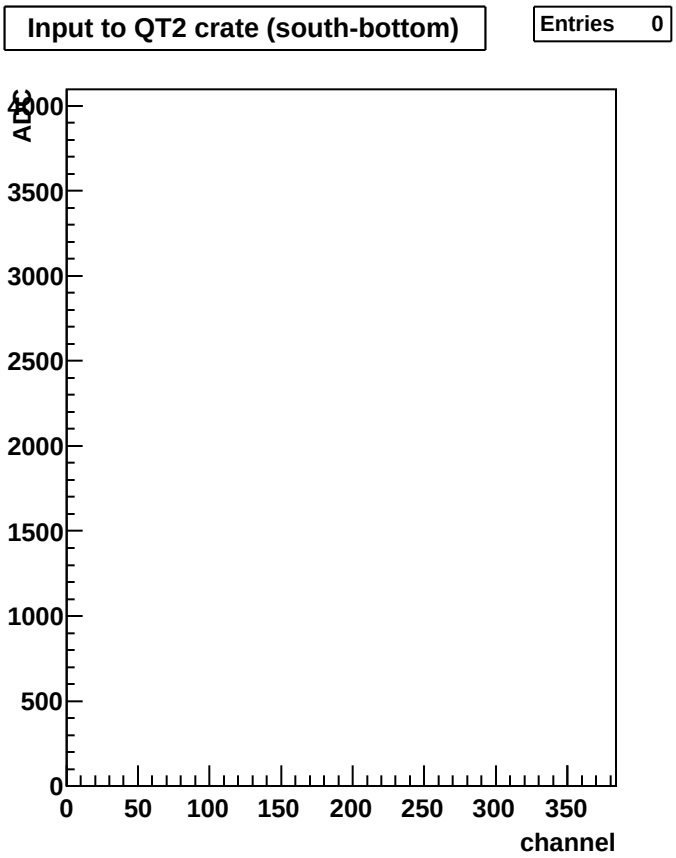

VPD-TAC-E
Entries 36637
Mean 1433
RMS 148.3

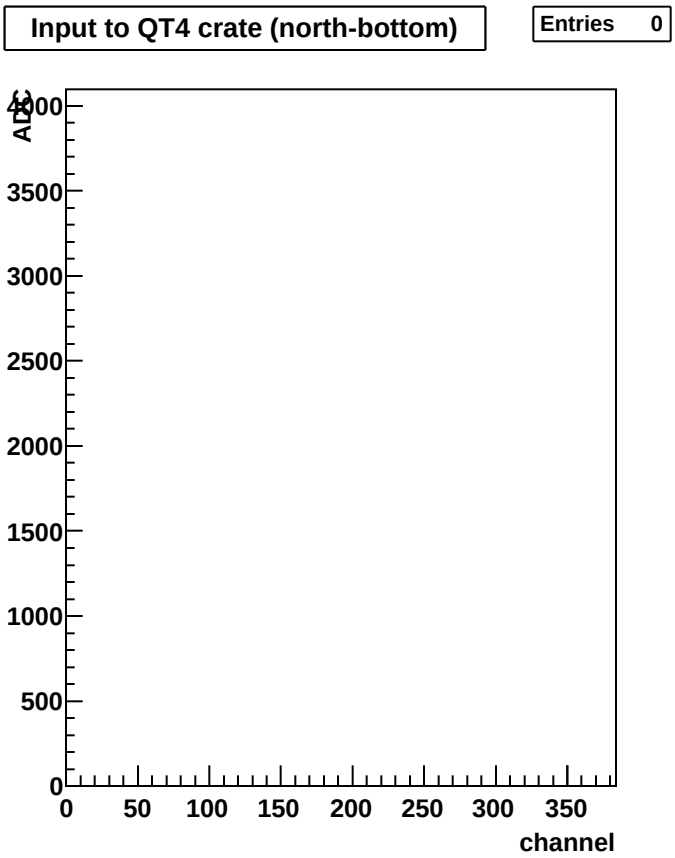

VPD-TAC-W
Entries 35328
Mean 1416
RMS 164.6

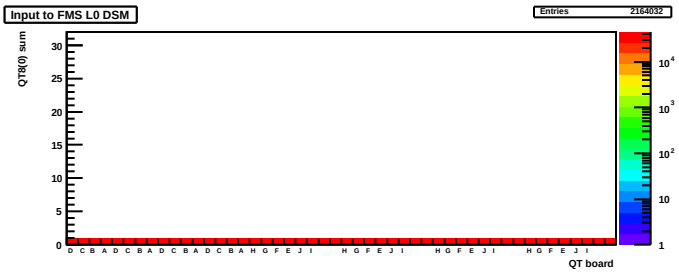

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 31209
Mean 4112
RMS 191.2

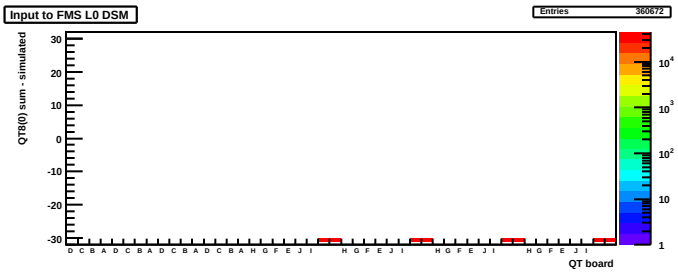

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

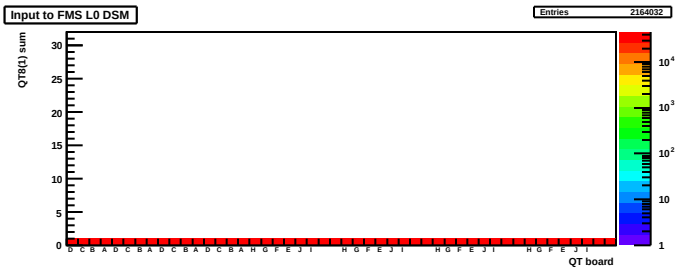

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

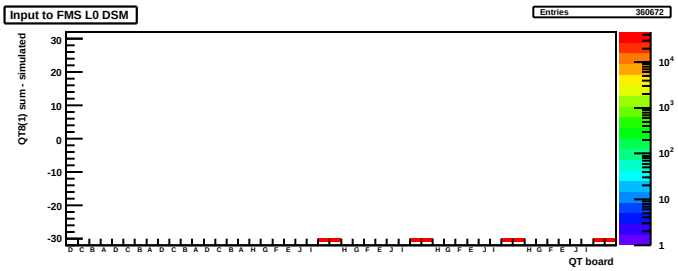

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

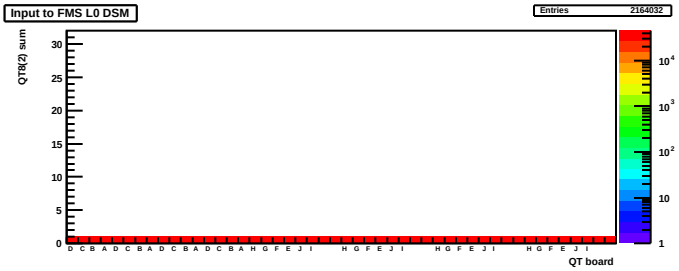

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

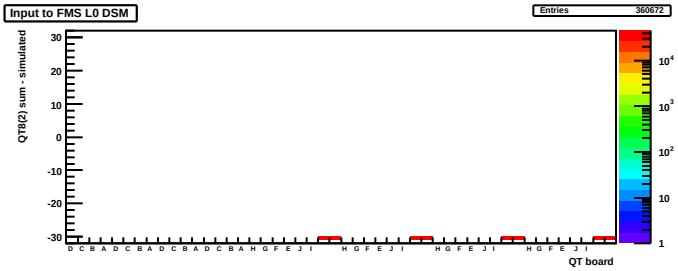

Input to FMS L0 DSM
Entries 2164032
QT8(0) sum
QT board

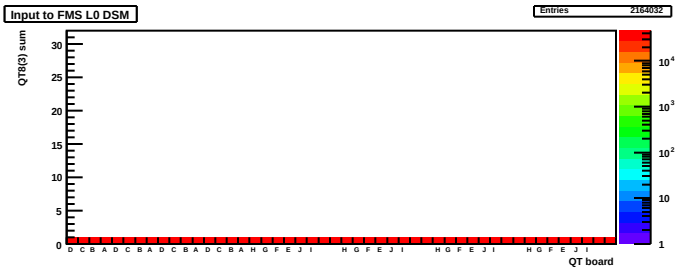

Input to FMS L0 DSM
Entries 360672
QT8(0) sum - simulated
QT board

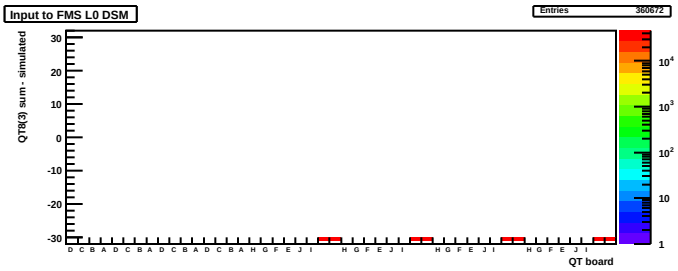

Input to FMS L0 DSM
Entries 2164032
QT8(1) sum
QT board

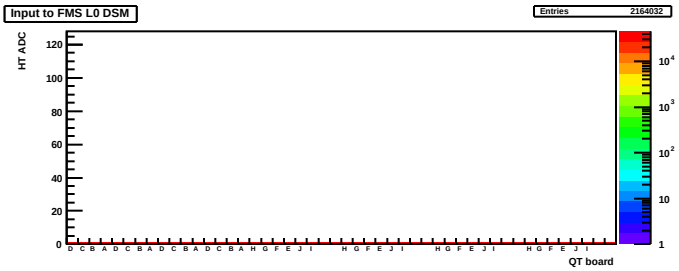

Input to FMS L0 DSM
Entries 360672
QT8(1) sum - simulated
QT board

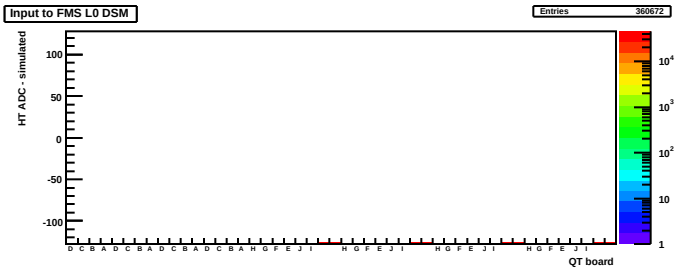

Input to FMS L0 DSM
Entries 2164032
QT8(2) sum
QT board

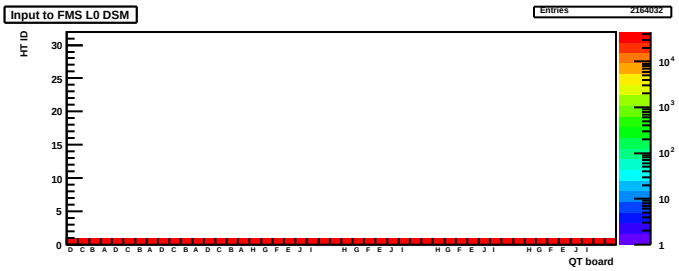

Input to FMS L0 DSM
Entries 360672
QT8(2) sum - simulated
QT board

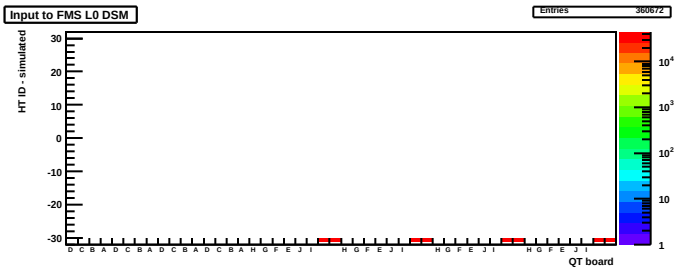

Input to FMS L0 DSM
Entries 2164032
QT8(3) sum
QT board

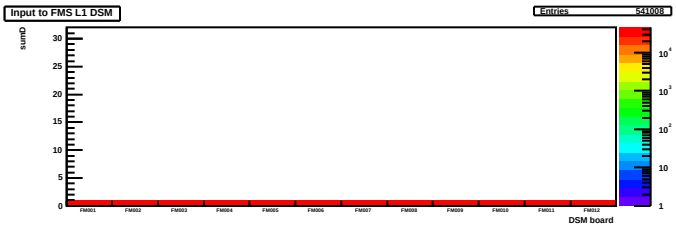

Input to FMS L0 DSM
Entries 360672
QT8(3) sum - simulated
QT board

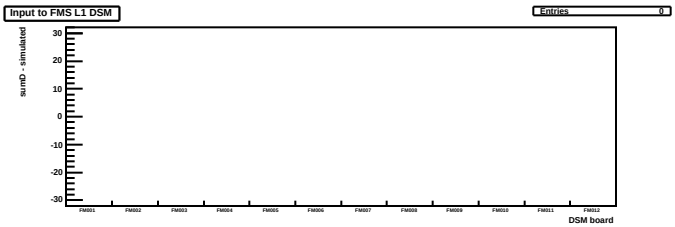

Input to FMS L0 DSM
Entries 2164032
HT ADC
QT board

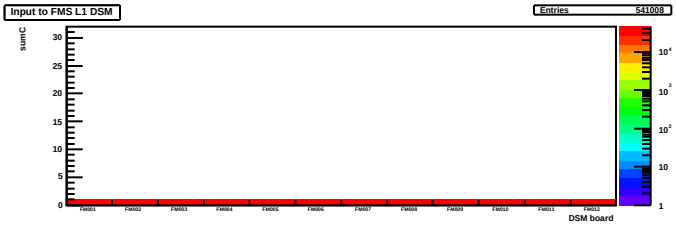

Input to FMS L0 DSM
Entries 360672
HT ADC - simulated
QT board

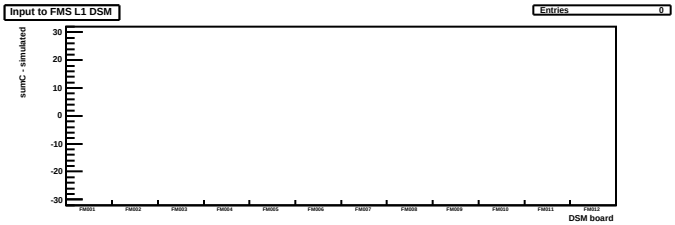

Input to FMS L0 DSM
Entries 2164032
HT ID
QT board

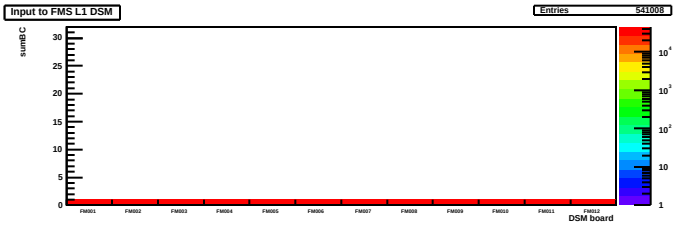

Input to FMS L0 DSM
Entries 360672
HT ID - simulated
QT board

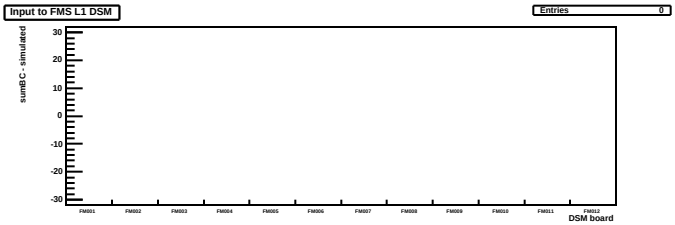

Input to FMS L1 DSM
Entries 541008
sumD
DSM board

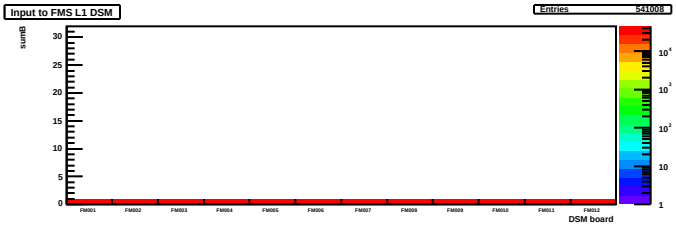

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

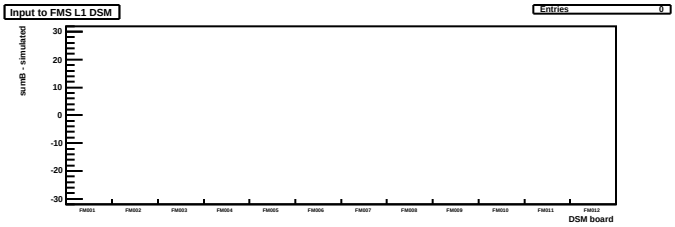

Input to FMS L1 DSM
Entries 541008
sumC
DSM board

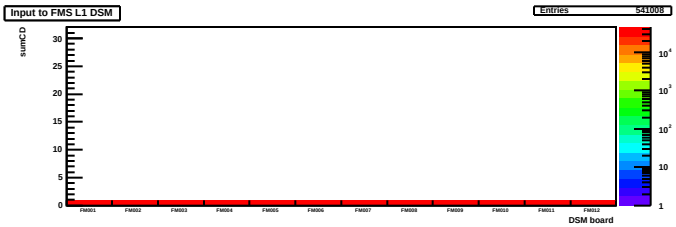

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

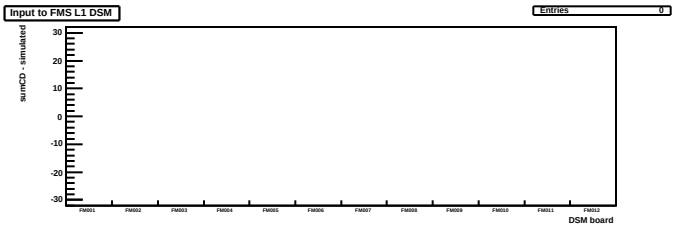

Input to FMS L1 DSM
Entries 541008
sumBC
DSM board

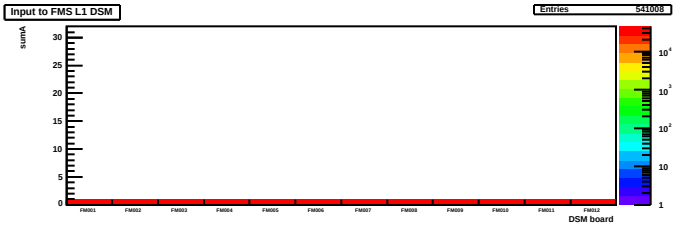

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

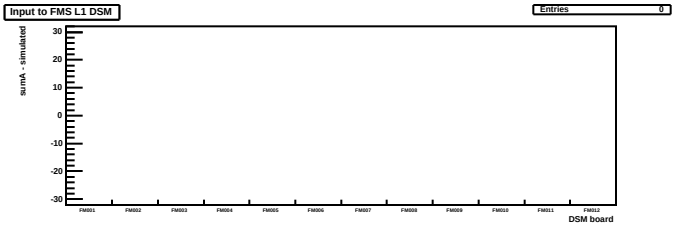

Input to FMS L1 DSM
Entries 541008
sumB
DSM board

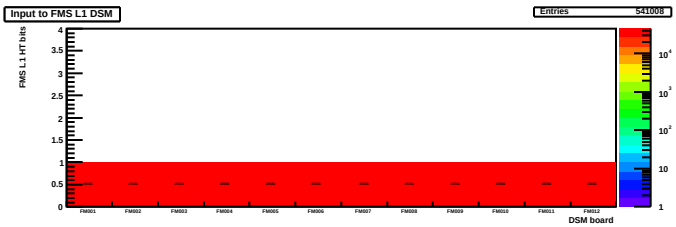

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

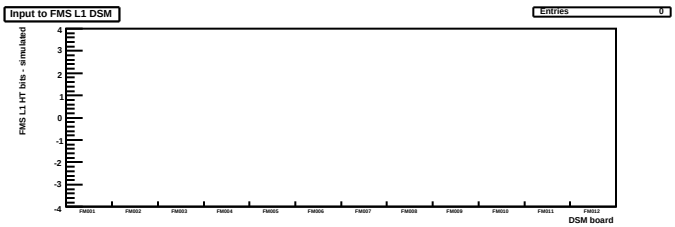

Input to FMS L1 DSM
Entries 541008
sumCD
DSM board

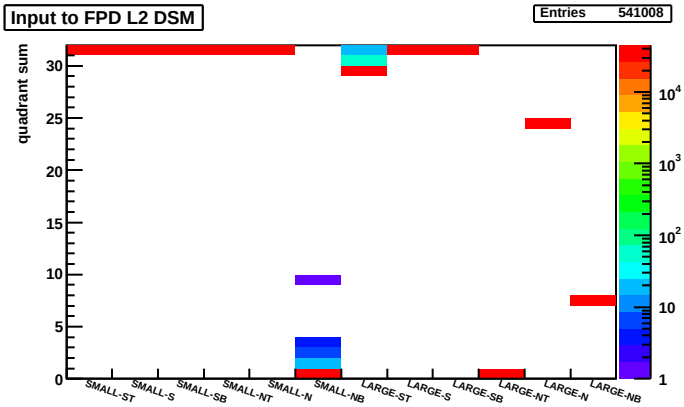

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

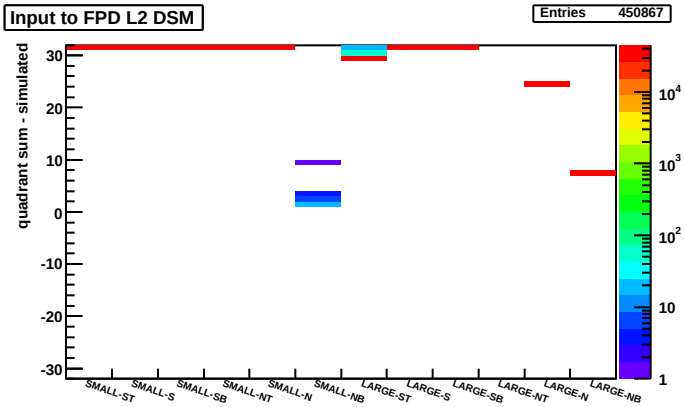

Input to FMS L1 DSM
Entries 541008
sumA
DSM board

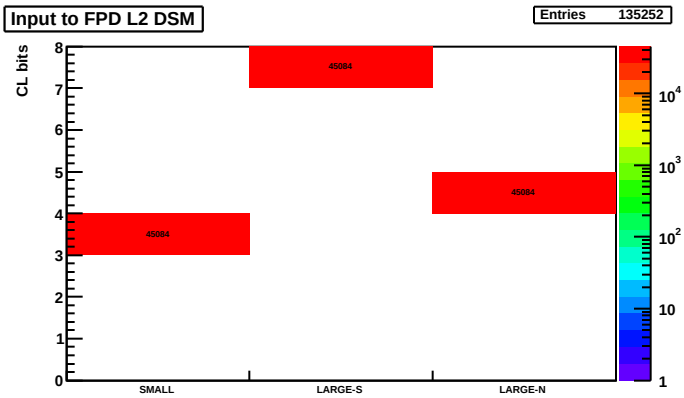

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

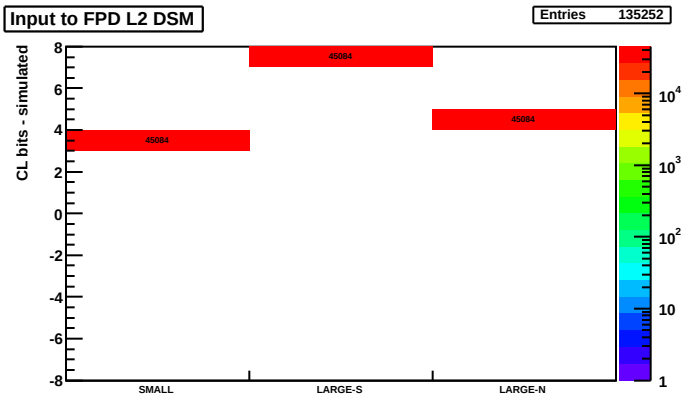

Input to FMS L1 DSM
Entries 541008
FMS L1 HT bits
DSM board

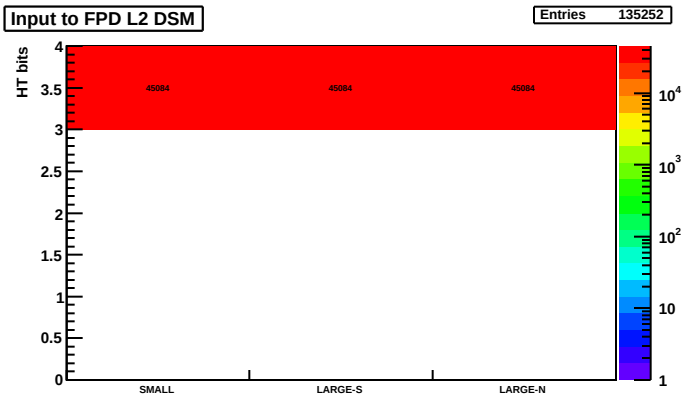

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

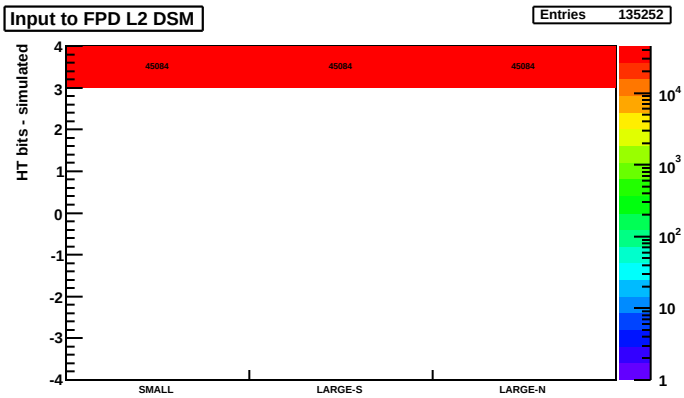

Input to FPD L2 DSM
Entries 541008
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

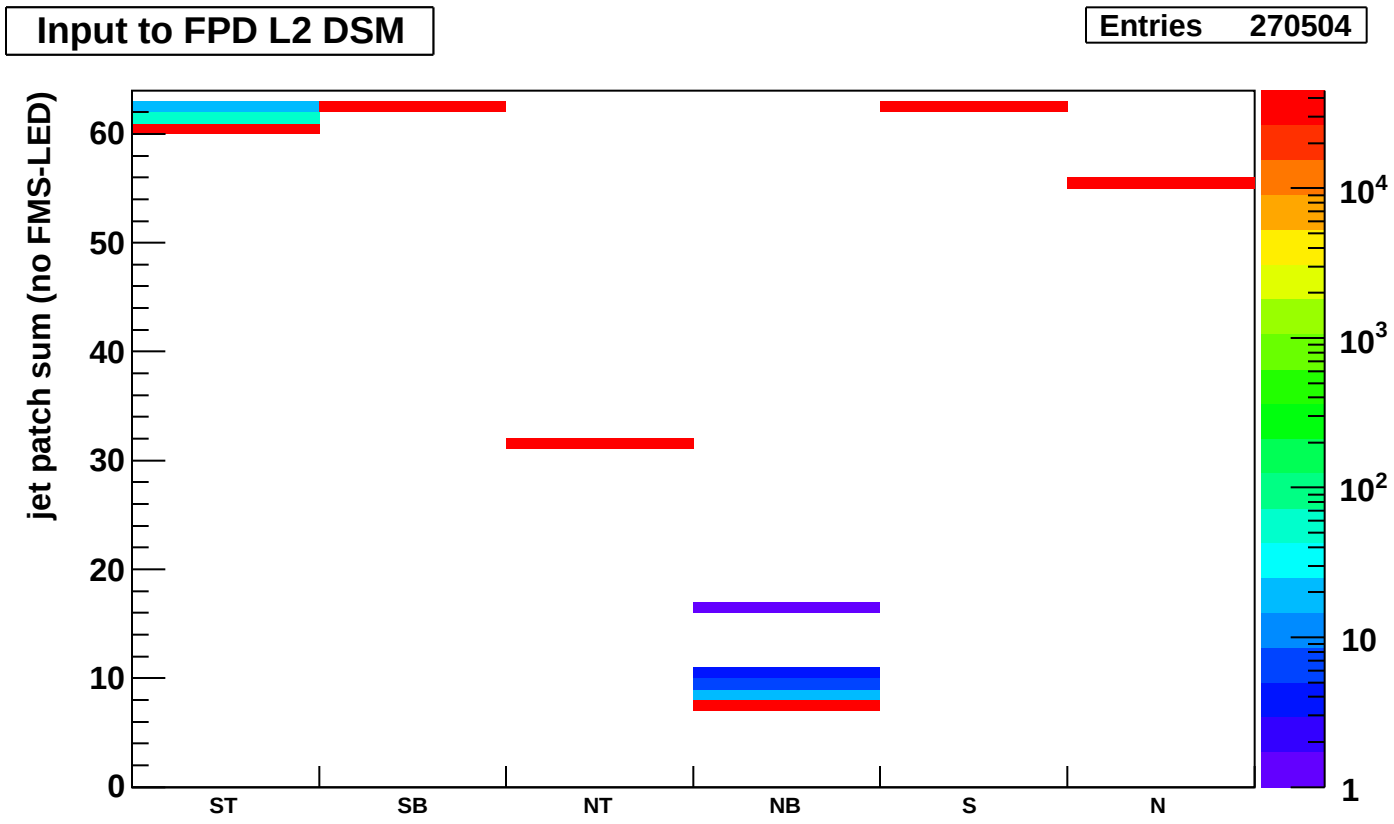

Input to FPD L2 DSM
Entries 450867
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

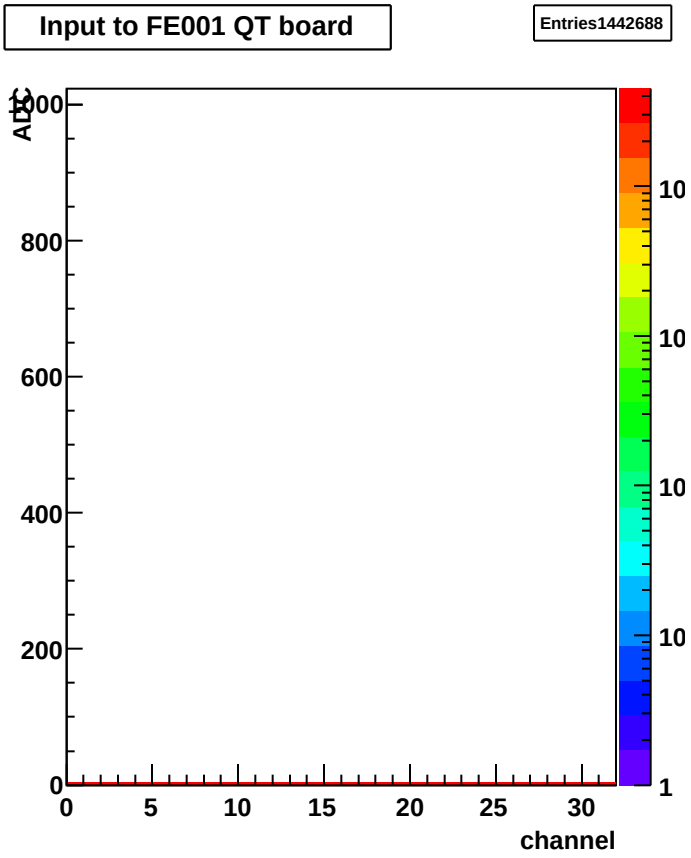

Input to FPD L2 DSM
Entries 135252
CL bits
45084
45084
45084
SMALL
LARGE-S
LARGE-N

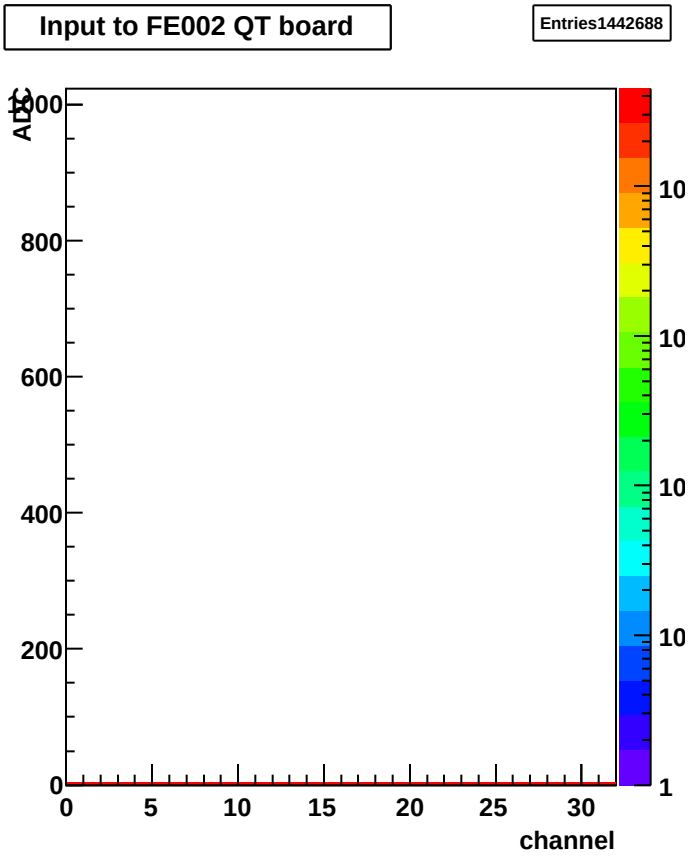

Input to FPD L2 DSM
Entries 135252
CL bits - simulated
45084
45084
45084
SMALL
LARGE-S
LARGE-N

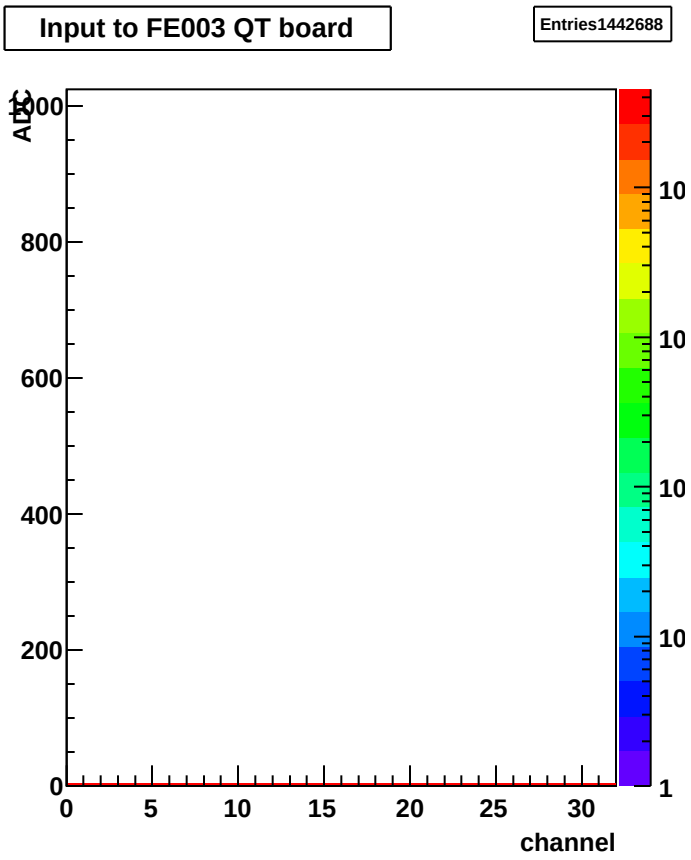

Input to FPD L2 DSM
Entries 135252
HT bits
45084
45084
45084
SMALL
LARGE-S
LARGE-N

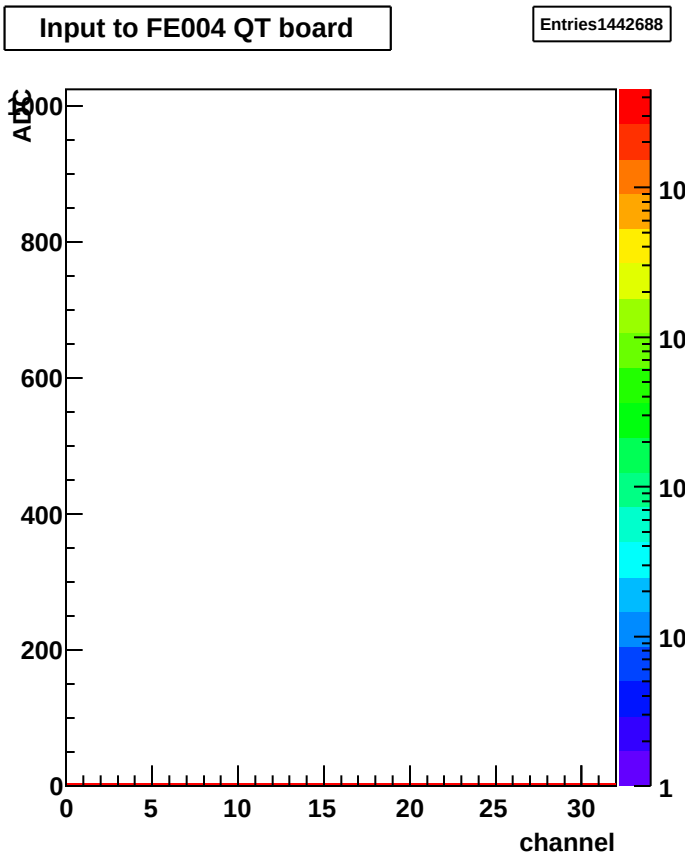

Input to FPD L2 DSM
Entries 135252
HT bits - simulated
45084
45084
45084
SMALL
LARGE-S
LARGE-N

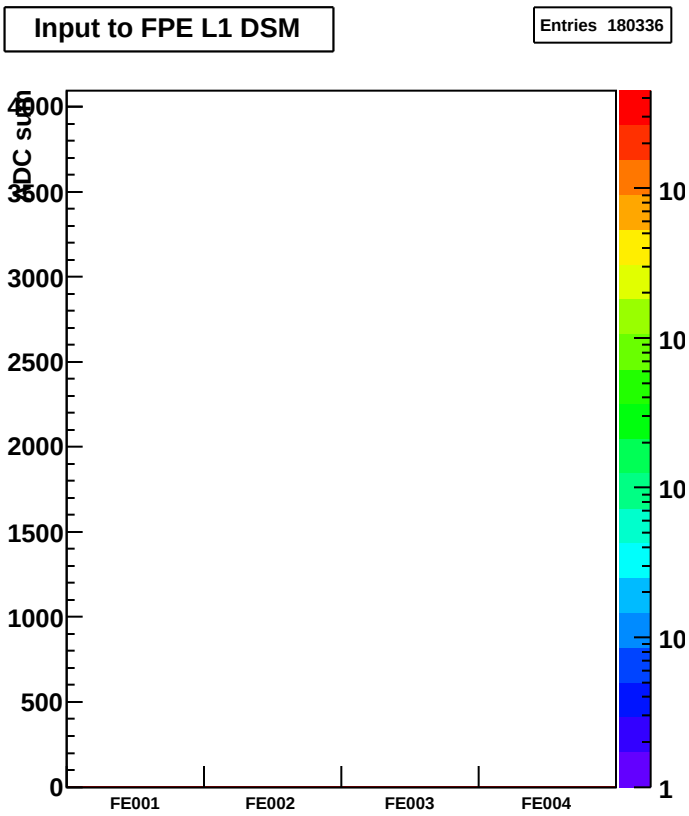

Input to FPD L2 DSM
Entries 270504
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

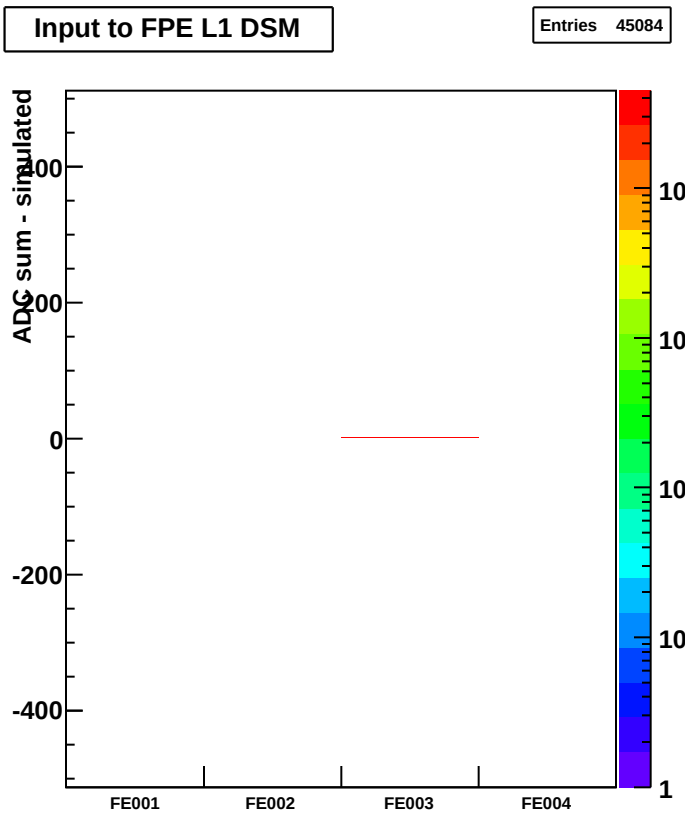

Input to FE001 QT board
Entries1442688
ADC
channel

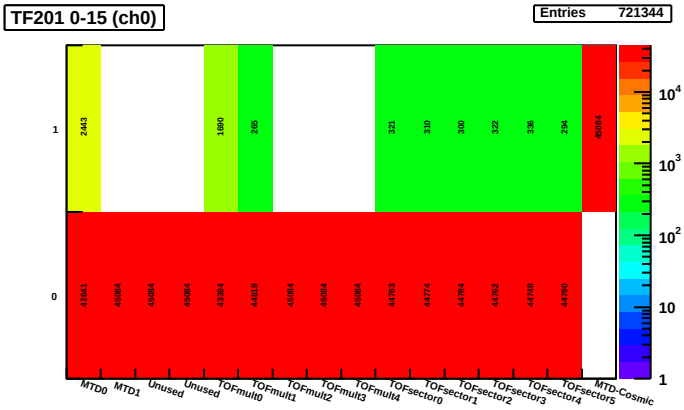

Input to FE002 QT board
Entries1442688
ADC
channel

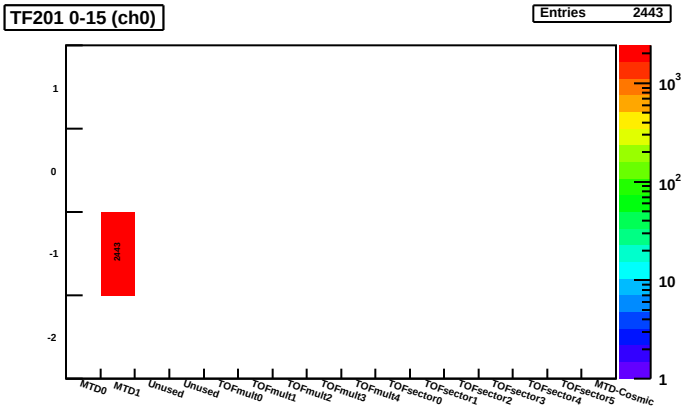

Input to FE003 QT board
Entries1442688
ADC
channel

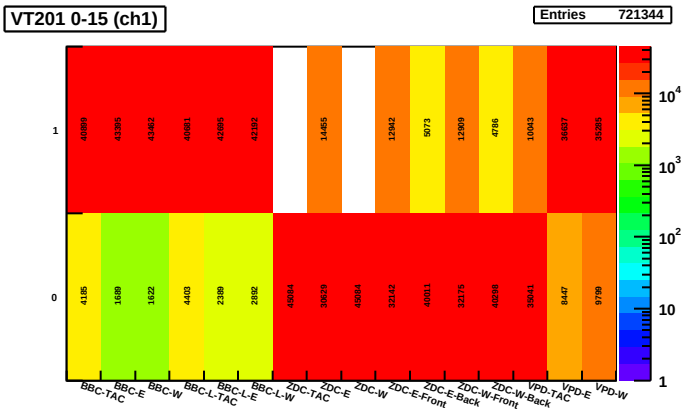

Input to FE004 QT board
Entries1442688
ADC
channel

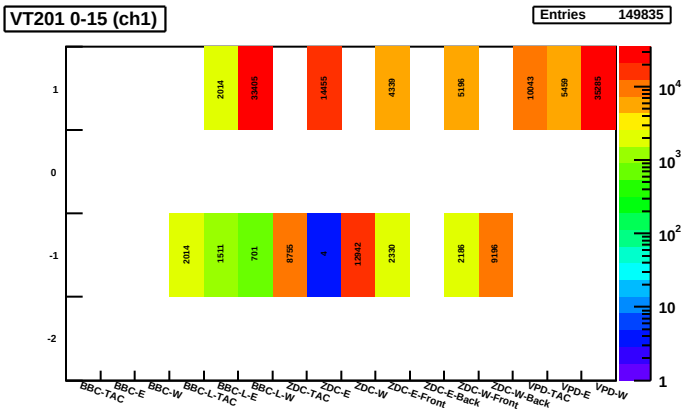

Input to FPE L1 DSM
Entries 180336
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

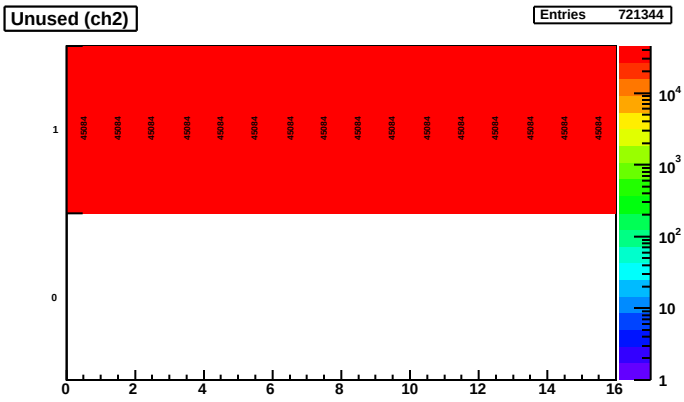

Input to FPE L1 DSM
Entries 45084
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


TF201 0-15 (ch0)
Entries 721344
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 2443
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 721344
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 149835
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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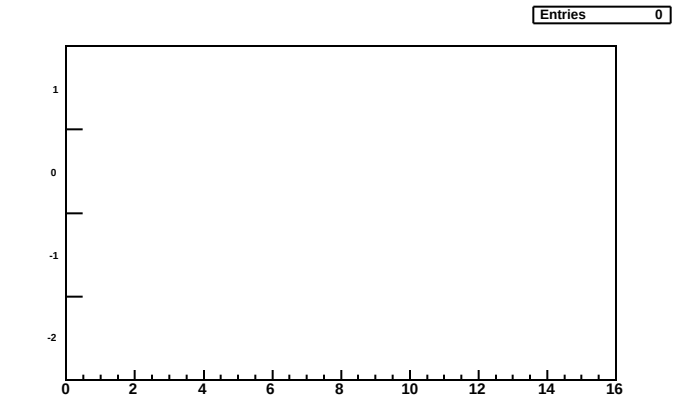
Entries 721344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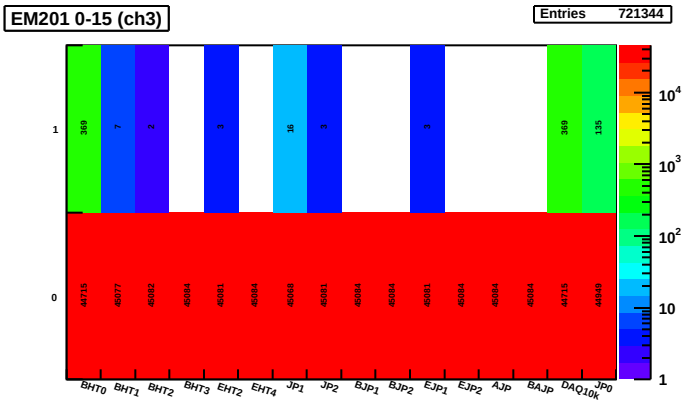
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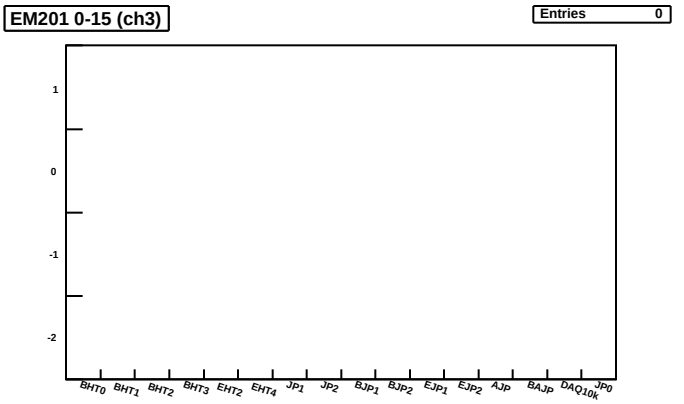
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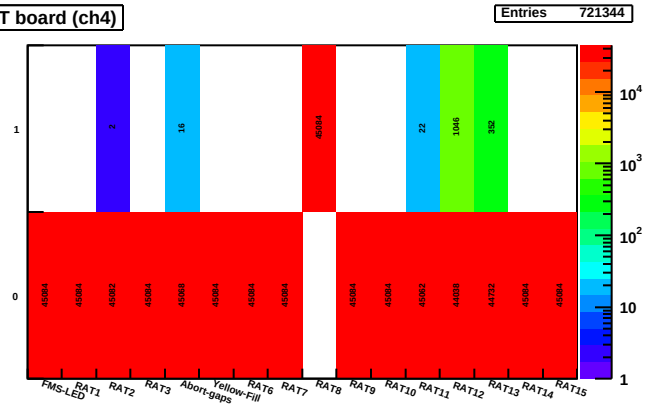
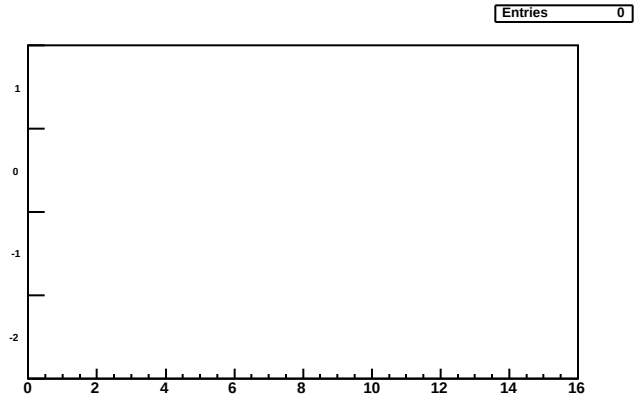
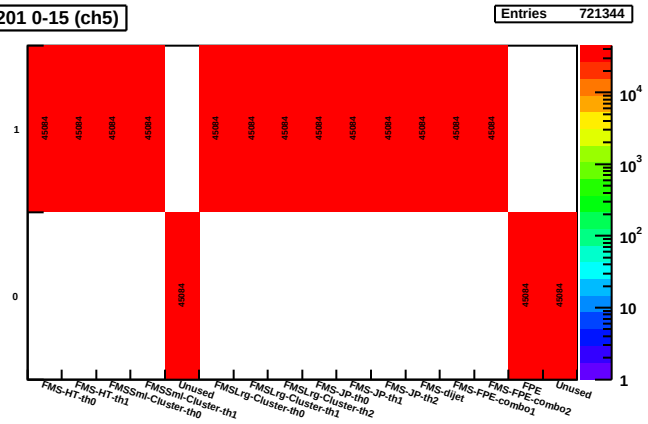
RAT board (ch4)





FP201 0-15 (ch5)



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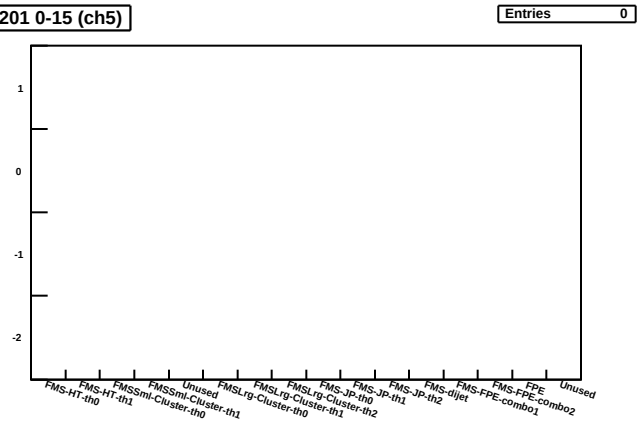
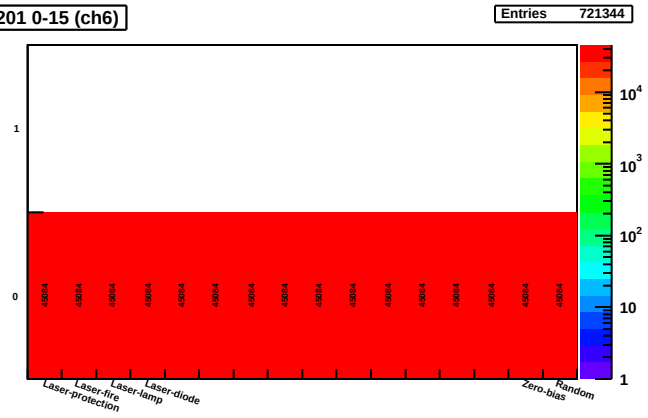
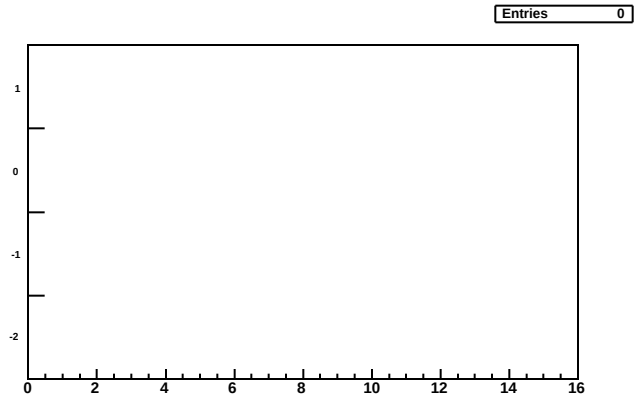
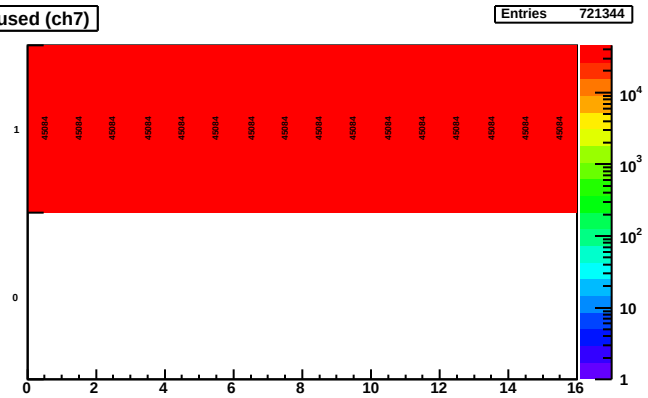
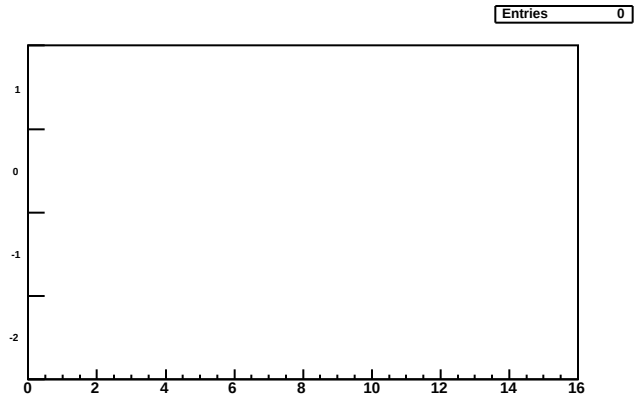


ST201 0-15 (ch6)





Unused (ch7)