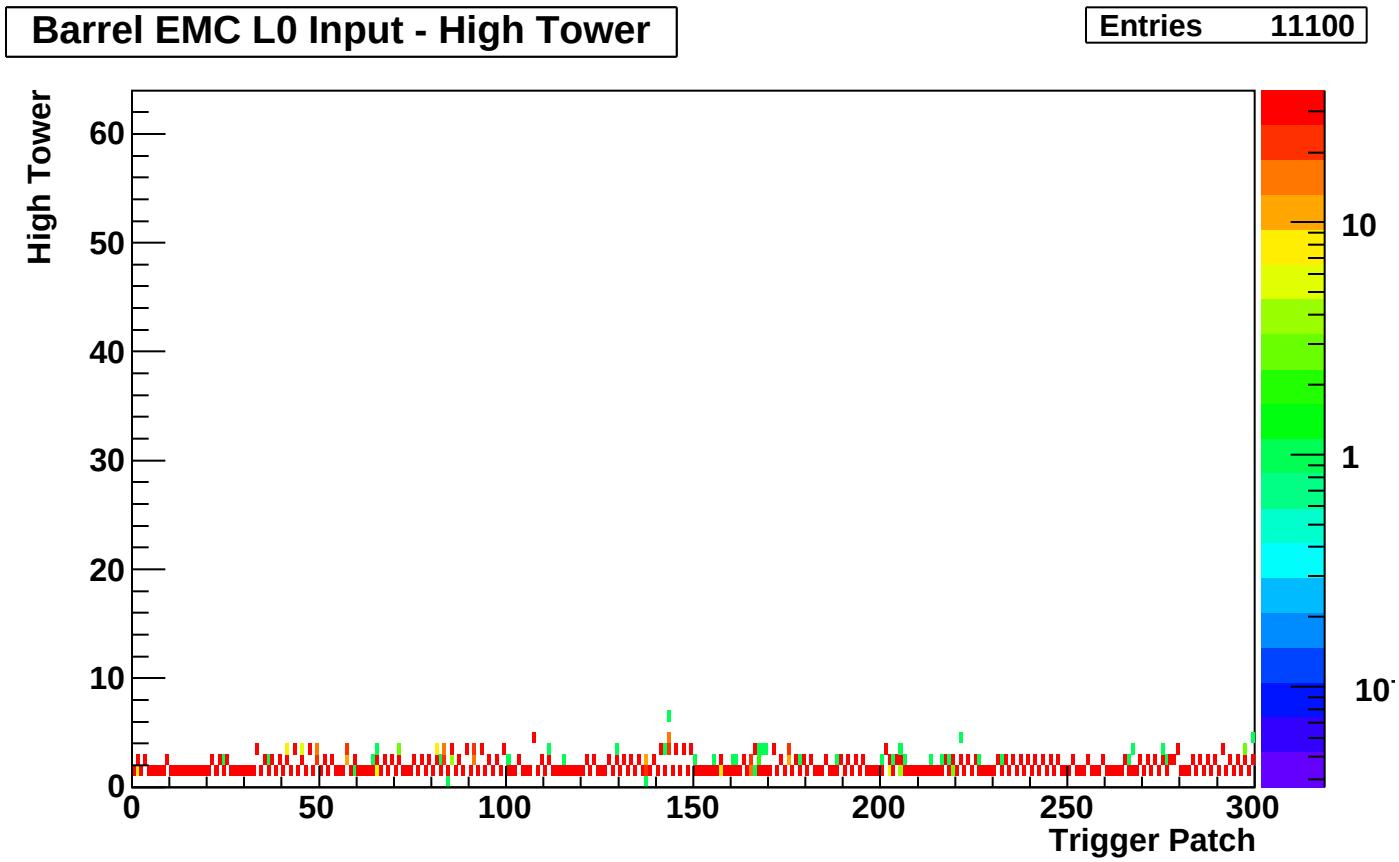
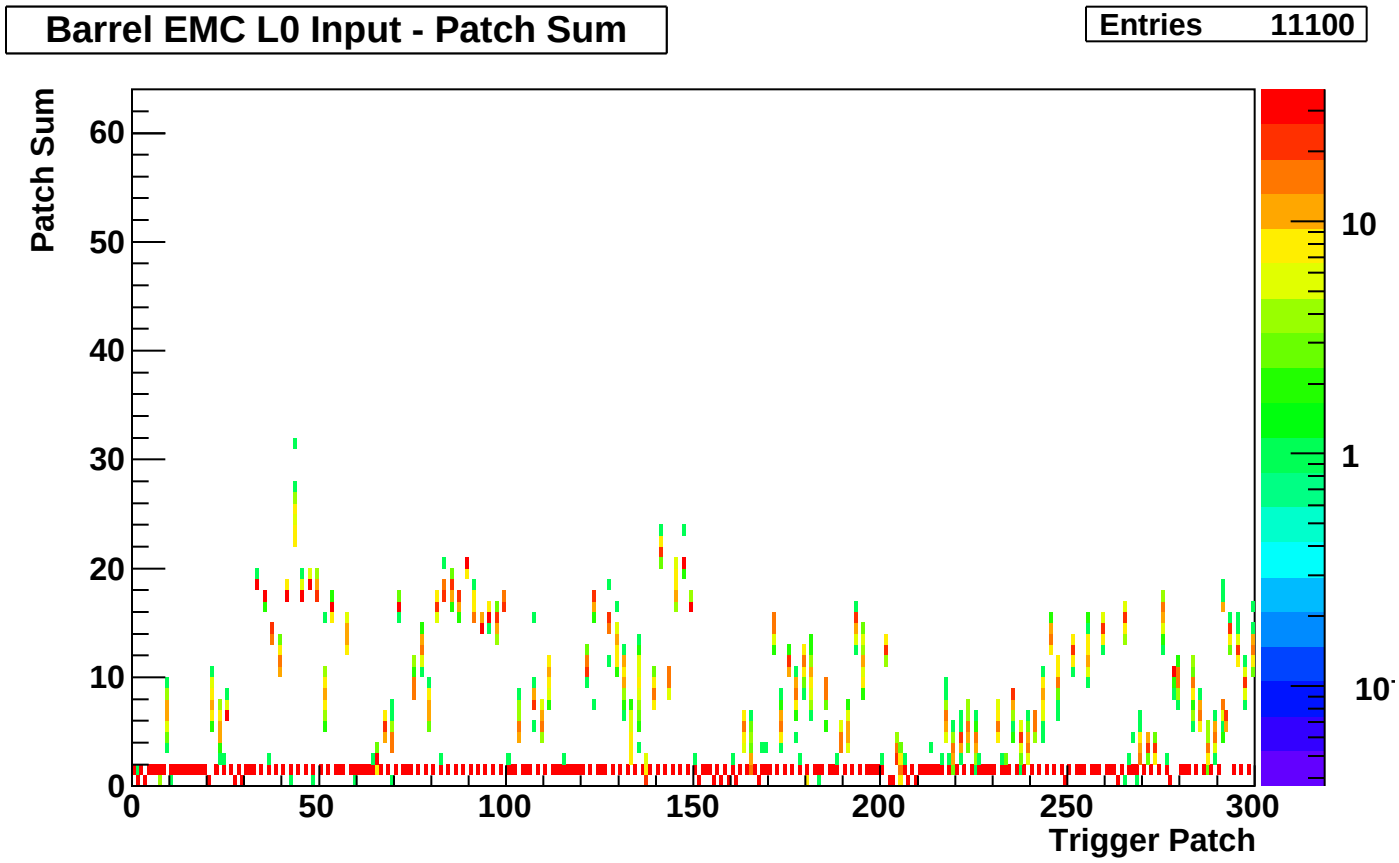
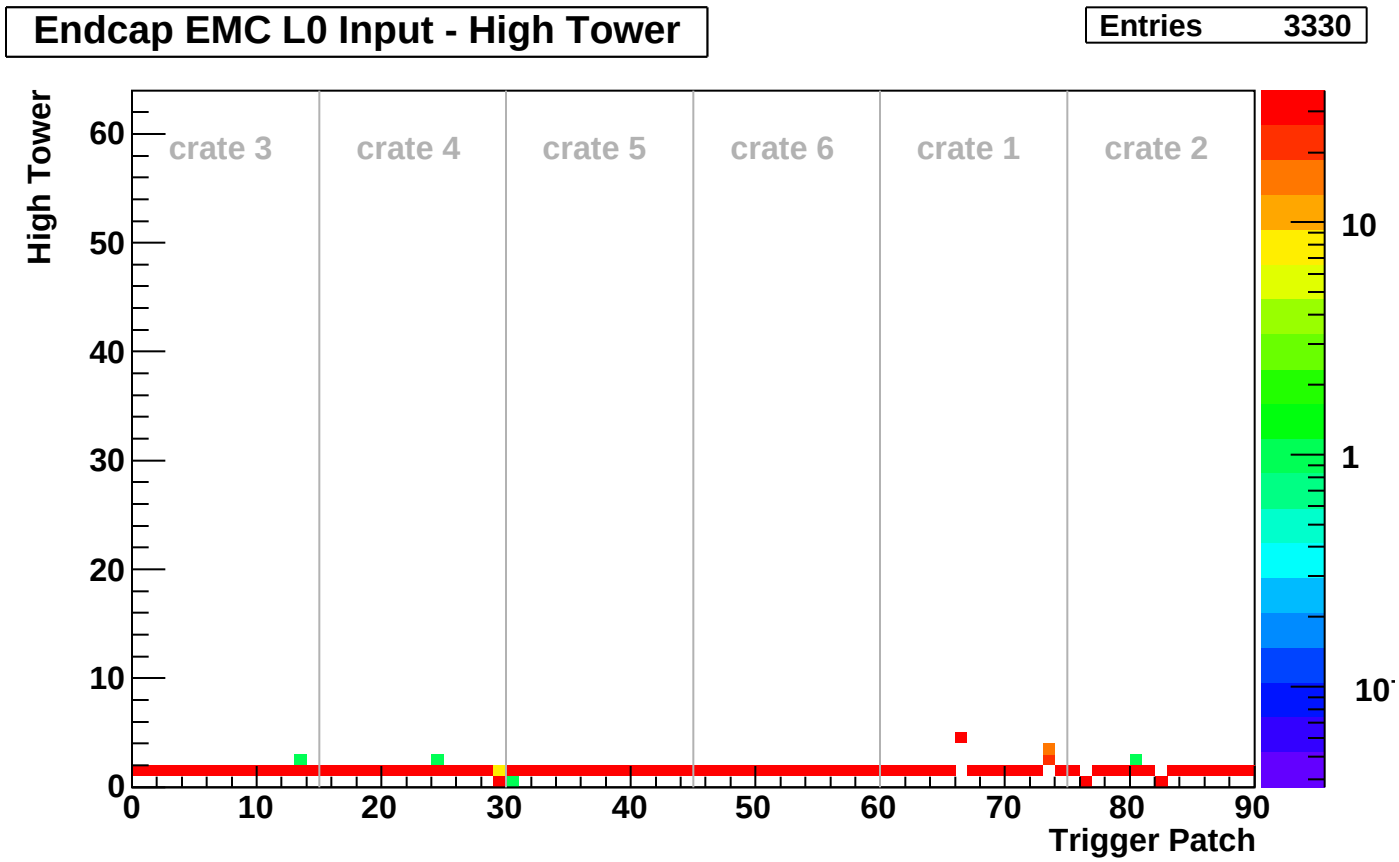
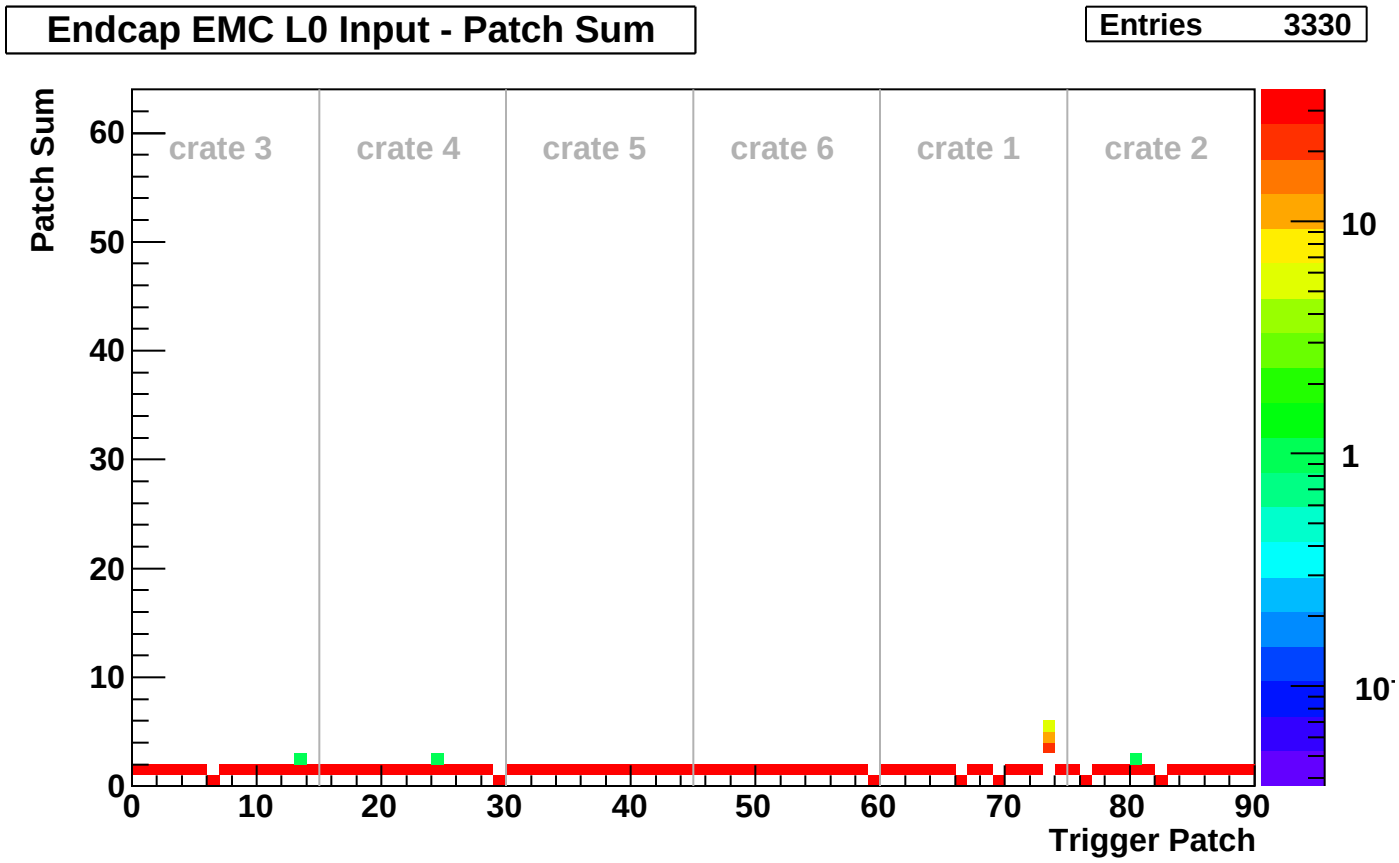
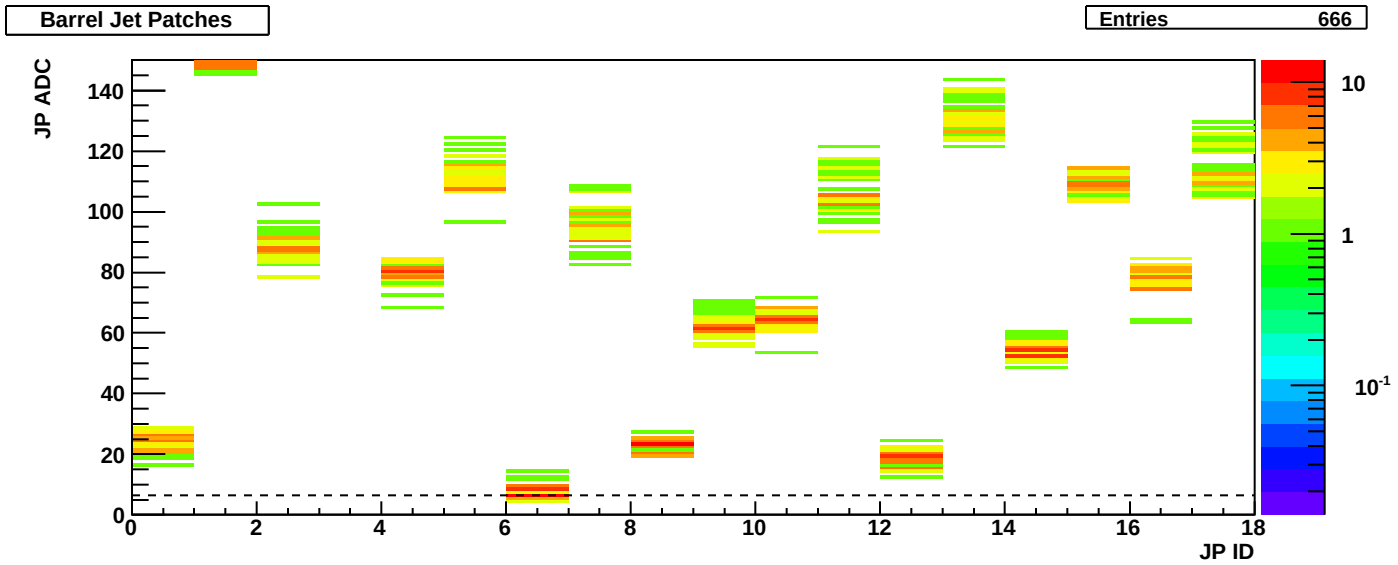
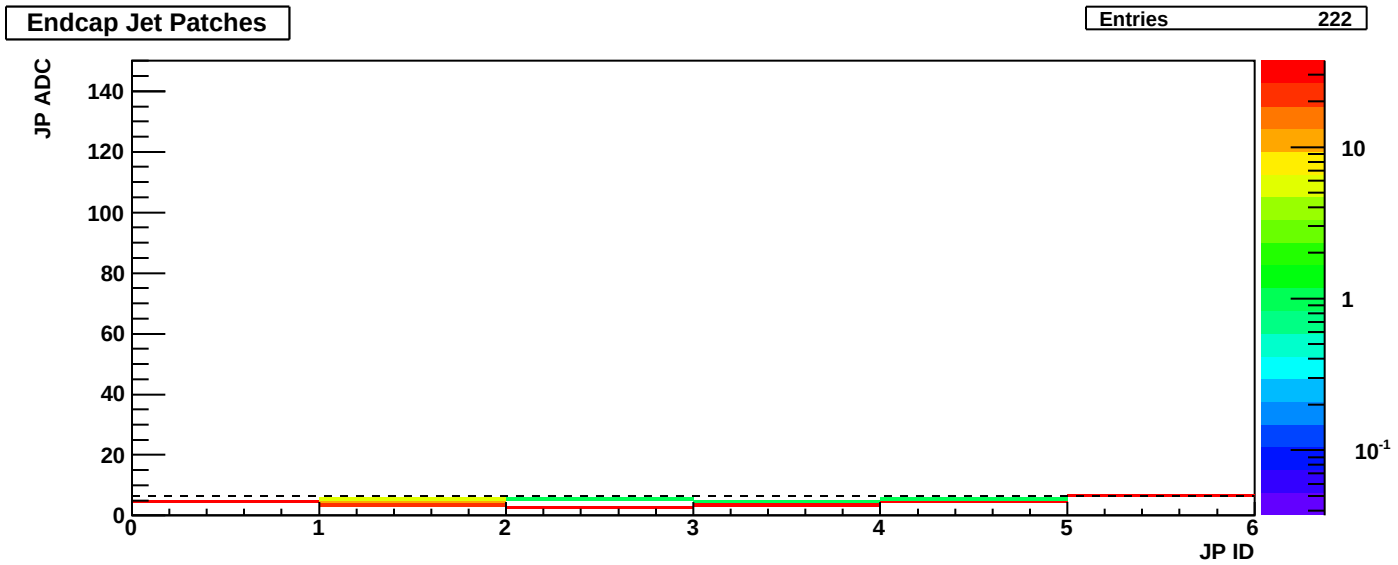

Barrel EMC L0 Input - High Tower
Entries 11100
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10

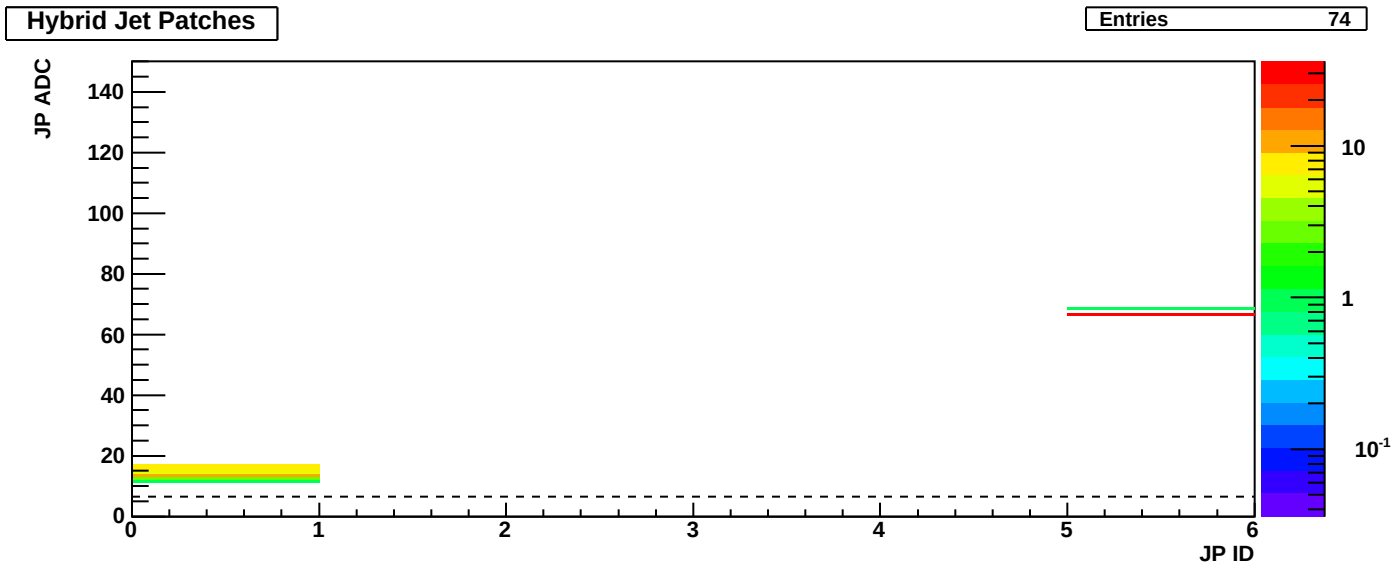

Barrel EMC L0 Input - Patch Sum
Entries 11100
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10

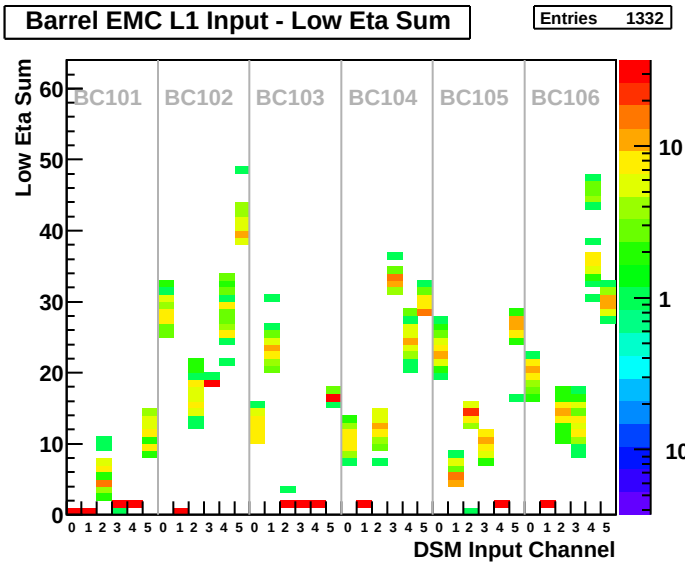

Endcap EMC L0 Input - High Tower
Entries 3330
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10

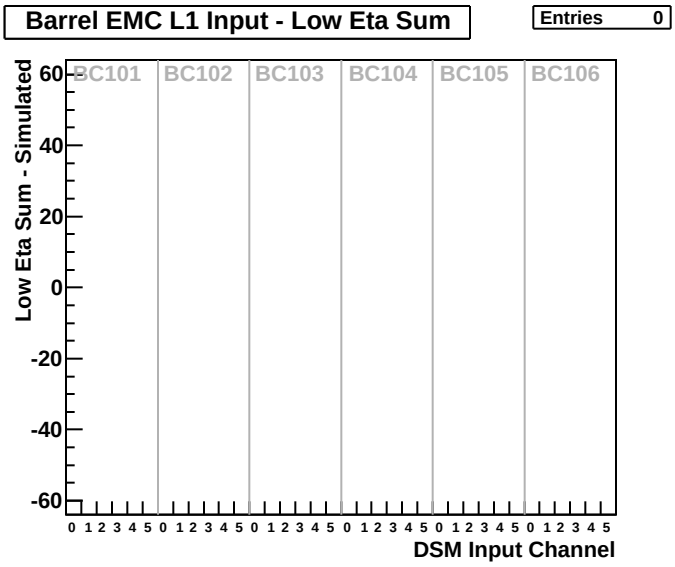

Endcap EMC L0 Input - Patch Sum
Entries 3330
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10

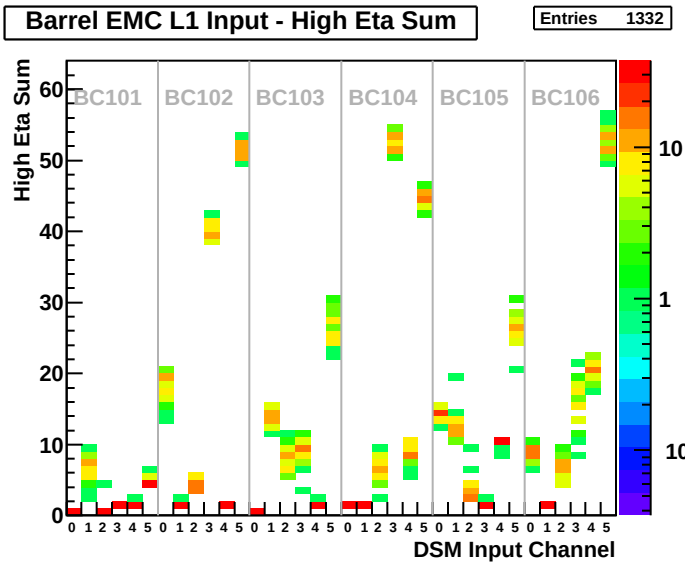

Barrel Jet Patches
Entries 666
JP ADC
JP ID

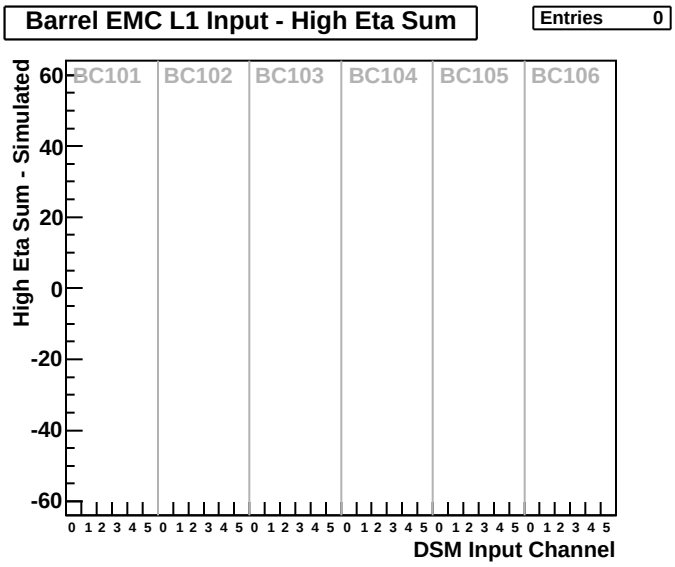

Endcap Jet Patches
Entries 222
JP ADC
JP ID

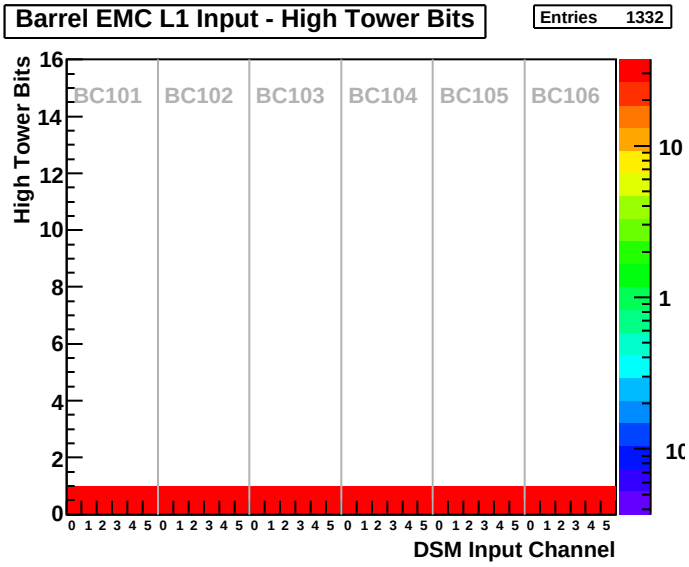

Hybrid Jet Patches
Entries 74
JP ADC
JP ID

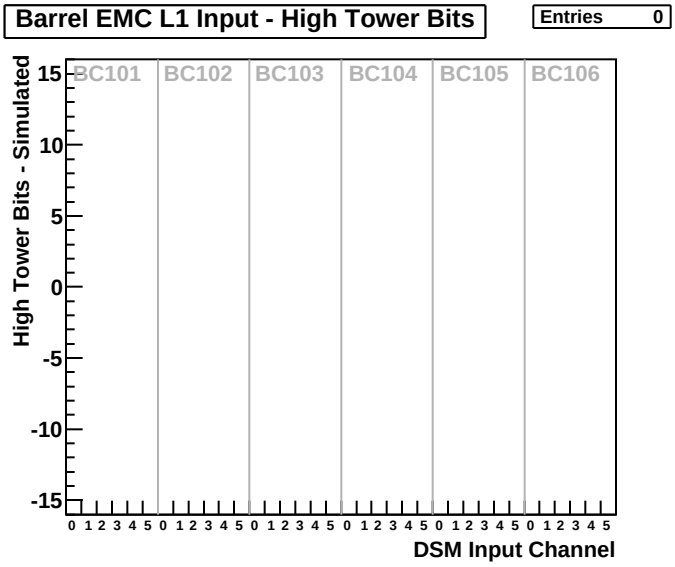

Barrel EMC L1 Input - Low Eta Sum
Entries 1332
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

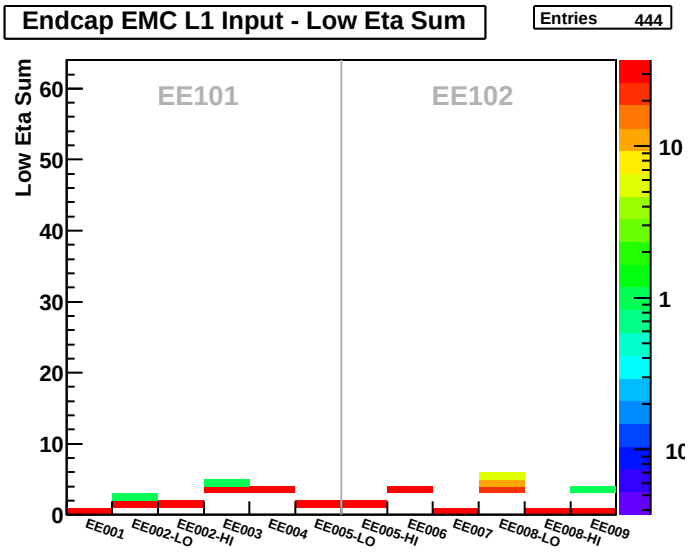

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

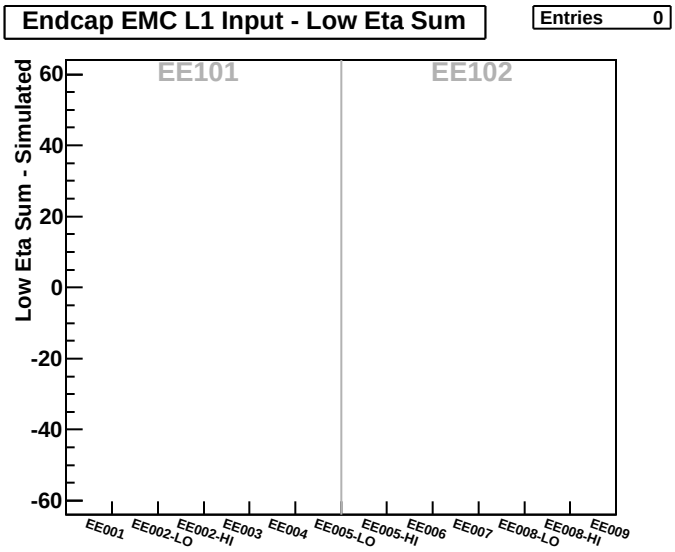

Barrel EMC L1 Input - High Eta Sum
Entries 1332
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

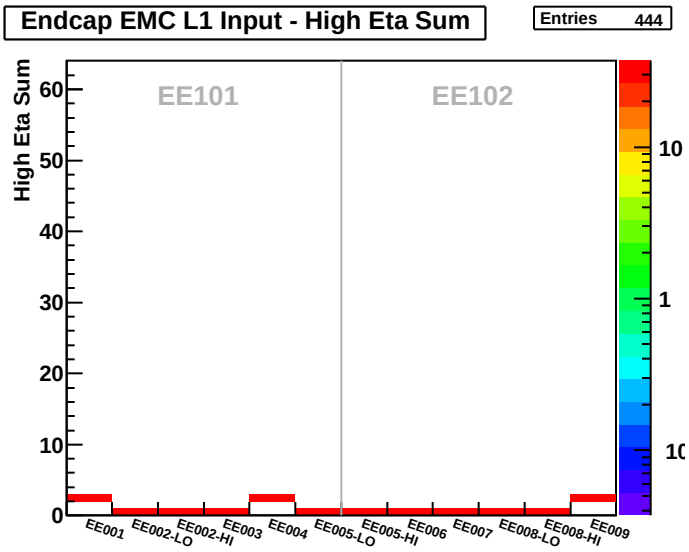

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

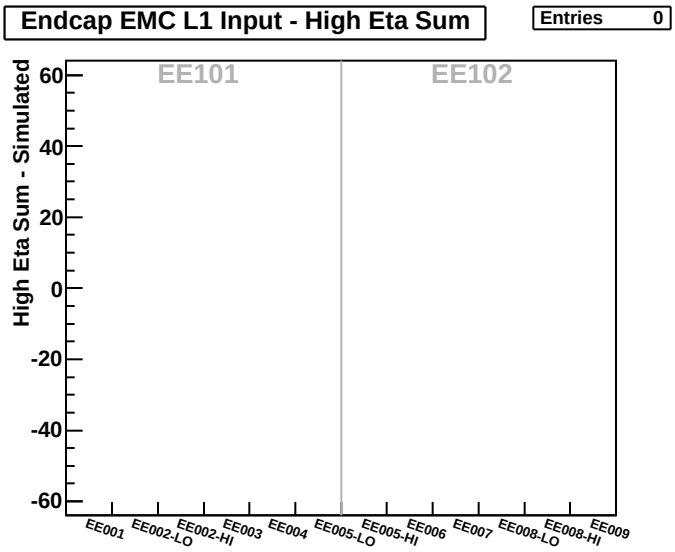

Barrel EMC L1 Input - High Tower Bits
Entries 1332
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

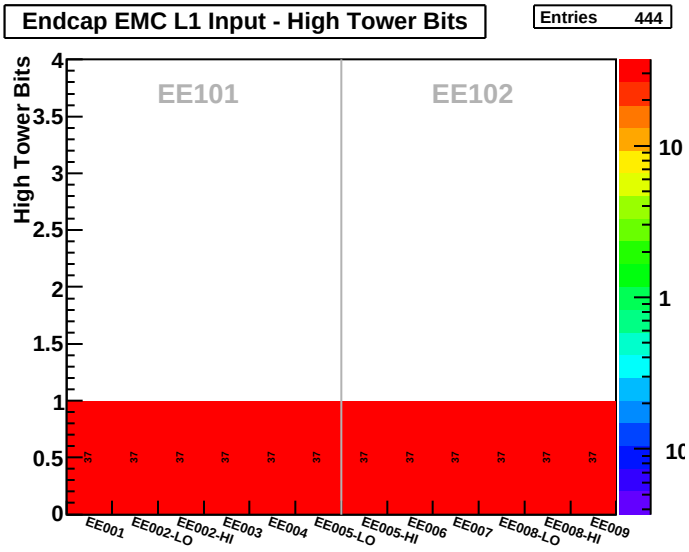

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

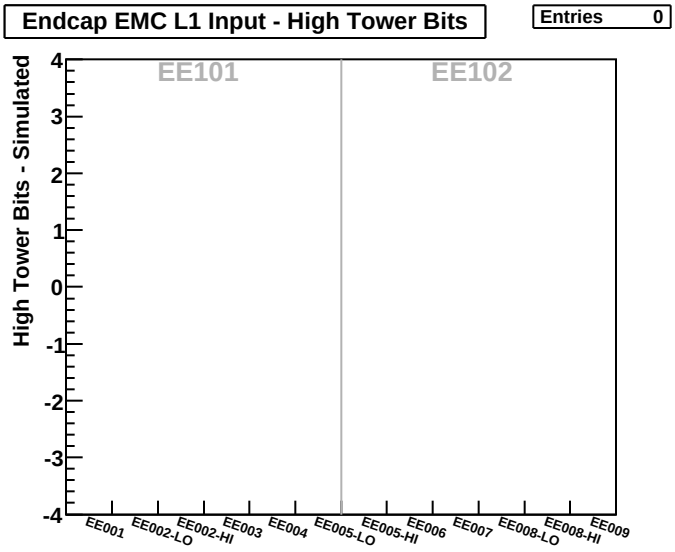

Endcap EMC L1 Input - Low Eta Sum
Entries 444
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

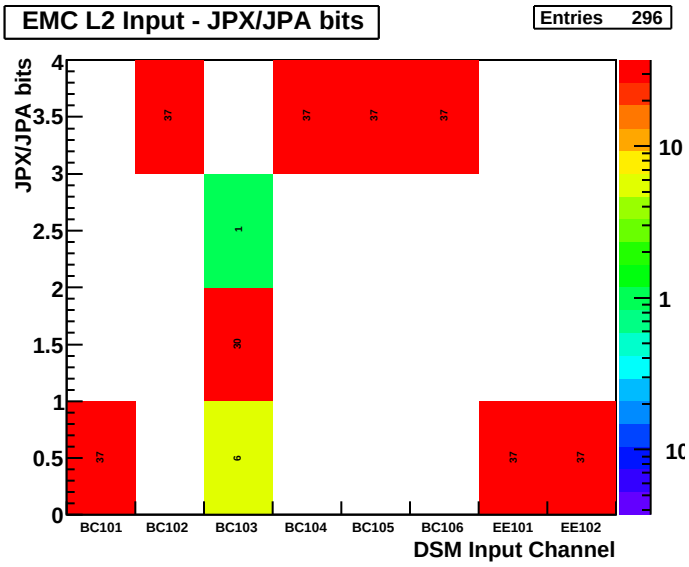

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

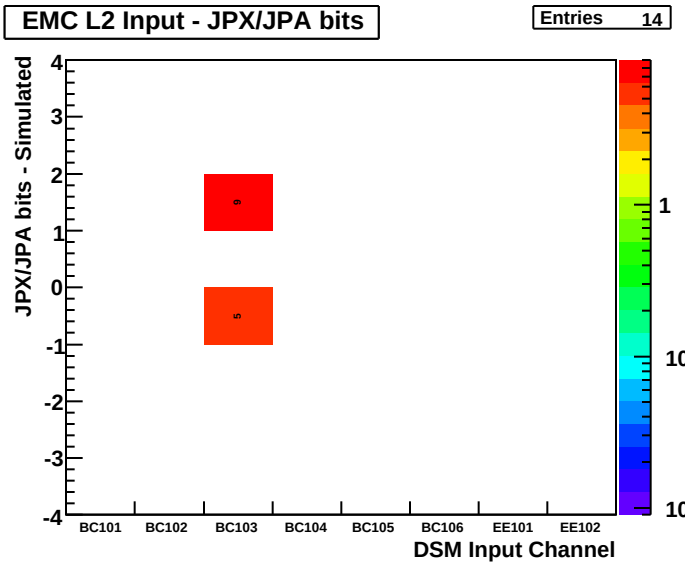

Endcap EMC L1 Input - High Eta Sum
Entries 444
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

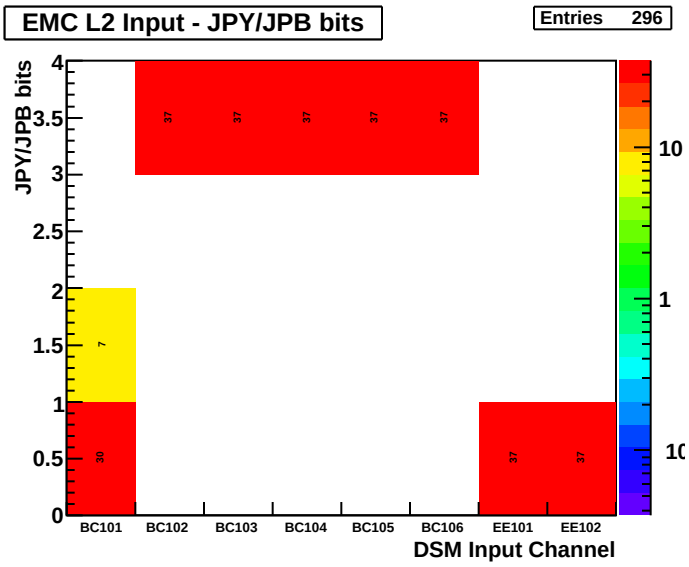

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

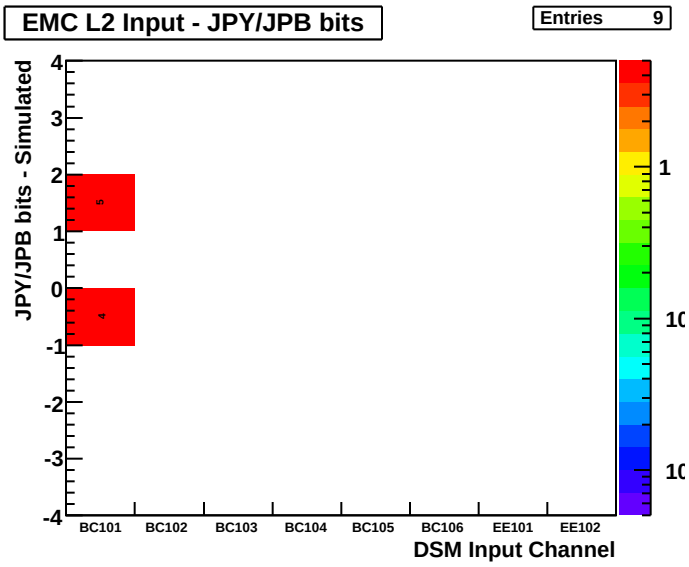

Endcap EMC L1 Input - High Tower Bits
Entries 444
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

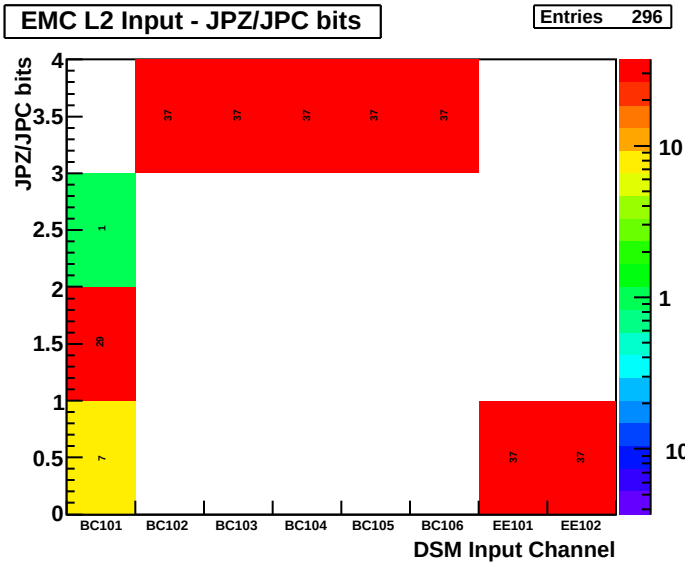

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

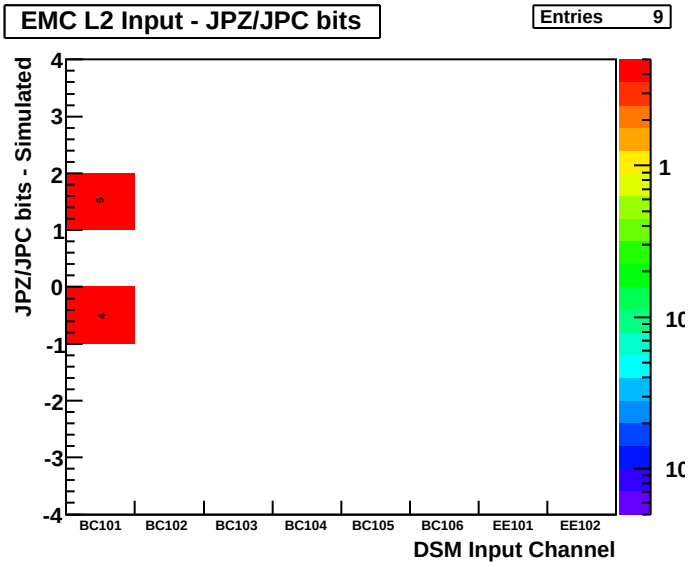

EMC L2 Input - JPX/JPA bits
Entries 296
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

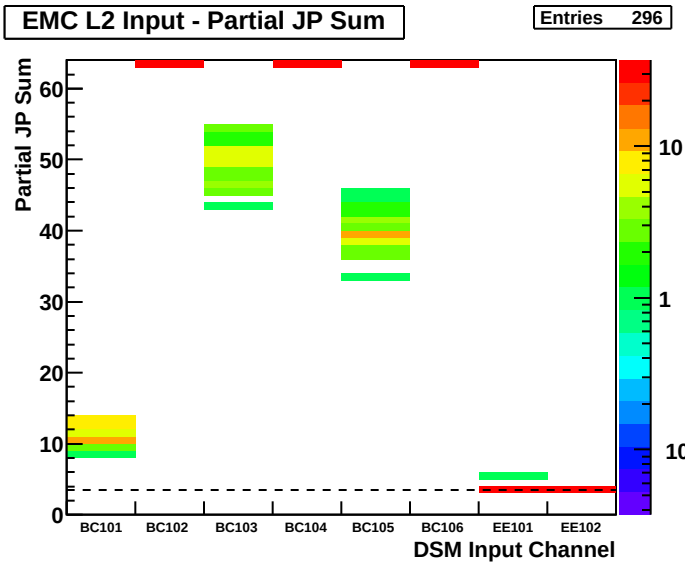

EMC L2 Input - JPX/JPA bits
Entries 14
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

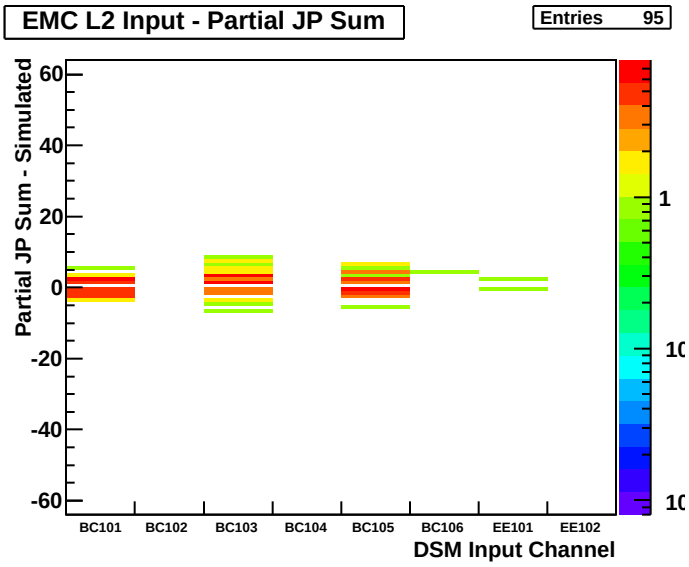

EMC L2 Input - JPY/JPB bits
Entries 296
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

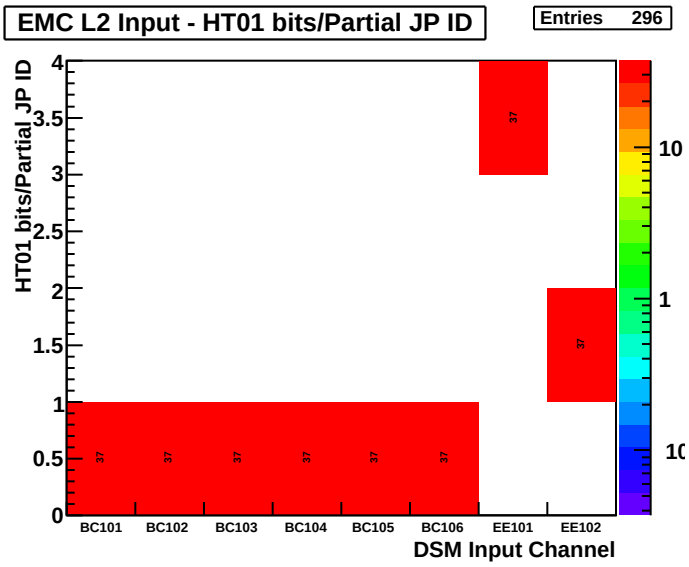

EMC L2 Input - JPY/JPB bits
Entries 9
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

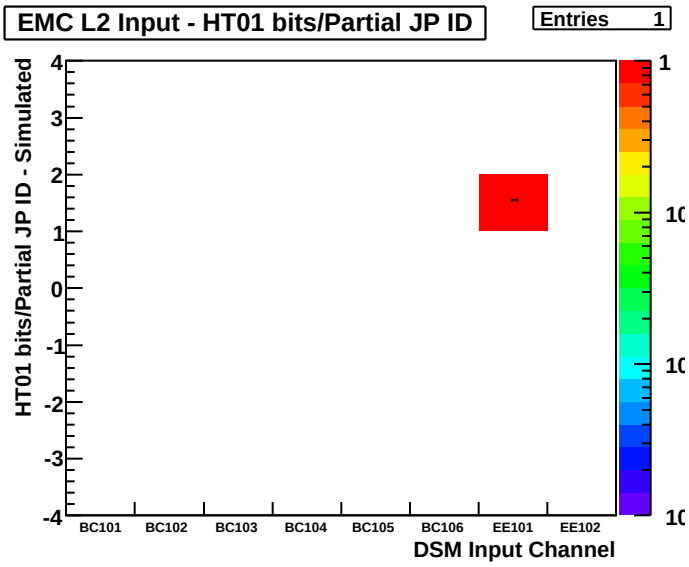

EMC L2 Input - JPZ/JPC bits
Entries 296
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

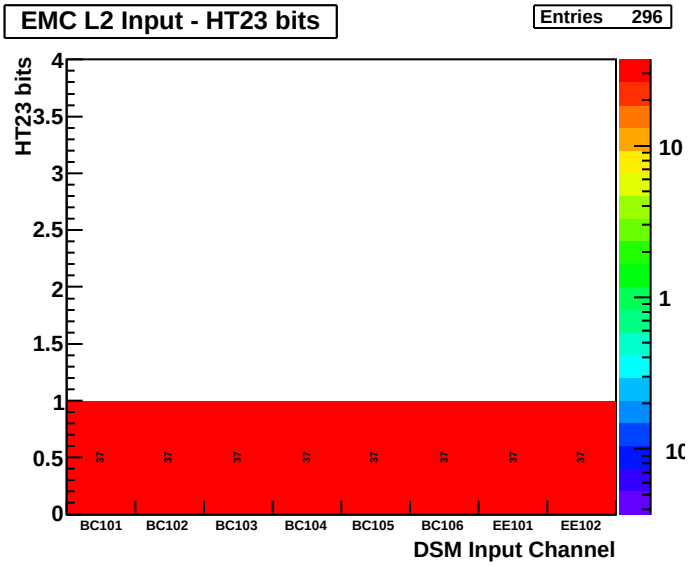

EMC L2 Input - JPZ/JPC bits
Entries 9
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

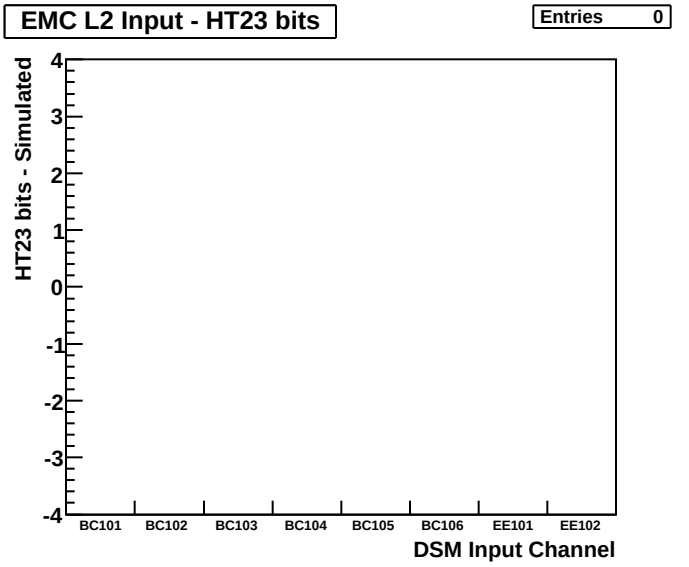

EMC L2 Input - Partial JP Sum
Entries 296
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

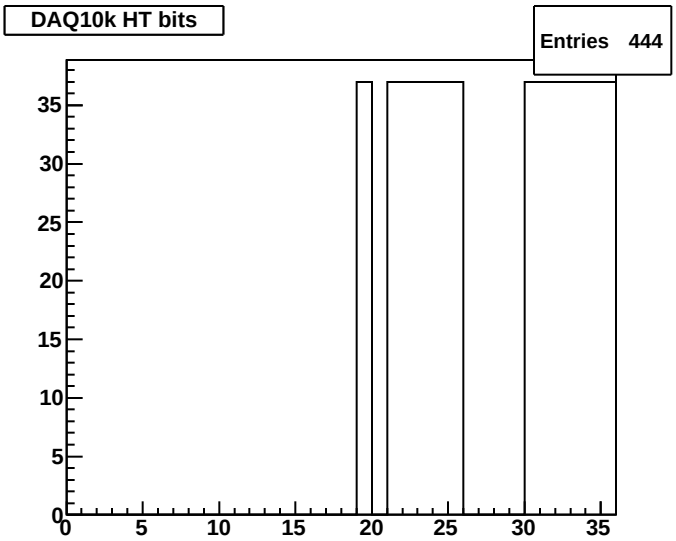

EMC L2 Input - Partial JP Sum
Entries 95
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

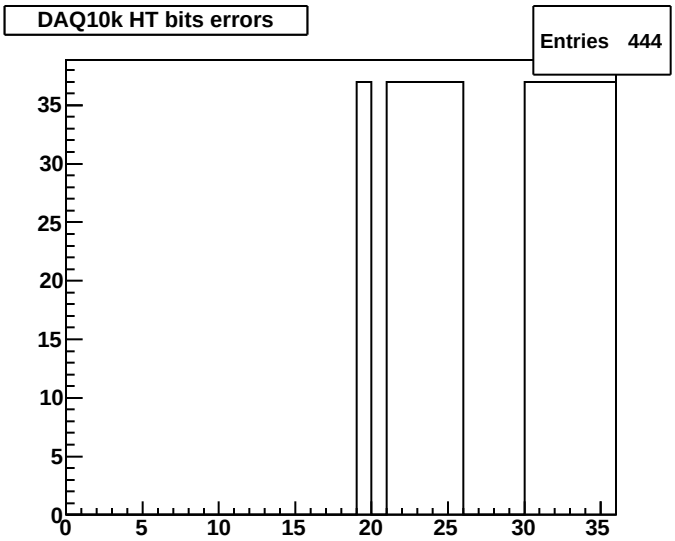

EMC L2 Input - HT01 bits/Partial JP ID
Entries 296
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

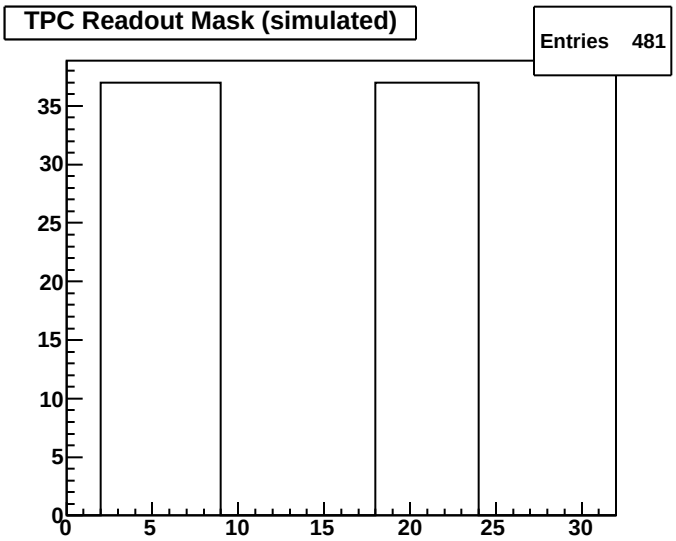

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

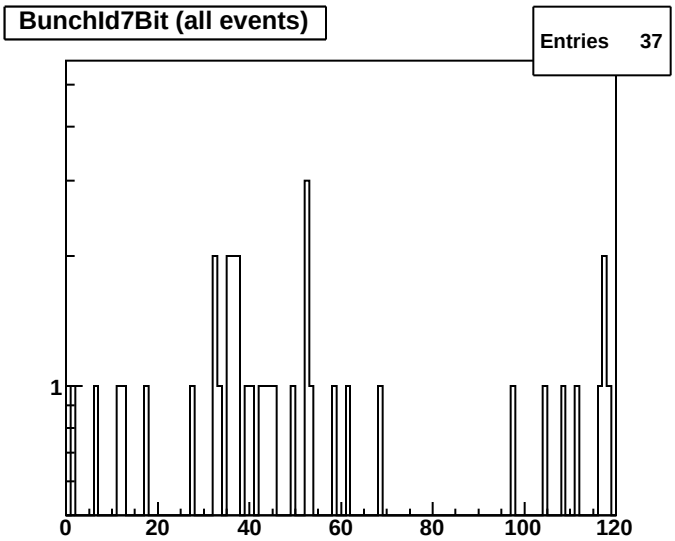

EMC L2 Input - HT23 bits
Entries 296
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

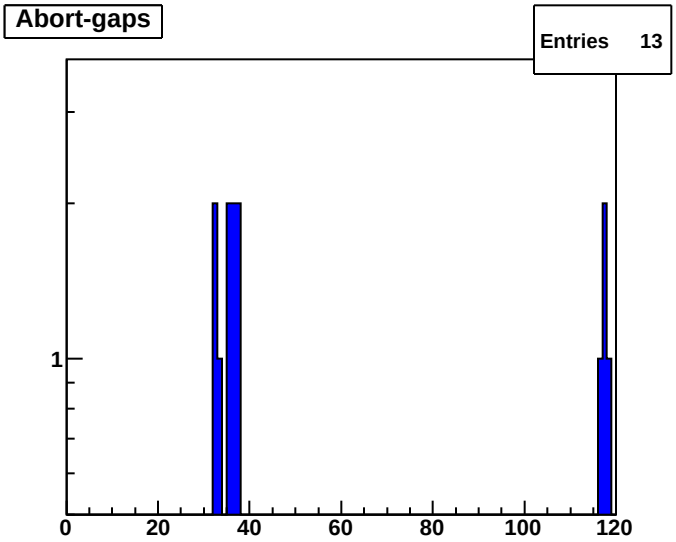

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

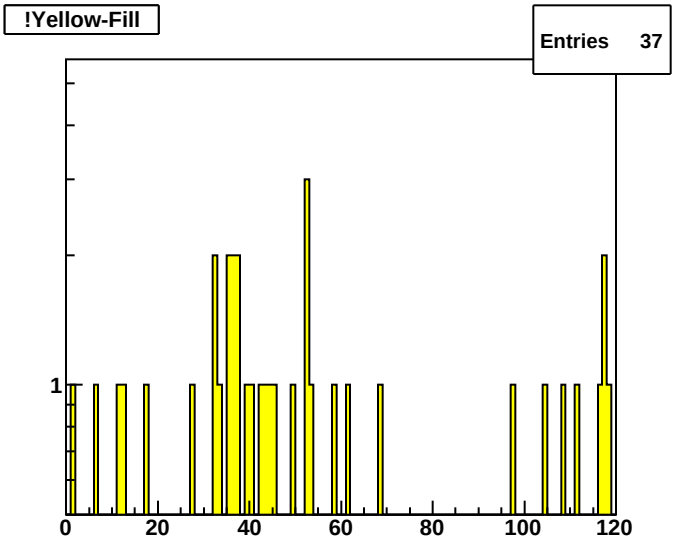

DAQ10k HT bits
Entries 444

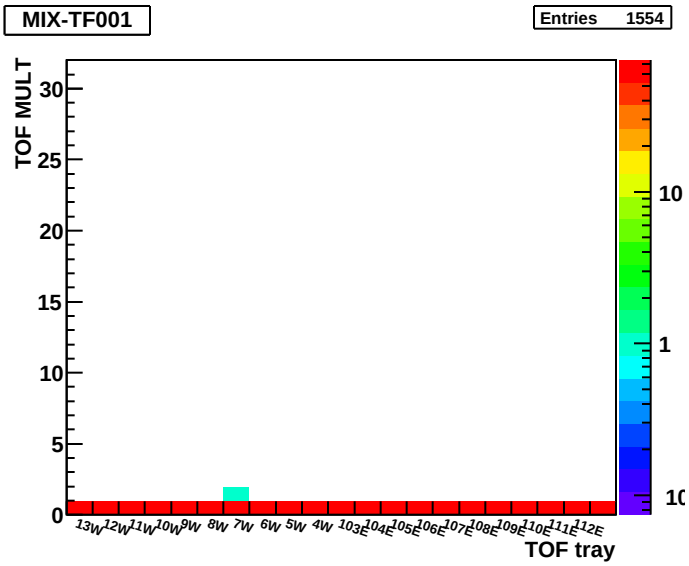

DAQ10k HT bits errors
Entries 444

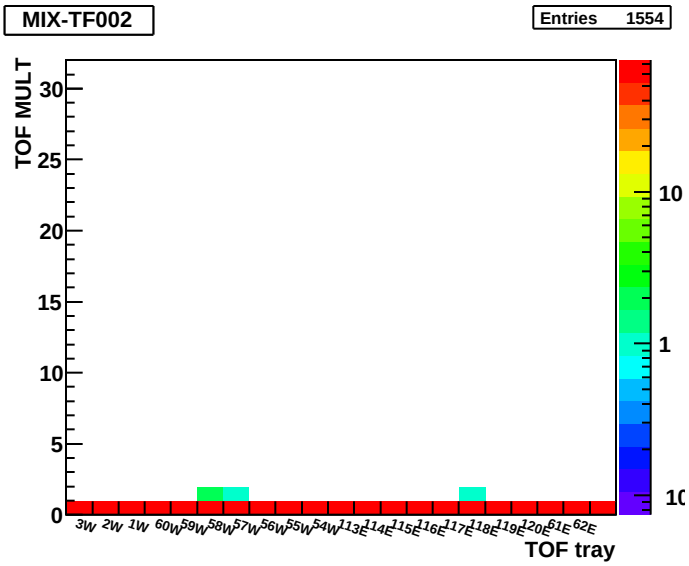

TPC Readout Mask (simulated)
Entries 481

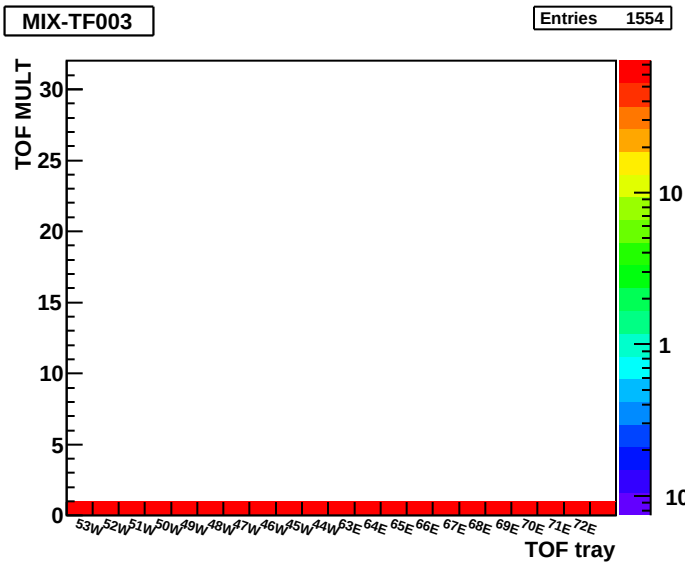

BunchId7Bit (all events)
Entries 37

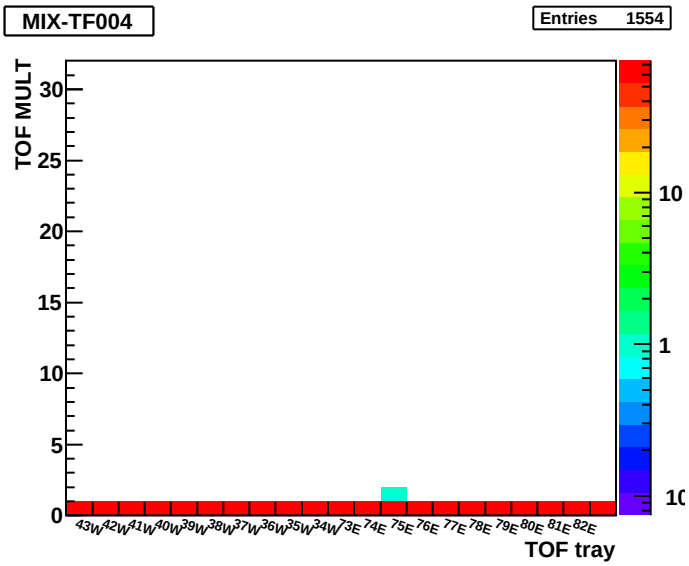

Abort-gaps
Entries 13

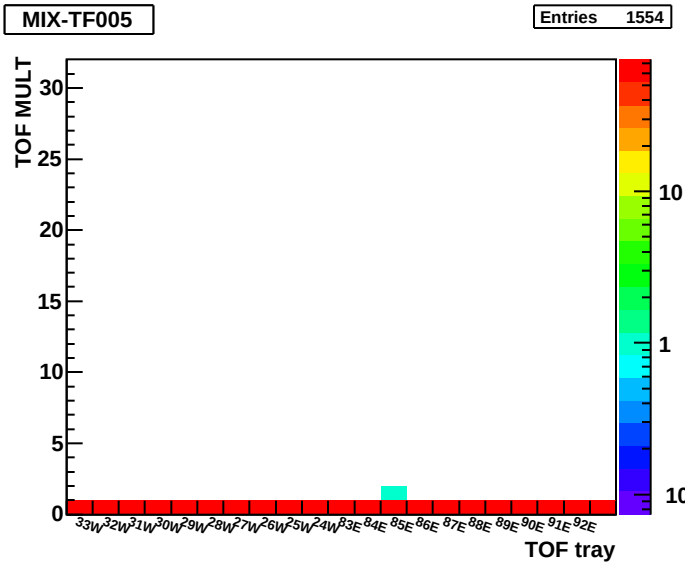

!Yellow-Fill
Entries 37

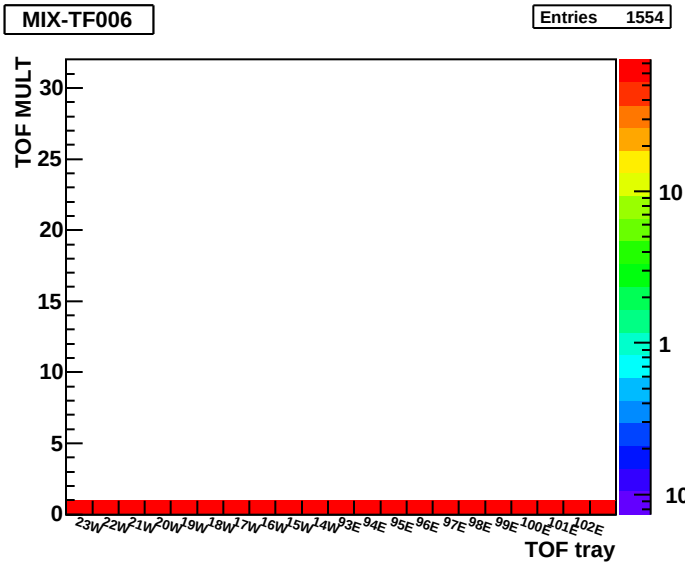

MIX-TF001
Entries 1554
TOF MULT
TOF tray

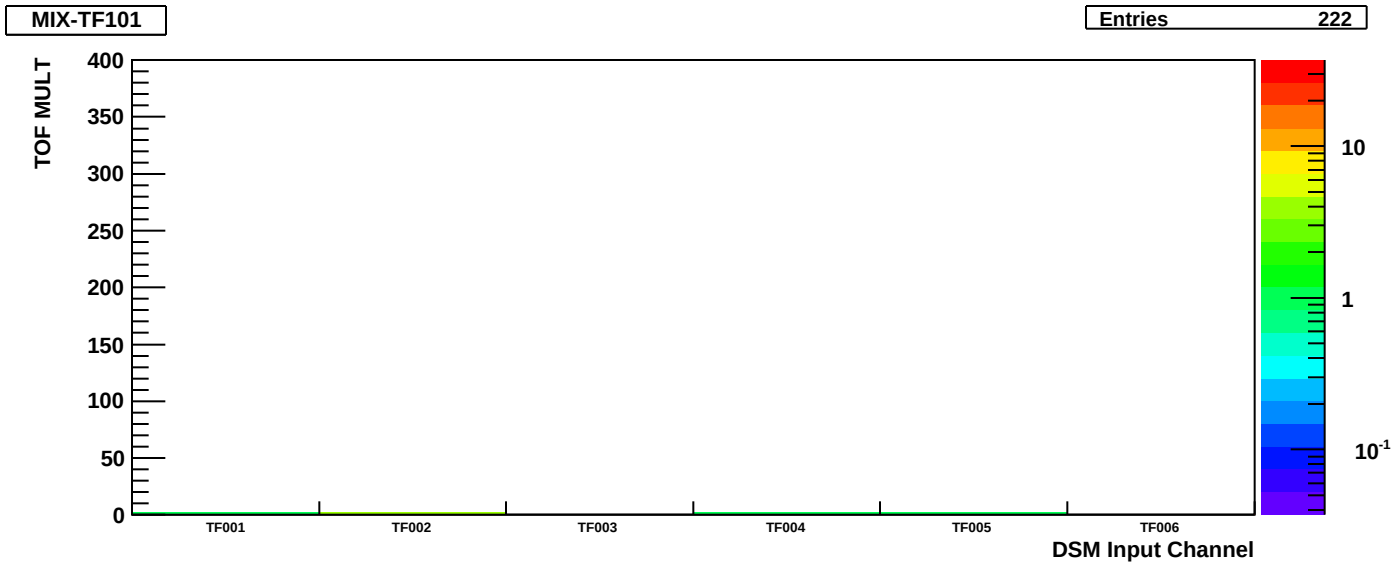

MIX-TF002
Entries 1554
TOF MULT
TOF tray

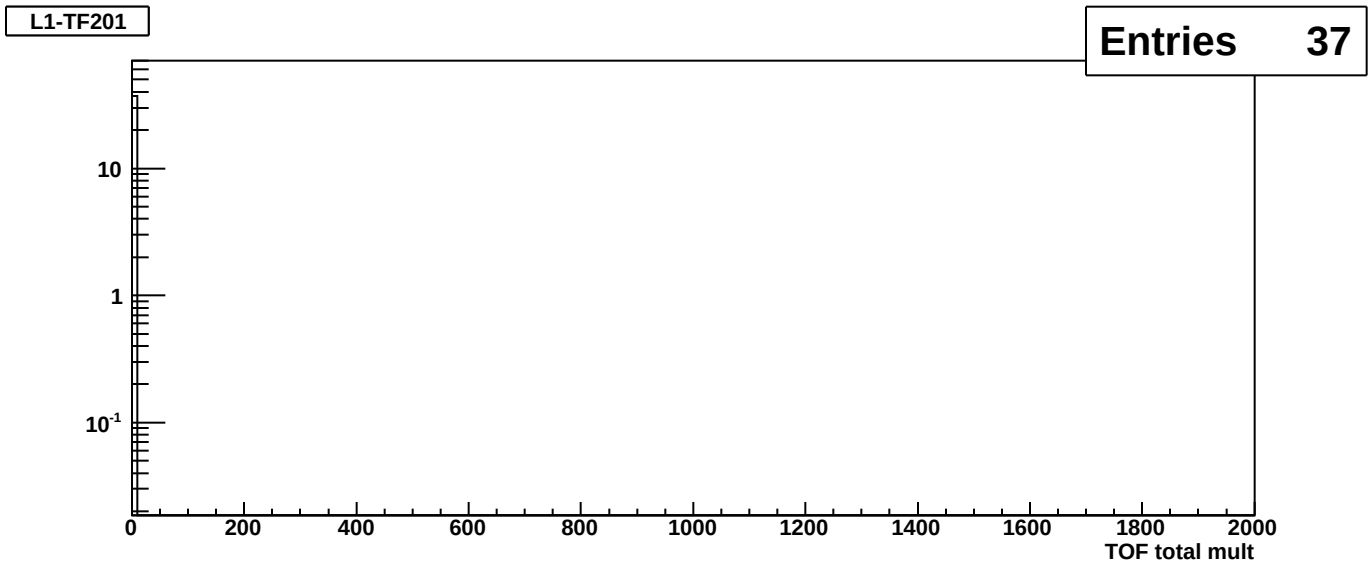

MIX-TF003
Entries 1554
TOF MULT
TOF tray

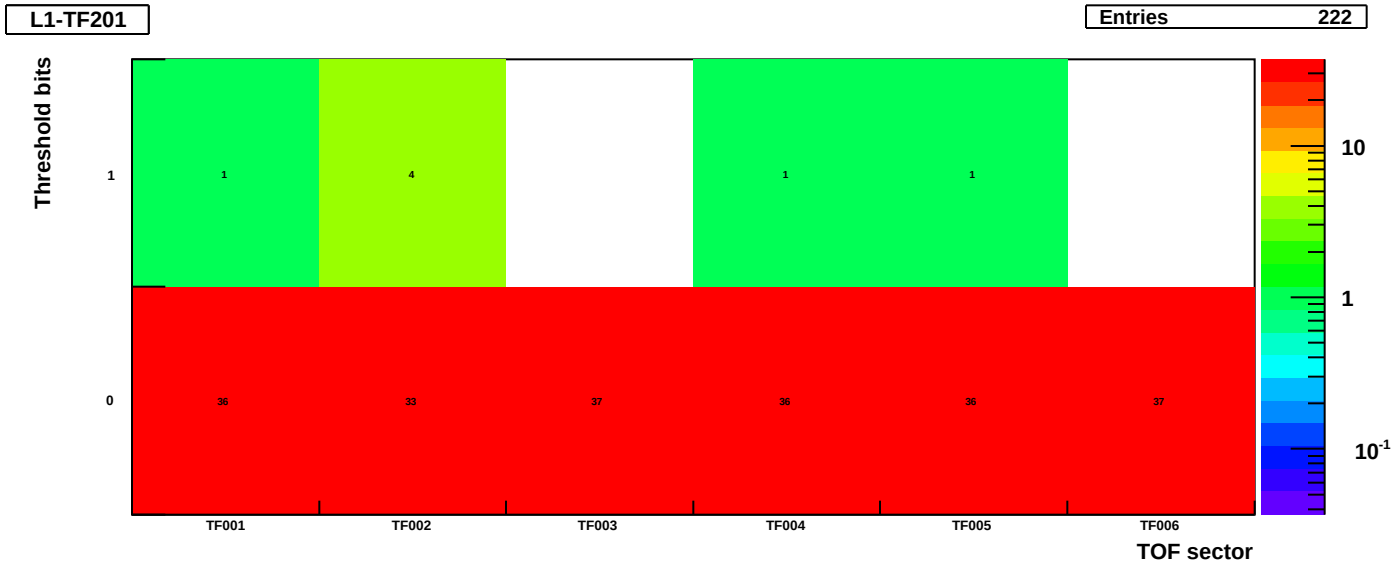

MIX-TF004
Entries 1554
TOF MULT
TOF tray

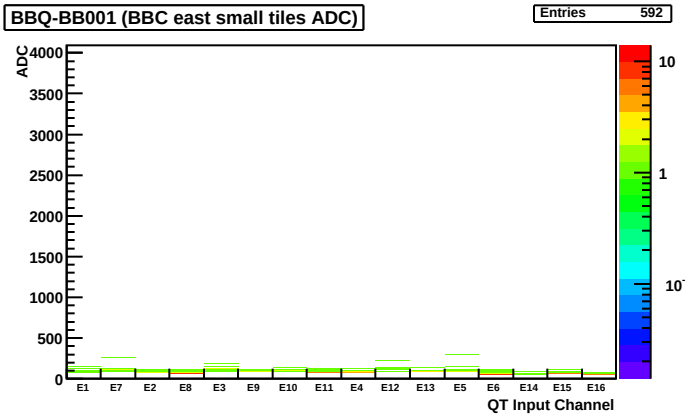

MIX-TF005
Entries 1554
TOF MULT
TOF tray

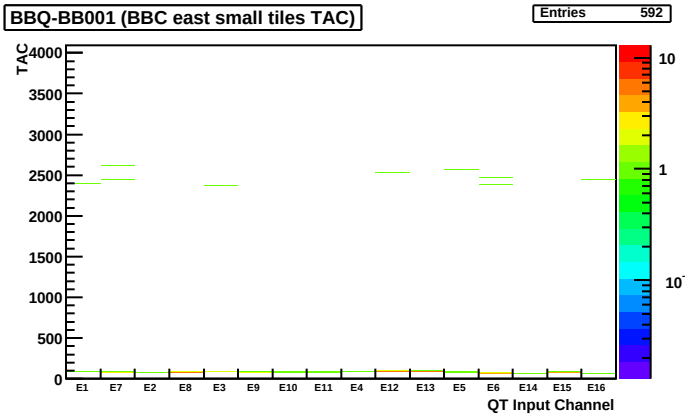

MIX-TF006
Entries 1554
TOF MULT
TOF tray

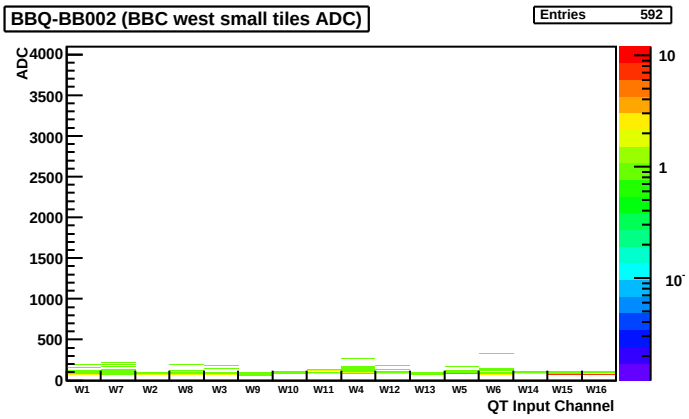

MIX-TF101
Entries 222
TOF MULT
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10
1
10^{-1}

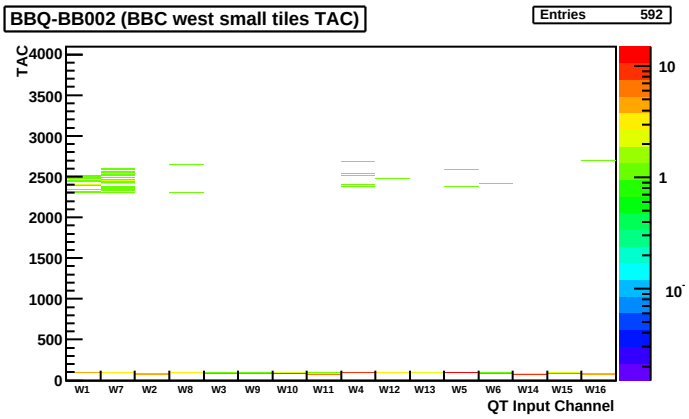

L1-TF201
Entries 37
10
1
10^{-1}
0
200
400
600
800
1000
1200
1400
1600
1800
2000
TOF total mult

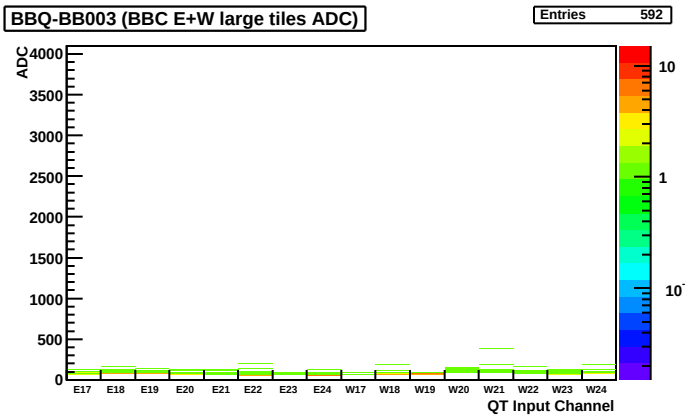

L1-TF201
Entries 222
Threshold bits
1
0
1
4
1
1
36
33
37
36
36
37
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10
1
10^{-1}

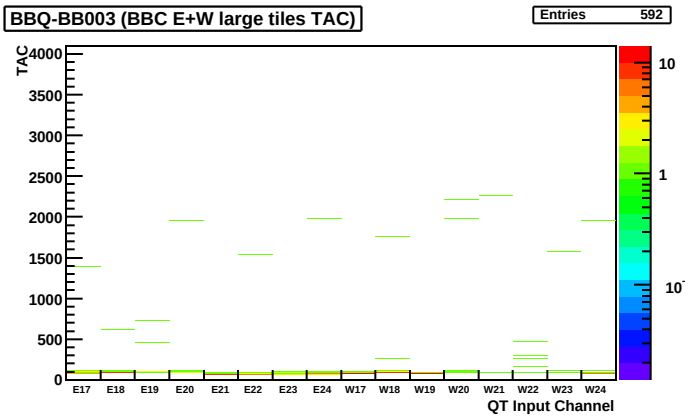

BBQ-BB001 (BBC east small tiles ADC)
Entries 592
ADC
QT Input Channel

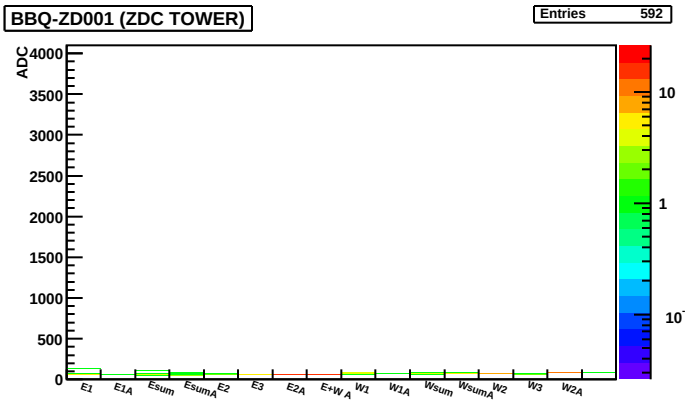

BBQ-BB001 (BBC east small tiles TAC)
Entries 592
TAC
QT Input Channel

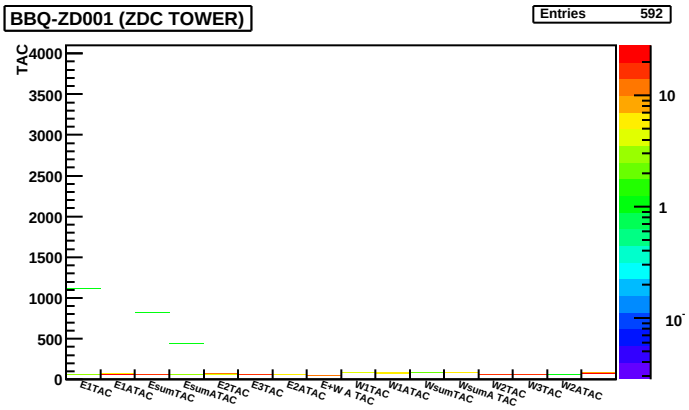

BBQ-BB002 (BBC west small tiles ADC)
Entries 592
ADC
QT Input Channel

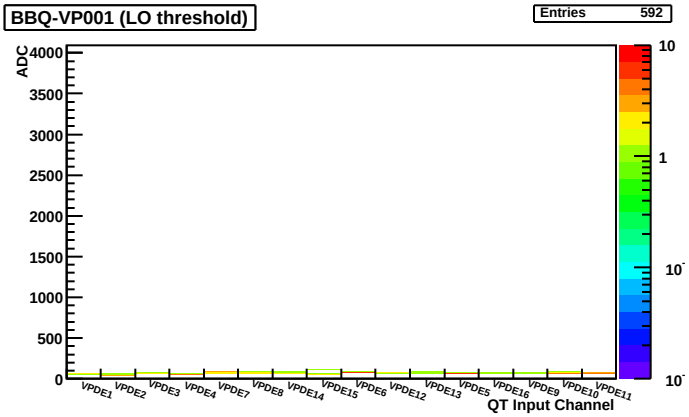

BBQ-BB002 (BBC west small tiles TAC)
Entries 592
TAC
QT Input Channel

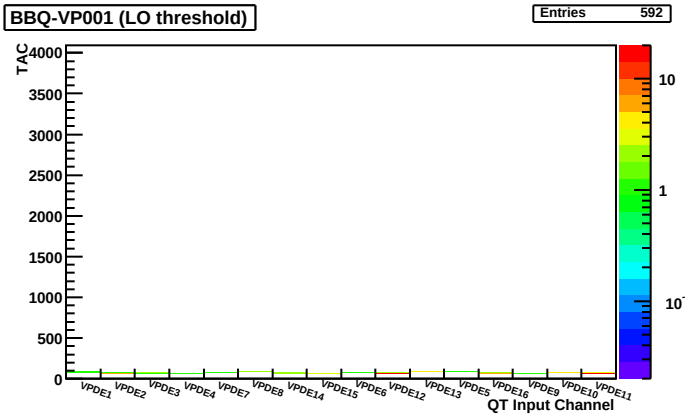

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 592
ADC
QT Input Channel

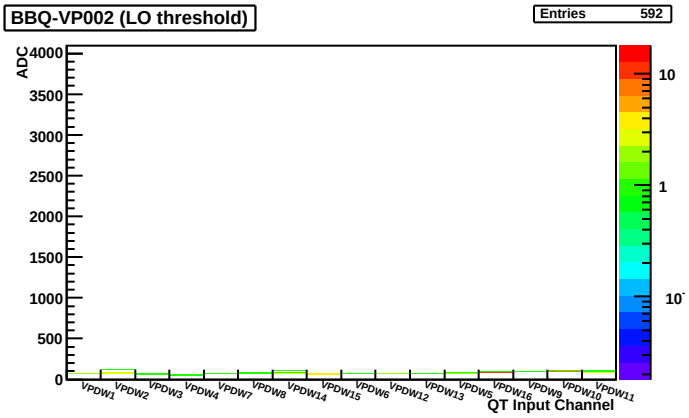

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 592
TAC
QT Input Channel

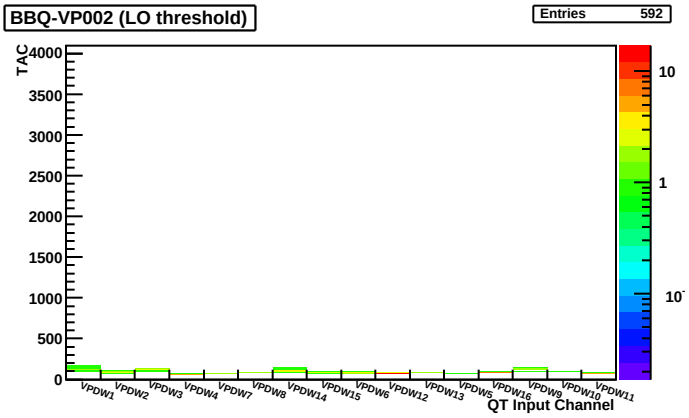

BBQ-ZD001 (ZDC TOWER)
Entries 592
ADC

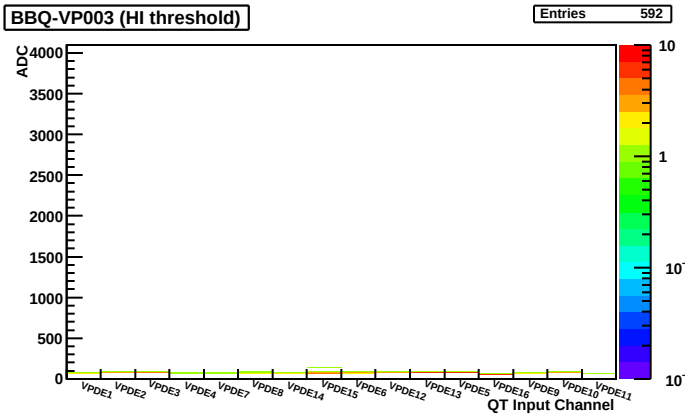

BBQ-ZD001 (ZDC TOWER)
Entries 592
TAC

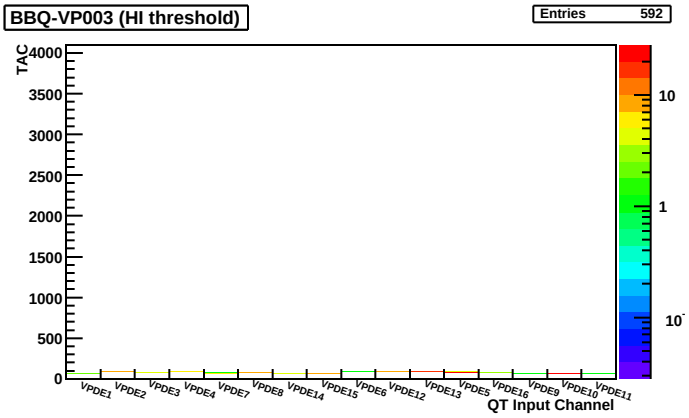

BBQ-VP001 (LO threshold)
Entries 592
ADC
QT Input Channel

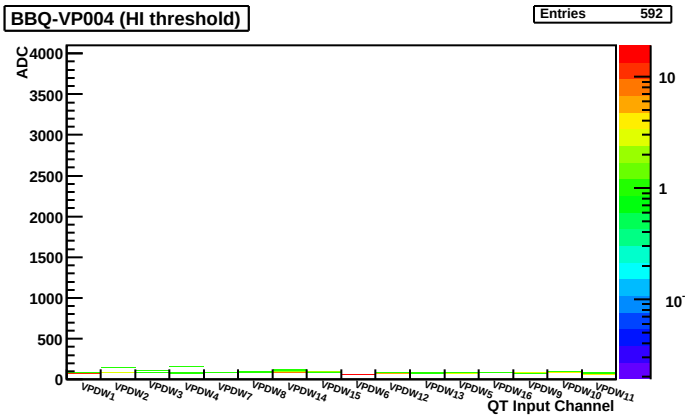

BBQ-VP001 (LO threshold)
Entries 592
TAC
QT Input Channel

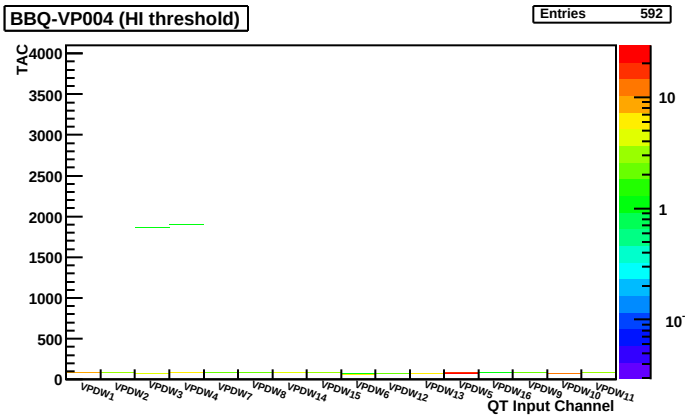

BBQ-VP002 (LO threshold)
Entries 592
ADC
QT Input Channel

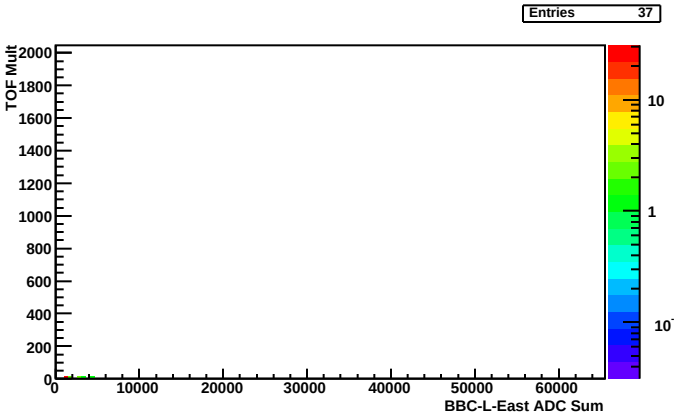

BBQ-VP002 (LO threshold)
Entries 592
TAC
QT Input Channel

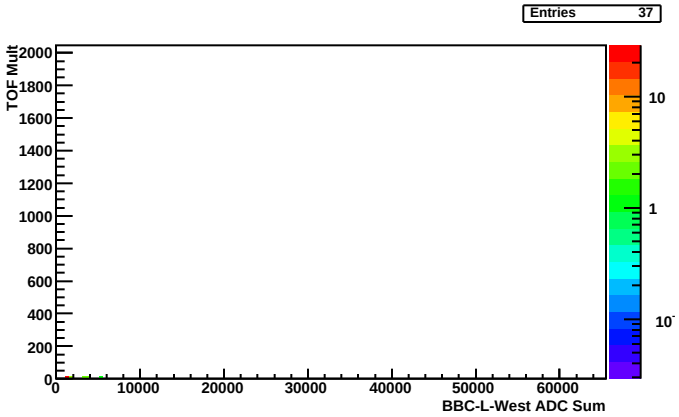

BBQ-VP003 (HI threshold)
Entries 592
ADC
QT Input Channel

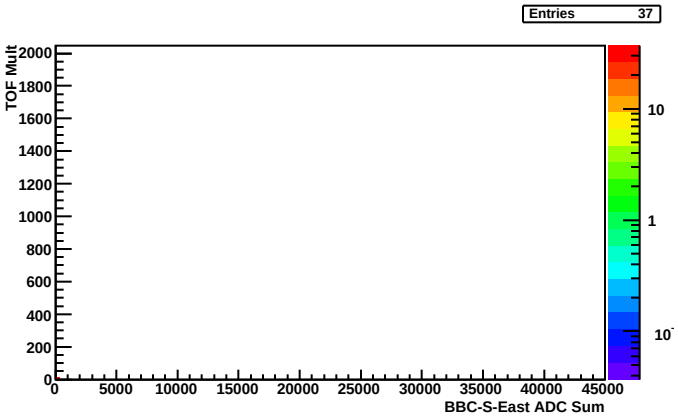

BBQ-VP003 (HI threshold)
Entries 592
TAC
QT Input Channel

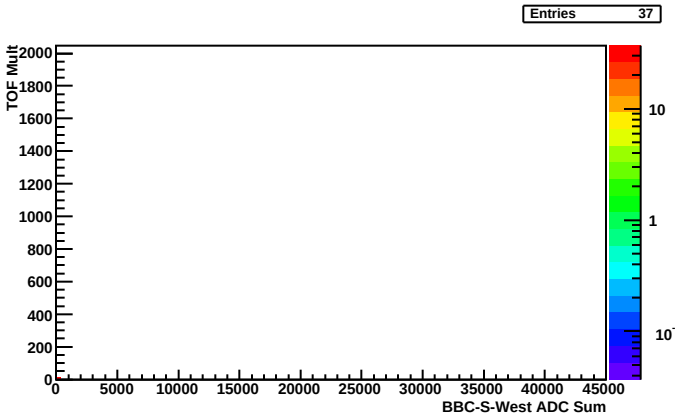

BBQ-VP004 (HI threshold)
Entries 592
ADC
QT Input Channel

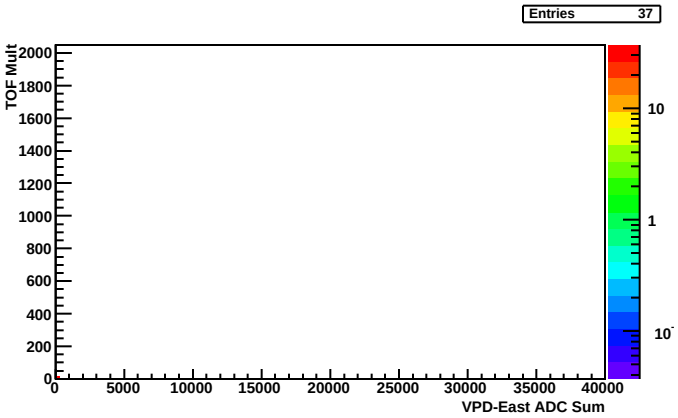

BBQ-VP004 (HI threshold)
Entries 592
TAC
QT Input Channel

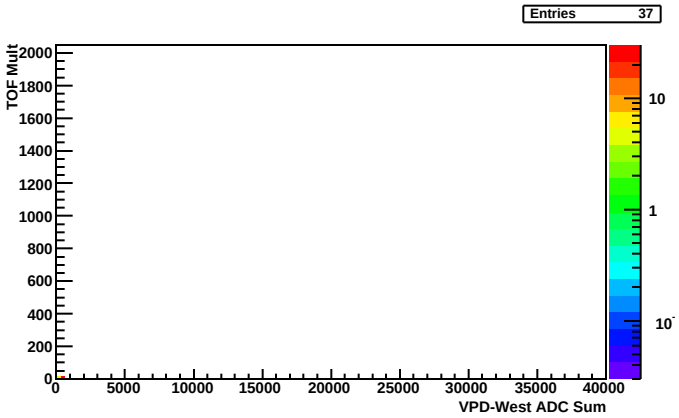

Entries 37
TOF Mult
BBC-L-East ADC Sum

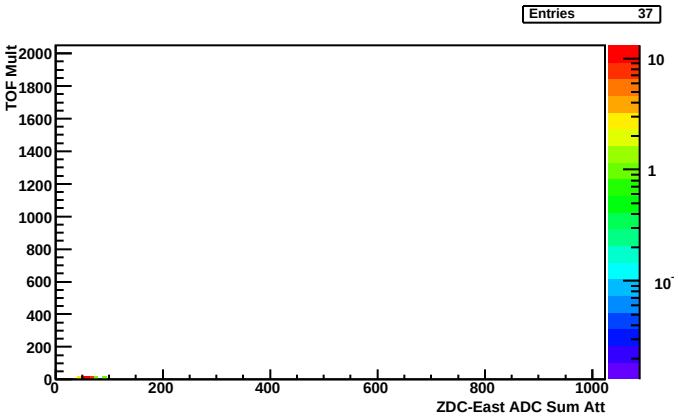

Entries 37
TOF Mult
BBC-L-West ADC Sum

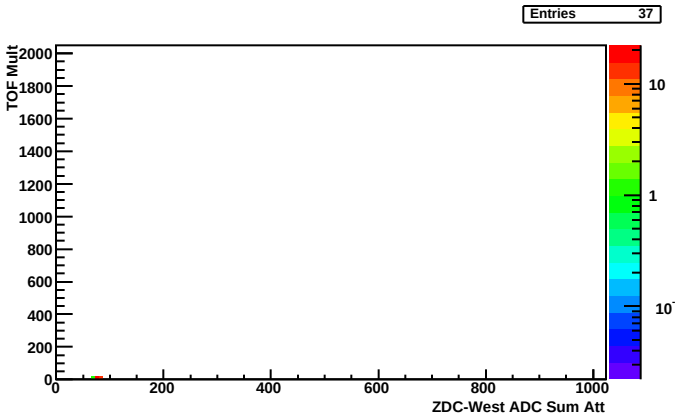

Entries 37
TOF Mult
BBC-S-East ADC Sum

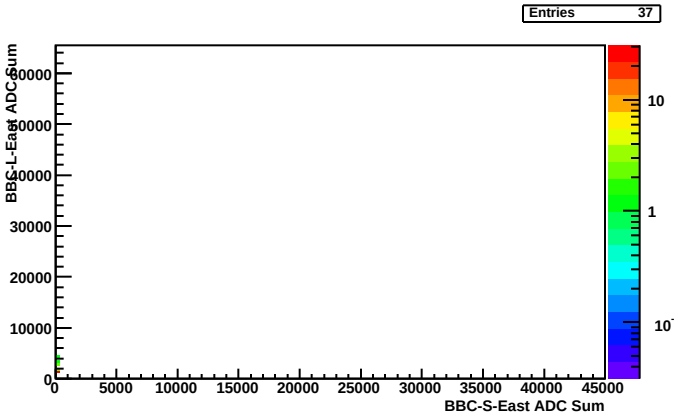

Entries 37
TOF Mult
BBC-S-West ADC Sum

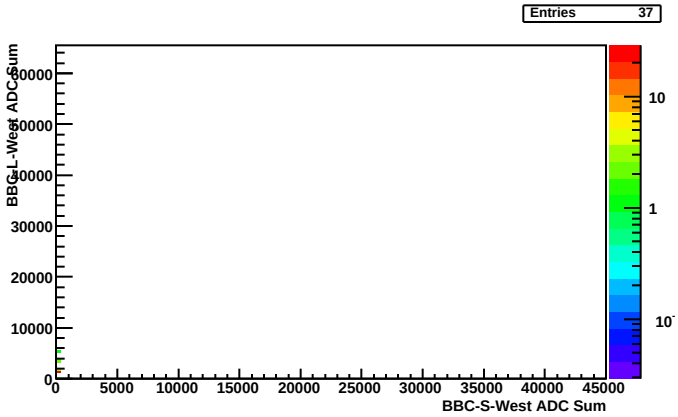

Entries 37
TOF Mult
VPD-East ADC Sum

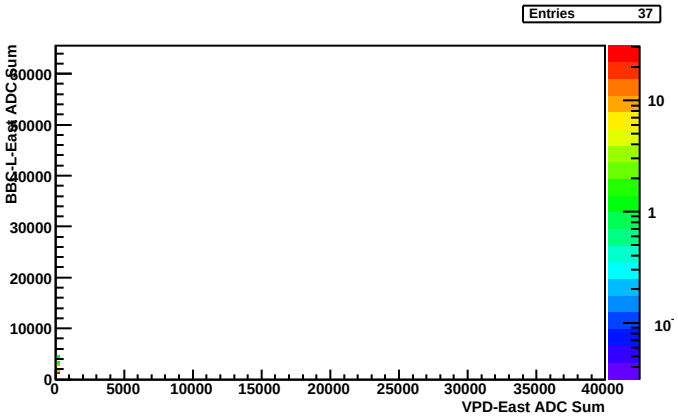

Entries 37
TOF Mult
VPD-West ADC Sum

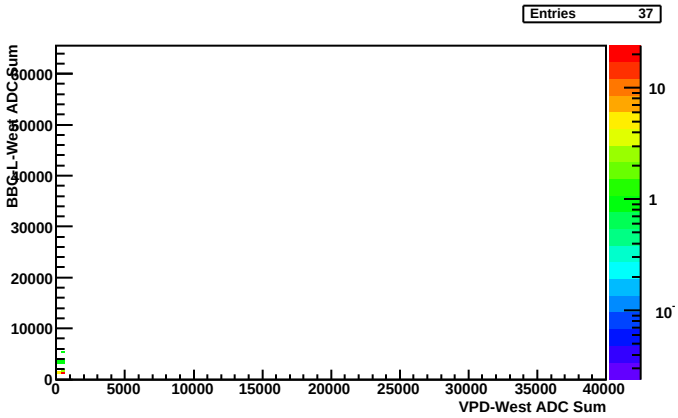

Entries 37
TOF Mult
ZDC-East ADC Sum Att

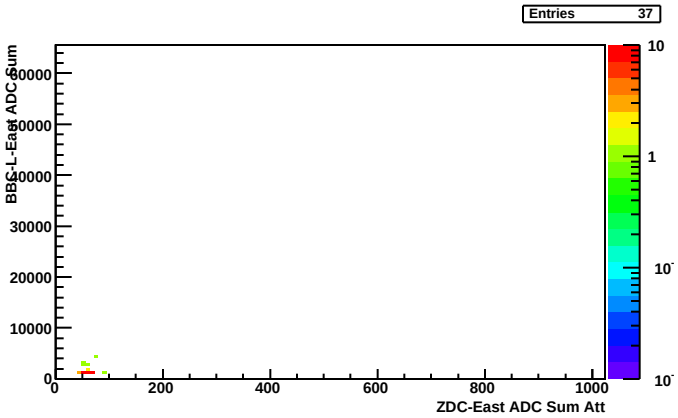

Entries 37
TOF Mult
ZDC-West ADC Sum Att

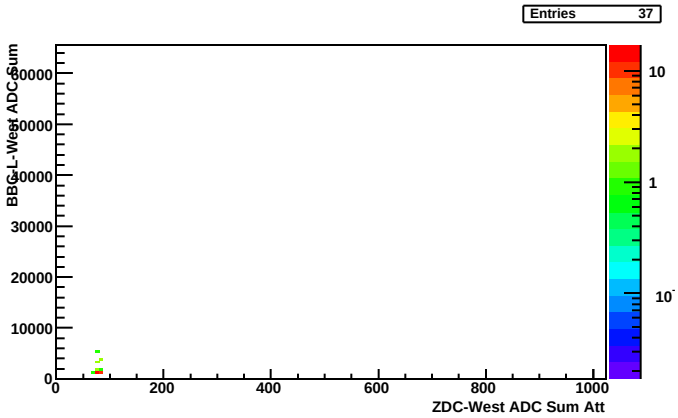

Entries 37
BBC-L-East ADC Sum
BBC-S-East ADC Sum

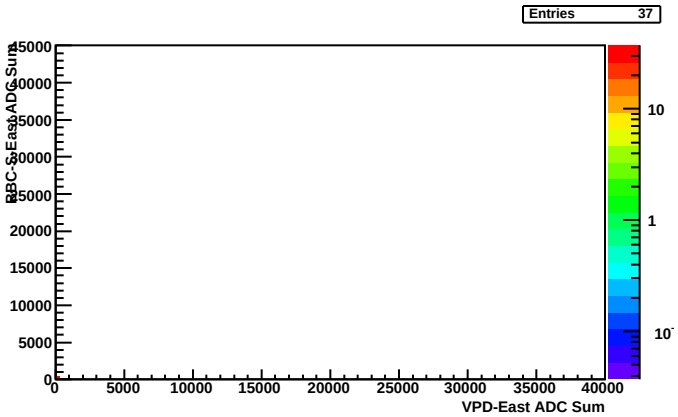

Entries 37
BBC-L-West ADC Sum
BBC-S-West ADC Sum

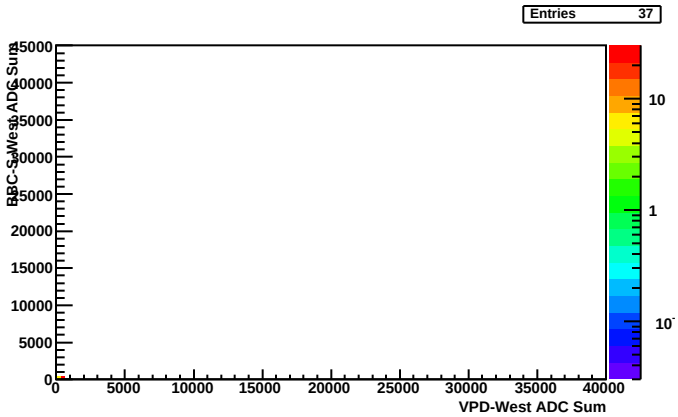

Entries 37
BBC-L-East ADC Sum
VPD-East ADC Sum

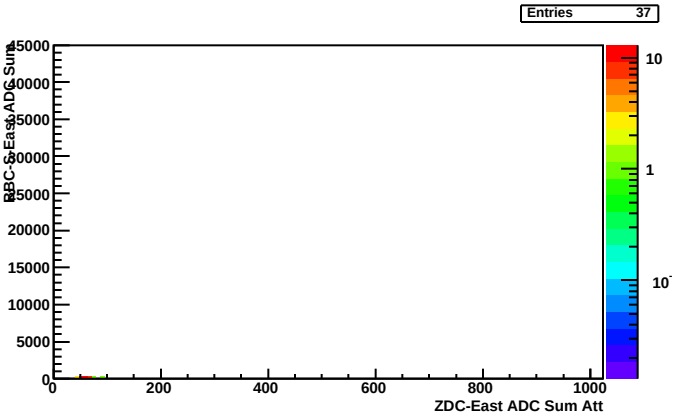

Entries 37
BBC-L-West ADC Sum
VPD-West ADC Sum

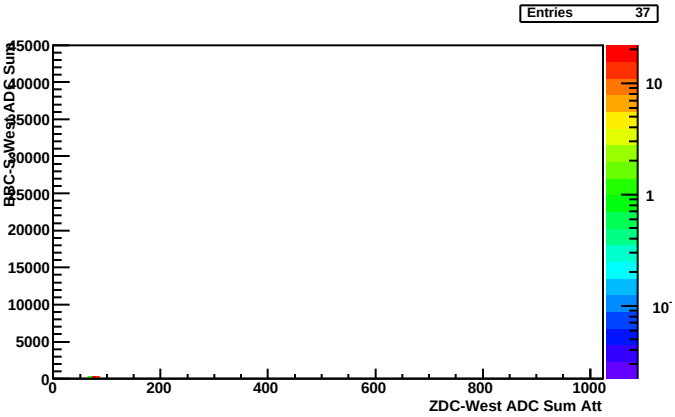

Entries 37
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

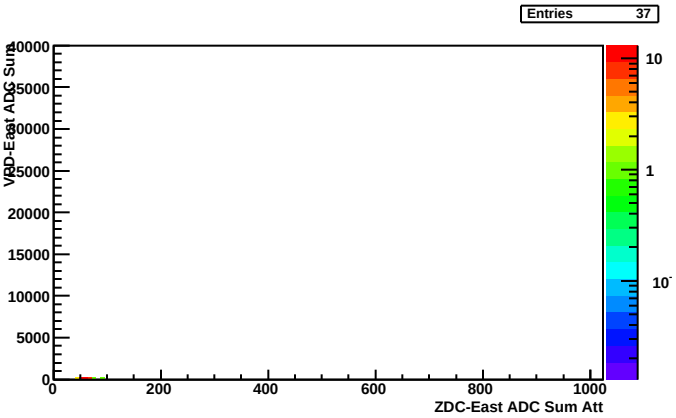

Entries 37
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

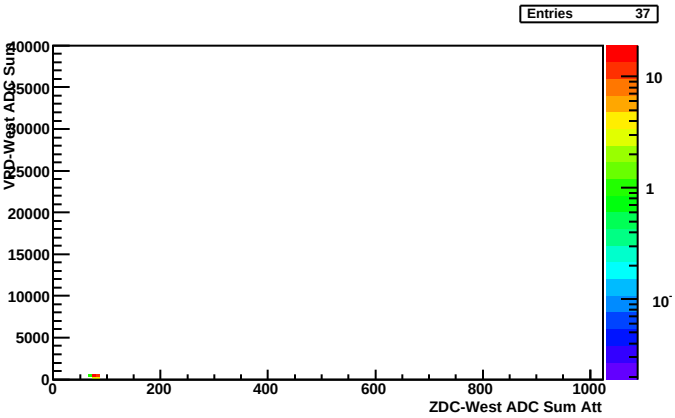

Entries 37
BBC-S-East ADC Sum
VPD-East ADC Sum

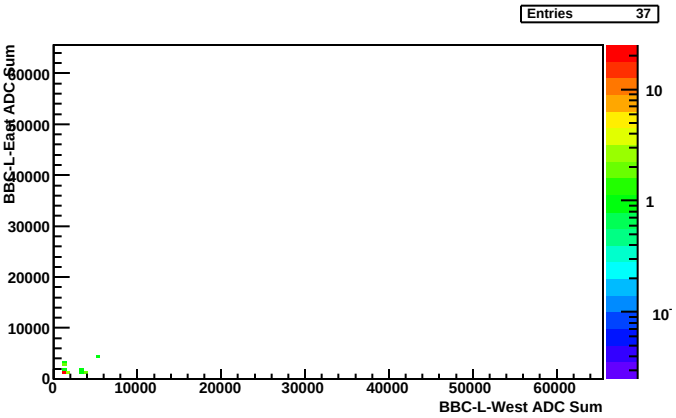

Entries 37
BBC-S-West ADC Sum
VPD-West ADC Sum

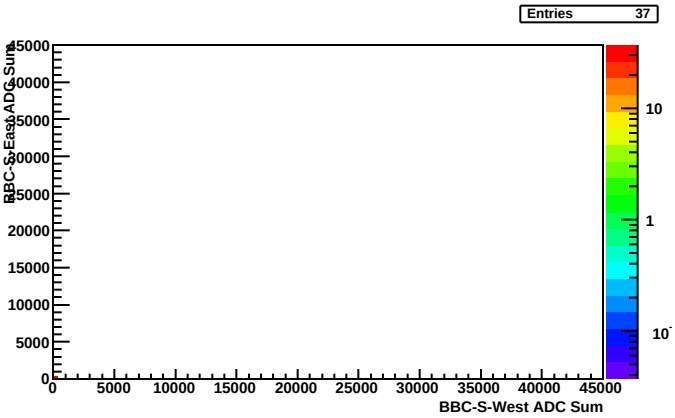

Entries 37
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

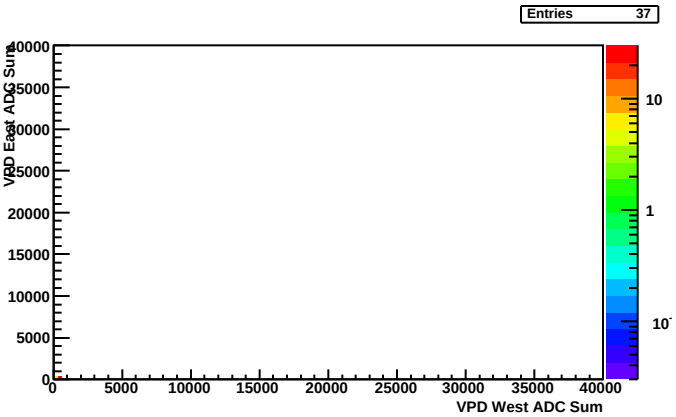

Entries 37
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

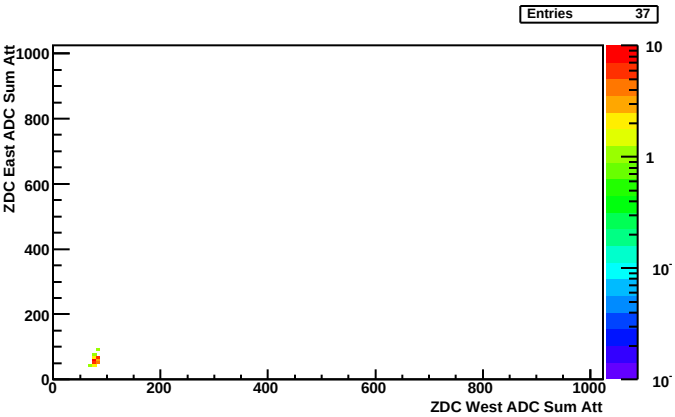

Entries 37
VPD-East ADC Sum
ZDC-East ADC Sum Att

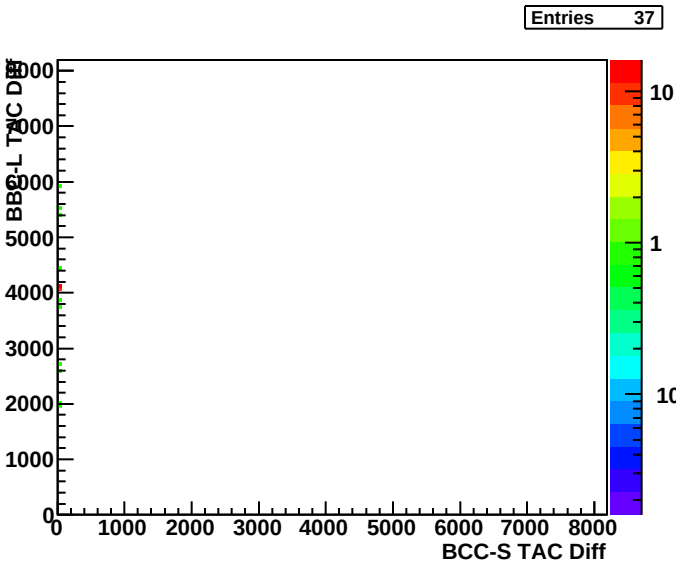

Entries 37
VPD-West ADC Sum
ZDC-West ADC Sum Att

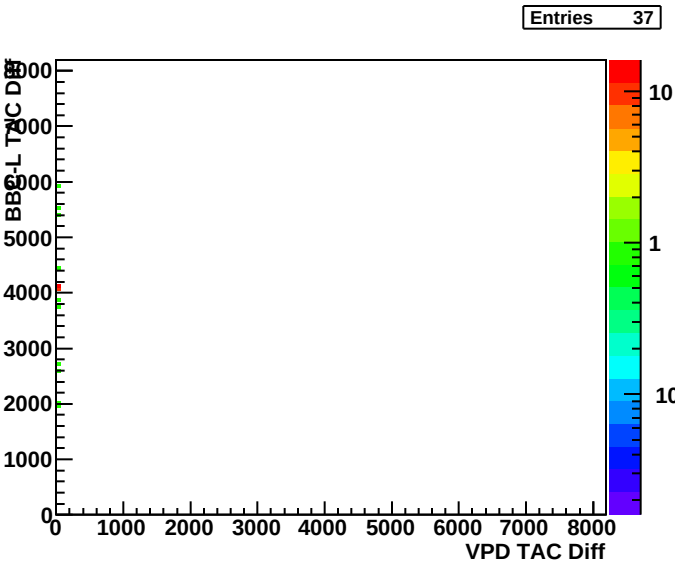

Entries 37
BBC-L-East ADC Sum
BBC-L-West ADC Sum

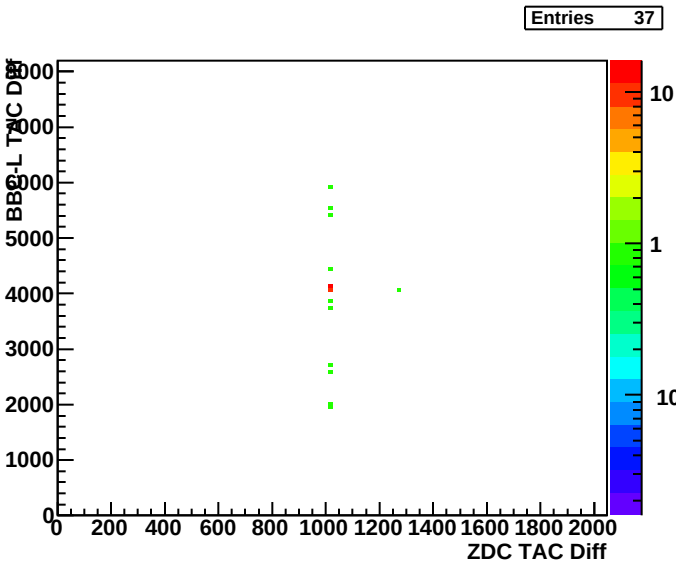

Entries 37
BBC-S-East ADC Sum
BBC-S-West ADC Sum

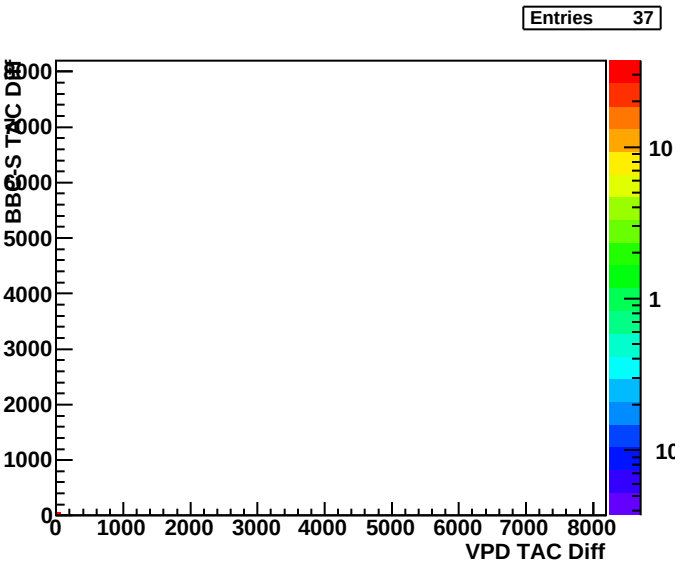

Entries 37
VPD East ADC Sum
VPD West ADC Sum

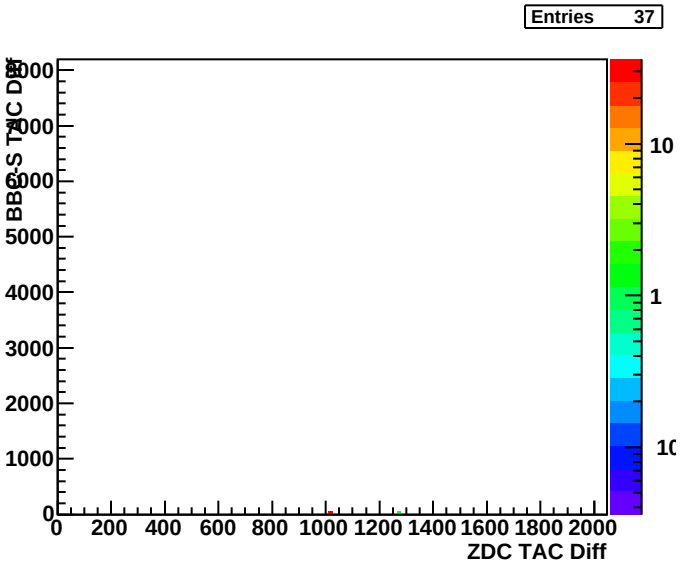

Entries 37
ZDC East ADC Sum Att
ZDC West ADC Sum Att

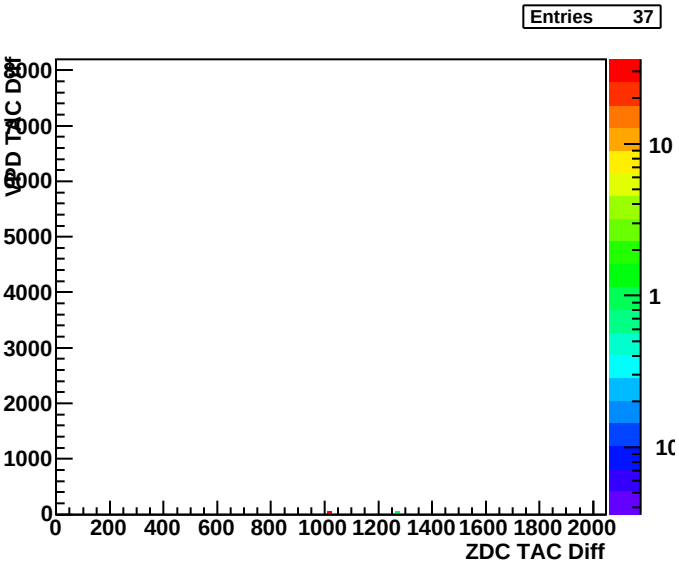

Entries 37
BBC-L TAC Diff
BCC-S TAC Diff

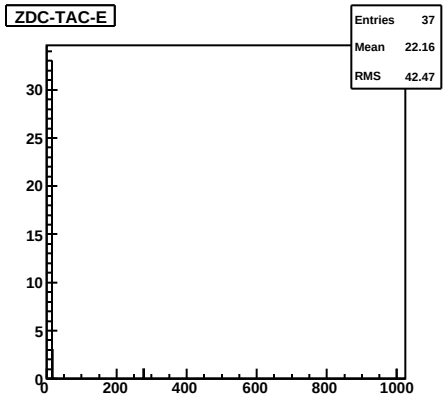

Entries 37
BBC-L TAC Diff
VPD TAC Diff

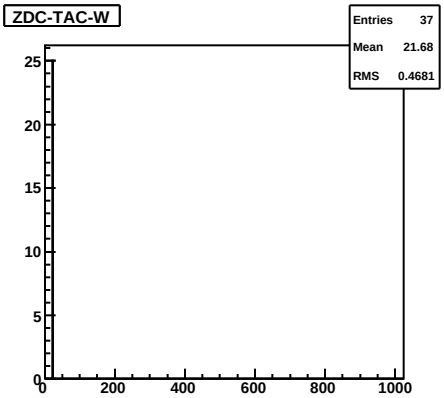

Entries 37
BBC-L TAC Diff
ZDC TAC Diff

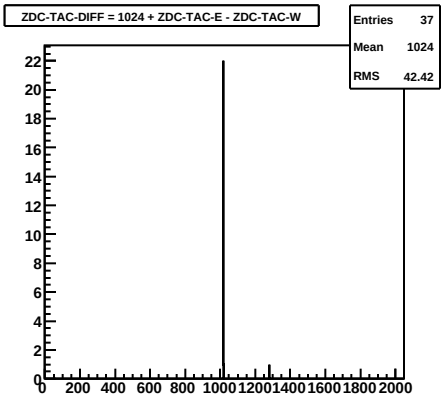

Entries 37
BBC-S TAC Diff
VPD TAC Diff

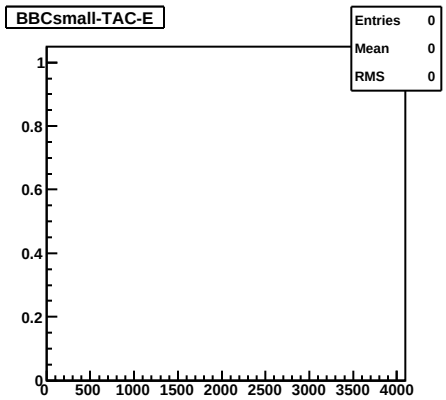

Entries 37
BBC-S TAC Diff
ZDC TAC Diff

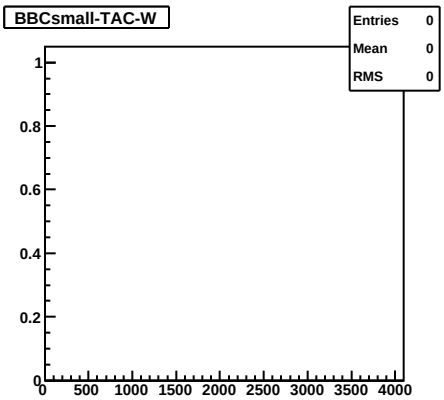

Entries 37
VPD TAC Diff
ZDC TAC Diff

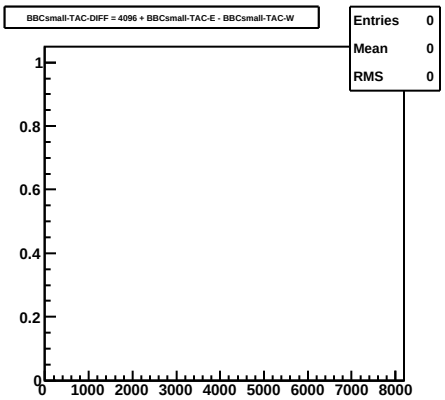

ZDC-TAC-E
Entries 37
Mean 22.16
RMS 42.47

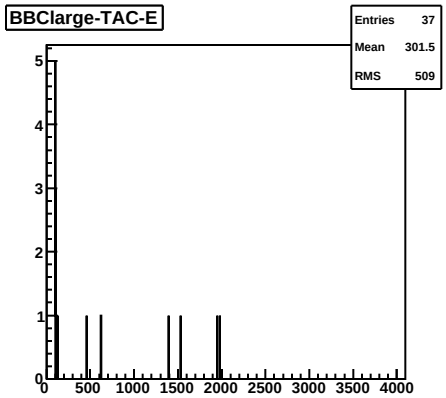

ZDC-TAC-W
Entries 37
Mean 21.68
RMS 0.4681

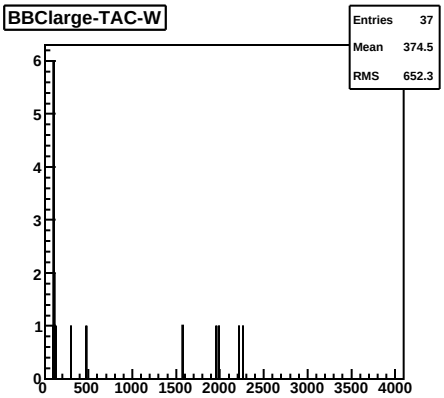

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 37
Mean 1024
RMS 42.42

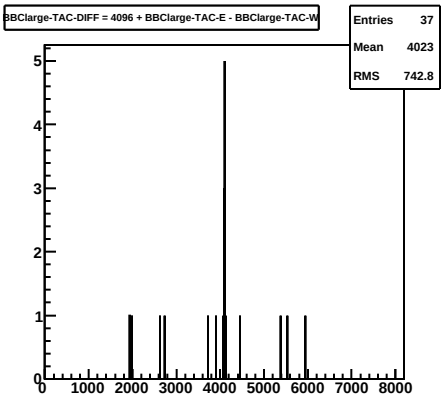

BBCsmall-TAC-E
Entries 0
Mean 0
RMS 0

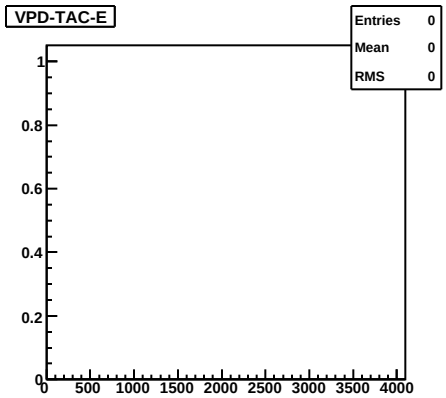

BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

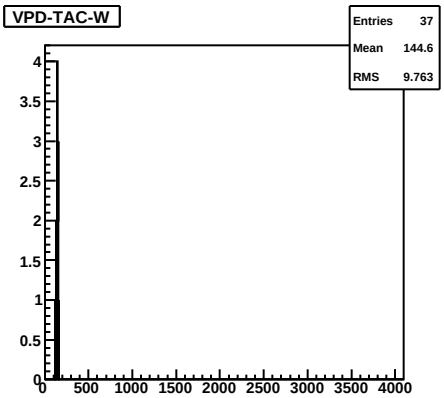

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

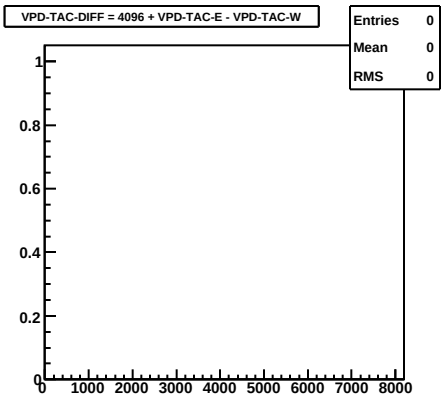

BBClarge-TAC-E
Entries 37
Mean 301.5
RMS 509

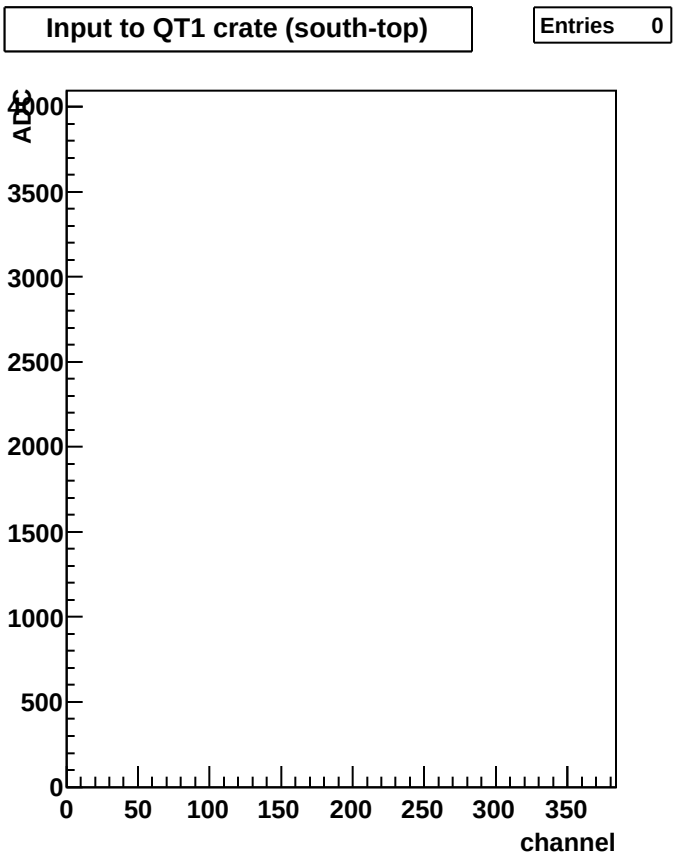

BBClarge-TAC-W
Entries 37
Mean 374.5
RMS 652.3

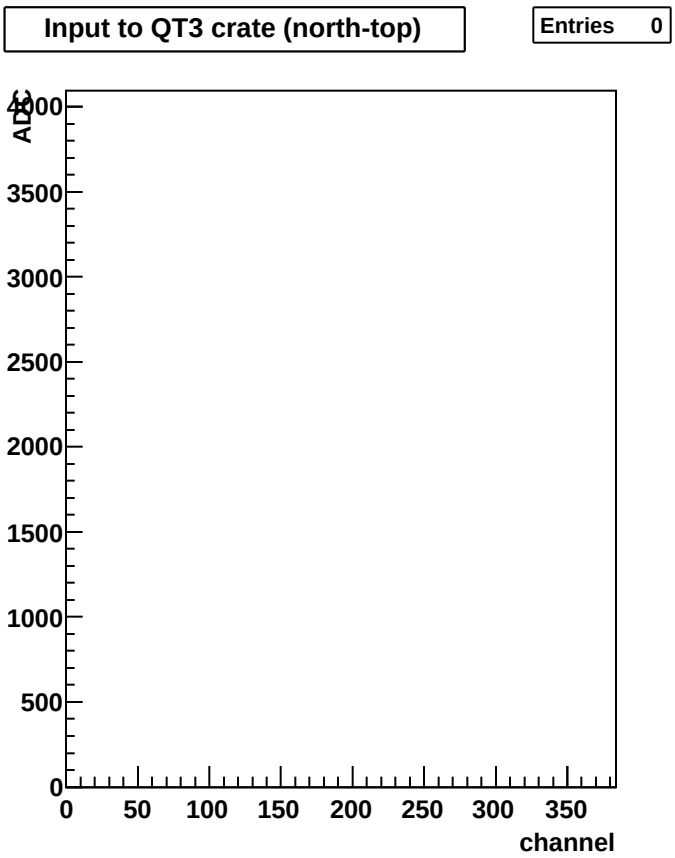

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 37
Mean 4023
RMS 742.8

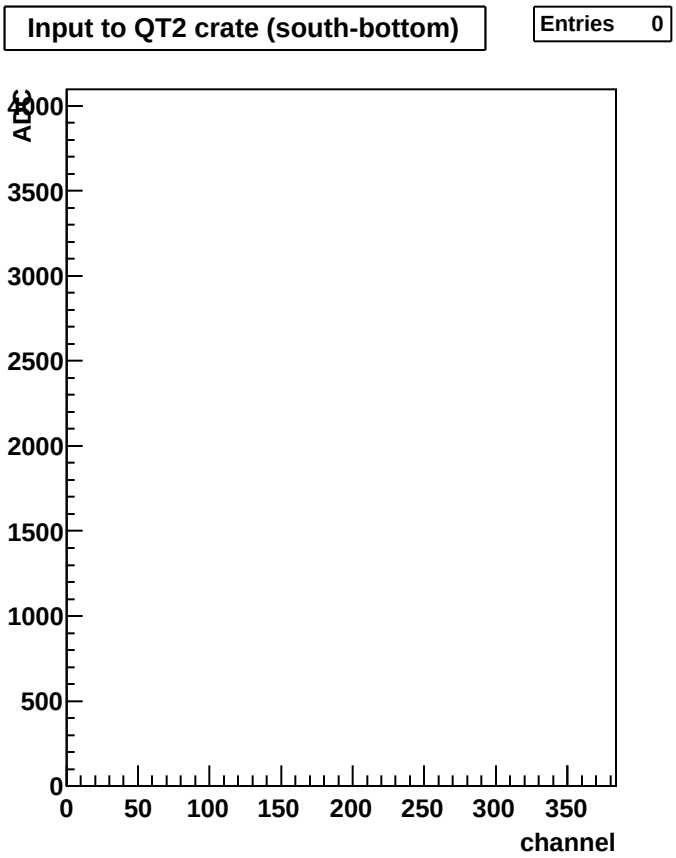

VPD-TAC-E
Entries 0
Mean 0
RMS 0

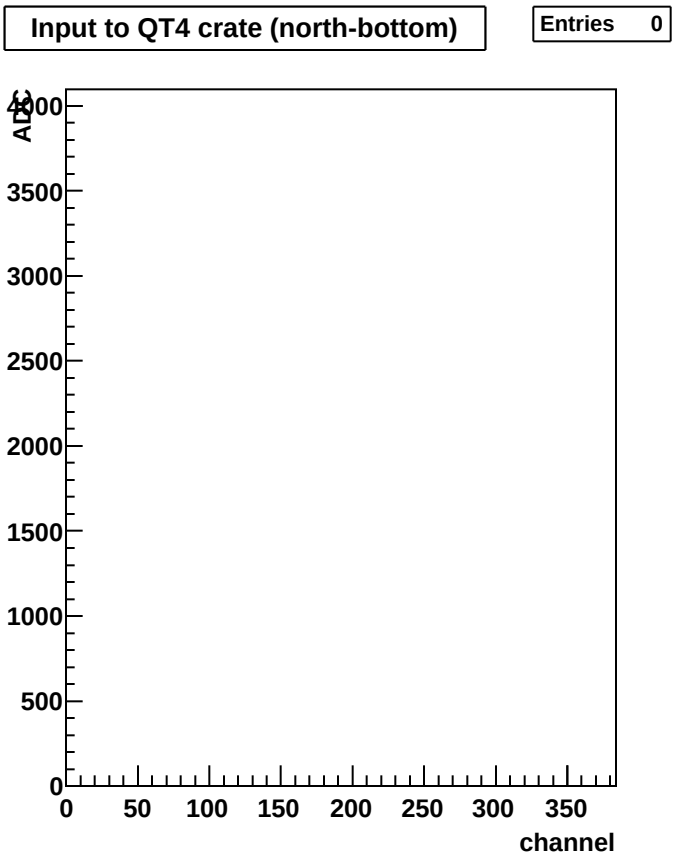

VPD-TAC-W
Entries 37
Mean 144.6
RMS 9.763

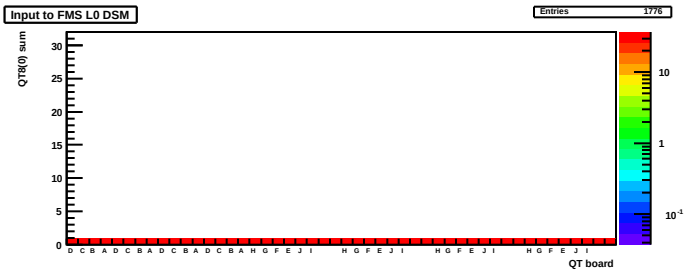

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

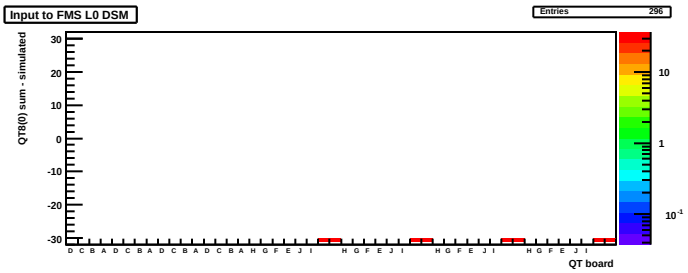

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

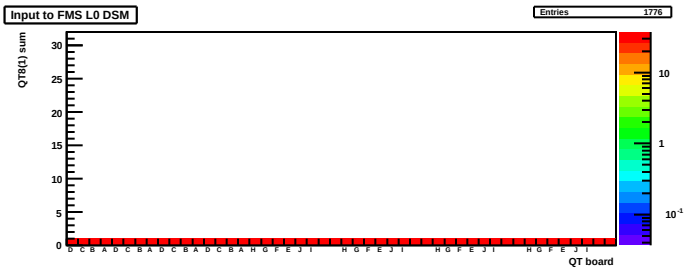

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

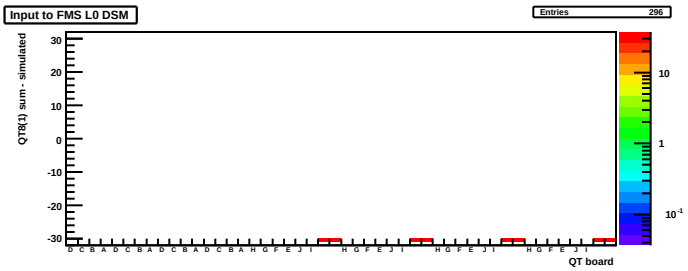

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

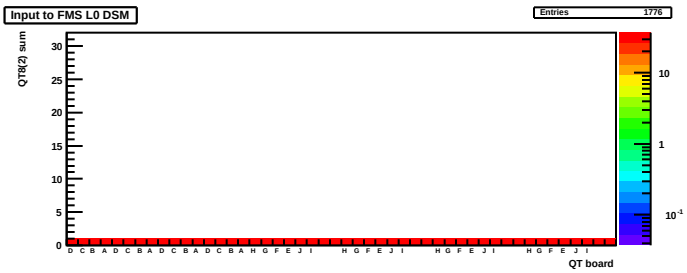

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

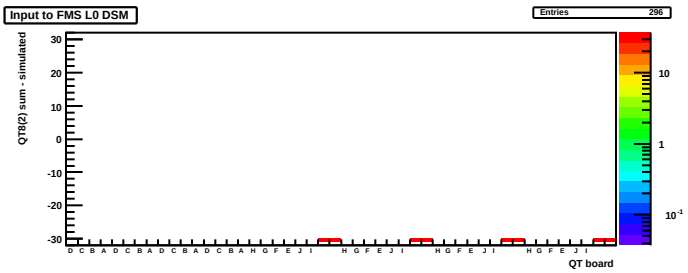

Input to FMS L0 DSM
Entries 1776
QT8(0) sum
QT board

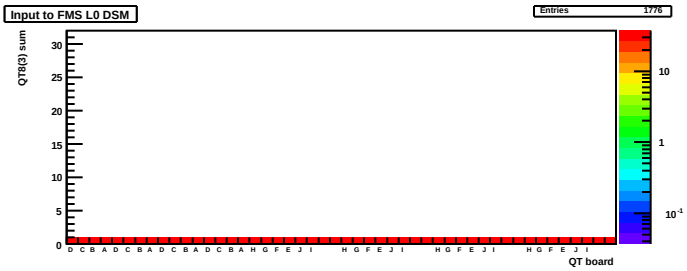

Input to FMS L0 DSM
Entries 296
QT8(0) sum - simulated
QT board

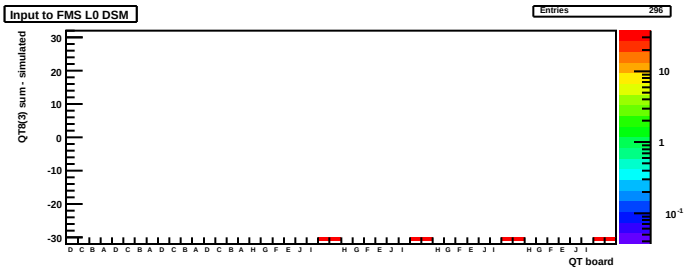

Input to FMS L0 DSM
Entries 1776
QT8(1) sum
QT board

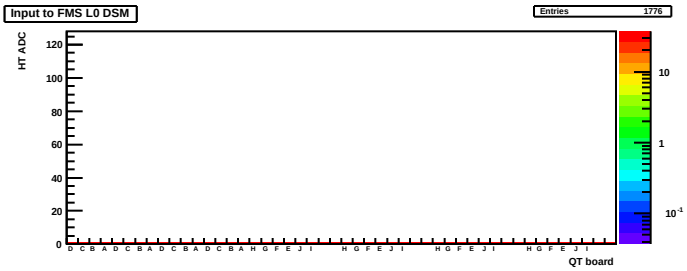

Input to FMS L0 DSM
Entries 296
QT8(1) sum - simulated
QT board

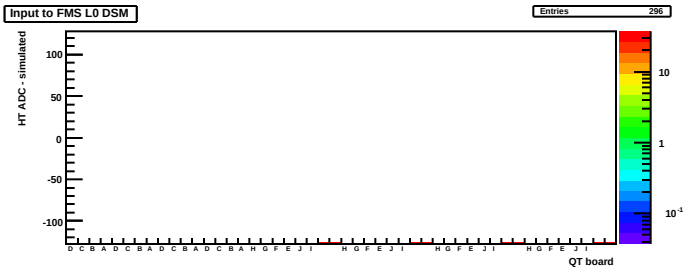

Input to FMS L0 DSM
Entries 1776
QT8(2) sum
QT board

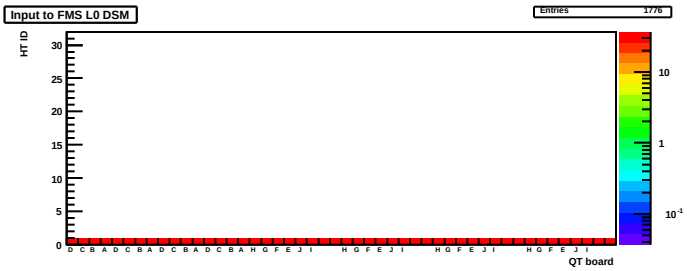

Input to FMS L0 DSM
Entries 296
QT8(2) sum - simulated
QT board

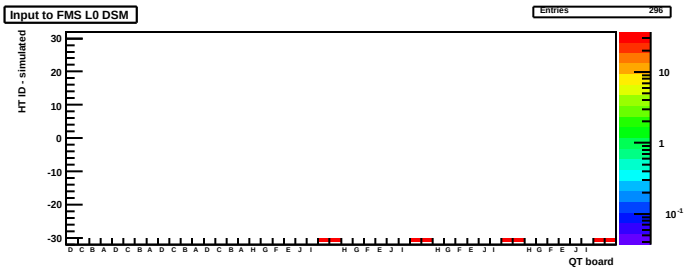

Input to FMS L0 DSM
Entries 1776
QT8(3) sum
QT board

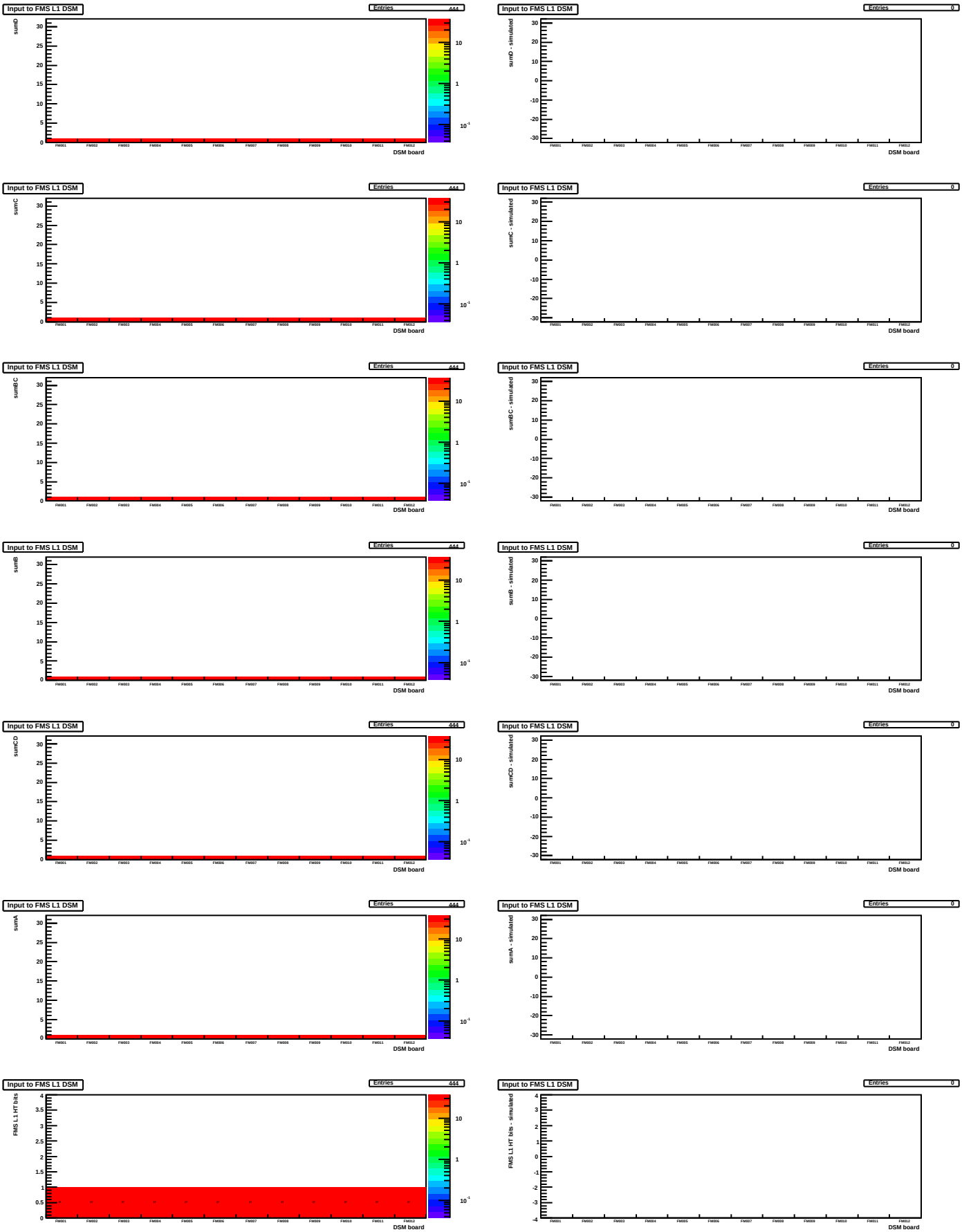

Input to FMS L0 DSM
Entries 296
QT8(3) sum - simulated
QT board

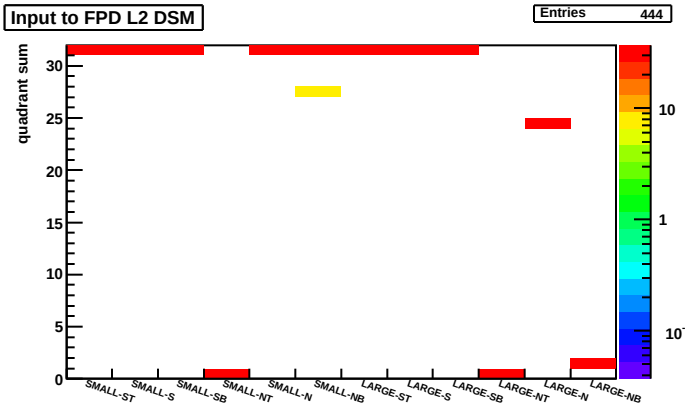

Input to FMS L0 DSM
Entries 1776
HT ADC
QT board

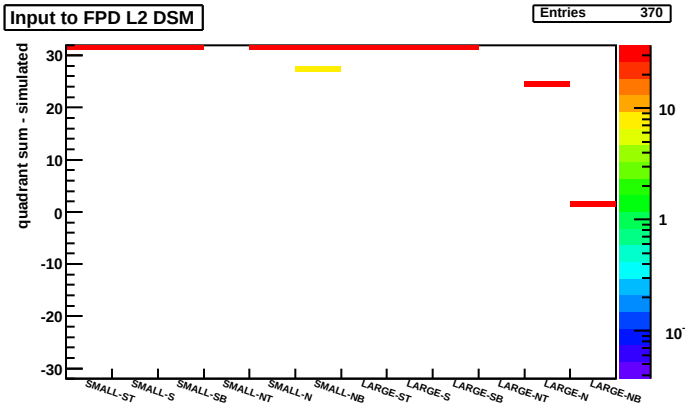

Input to FMS L0 DSM
Entries 296
HT ADC - simulated
QT board

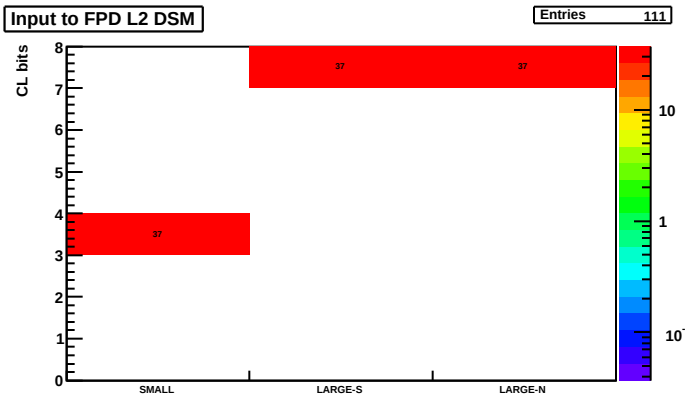

Input to FMS L0 DSM
Entries 1776
HT ID
QT board

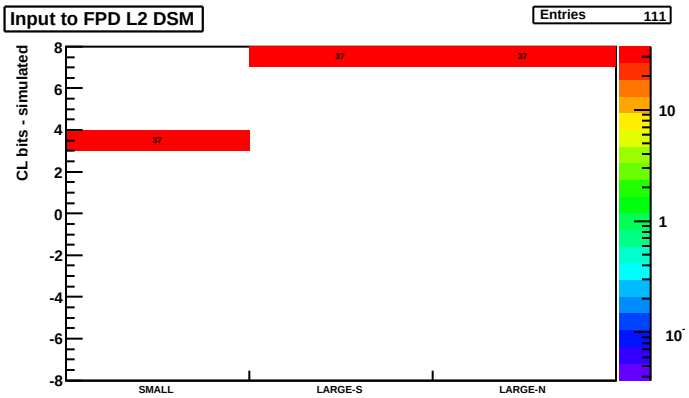

Input to FMS L0 DSM
Entries 296
HT ID - simulated
QT board

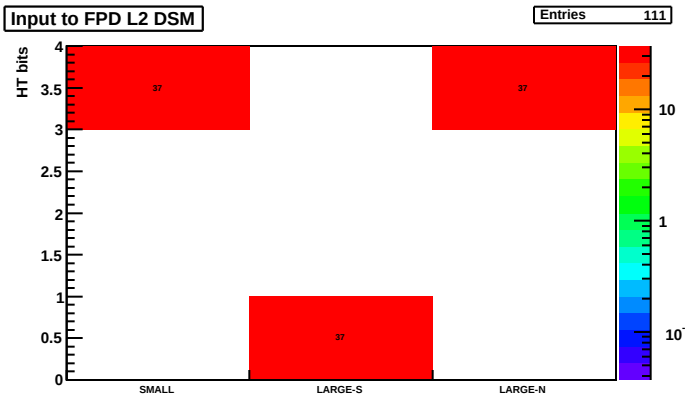

Input to FMS L1 DSM
Entries 444
sumD
sumC
sumBC
sumB
sumCD
sumA
FMS L1 HT bits
Entries 0
sumD - simulated
sumC - simulated
sumBC - simulated
sumB - simulated
sumCD - simulated
sumA - simulated
FMS L1 HT bits - simulated
DSM board

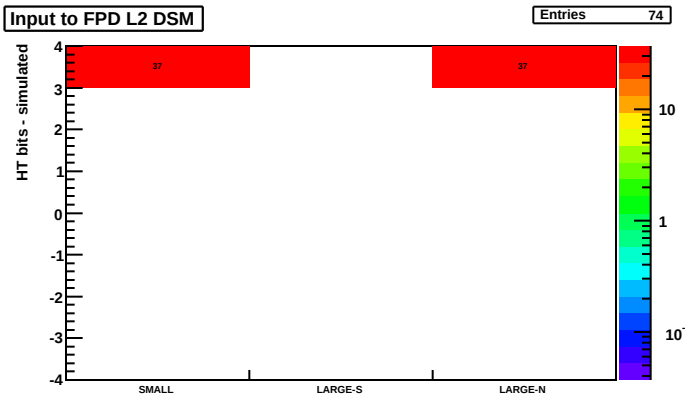

Input to FPD L2 DSM
Entries 444
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

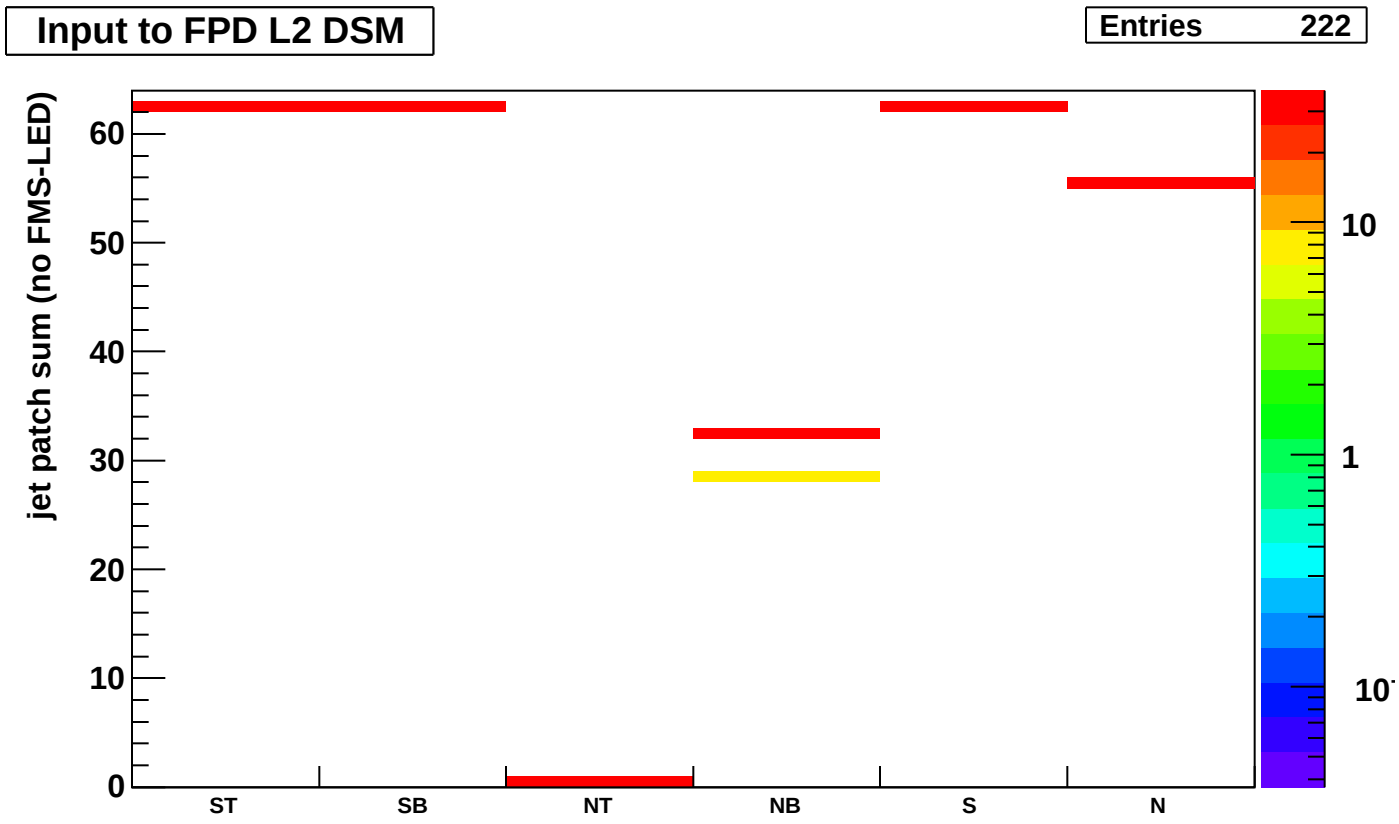

Input to FPD L2 DSM
Entries 370
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

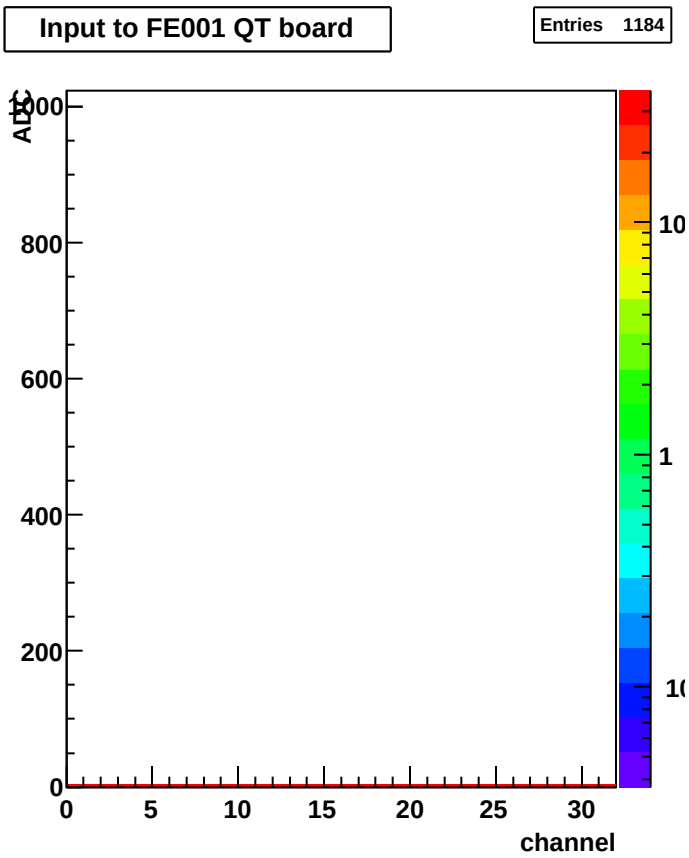

Input to FPD L2 DSM
Entries 111
CL bits
SMALL
LARGE-S
LARGE-N

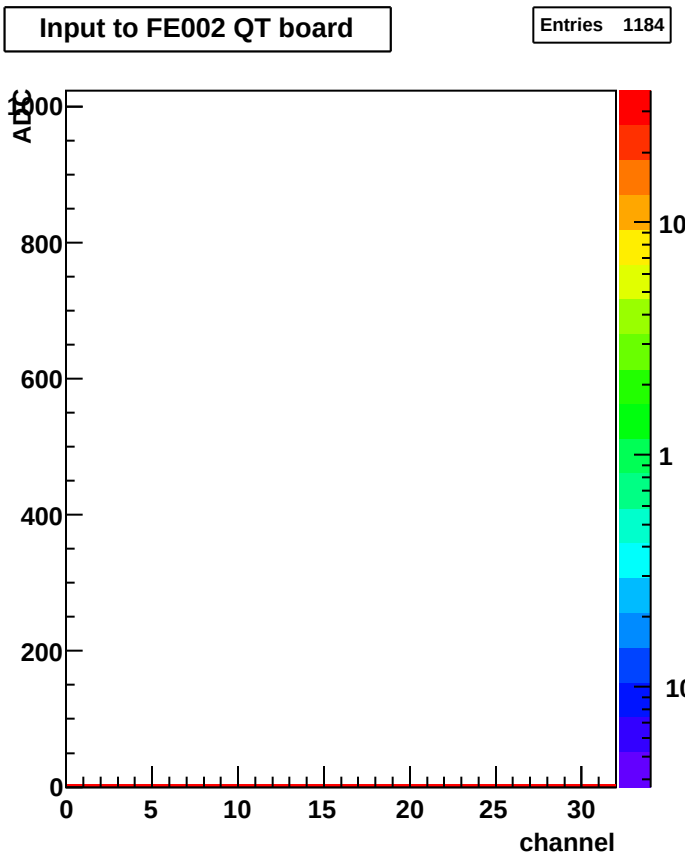

Input to FPD L2 DSM
Entries 111
CL bits - simulated
SMALL
LARGE-S
LARGE-N

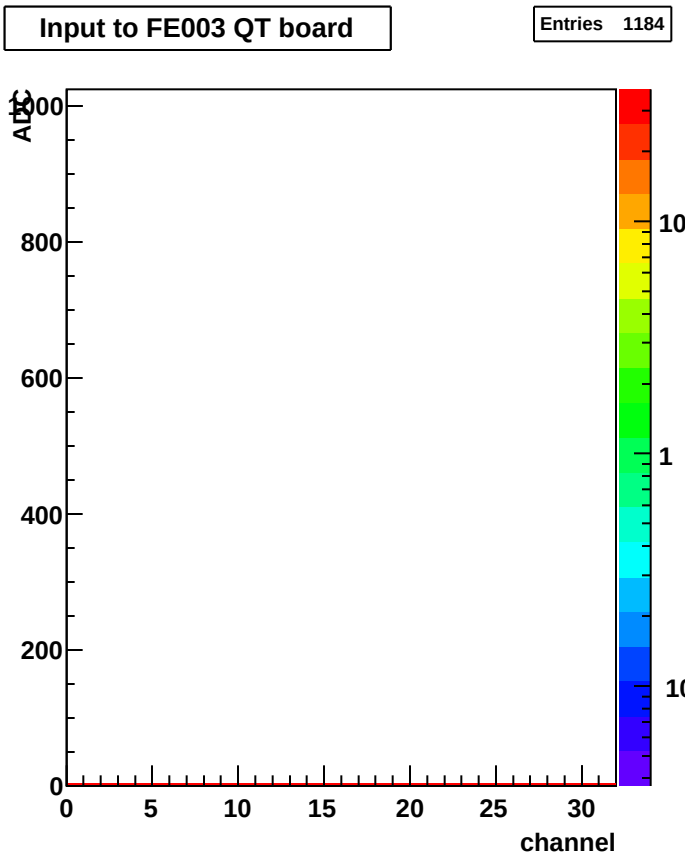

Input to FPD L2 DSM
Entries 111
HT bits
SMALL
LARGE-S
LARGE-N

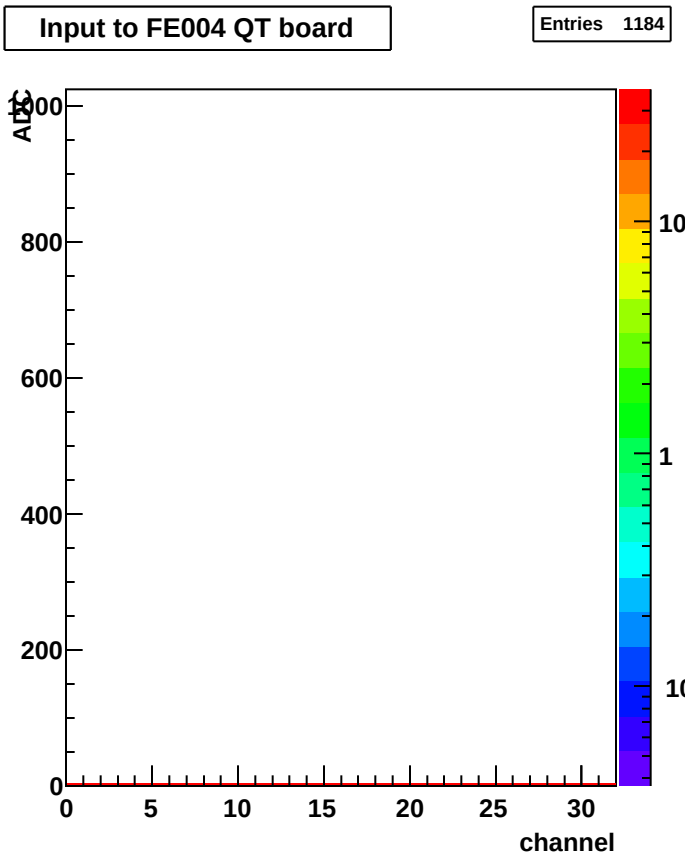

Input to FPD L2 DSM
Entries 74
HT bits - simulated
SMALL
LARGE-S
LARGE-N

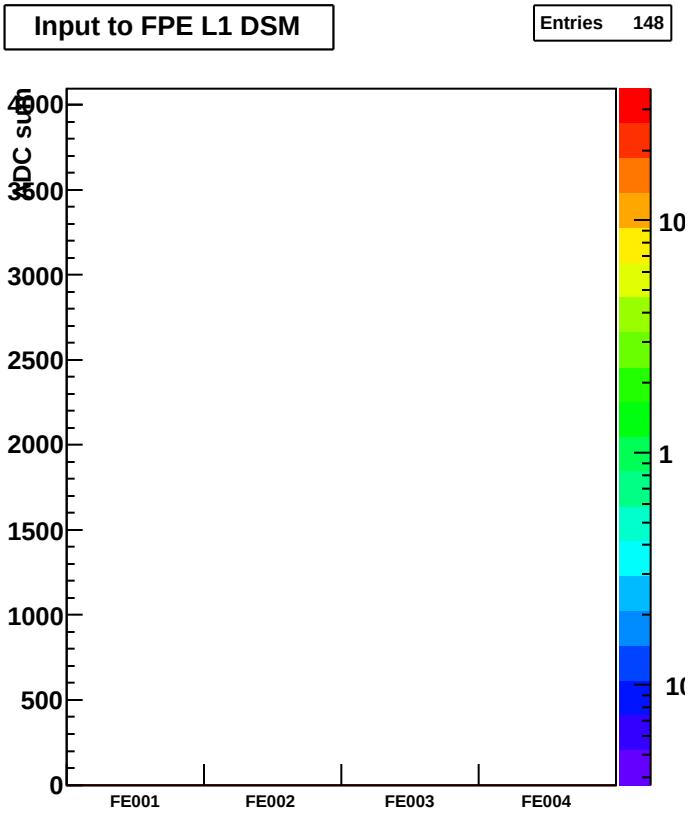

Input to FPD L2 DSM
Entries 222
jet patch sum (no FMS-LED)
60
50
40
30
20
10
0
ST
SB
NT
NB
S
N
10
1
10

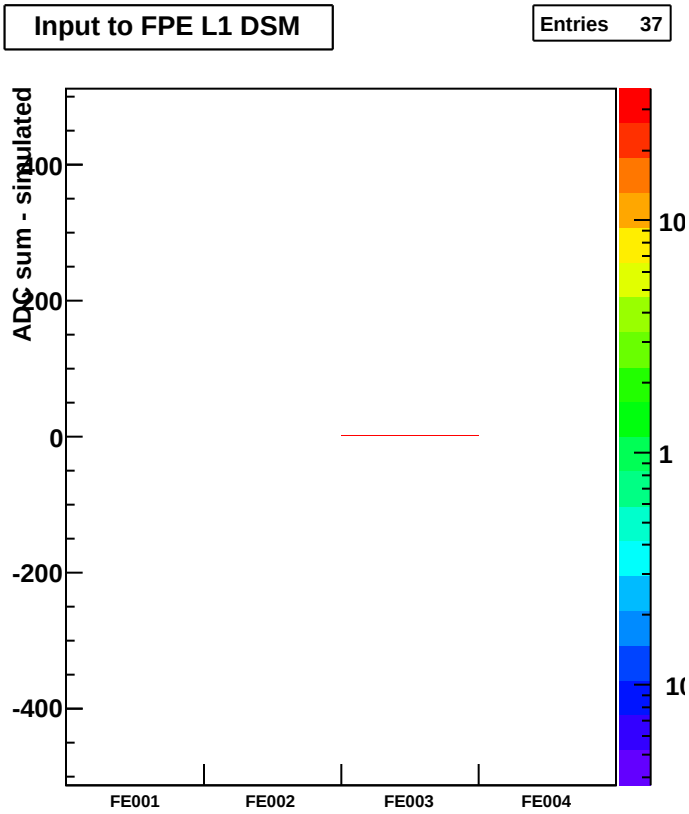

Input to FE001 QT board
Entries 1184
ADC
channel

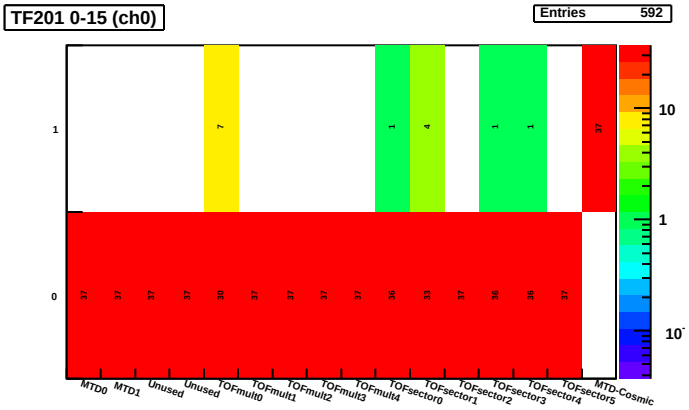

Input to FE002 QT board
Entries 1184
ADC
channel

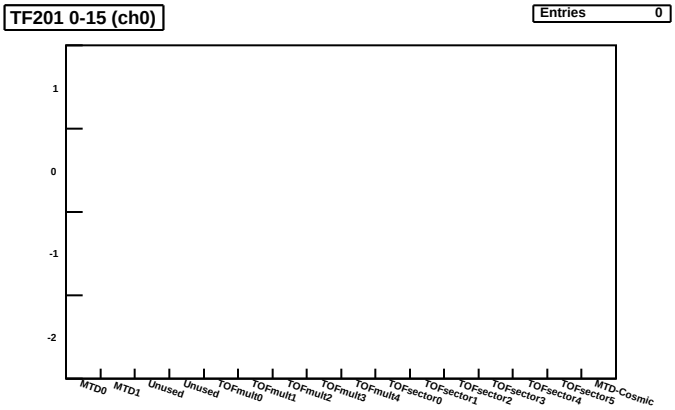

Input to FE003 QT board
Entries 1184
ADC
channel

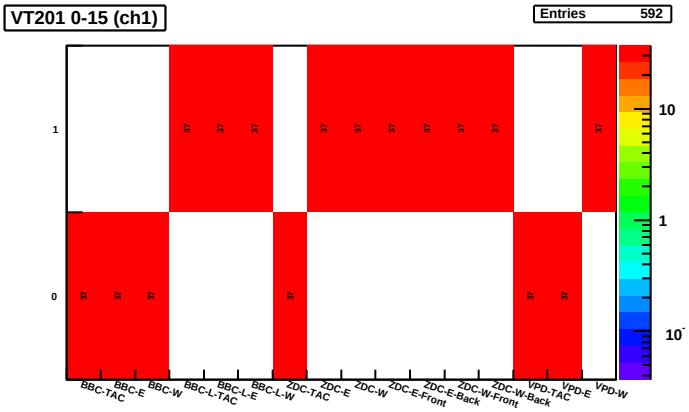

Input to FE004 QT board
Entries 1184
ADC
channel

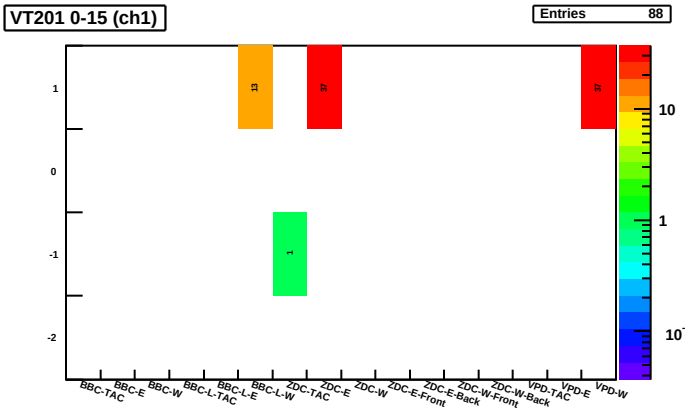

Input to FPE L1 DSM
Entries 148
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
1

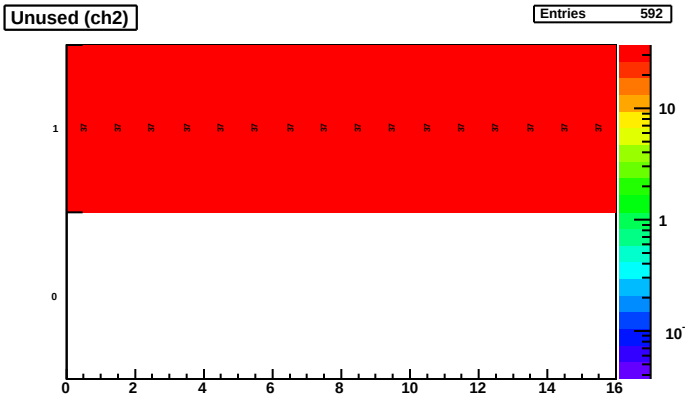

Input to FPE L1 DSM
Entries 37
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
1

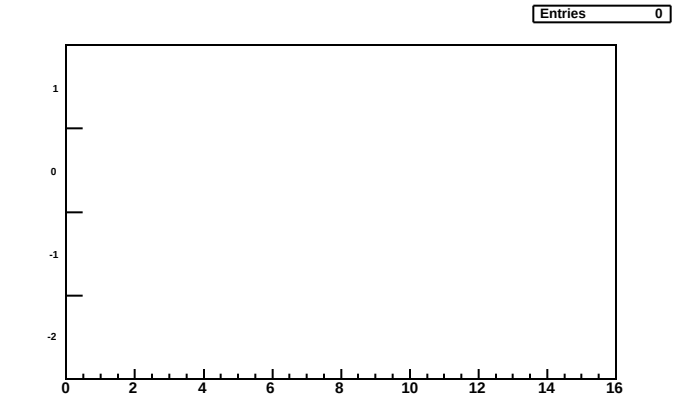

TF201 0-15 (ch0)
Entries 592
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

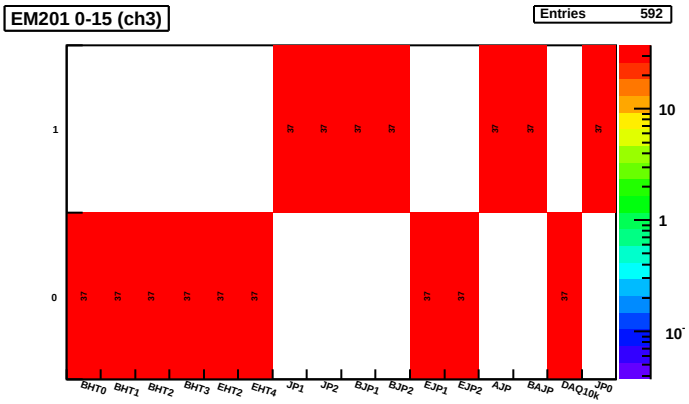

TF201 0-15 (ch0)
Entries 0
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

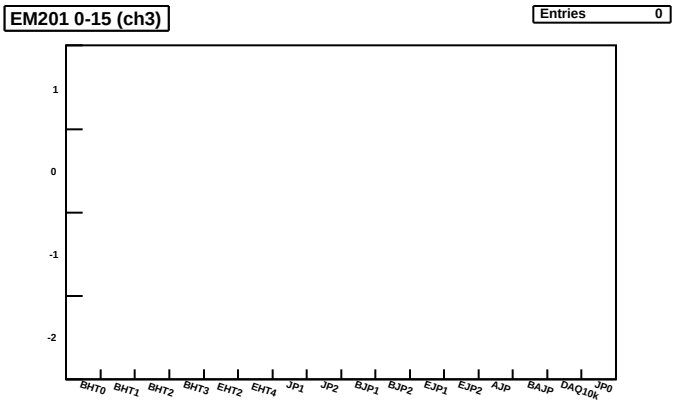

VT201 0-15 (ch1)
Entries 592
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 88
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

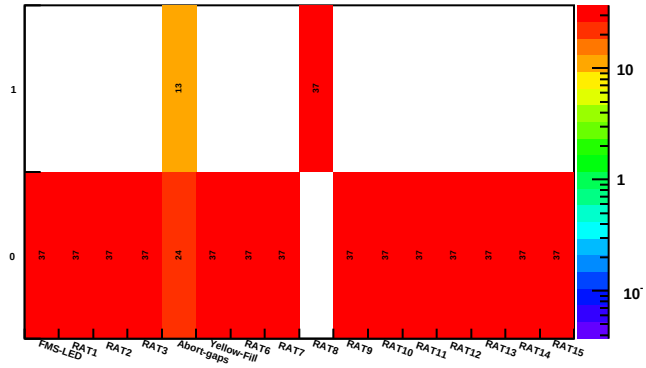

Unused (ch2)
Entries 592

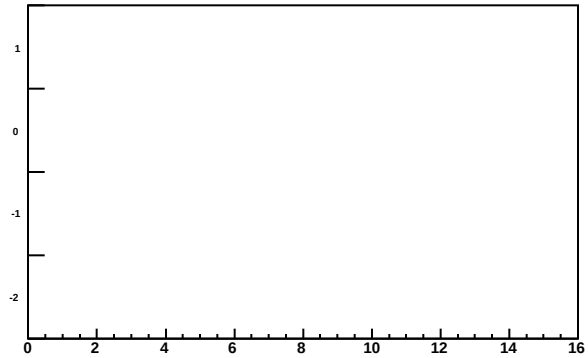

Entries 0


EM201 0-15 (ch3)
Entries 592
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

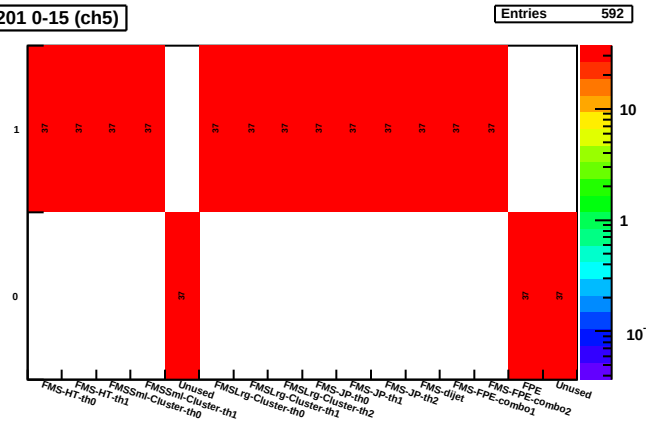

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

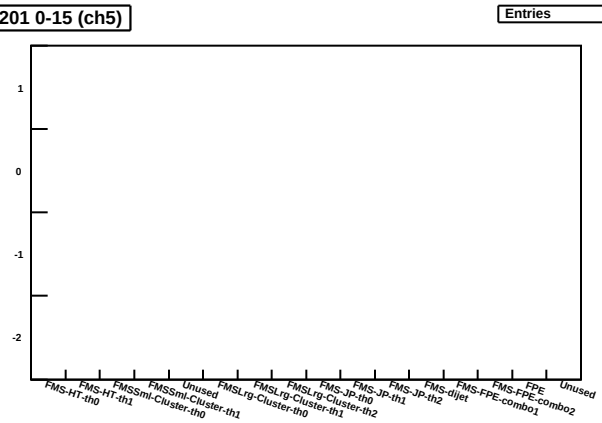




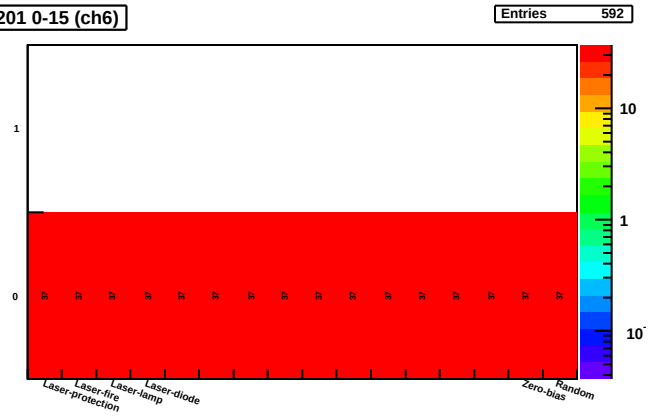
FP201 0-15 (ch5)

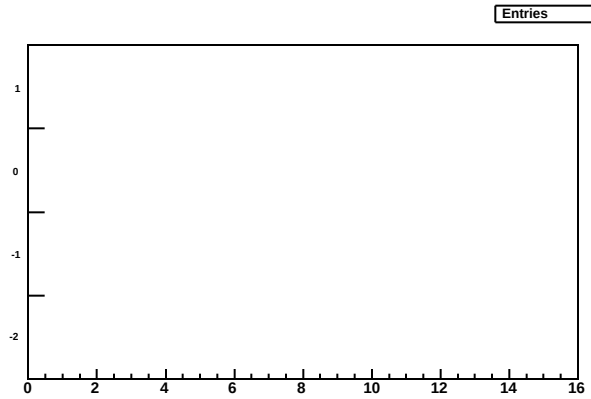
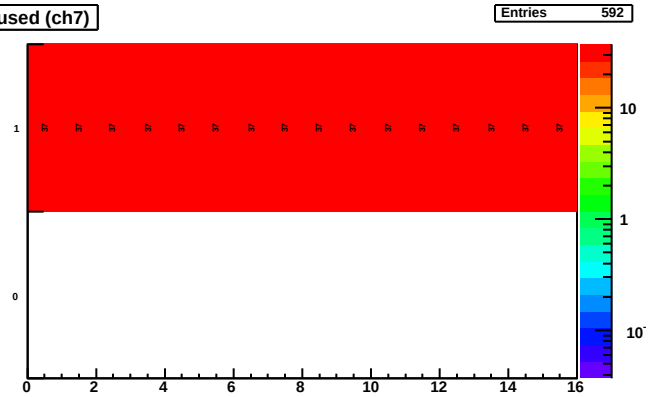
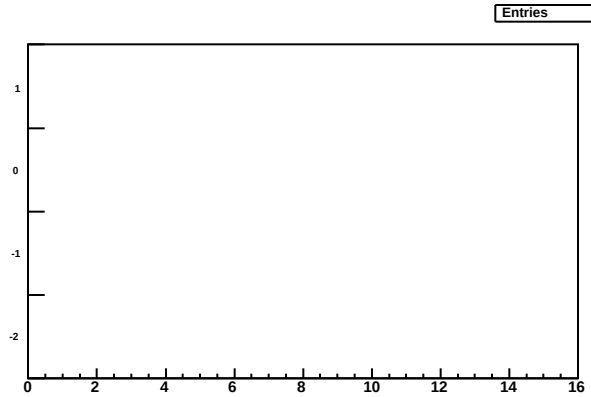


FP201 0-15 (ch5)



ST201 0-15 (ch6)

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