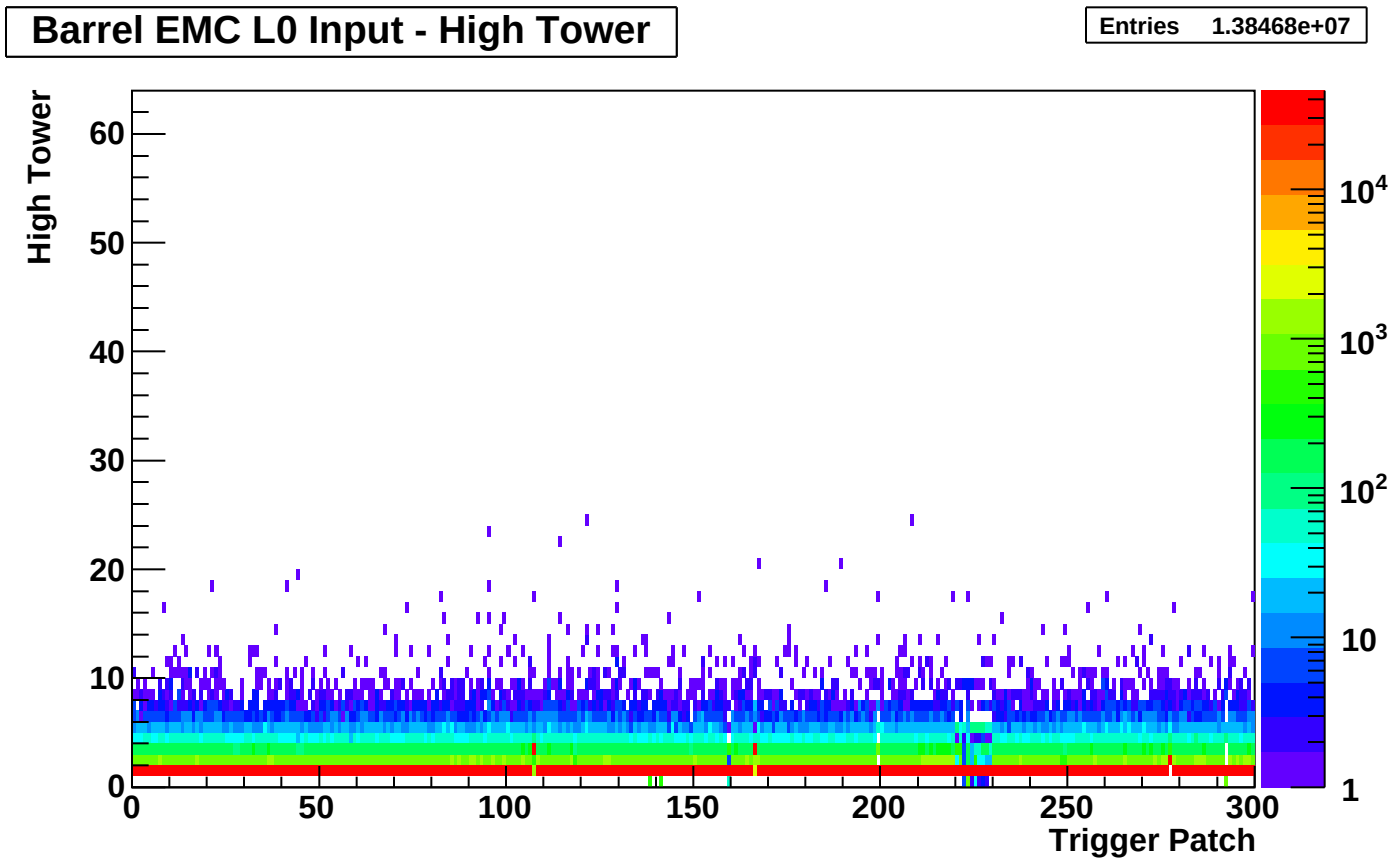
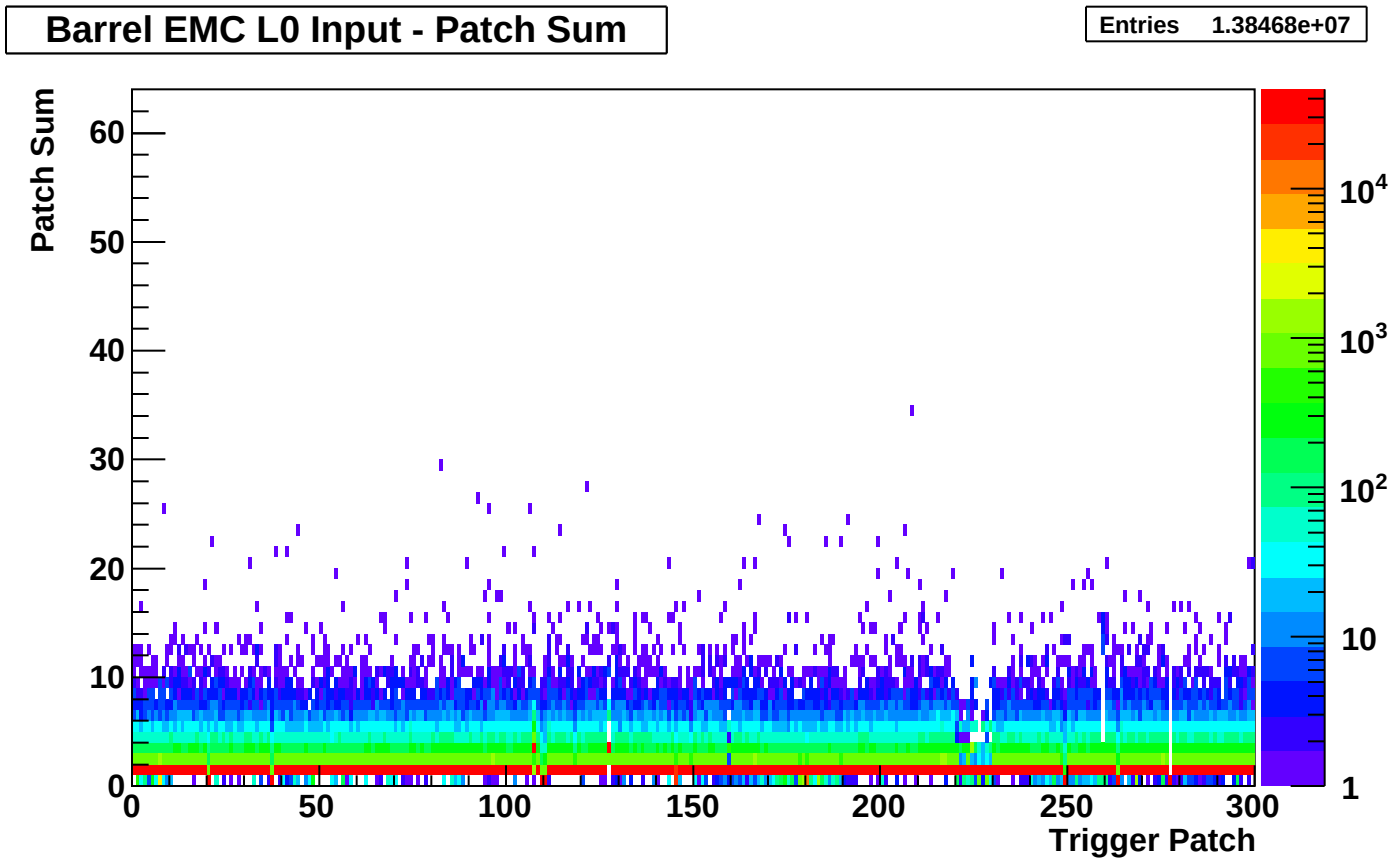
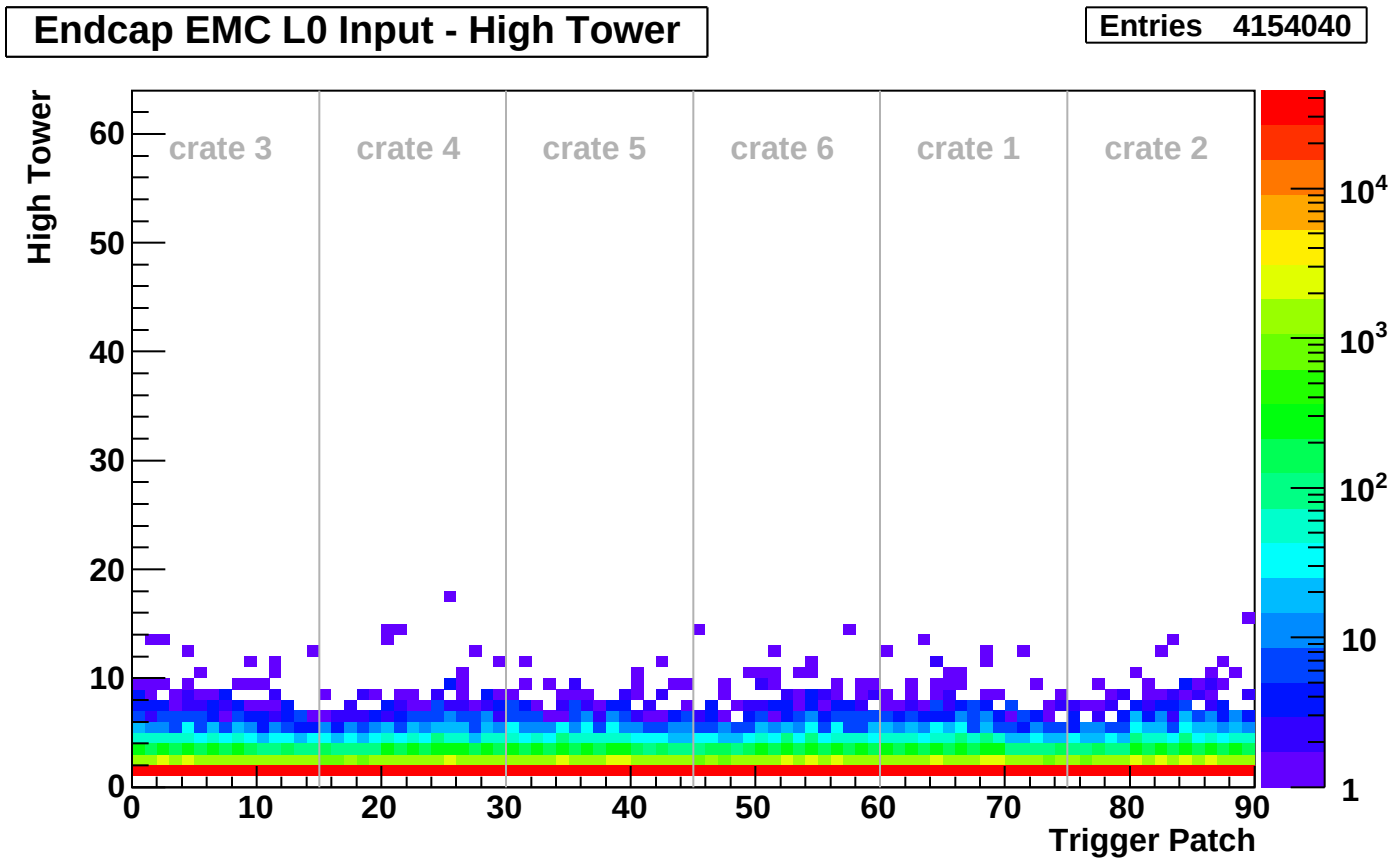
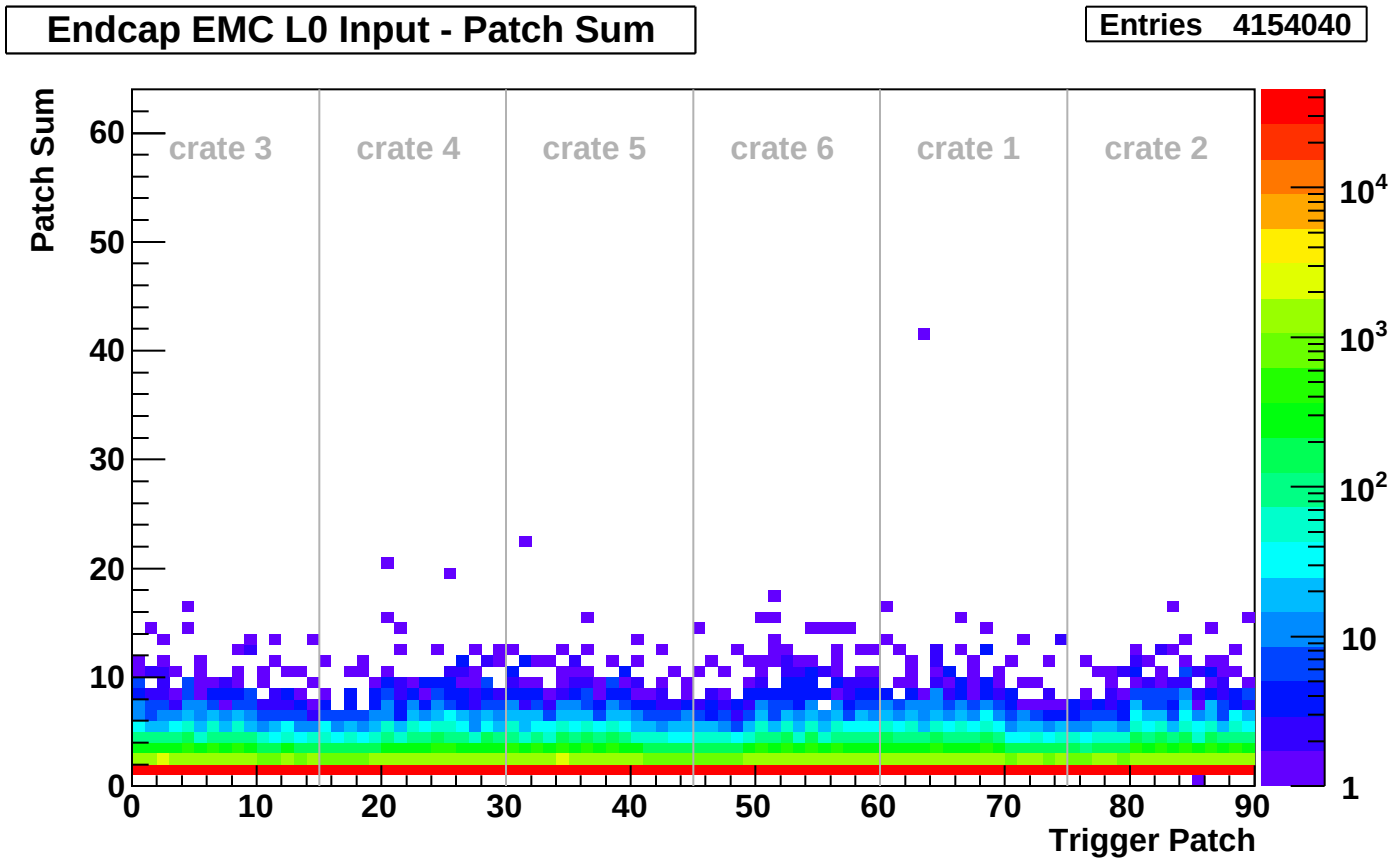
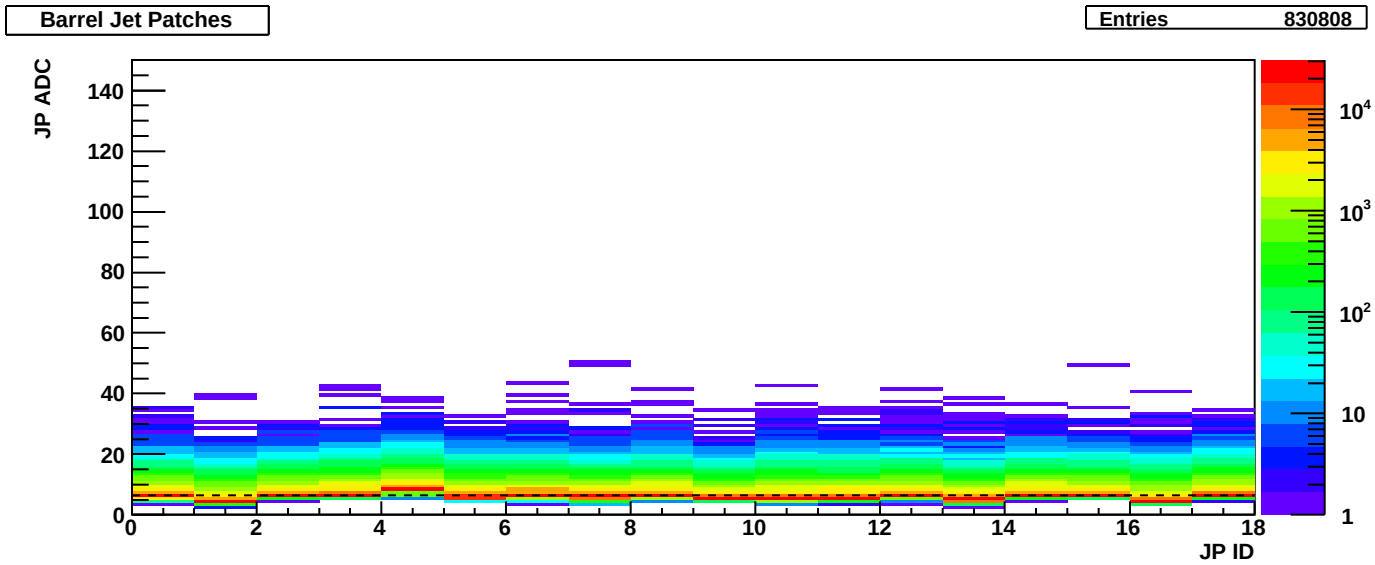
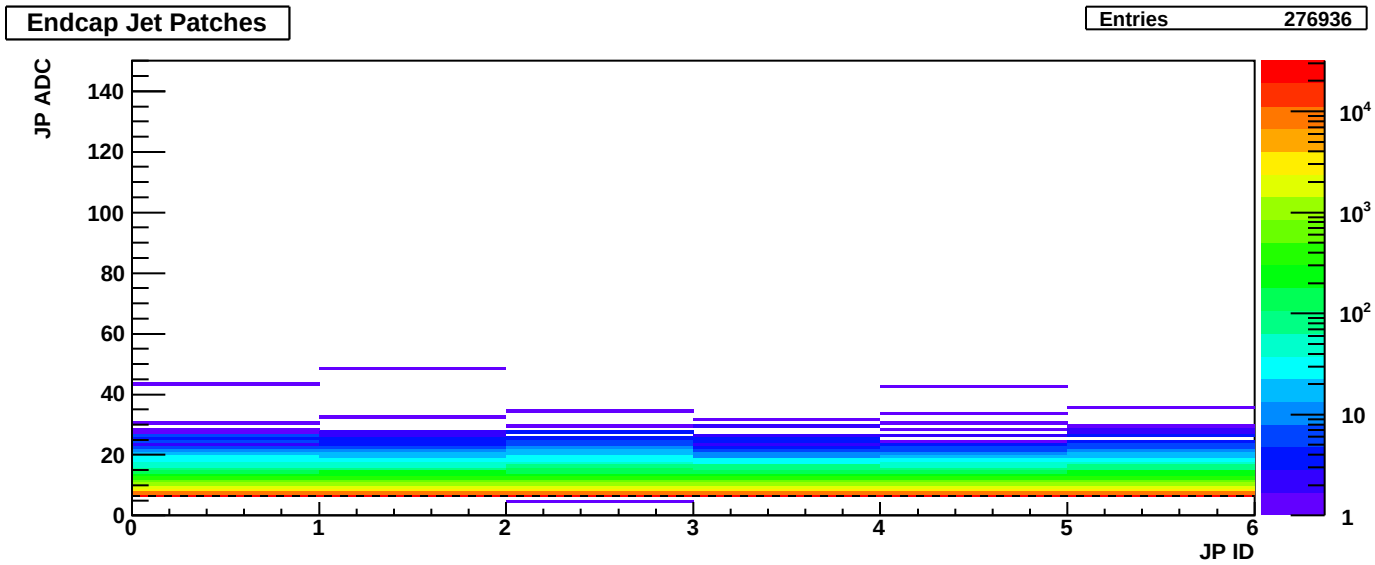

Barrel EMC L0 Input - High Tower
Entries 1.38468e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

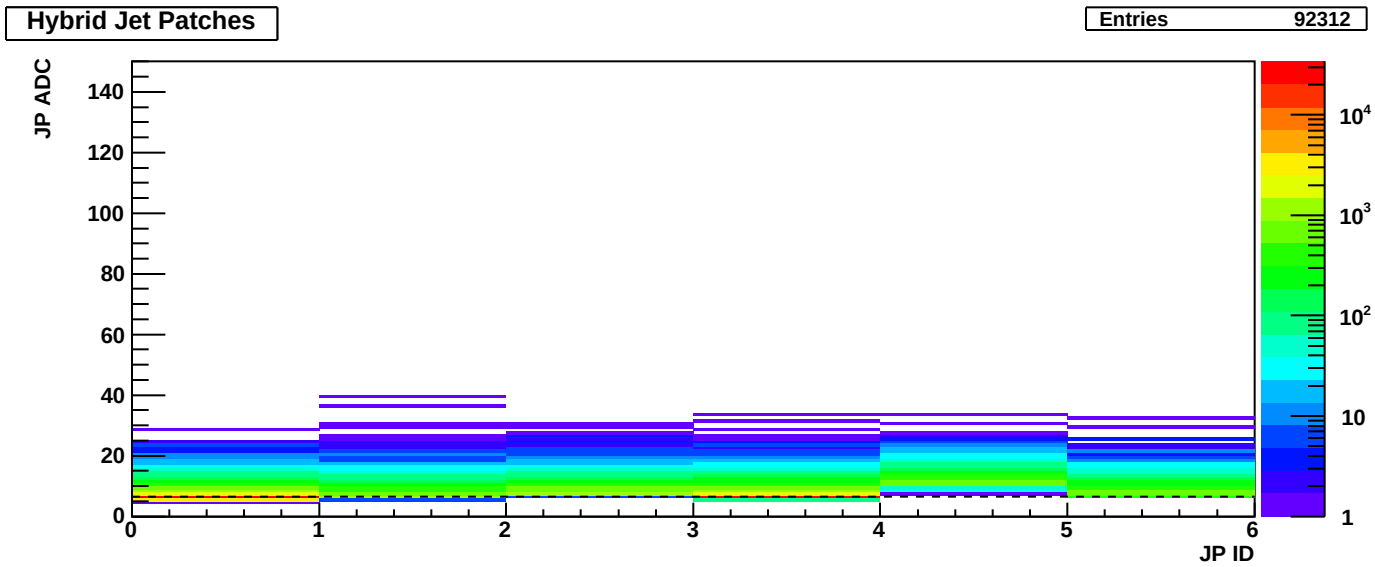

Barrel EMC L0 Input - Patch Sum
Entries 1.38468e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

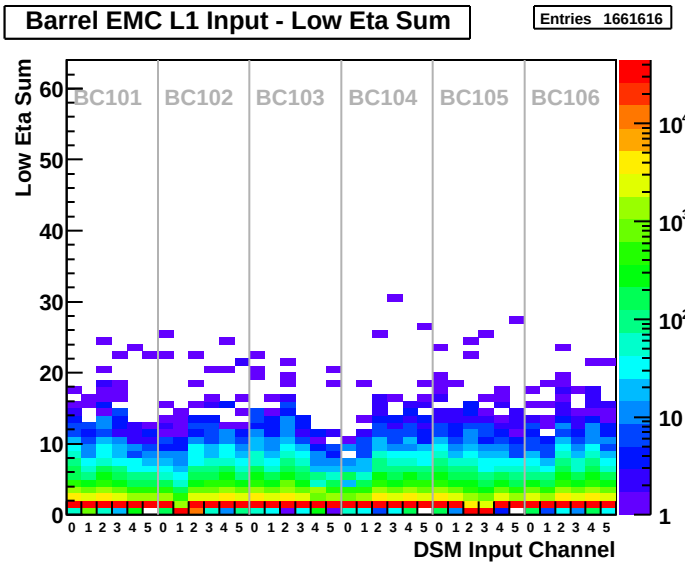

Endcap EMC L0 Input - High Tower
Entries 4154040
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

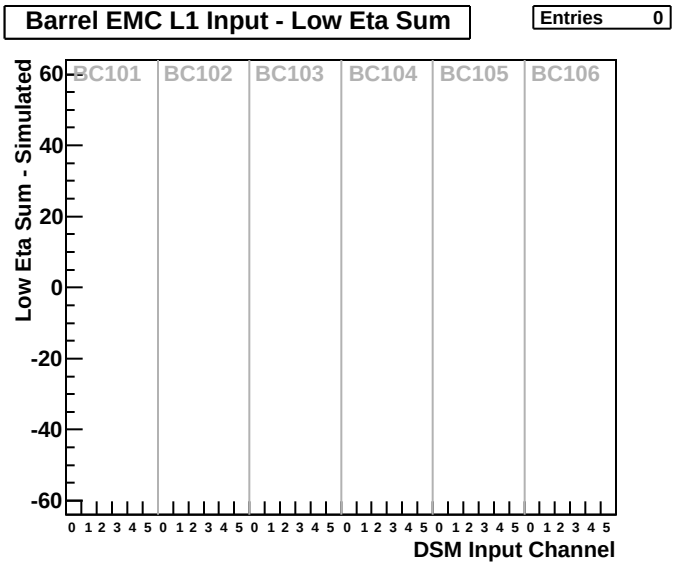

Endcap EMC L0 Input - Patch Sum
Entries 4154040
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

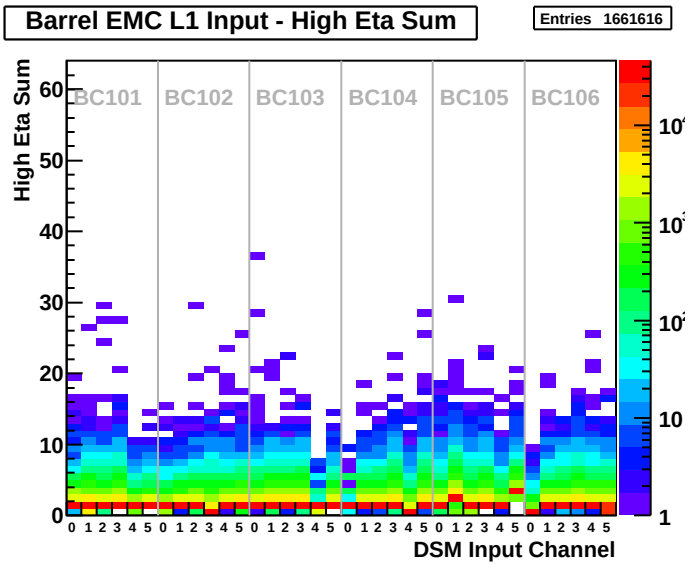

Barrel Jet Patches
Entries 830808
JP ADC
JP ID

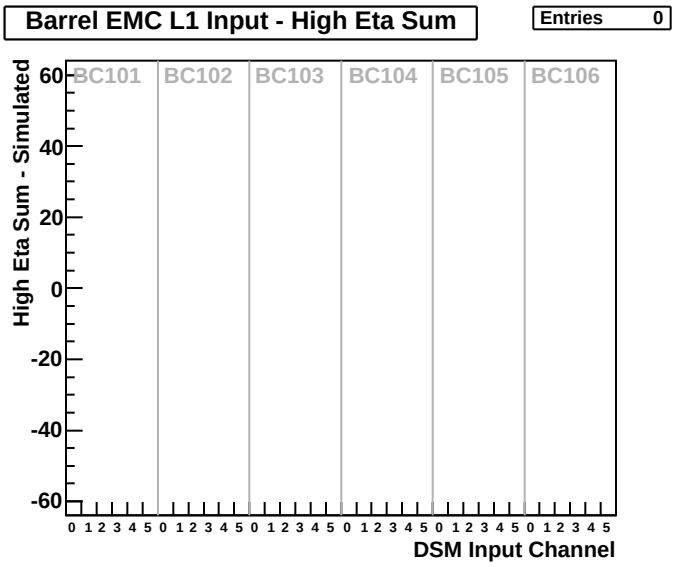

Endcap Jet Patches
Entries 276936
JP ADC
JP ID

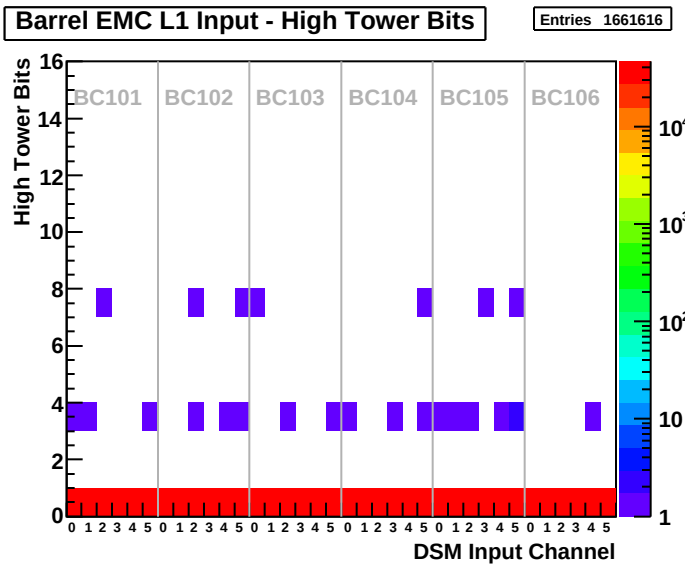

Hybrid Jet Patches
Entries 92312
JP ADC
JP ID

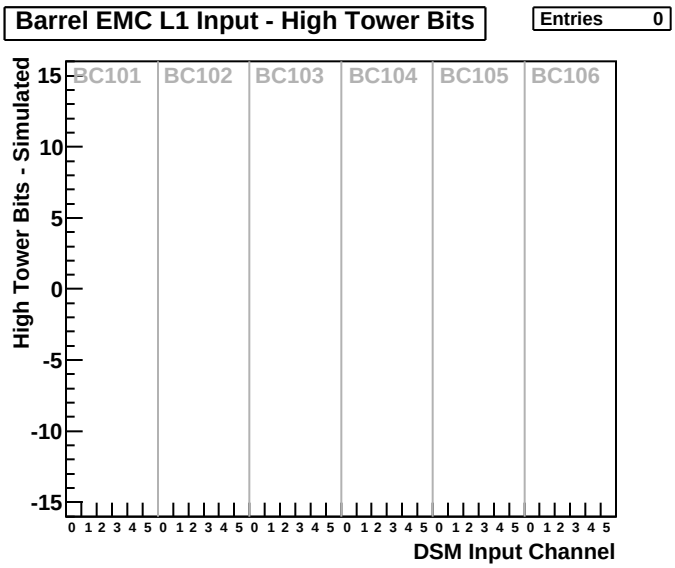

Barrel EMC L1 Input - Low Eta Sum
Entries 1661616
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

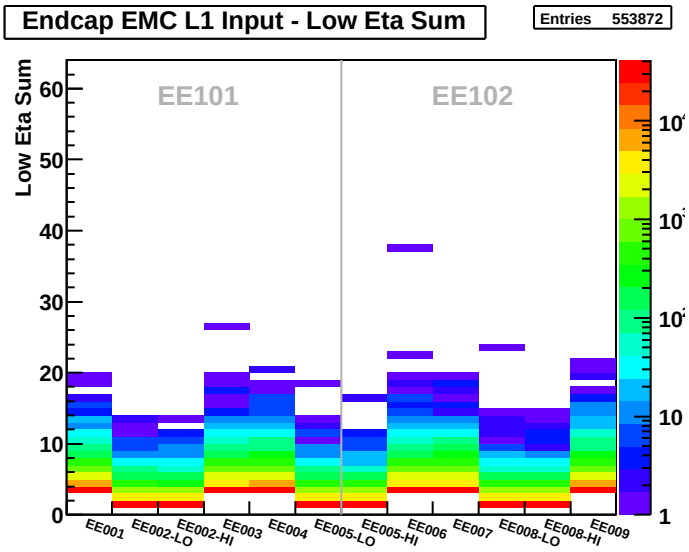

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

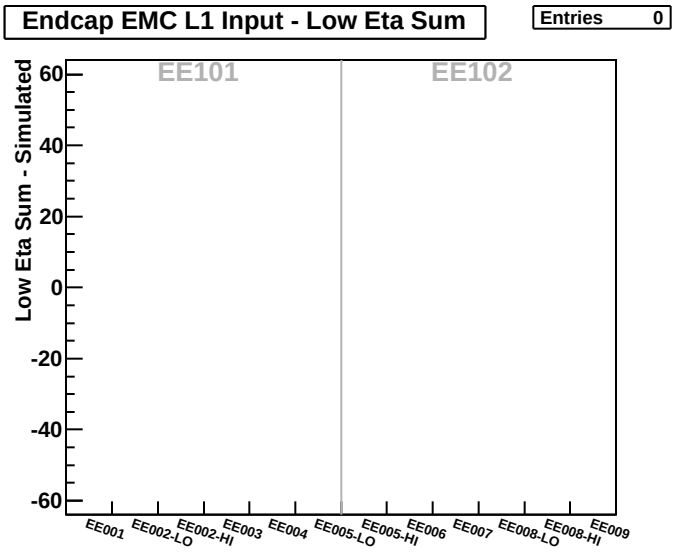

Barrel EMC L1 Input - High Eta Sum
Entries 1661616
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

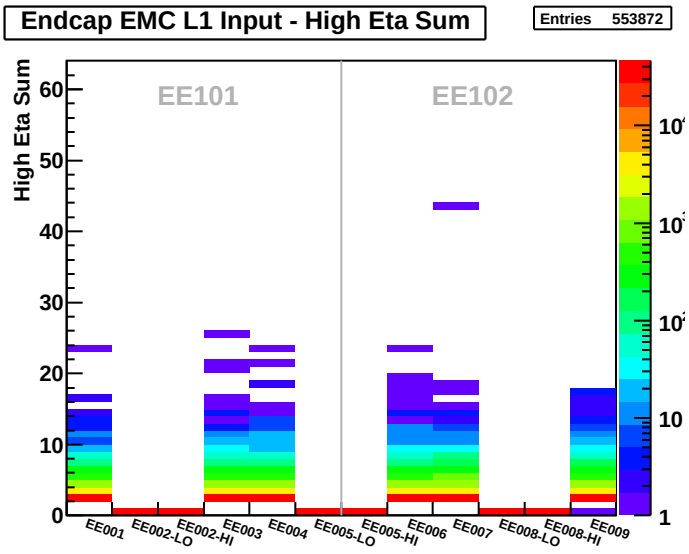

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

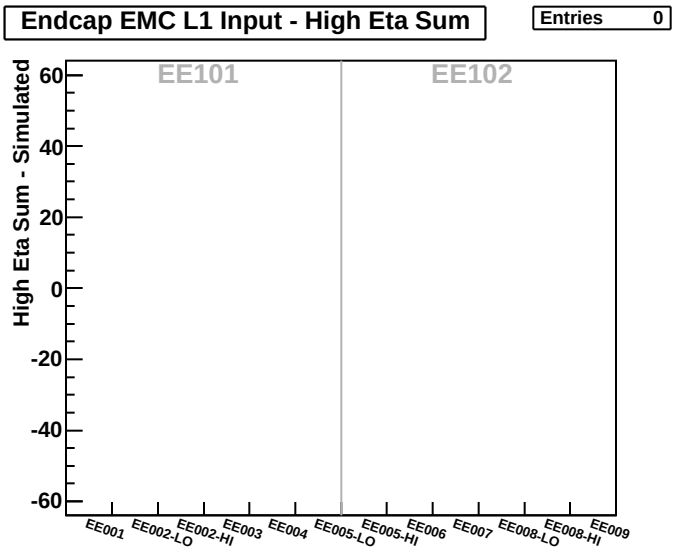

Barrel EMC L1 Input - High Tower Bits
Entries 1661616
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

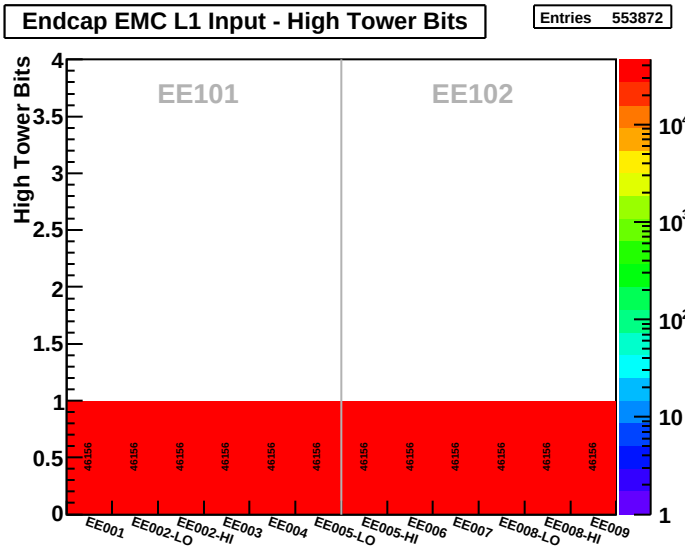

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

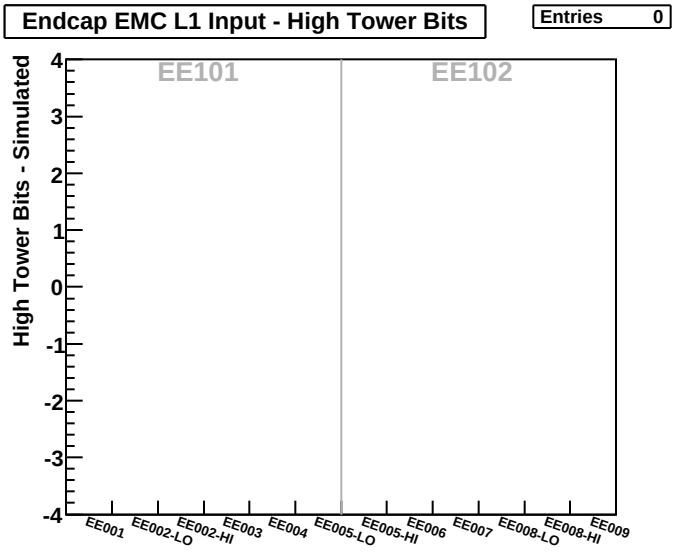

Endcap EMC L1 Input - Low Eta Sum
Entries 553872
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

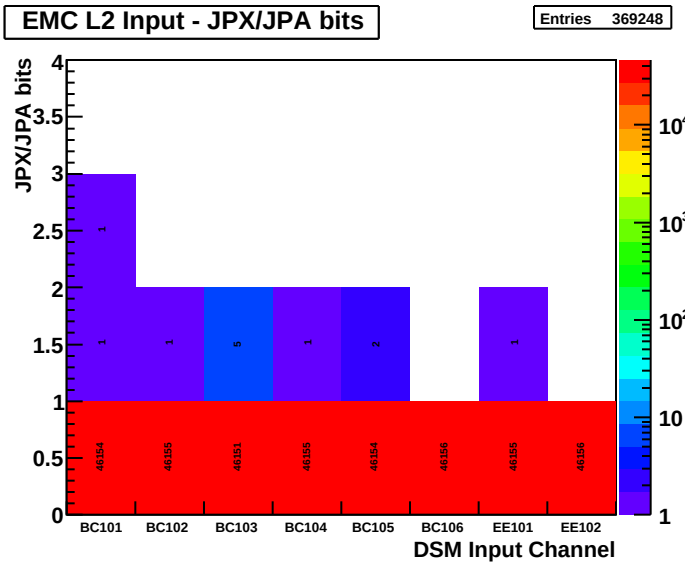

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

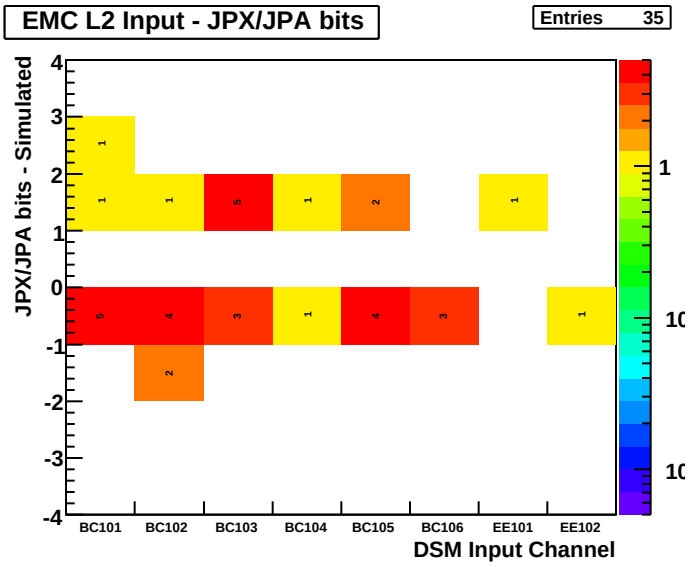

Endcap EMC L1 Input - High Eta Sum
Entries 553872
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

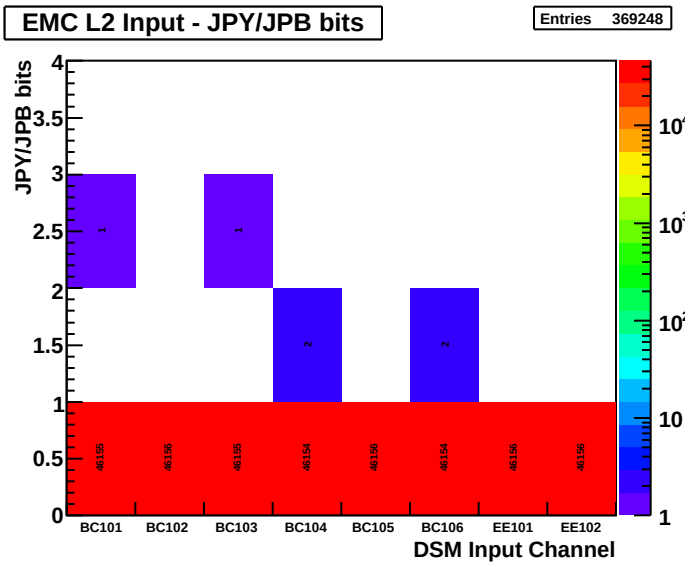

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

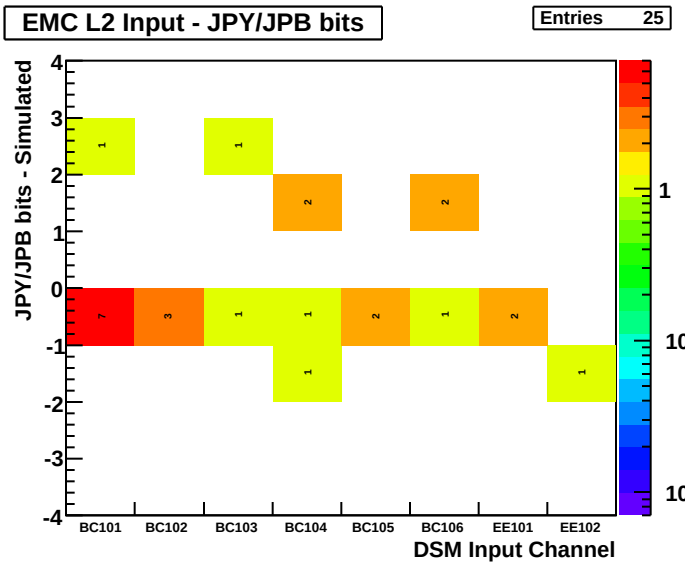

Endcap EMC L1 Input - High Tower Bits
Entries 553872
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

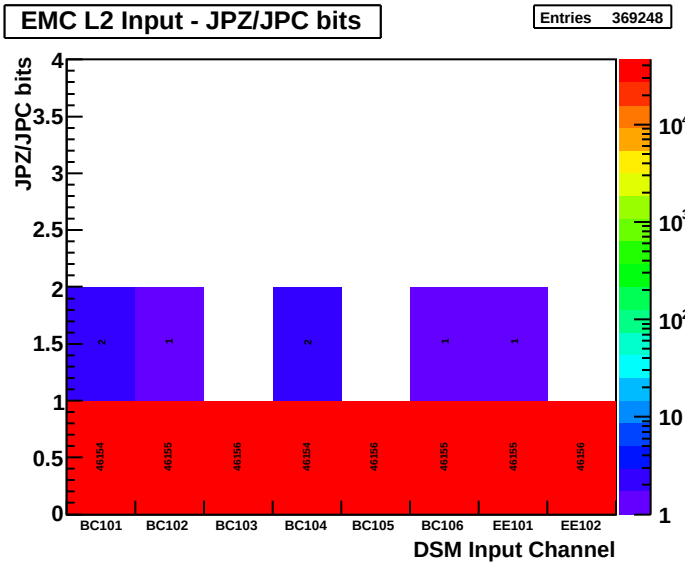

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

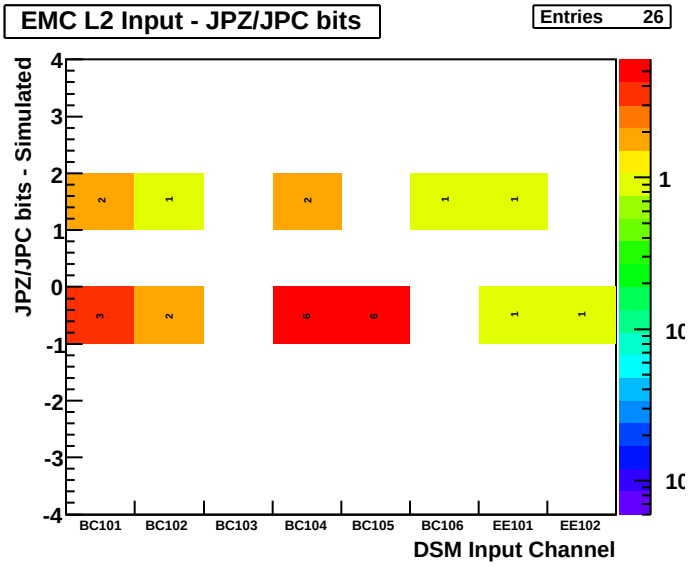

EMC L2 Input - JPX/JPA bits
Entries 369248
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

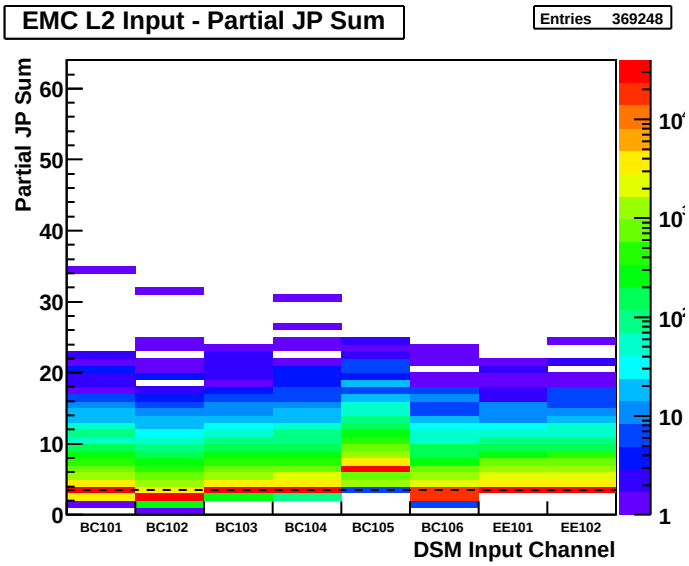

EMC L2 Input - JPX/JPA bits
Entries 35
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

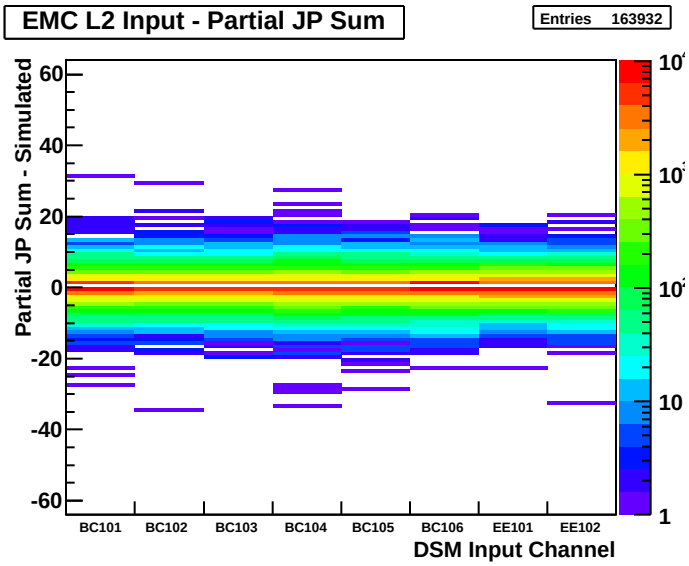

EMC L2 Input - JPY/JPB bits
Entries 369248
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

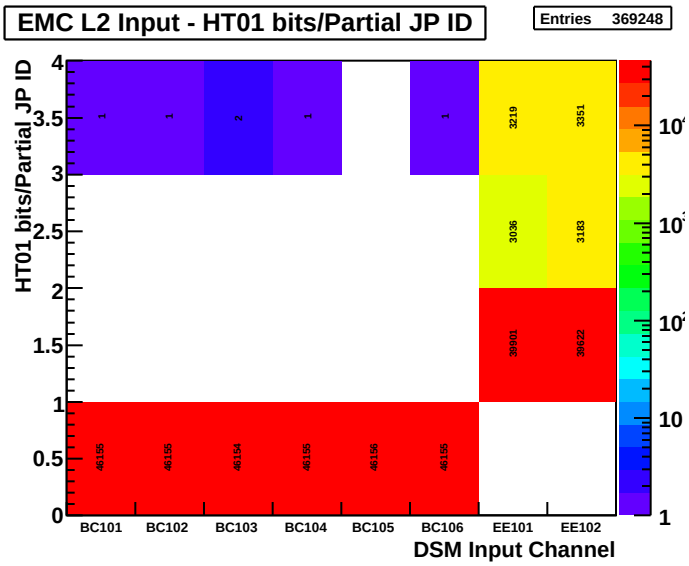

EMC L2 Input - JPY/JPB bits
Entries 25
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

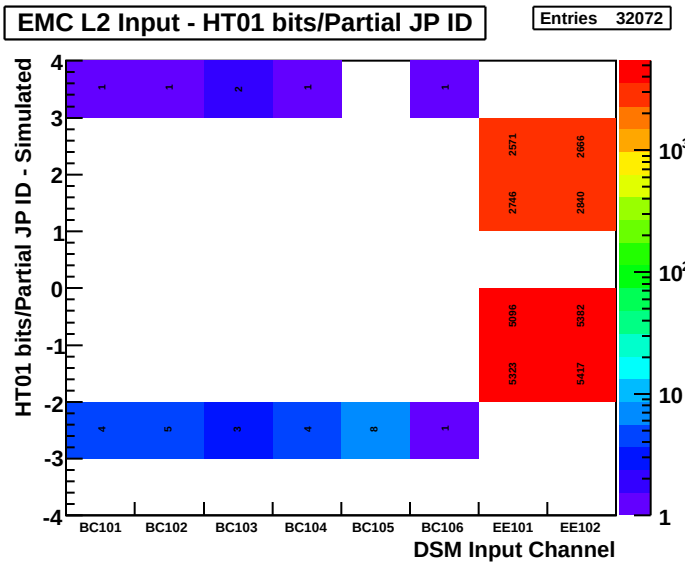

EMC L2 Input - JPZ/JPC bits
Entries 369248
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

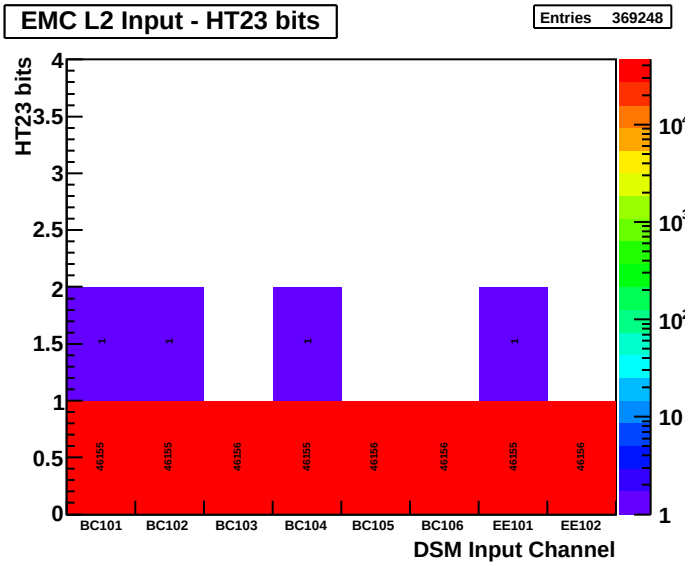

EMC L2 Input - JPZ/JPC bits
Entries 26
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

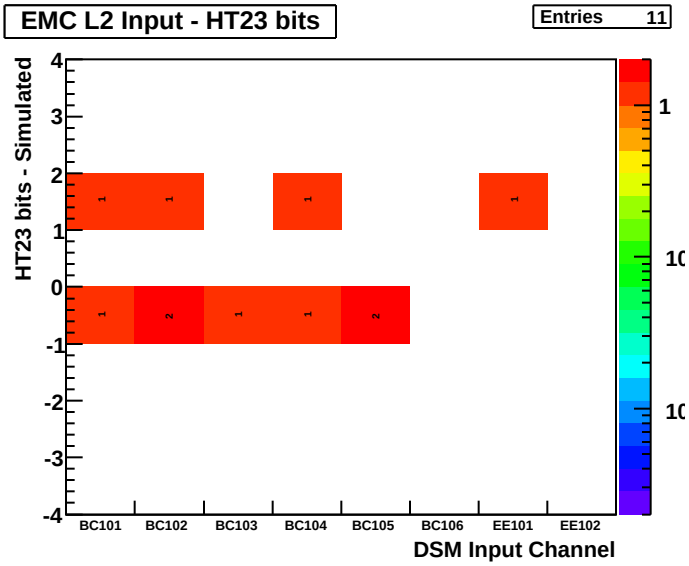

EMC L2 Input - Partial JP Sum
Entries 369248
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

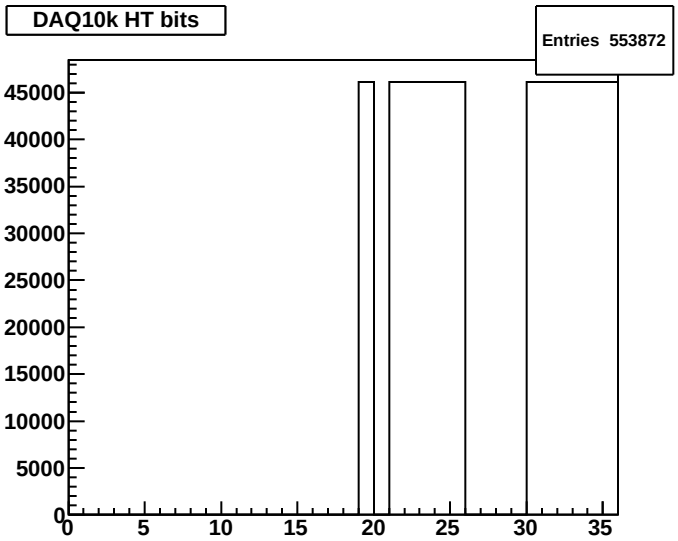

EMC L2 Input - Partial JP Sum
Entries 163932
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

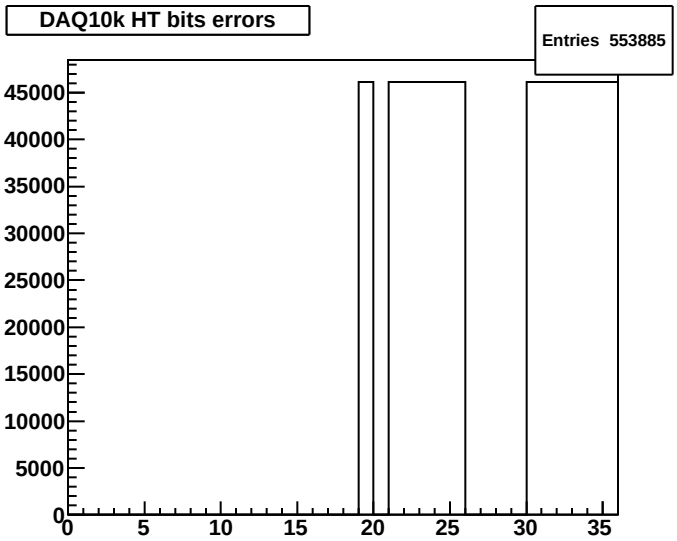

EMC L2 Input - HT01 bits/Partial JP ID
Entries 369248
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

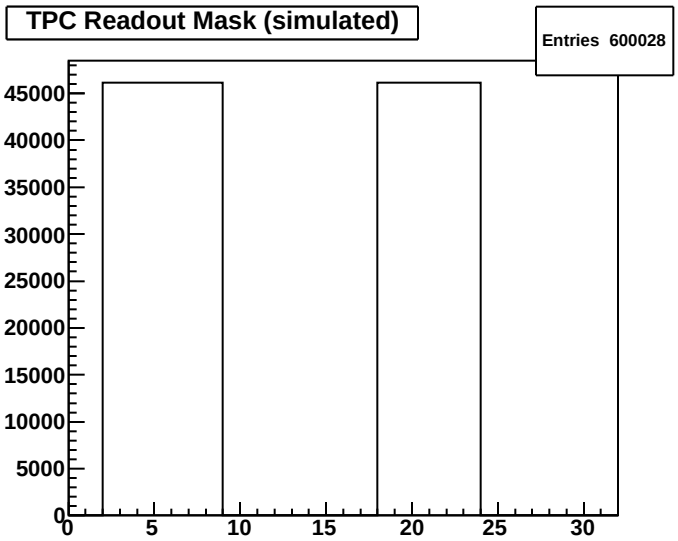

EMC L2 Input - HT01 bits/Partial JP ID
Entries 32072
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

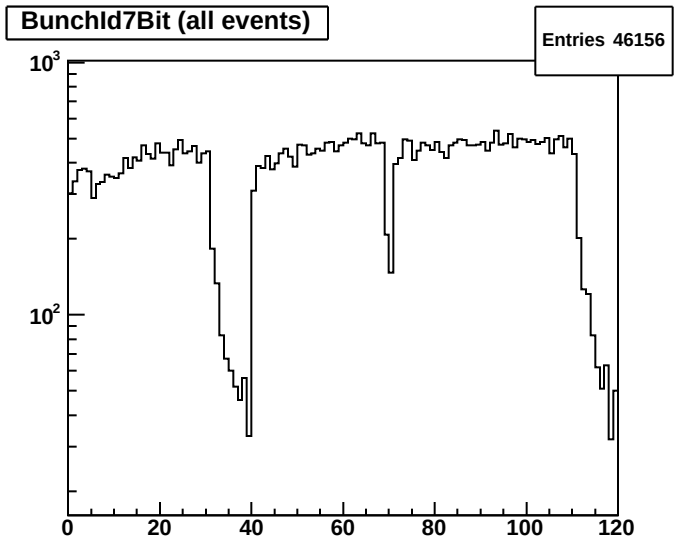

EMC L2 Input - HT23 bits
Entries 369248
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

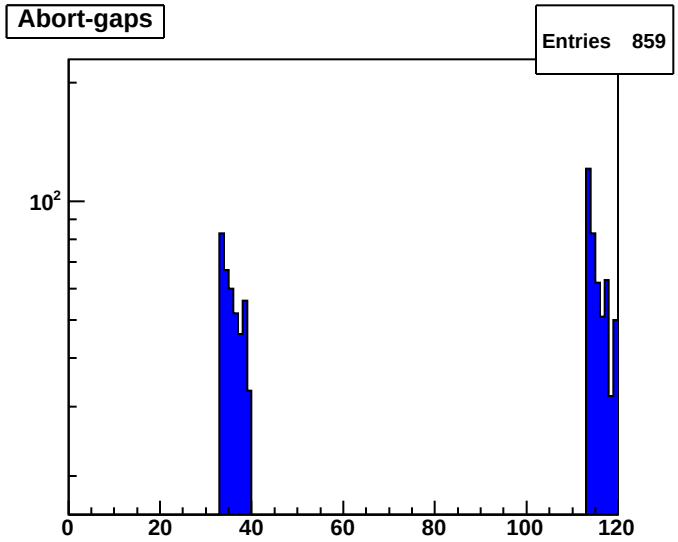

EMC L2 Input - HT23 bits
Entries 11
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

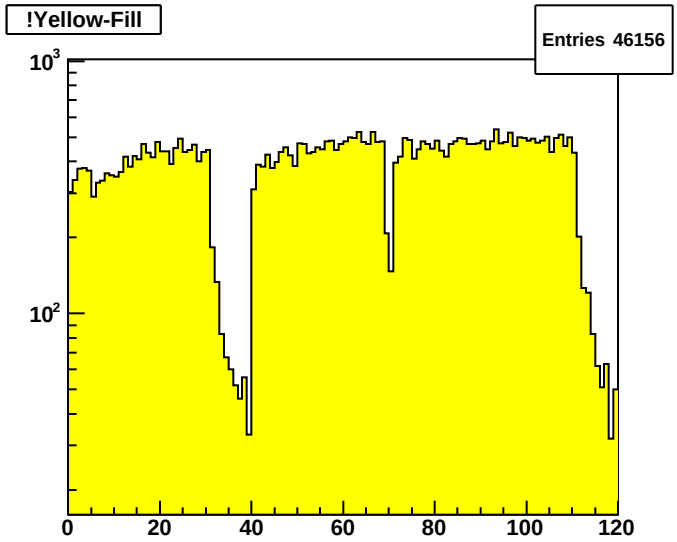

DAQ10k HT bits
Entries 553872

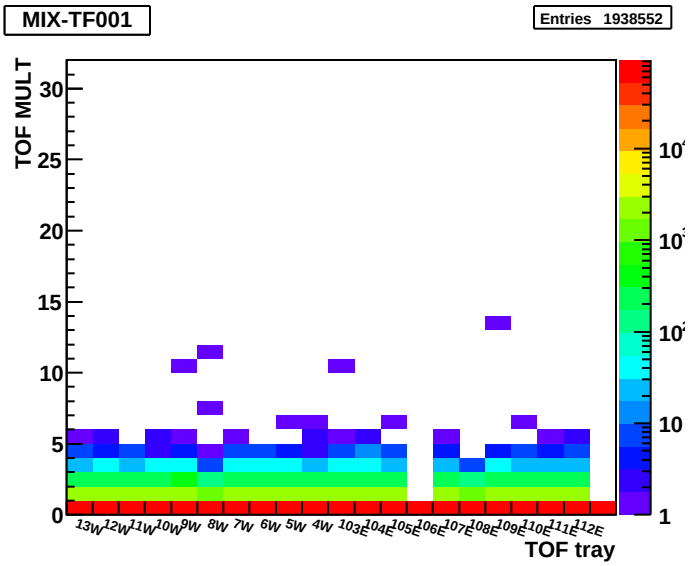

DAQ10k HT bits errors
Entries 553885

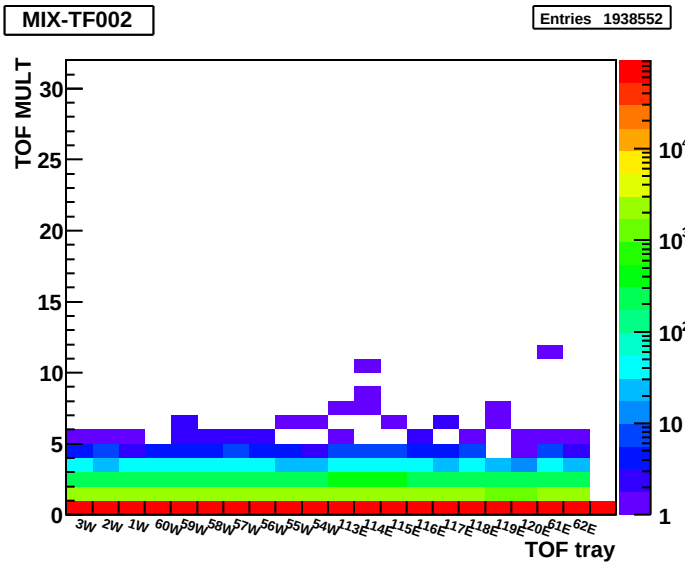

TPC Readout Mask (simulated)
Entries 600028

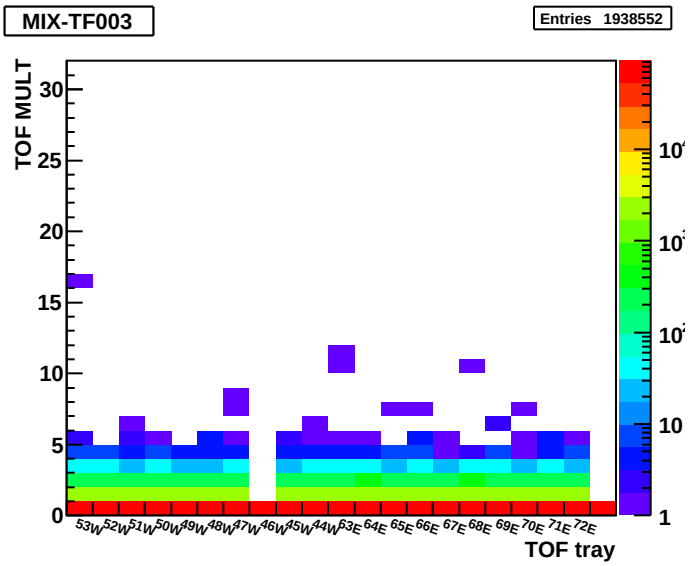

BunchId7Bit (all events)
Entries 46156

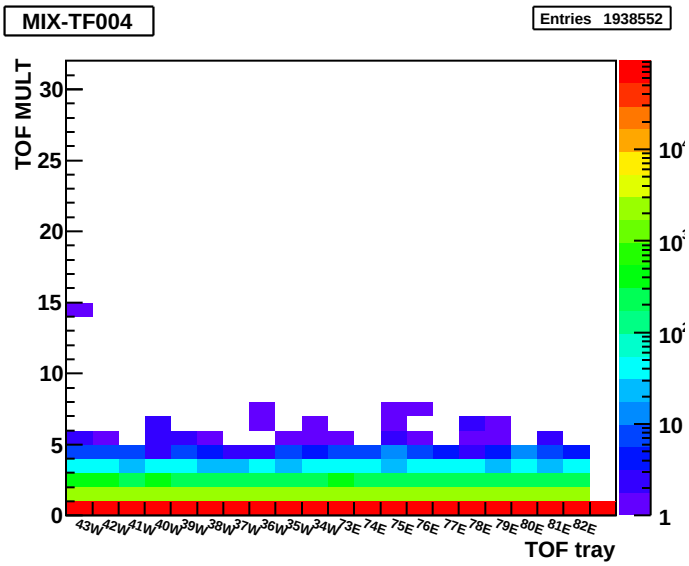

Abort-gaps
Entries 859

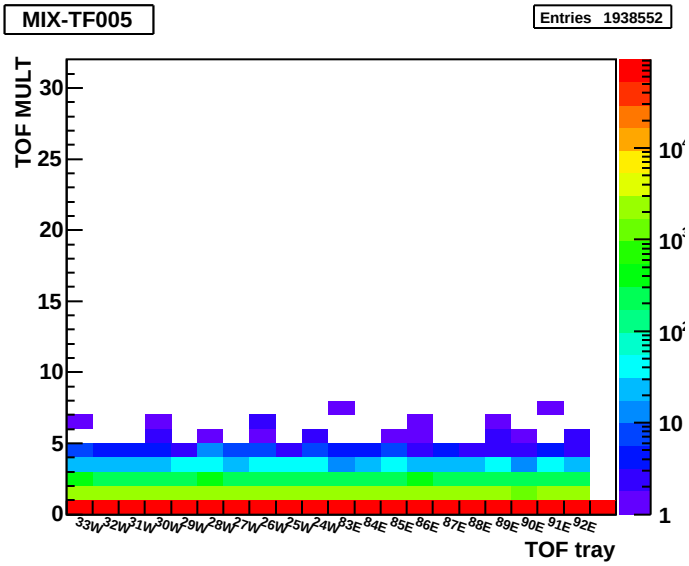

!Yellow-Fill
Entries 46156

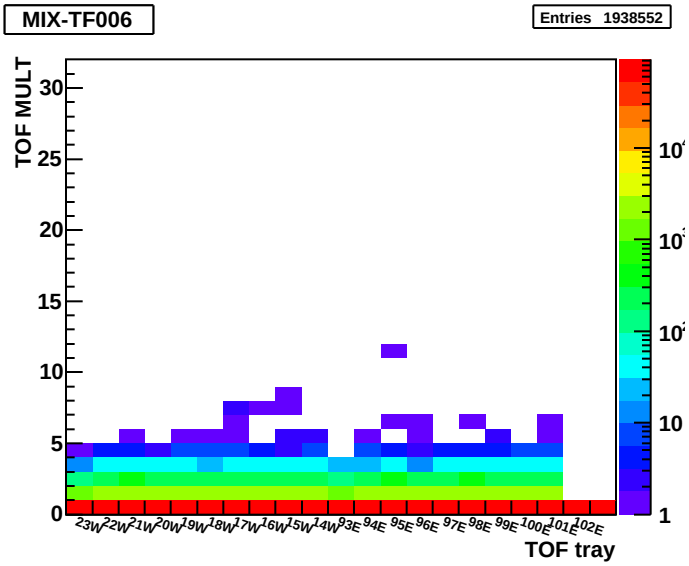

MIX-TF001
Entries 1938552
TOF MULT
TOF tray

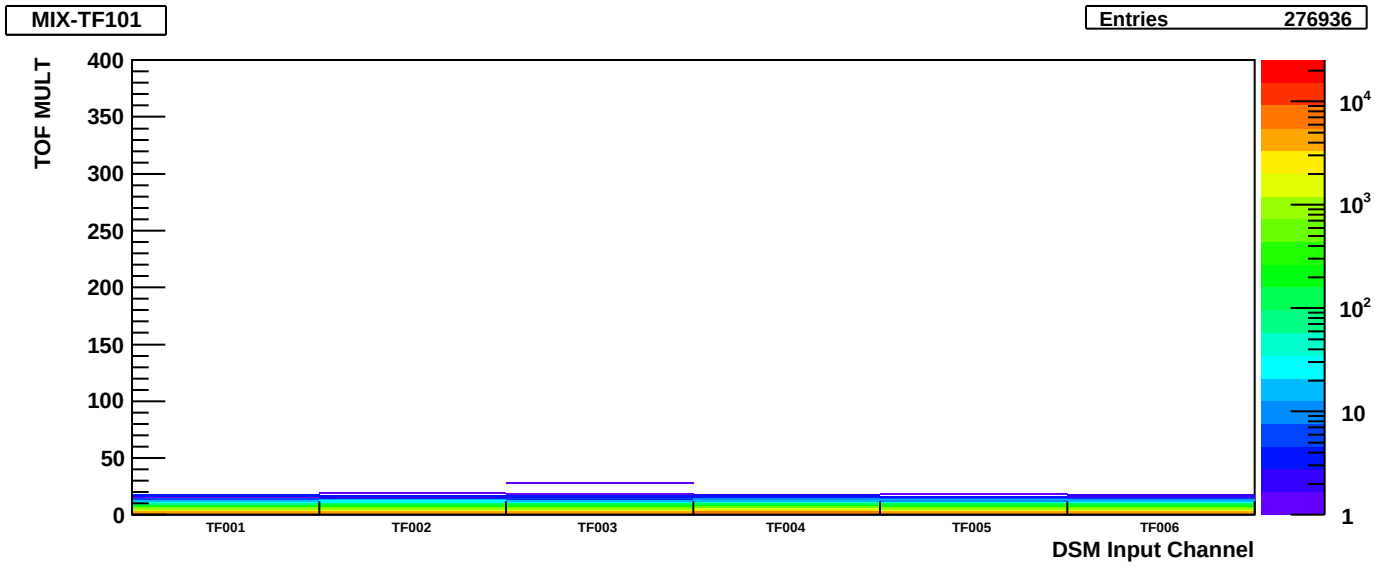

MIX-TF002
Entries 1938552
TOF MULT
TOF tray

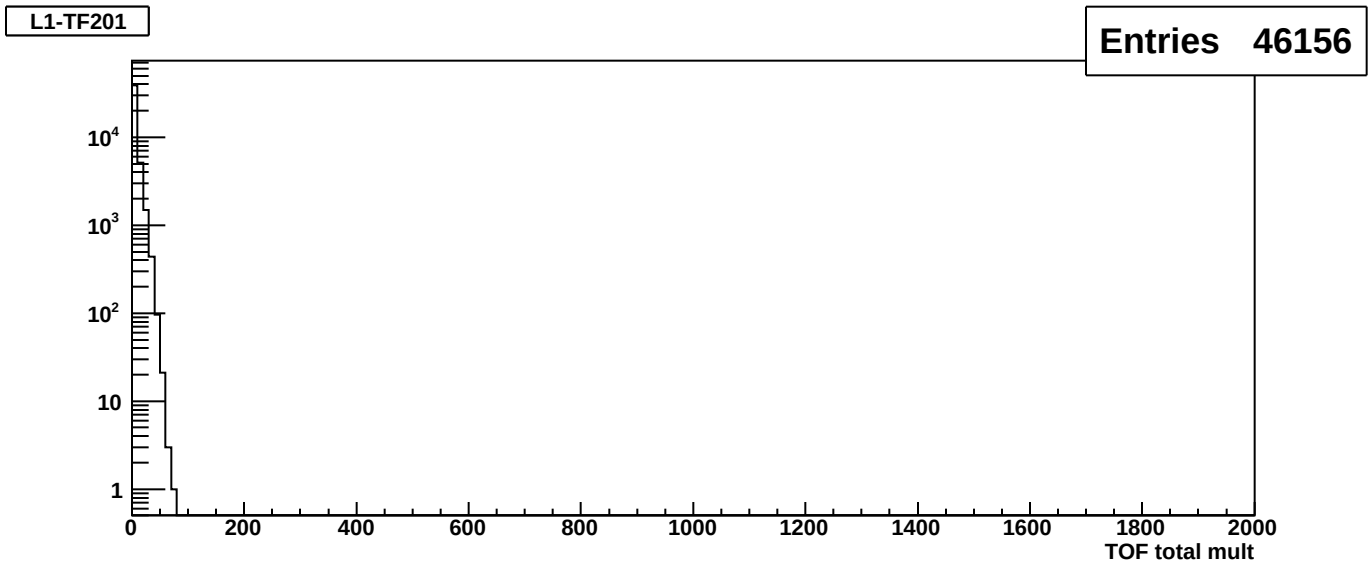

MIX-TF003
Entries 1938552
TOF MULT
TOF tray

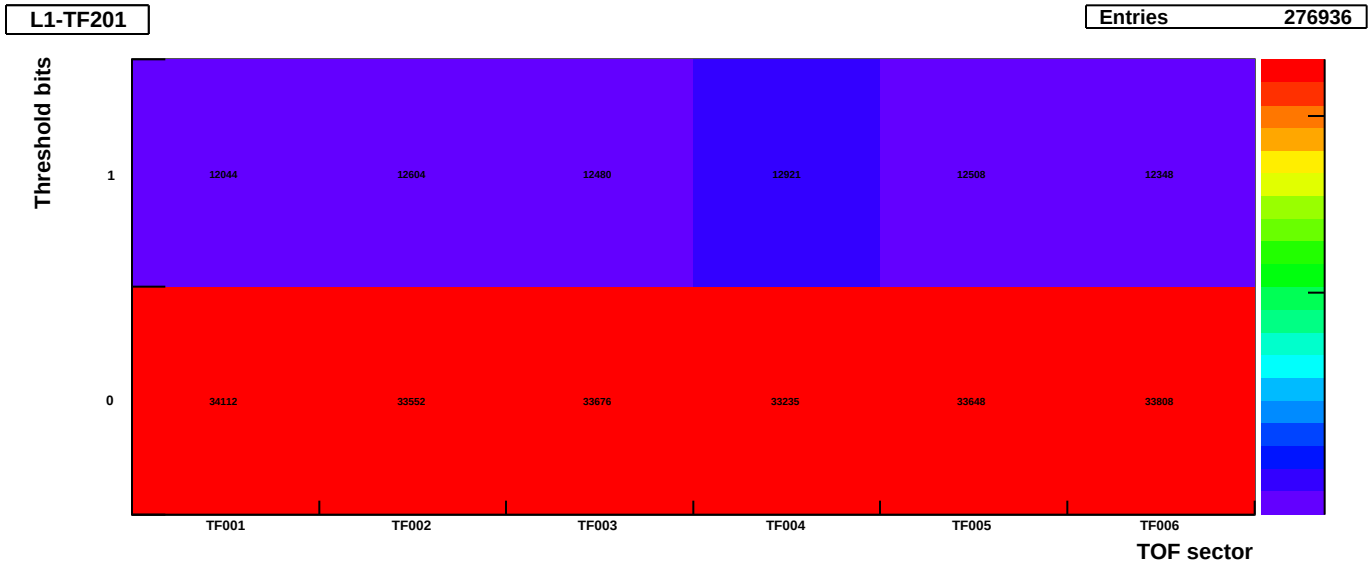

MIX-TF004
Entries 1938552
TOF MULT
TOF tray

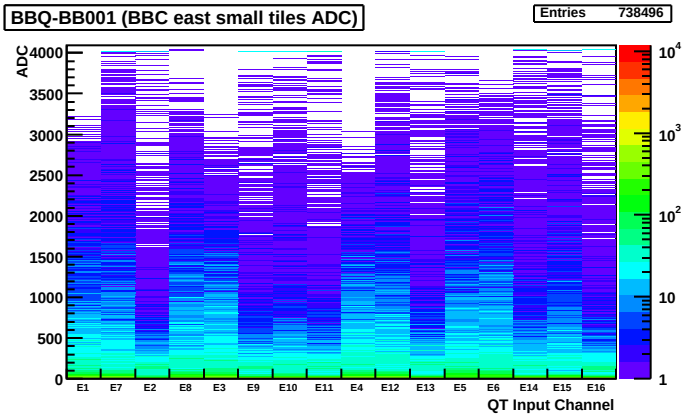

MIX-TF005
Entries 1938552
TOF MULT
TOF tray

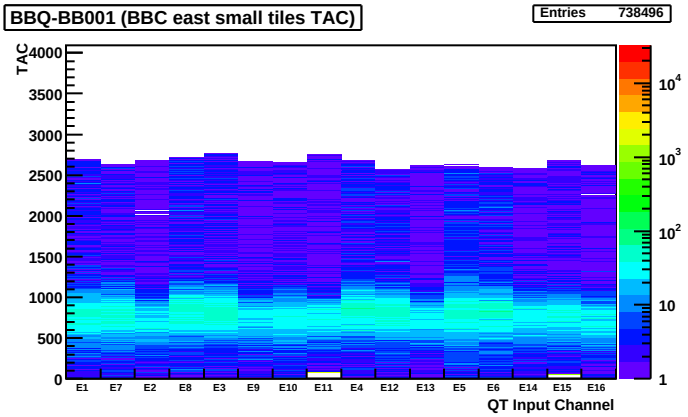

MIX-TF006
Entries 1938552
TOF MULT
TOF tray

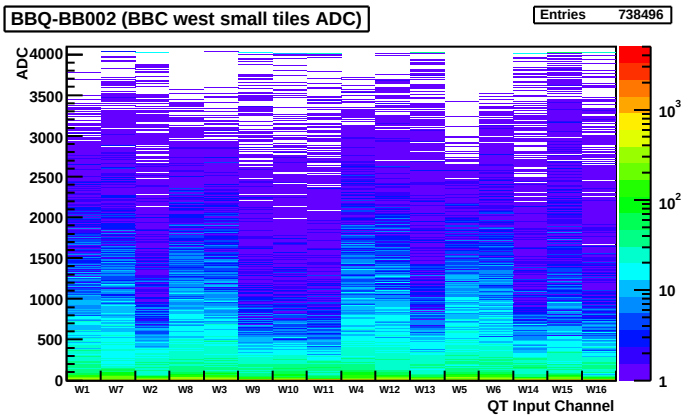

MIX-TF101
Entries 276936
TOF MULT
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

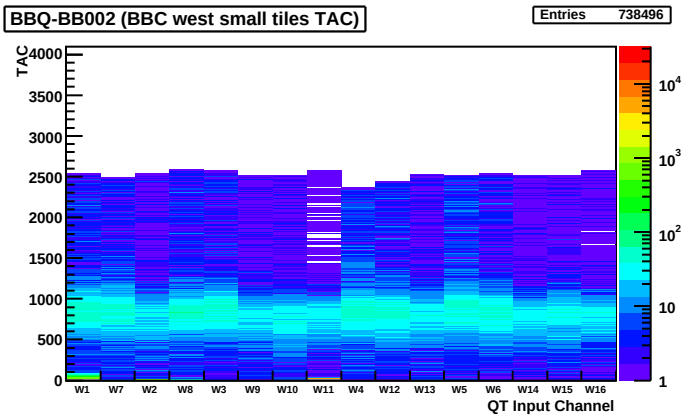

L1-TF201
Entries 46156
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
1600
1800
2000
TOF total mult

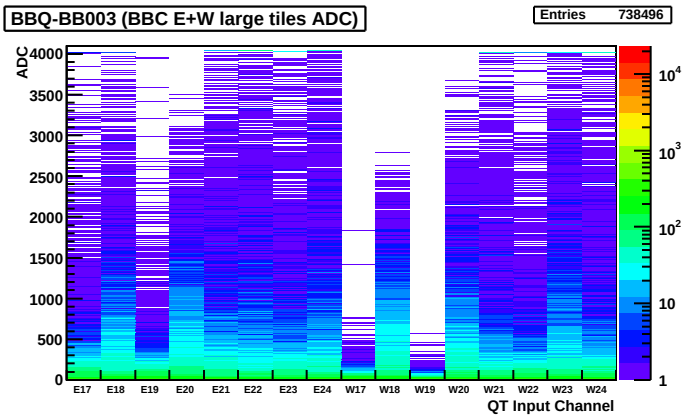

L1-TF201
Entries 276936
Threshold bits
1
0
12044
12604
12480
12921
12508
12348
34112
33552
33676
33235
33648
33808
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

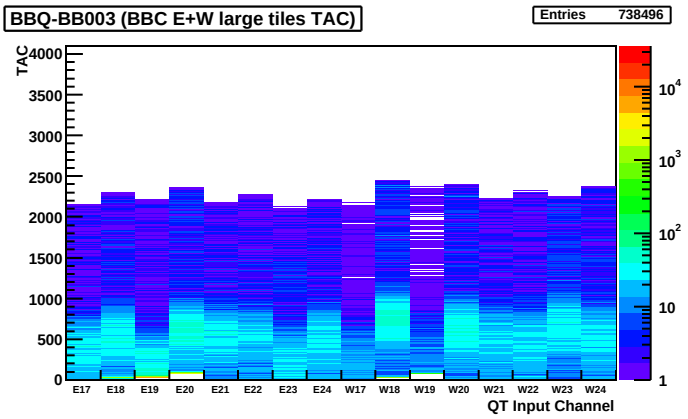

BBQ-BB001 (BBC east small tiles ADC)
Entries 738496
ADC
QT Input Channel

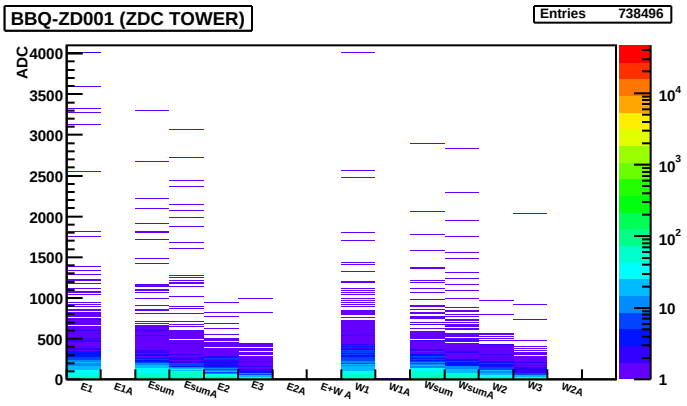

BBQ-BB001 (BBC east small tiles TAC)
Entries 738496
TAC
QT Input Channel

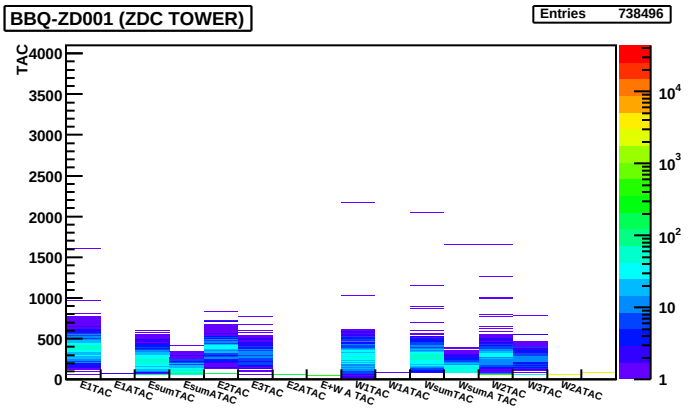

BBQ-BB002 (BBC west small tiles ADC)
Entries 738496
ADC
QT Input Channel

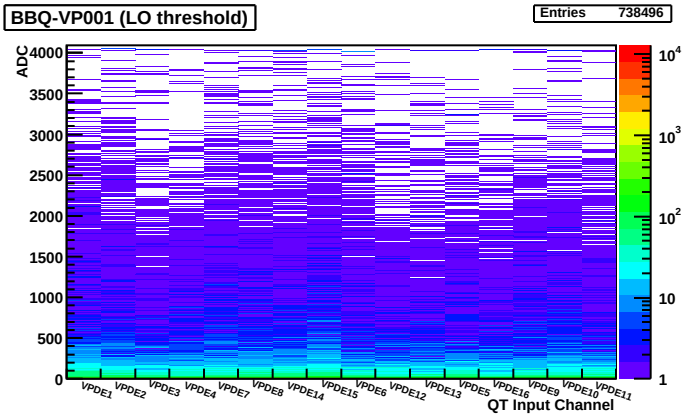

BBQ-BB002 (BBC west small tiles TAC)
Entries 738496
TAC
QT Input Channel

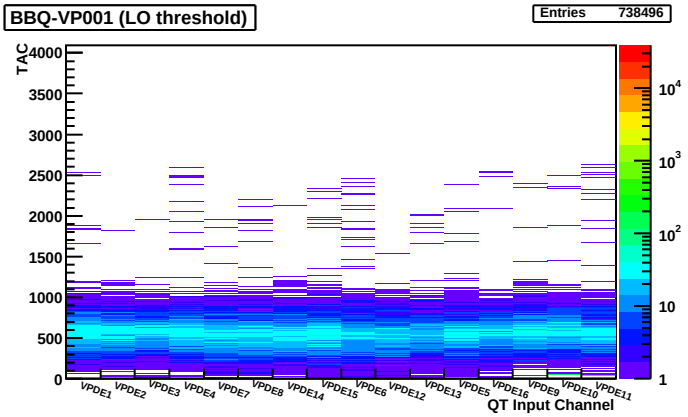

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 738496
ADC
QT Input Channel

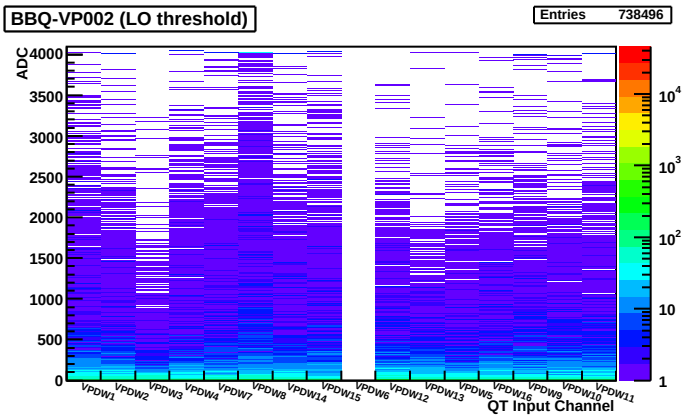

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 738496
TAC
QT Input Channel

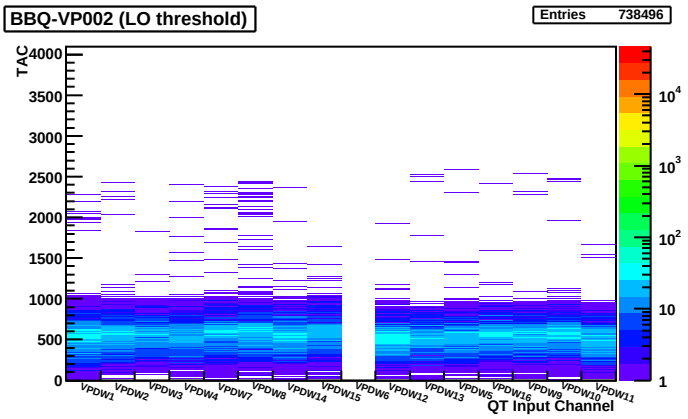

BBQ-ZD001 (ZDC TOWER)
Entries 738496
ADC

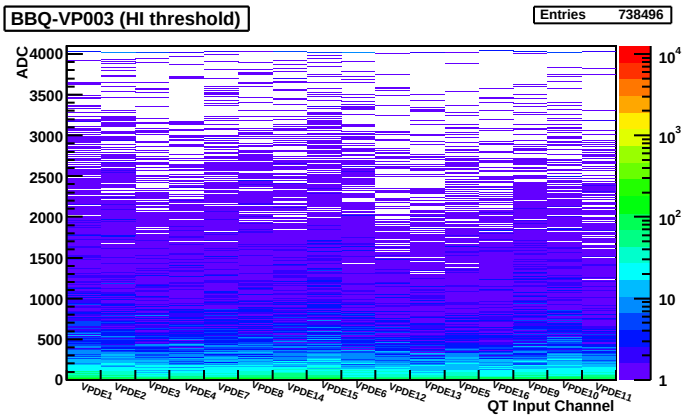

BBQ-ZD001 (ZDC TOWER)
Entries 738496
TAC

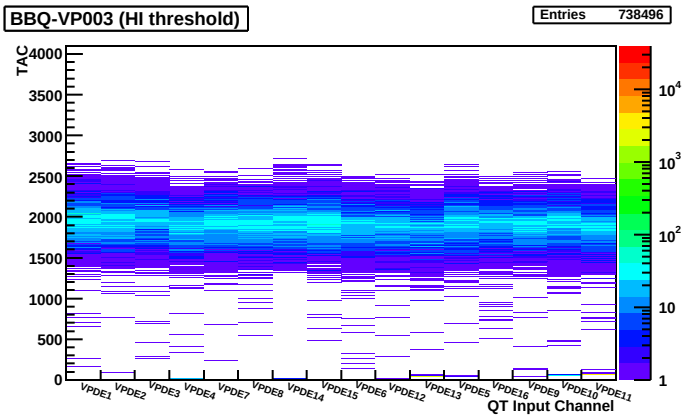

BBQ-VP001 (LO threshold)
Entries 738496
ADC
QT Input Channel

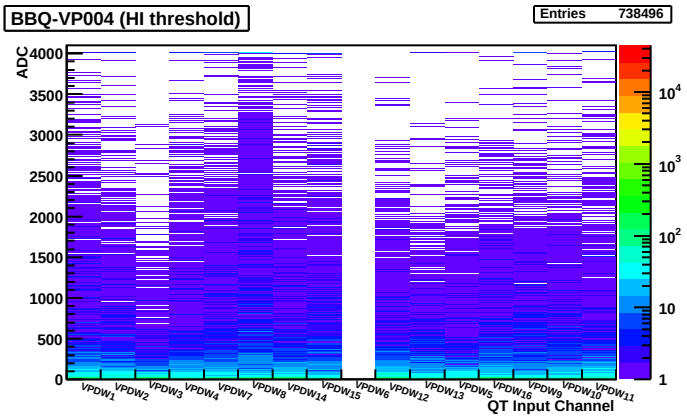

BBQ-VP001 (LO threshold)
Entries 738496
TAC
QT Input Channel

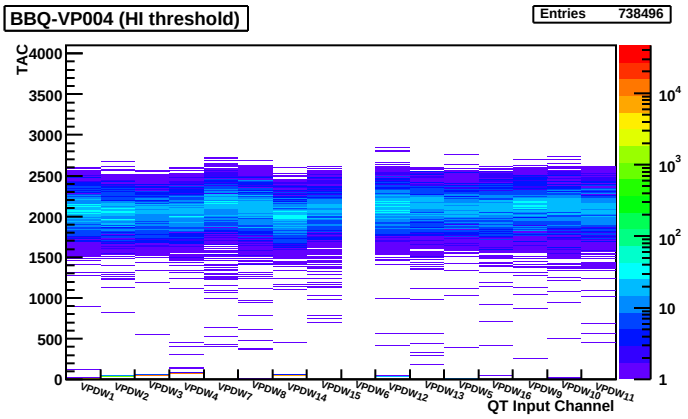

BBQ-VP002 (LO threshold)
Entries 738496
ADC
QT Input Channel

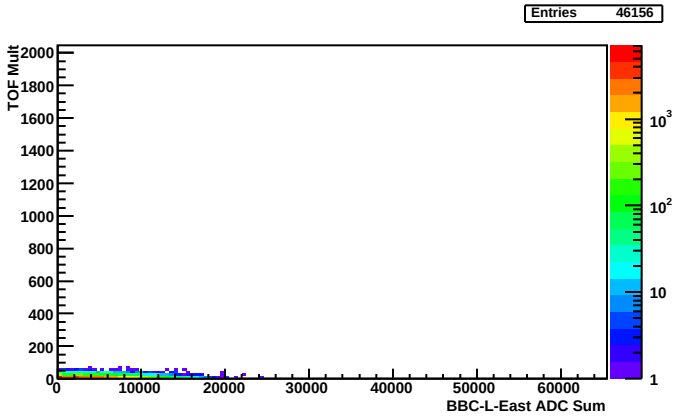

BBQ-VP002 (LO threshold)
Entries 738496
TAC
QT Input Channel

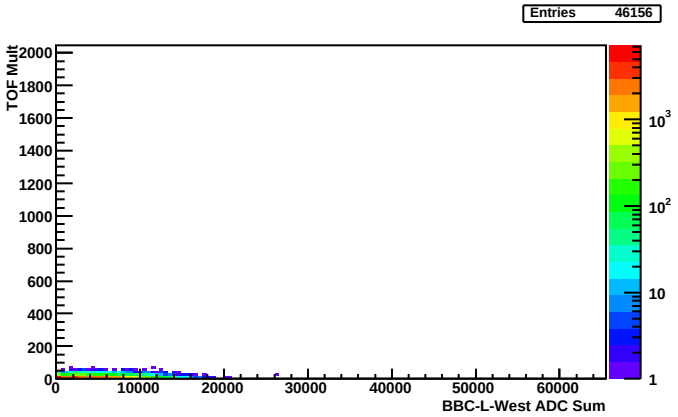

BBQ-VP003 (HI threshold)
Entries 738496
ADC
QT Input Channel

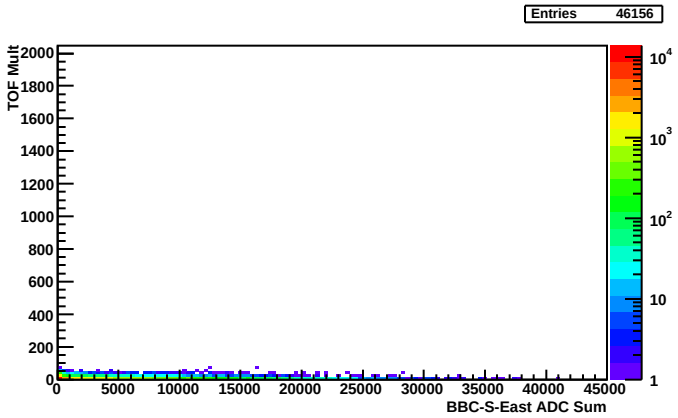

BBQ-VP003 (HI threshold)
Entries 738496
TAC
QT Input Channel

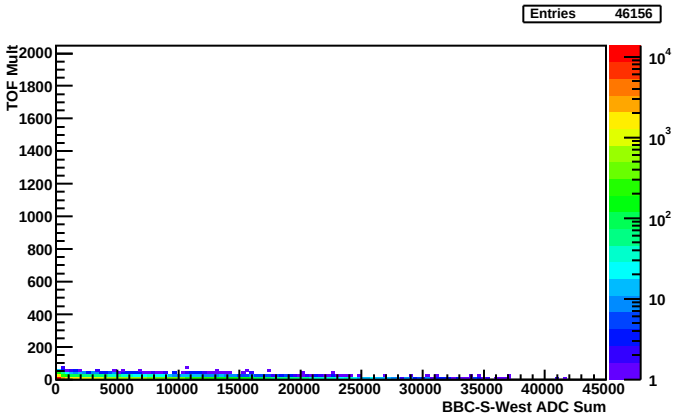

BBQ-VP004 (HI threshold)
Entries 738496
ADC
QT Input Channel

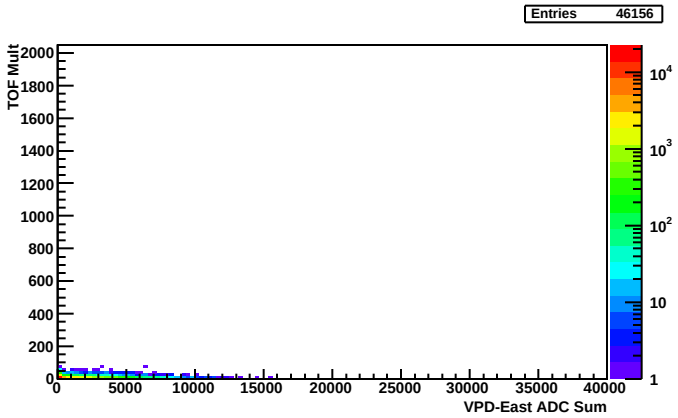

BBQ-VP004 (HI threshold)
Entries 738496
TAC
QT Input Channel

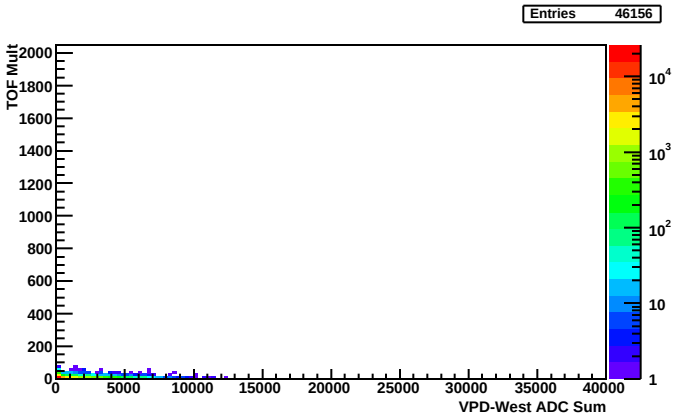

Entries 46156
TOF Mult
BBC-L-East ADC Sum

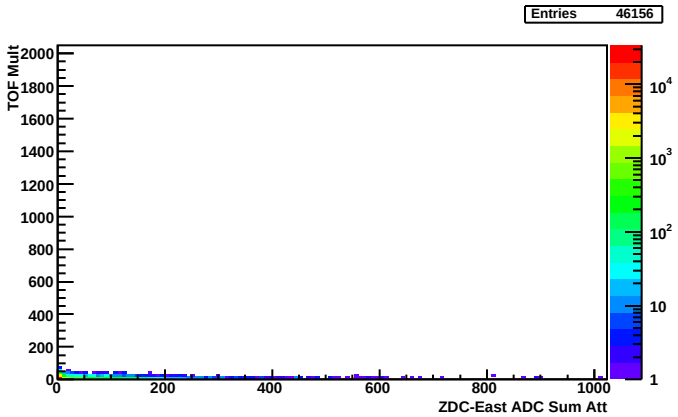

Entries 46156
TOF Mult
BBC-L-West ADC Sum

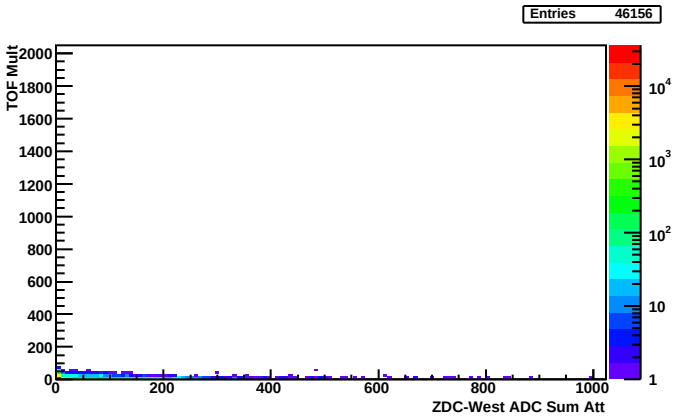

Entries 46156
TOF Mult
BBC-S-East ADC Sum

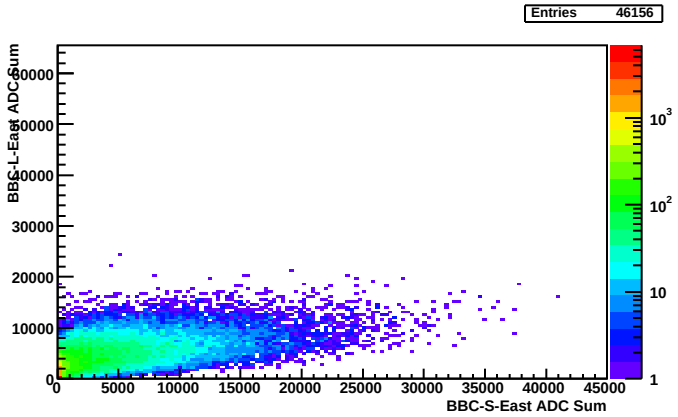

Entries 46156
TOF Mult
BBC-S-West ADC Sum

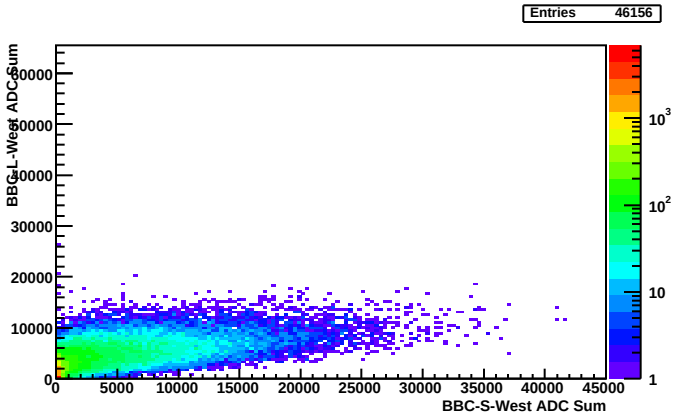

Entries 46156
TOF Mult
VPD-East ADC Sum

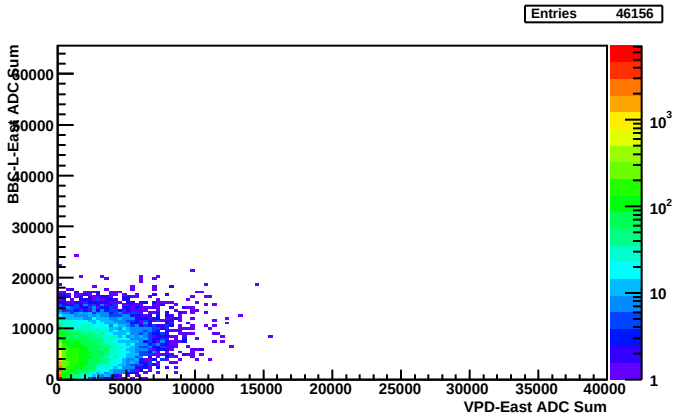

Entries 46156
TOF Mult
VPD-West ADC Sum

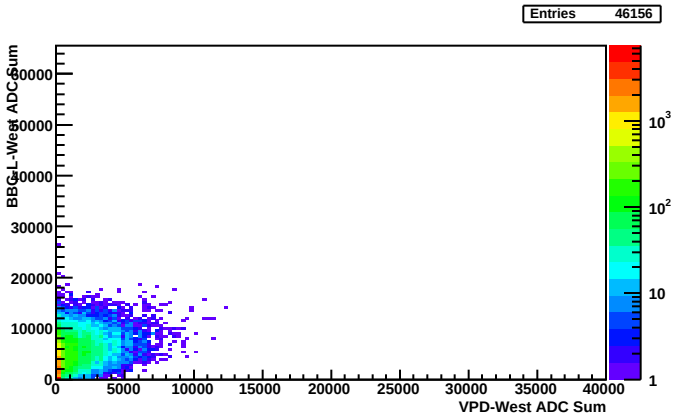

Entries 46156
TOF Mult
ZDC-East ADC Sum Att

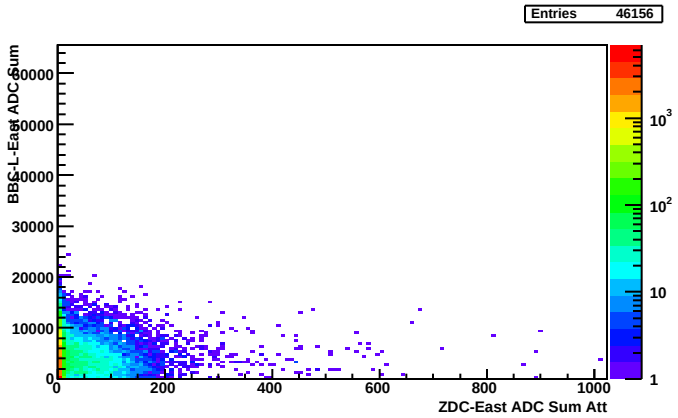

Entries 46156
TOF Mult
ZDC-West ADC Sum Att

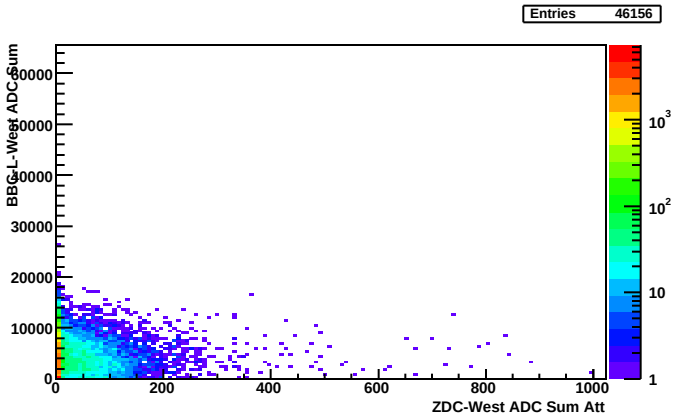

Entries 46156
BBC-L-East ADC Sum
BBC-S-East ADC Sum

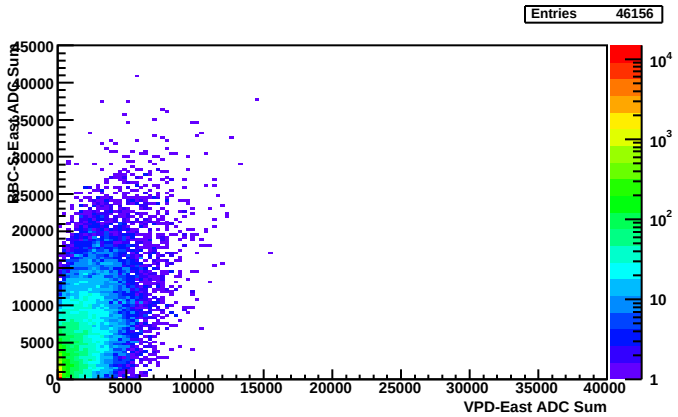

Entries 46156
BBC-L-West ADC Sum
BBC-S-West ADC Sum

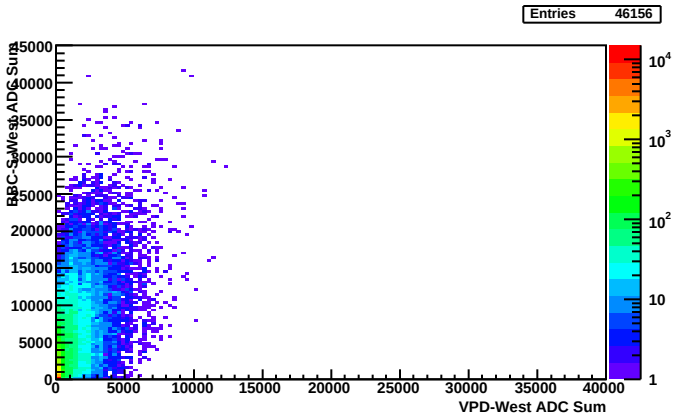

Entries 46156
BBC-L-East ADC Sum
VPD-East ADC Sum

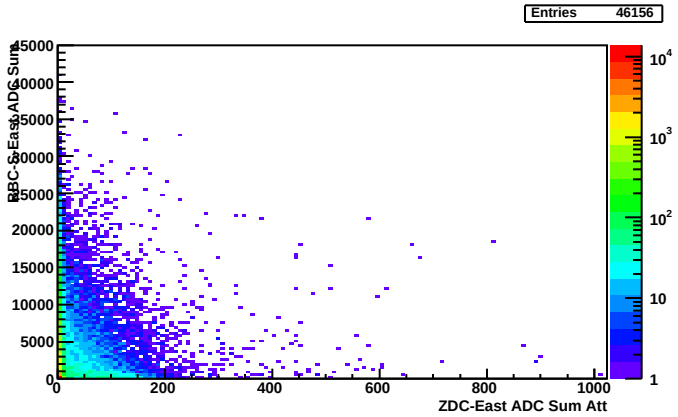

Entries 46156
BBC-L-West ADC Sum
VPD-West ADC Sum

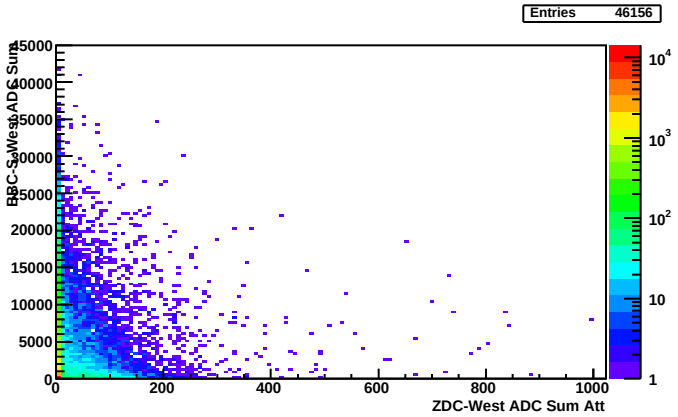

Entries 46156
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

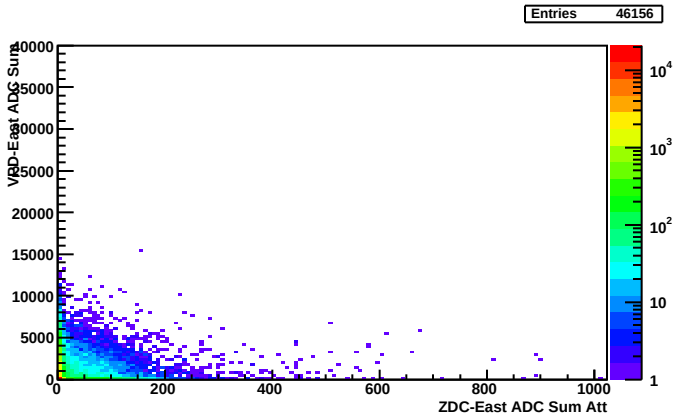

Entries 46156
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

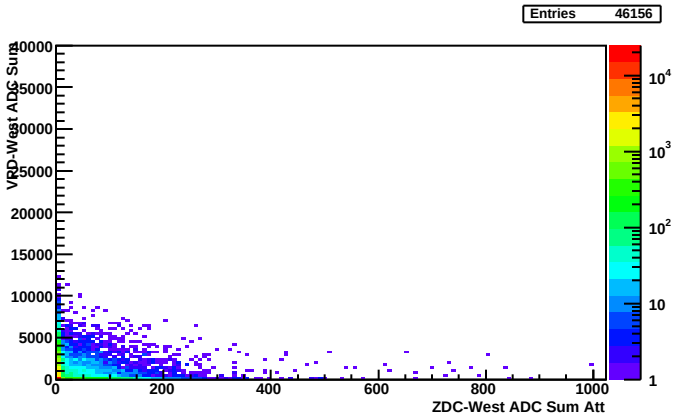

Entries 46156
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 46156
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 46156
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 46156
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 46156
VPD-East ADC Sum
ZDC-East ADC Sum Att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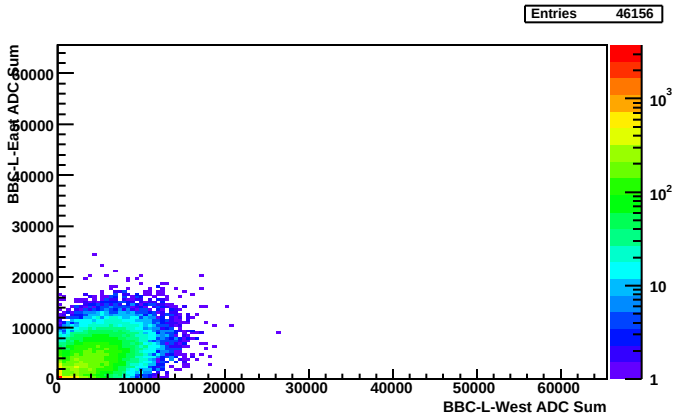
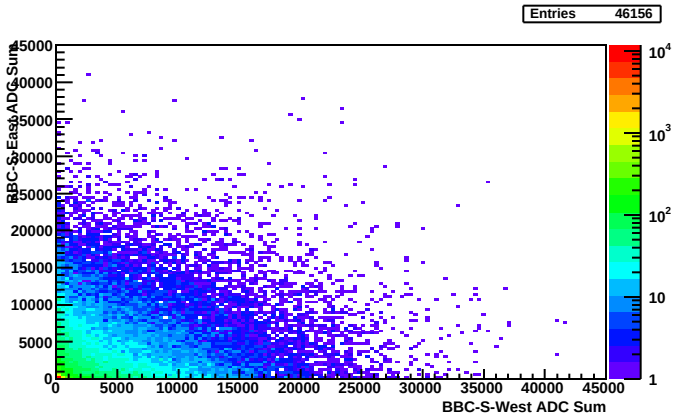
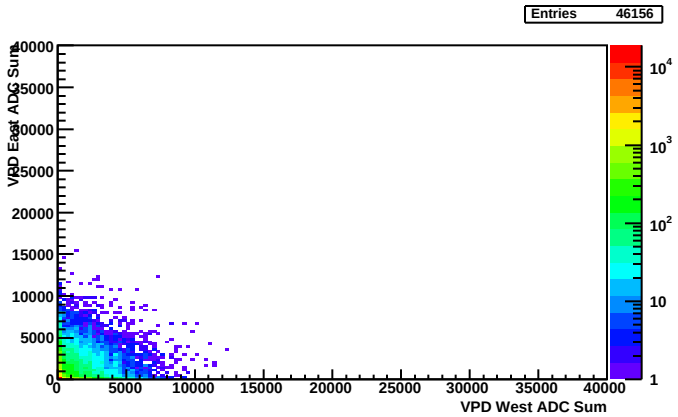
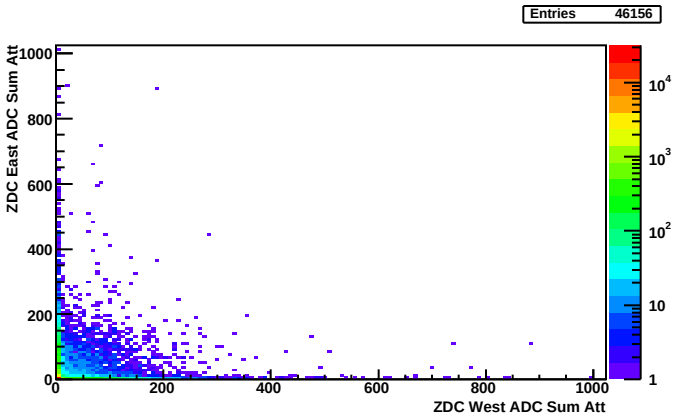
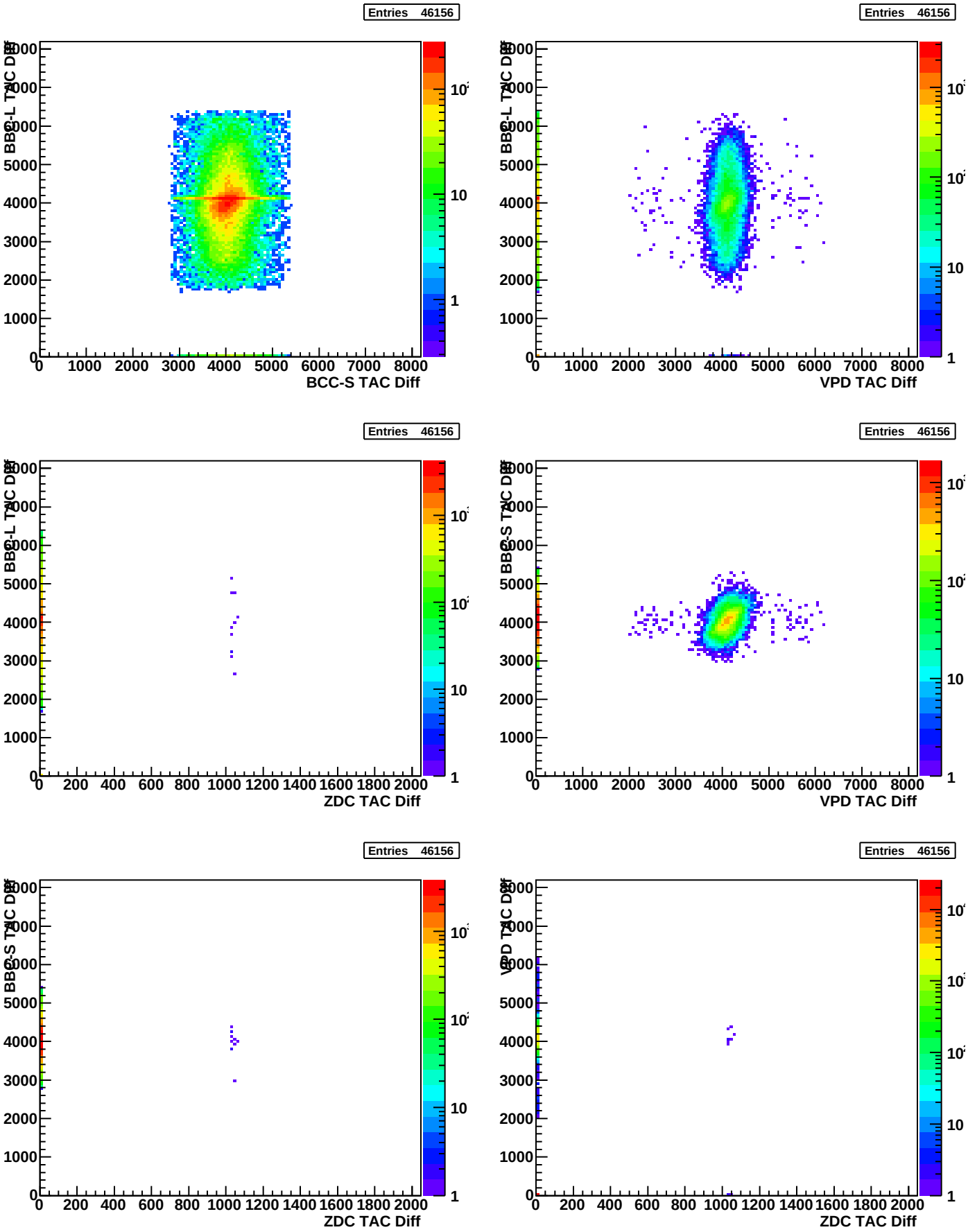
VPD-West ADC Sum
ZDC-West ADC Sum Att

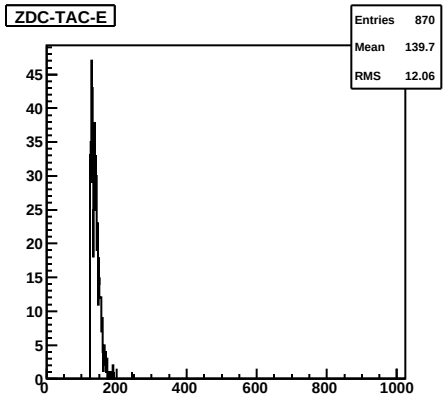

Entries 46156
BBC-L-East ADC Sum
BBC-L-West ADC Sum

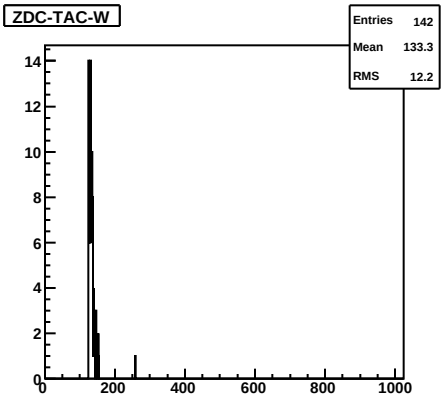

Entries 46156
BBC-S-East ADC Sum
BBC-S-West ADC Sum

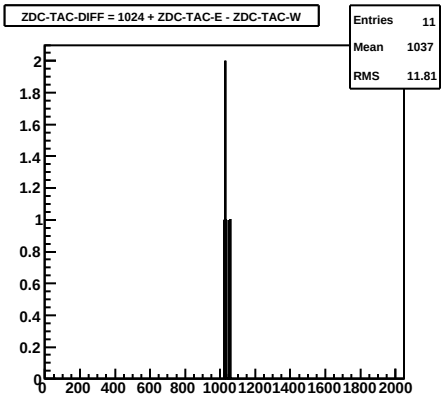

Entries 46156
VPD East ADC Sum
VPD West ADC Sum

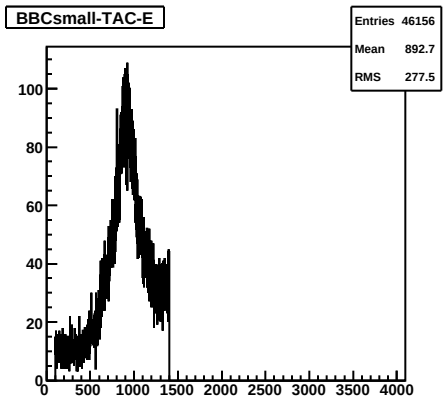

Entries 46156
ZDC East ADC Sum Att
ZDC West ADC Sum Att

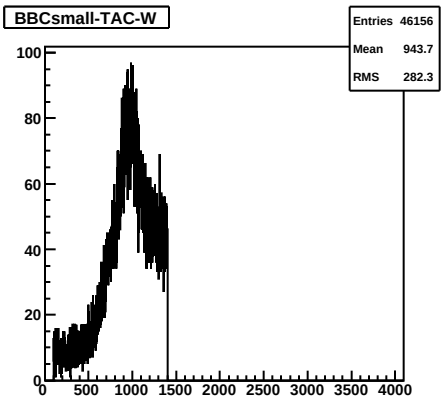

Entries 46156
BBC-L TAC Diff
BCC-S TAC Diff
Entries 46156
BBC-L TAC Diff
VPD TAC Diff
Entries 46156
BBC-L TAC Diff
ZDC TAC Diff
Entries 46156
BBC-S TAC Diff
VPD TAC Diff
Entries 46156
BBC-S TAC Diff
ZDC TAC Diff
Entries 46156
VPD TAC Diff
ZDC TAC Diff

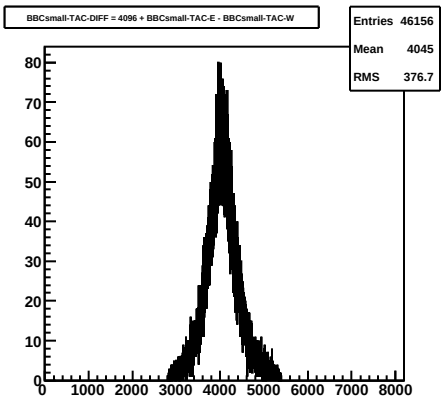

ZDC-TAC-E
Entries 870
Mean 139.7
RMS 12.06

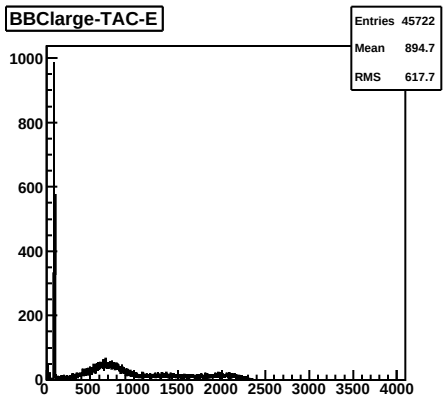

ZDC-TAC-W
Entries 142
Mean 133.3
RMS 12.2

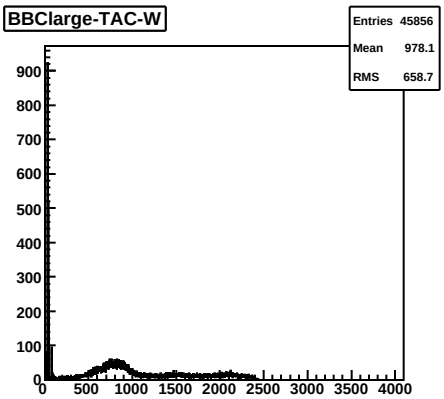

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 11
Mean 1037
RMS 11.81

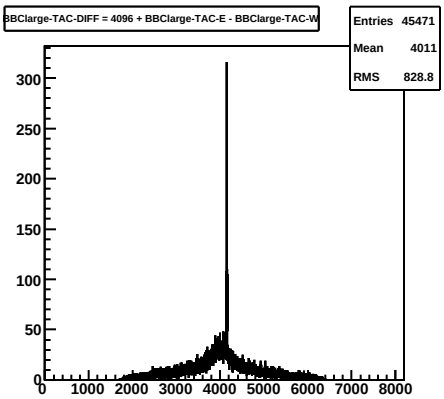

BBCsmall-TAC-E
Entries 46156
Mean 892.7
RMS 277.5

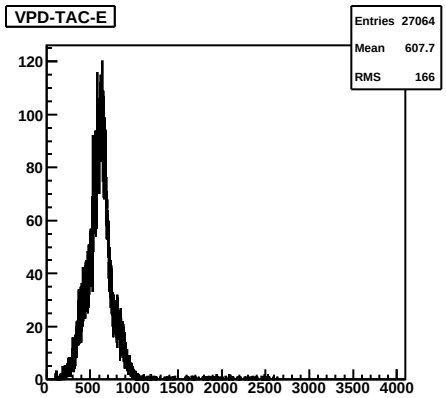

BBCsmall-TAC-W
Entries 46156
Mean 943.7
RMS 282.3

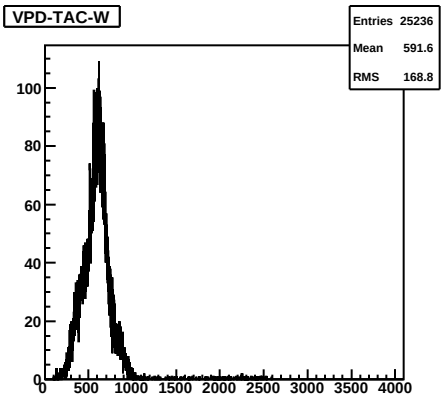

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 46156
Mean 4045
RMS 376.7

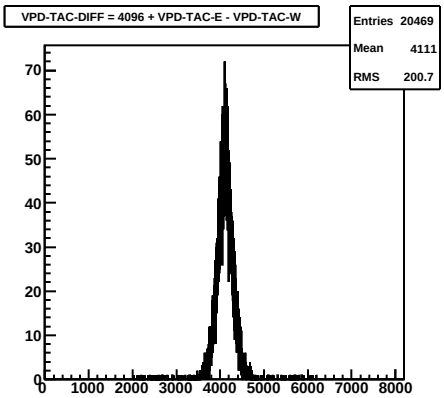

BBClarge-TAC-E
Entries 45722
Mean 894.7
RMS 617.7

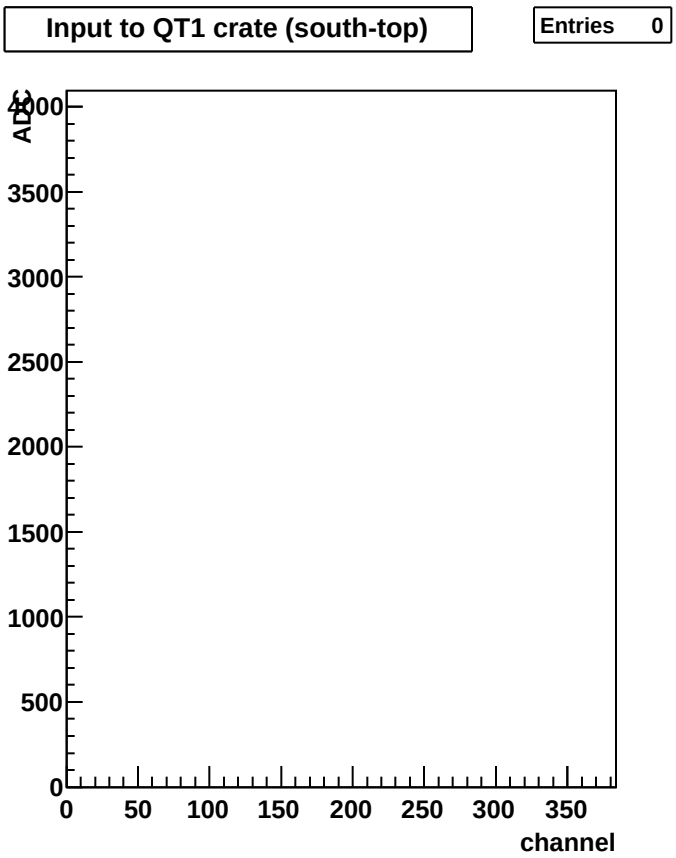

BBClarge-TAC-W
Entries 45856
Mean 978.1
RMS 658.7

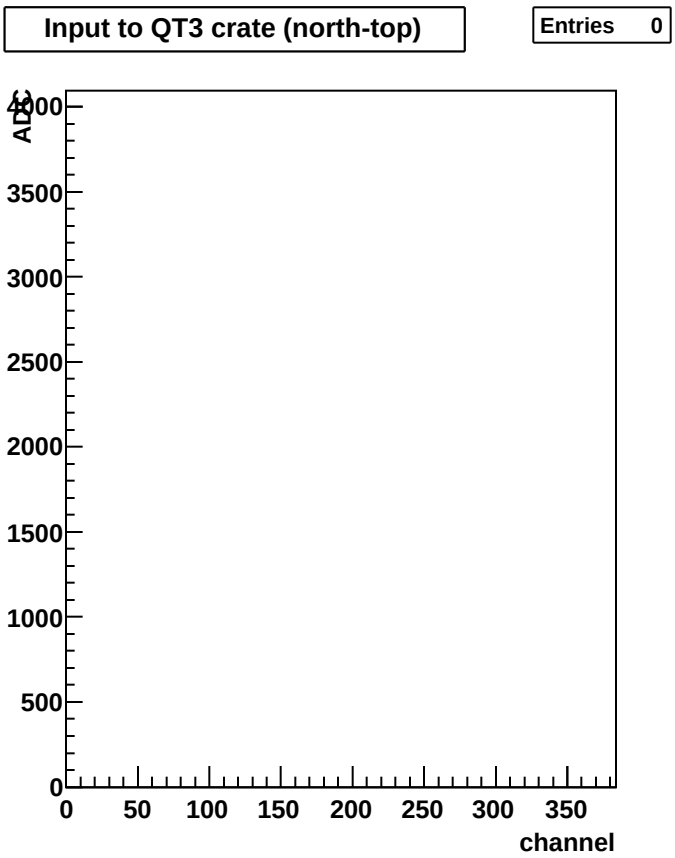

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 45471
Mean 4011
RMS 828.8

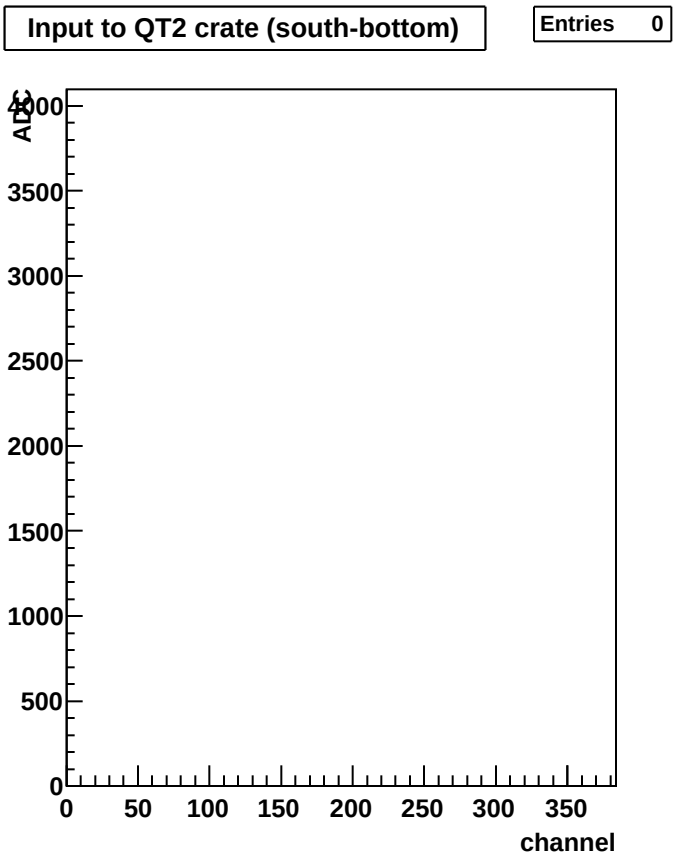

VPD-TAC-E
Entries 27064
Mean 607.7
RMS 166

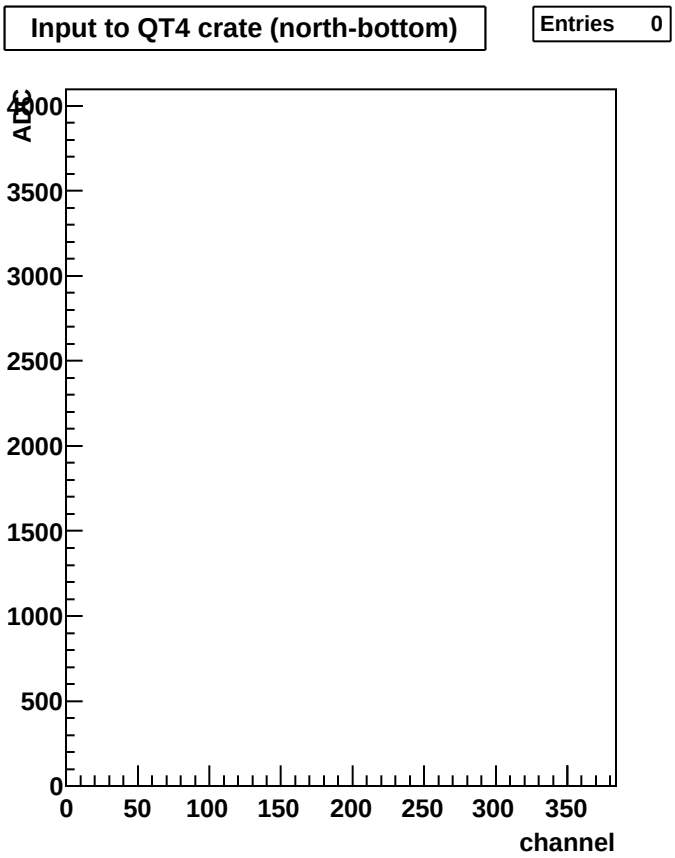

VPD-TAC-W
Entries 25236
Mean 591.6
RMS 168.8

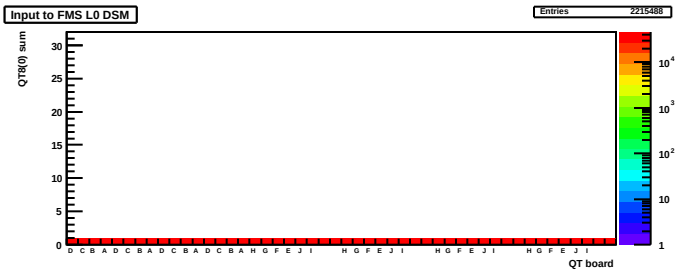

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 20469
Mean 4111
RMS 200.7

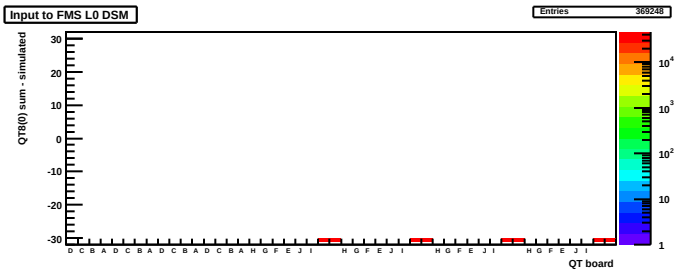

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

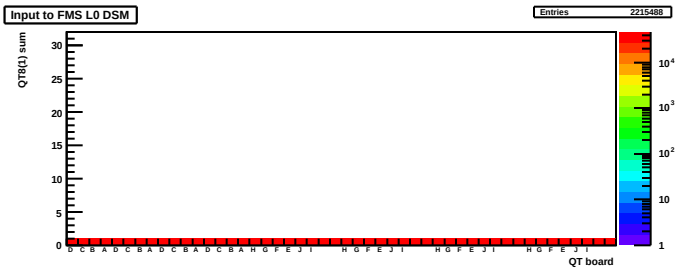

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

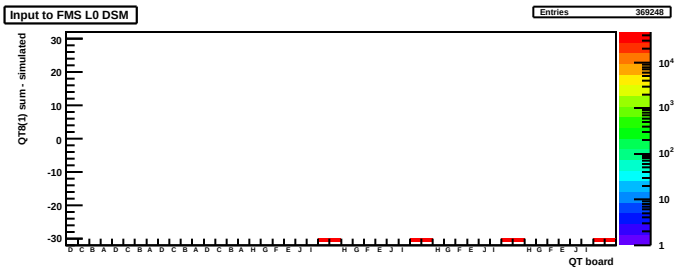

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

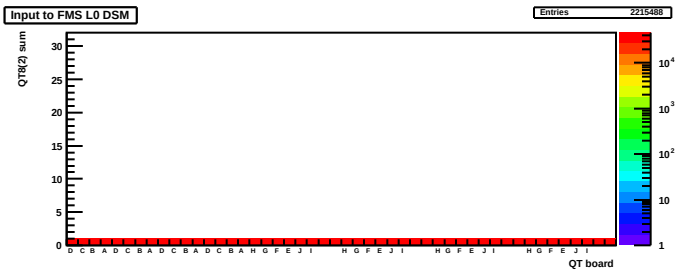

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

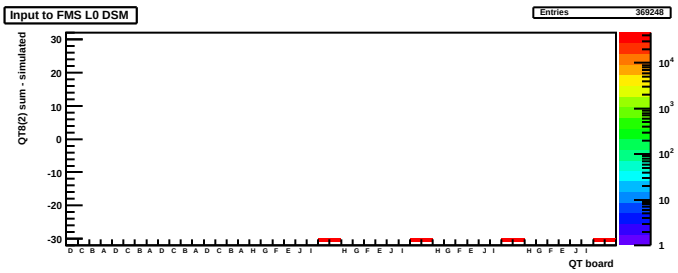

Input to FMS L0 DSM
Entries 2215488
QT8(0) sum
QT board

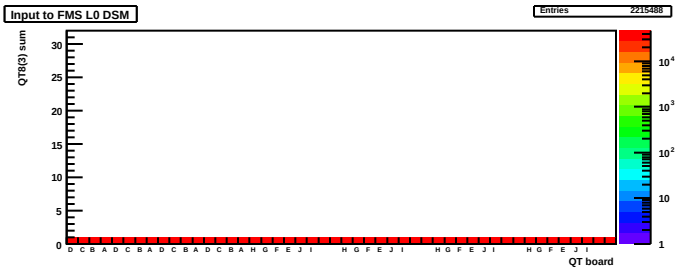

Input to FMS L0 DSM
Entries 369248
QT8(0) sum - simulated
QT board

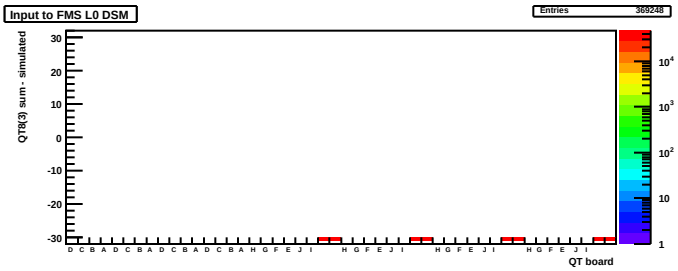

Input to FMS L0 DSM
Entries 2215488
QT8(1) sum
QT board

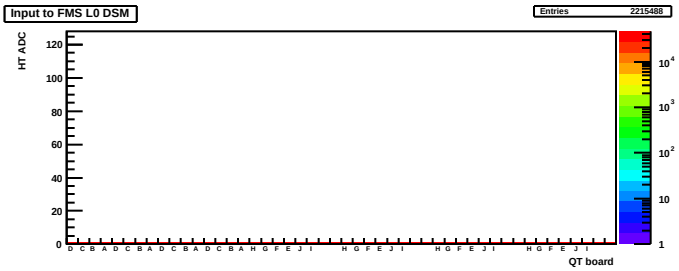

Input to FMS L0 DSM
Entries 369248
QT8(1) sum - simulated
QT board

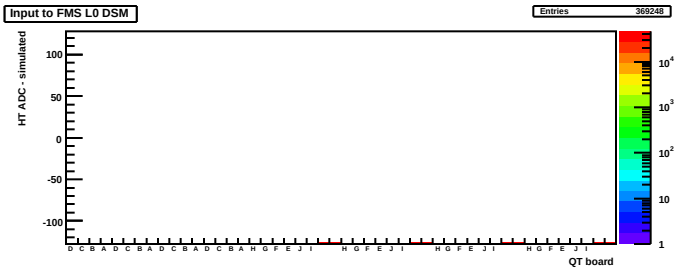

Input to FMS L0 DSM
Entries 2215488
QT8(2) sum
QT board

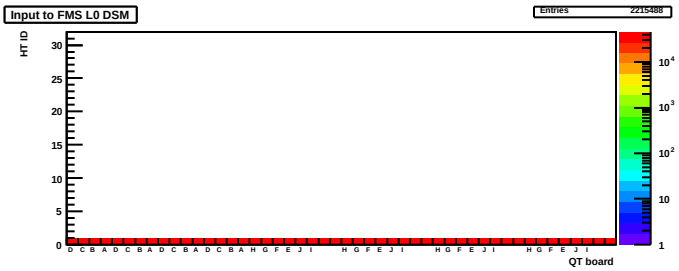

Input to FMS L0 DSM
Entries 369248
QT8(2) sum - simulated
QT board

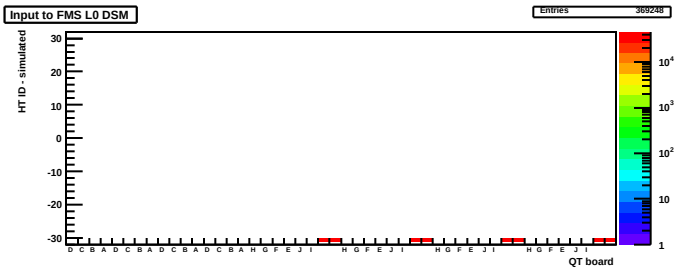

Input to FMS L0 DSM
Entries 2215488
QT8(3) sum
QT board

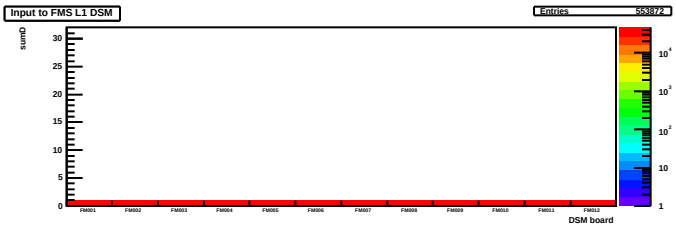

Input to FMS L0 DSM
Entries 369248
QT8(3) sum - simulated
QT board

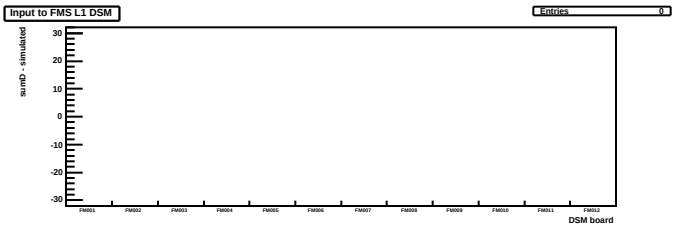

Input to FMS L0 DSM
Entries 2215488
HT ADC
QT board

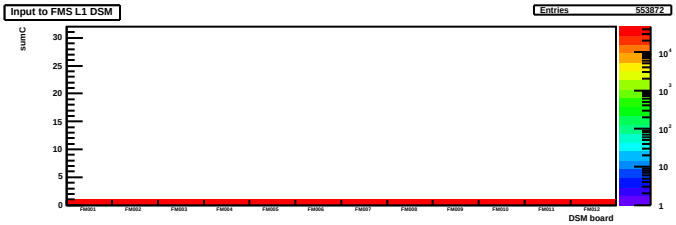

Input to FMS L0 DSM
Entries 369248
HT ADC - simulated
QT board

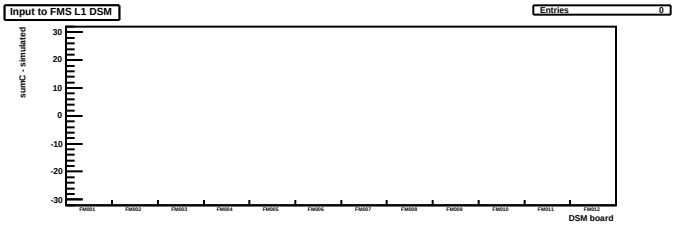

Input to FMS L0 DSM
Entries 2215488
HT ID
QT board

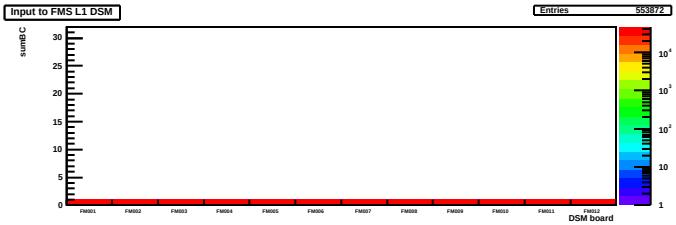

Input to FMS L0 DSM
Entries 369248
HT ID - simulated
QT board

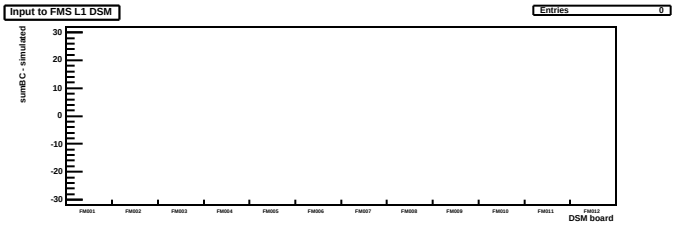

Input to FMS L1 DSM
Entries 553872
sumD
DSM board

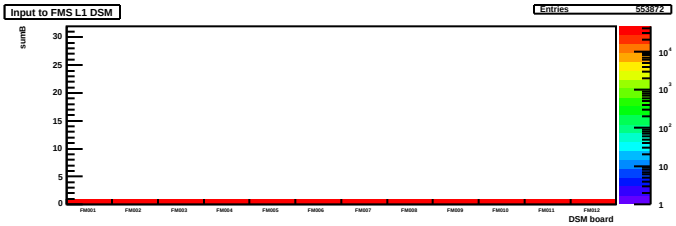

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

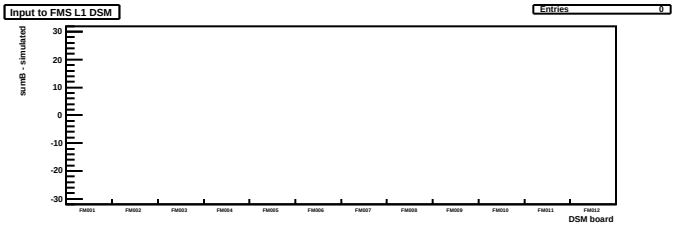

Input to FMS L1 DSM
Entries 553872
sumC
DSM board

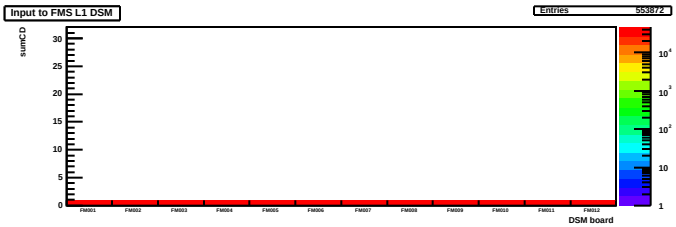

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

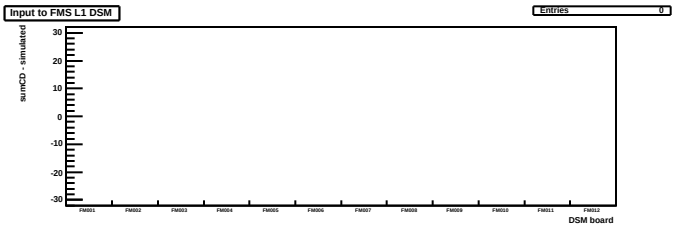

Input to FMS L1 DSM
Entries 553872
sumBC
DSM board

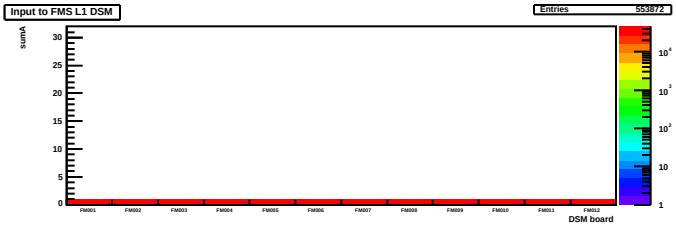

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

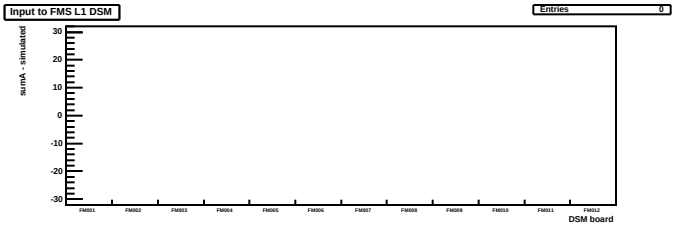

Input to FMS L1 DSM
Entries 553872
sumB
DSM board

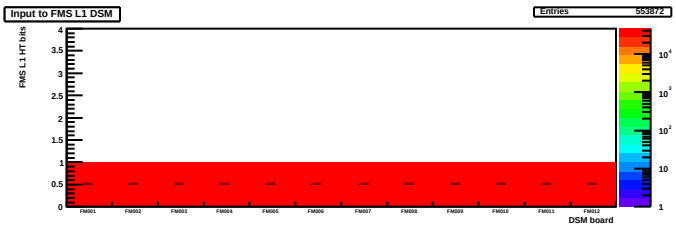

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

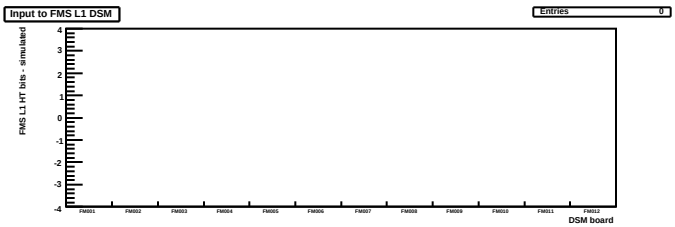

Input to FMS L1 DSM
Entries 553872
sumCD
DSM board

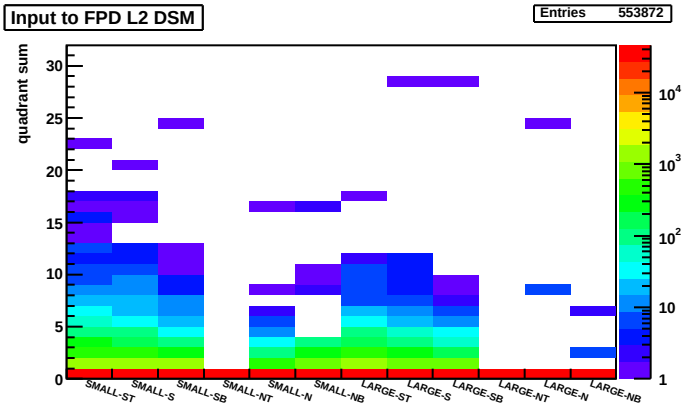

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

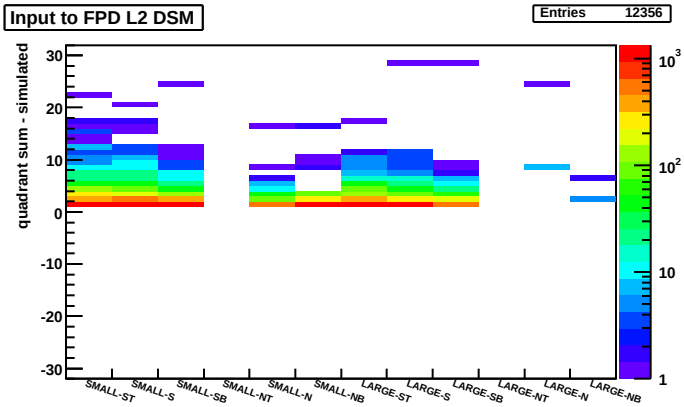

Input to FMS L1 DSM
Entries 553872
sumA
DSM board

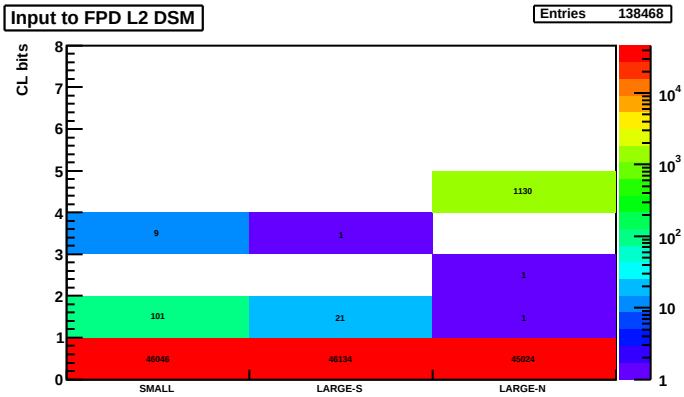

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

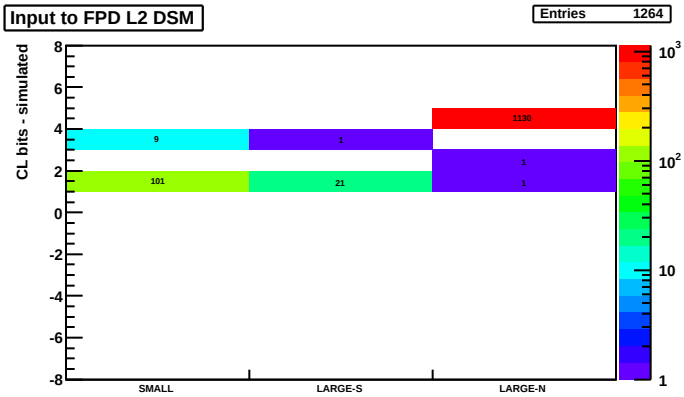

Input to FMS L1 DSM
Entries 553872
FMS L1 HT bits
DSM board

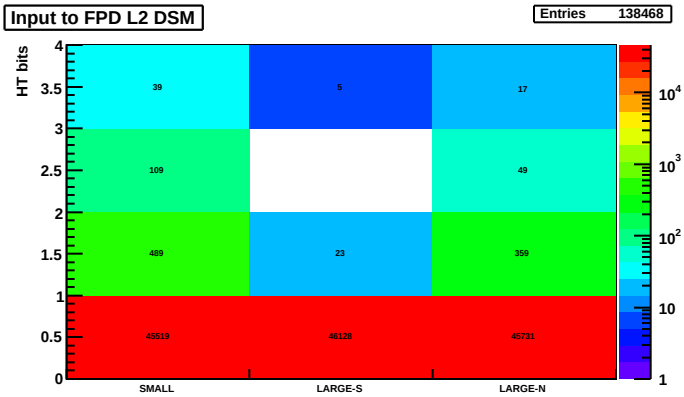

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

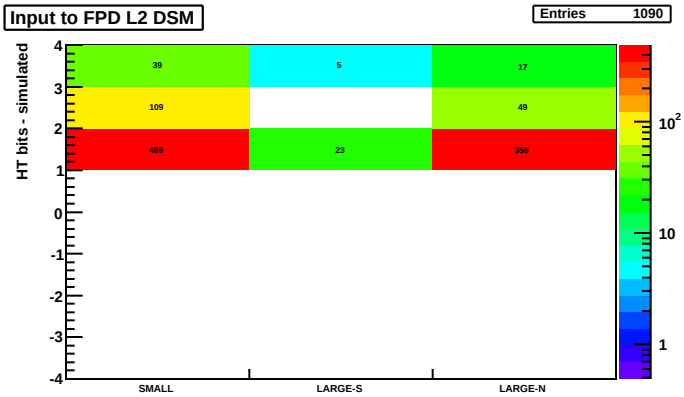

Input to FPD L2 DSM
Entries 553872
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

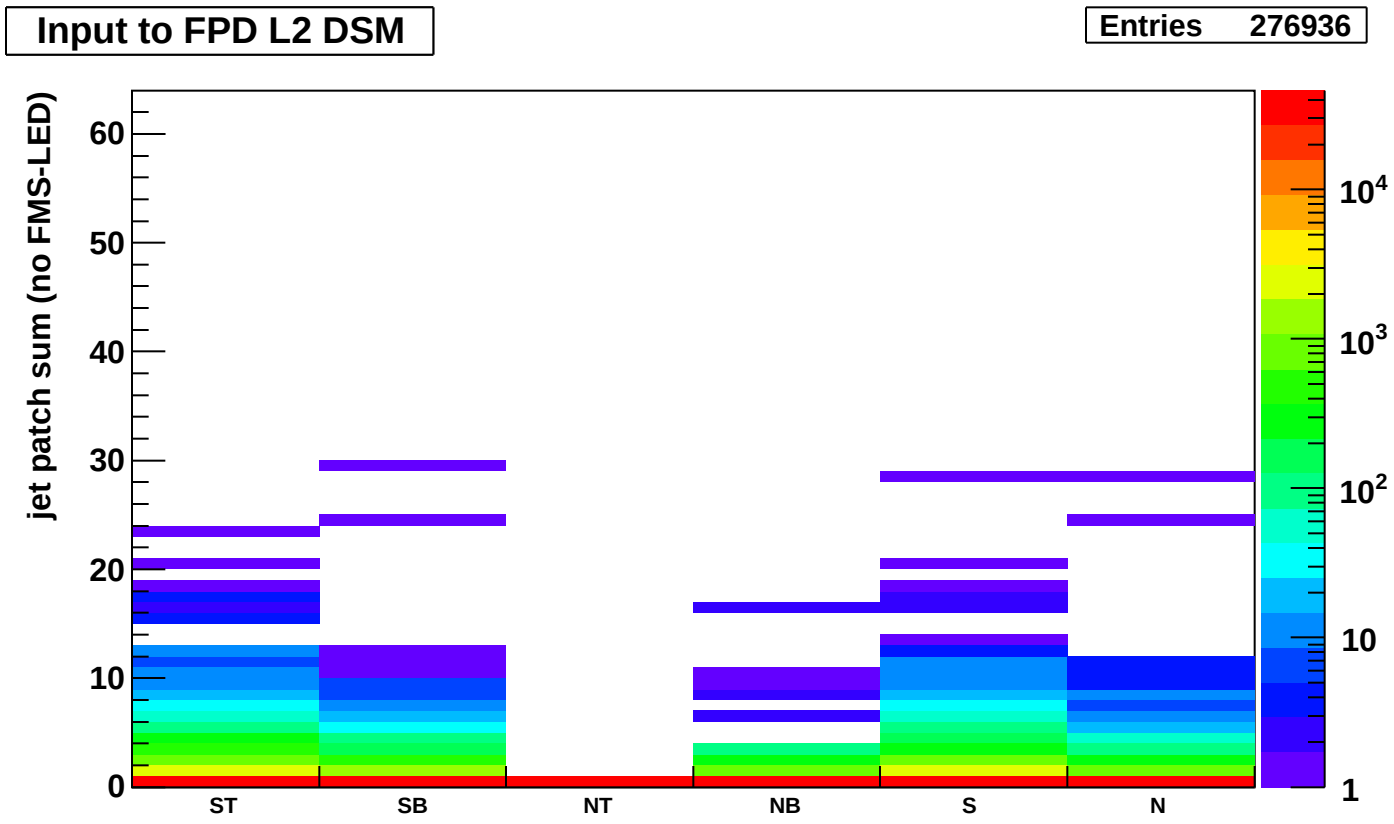

Input to FPD L2 DSM
Entries 12356
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

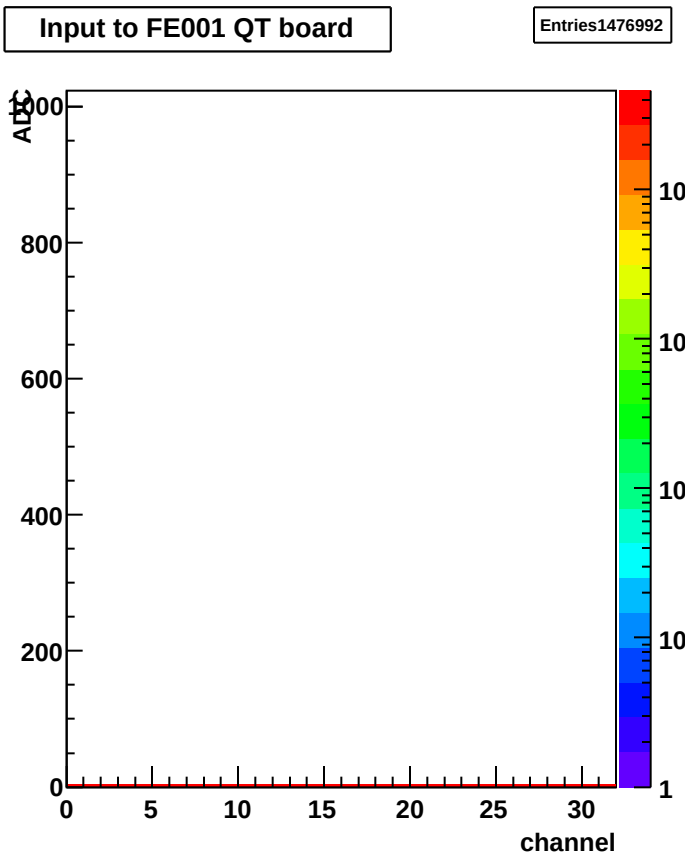

Input to FPD L2 DSM
Entries 138468
CL bits
9
1
1130
1
101
21
1
46046
46134
45024
SMALL
LARGE-S
LARGE-N

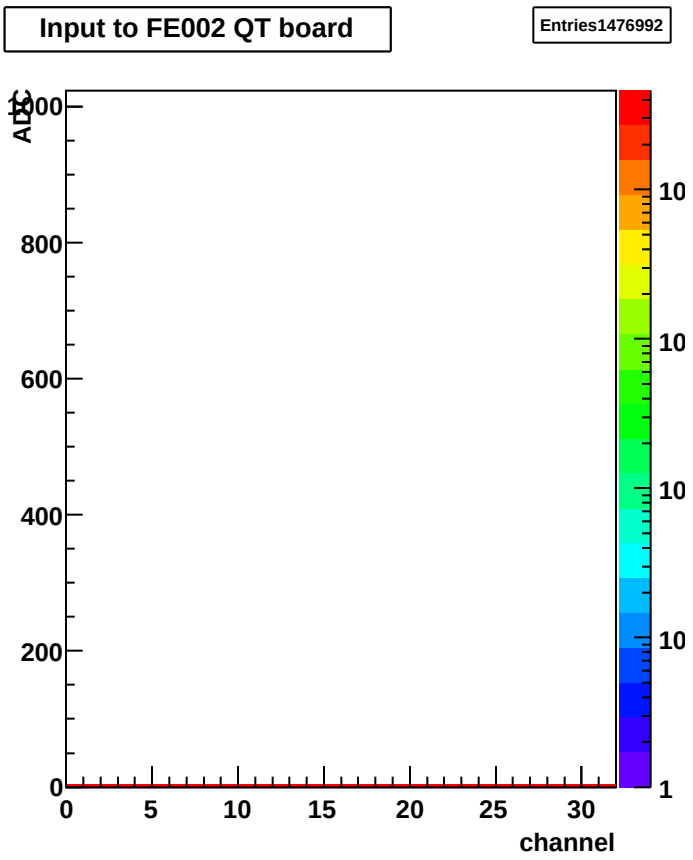

Input to FPD L2 DSM
Entries 1264
CL bits - simulated
9
1
1130
1
101
21
1
SMALL
LARGE-S
LARGE-N

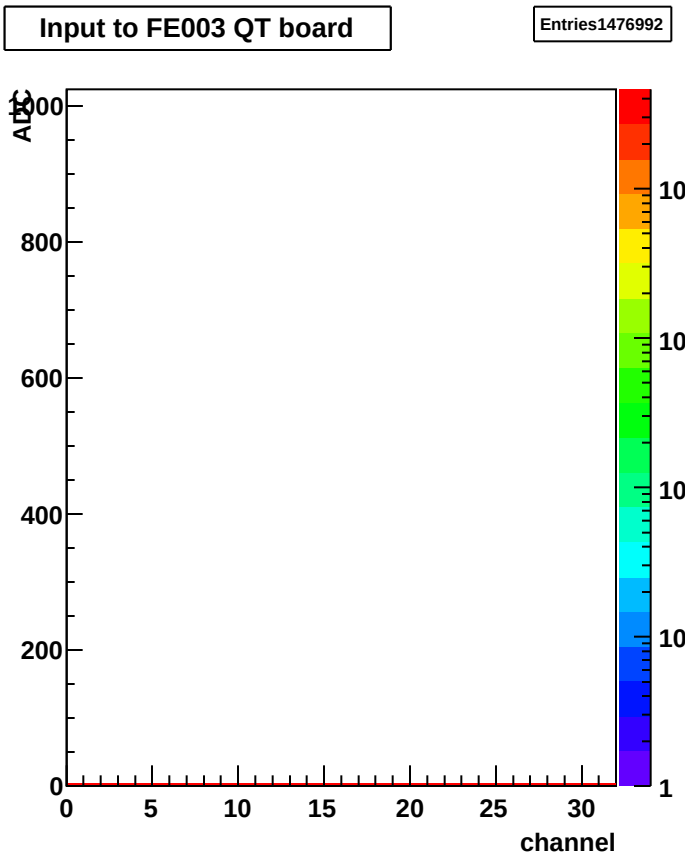

Input to FPD L2 DSM
Entries 138468
HT bits
39
5
17
109
49
489
23
359
45519
46128
45731
SMALL
LARGE-S
LARGE-N

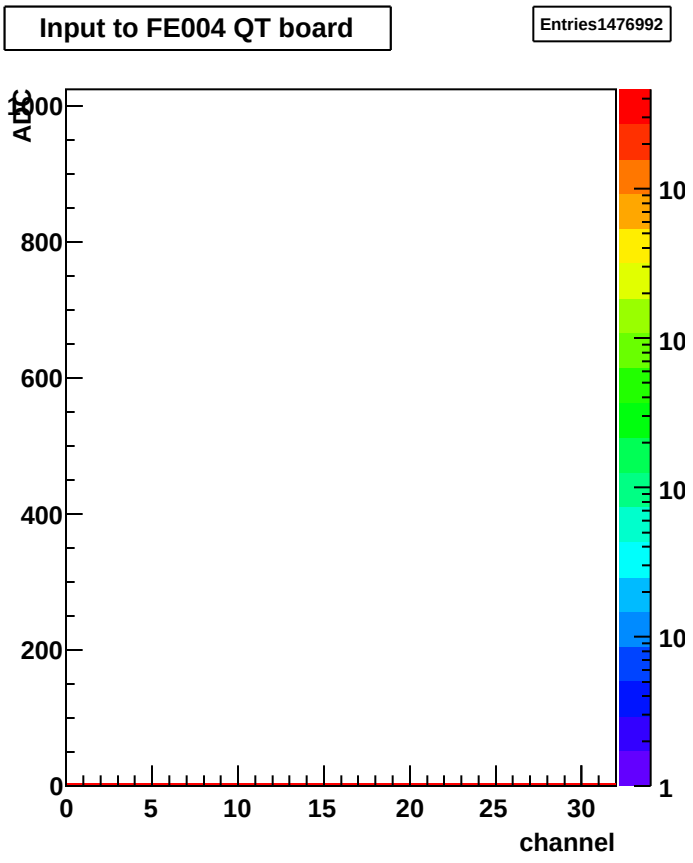

Input to FPD L2 DSM
Entries 1090
HT bits - simulated
39
5
17
109
49
489
23
359
SMALL
LARGE-S
LARGE-N

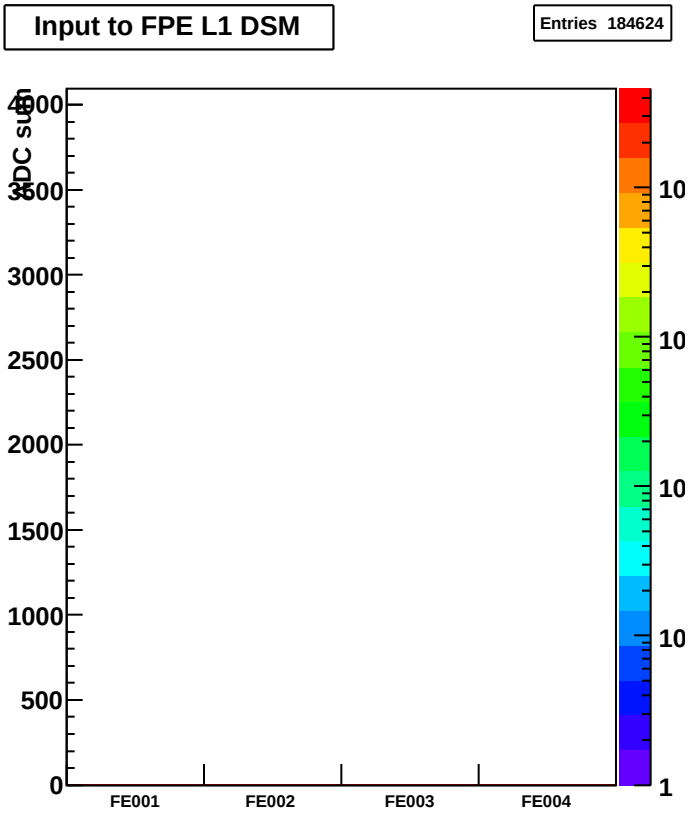

Input to FPD L2 DSM
Entries 276936
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

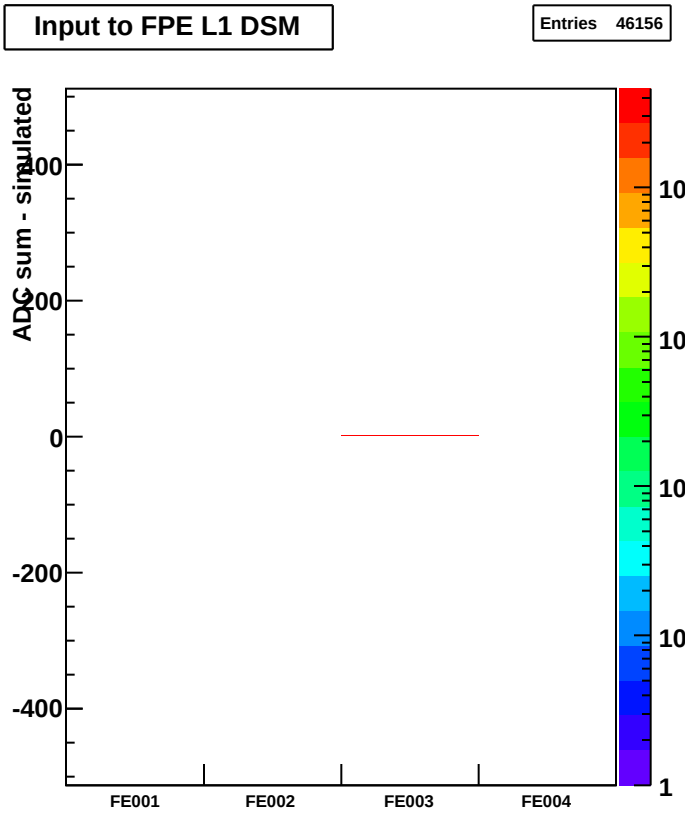

Input to FE001 QT board
Entries1476992
ADC
channel

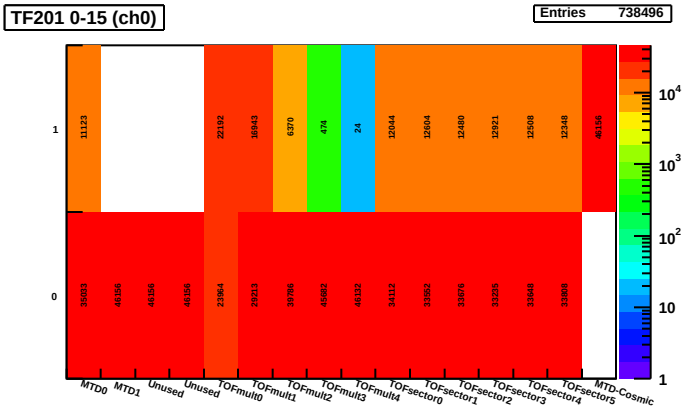

Input to FE002 QT board
Entries1476992
ADC
channel

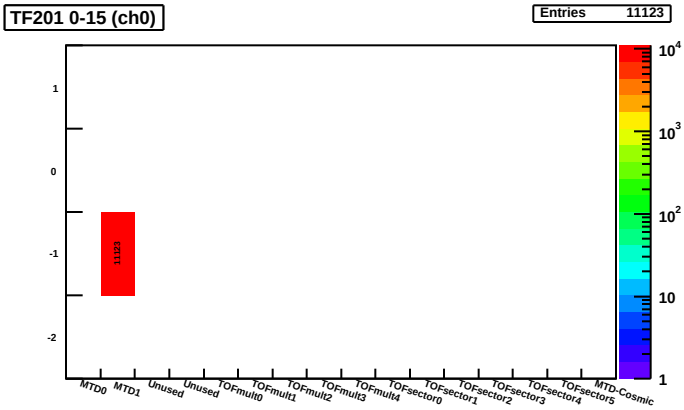

Input to FE003 QT board
Entries1476992
ADC
channel

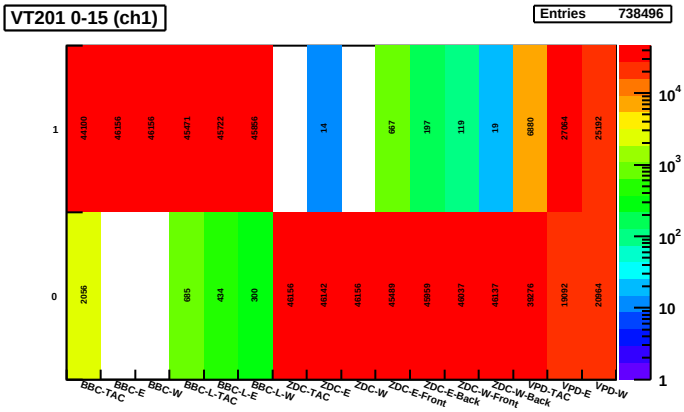

Input to FE004 QT board
Entries1476992
ADC
channel

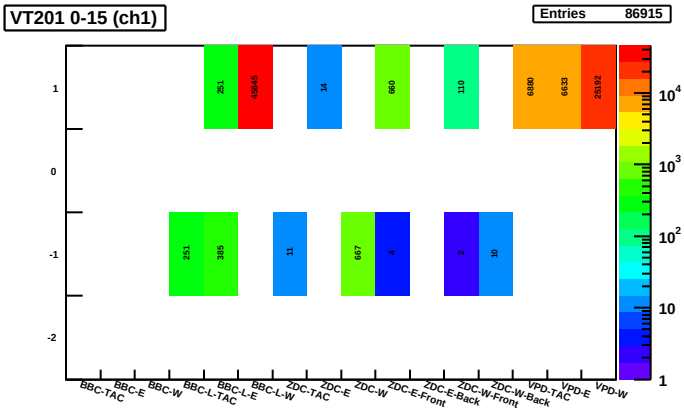

Input to FPE L1 DSM
Entries 184624
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 46156
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


TF201 0-15 (ch0)
Entries 738496
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 11123
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 738496
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 86915
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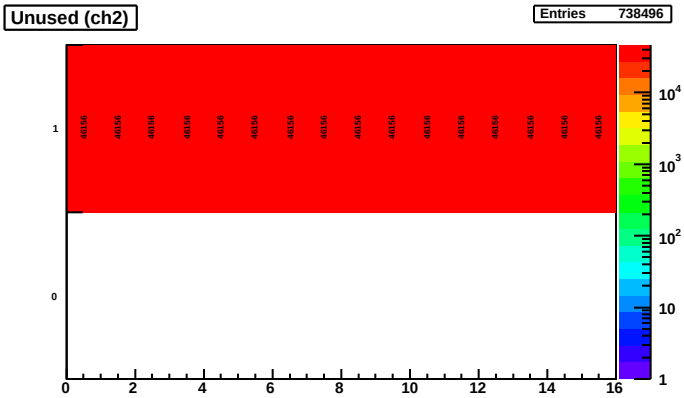
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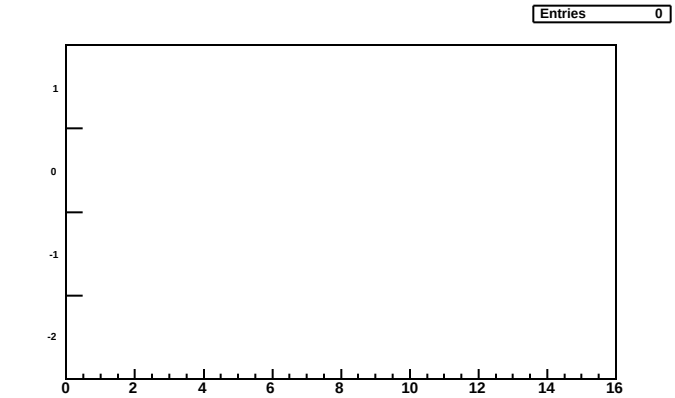
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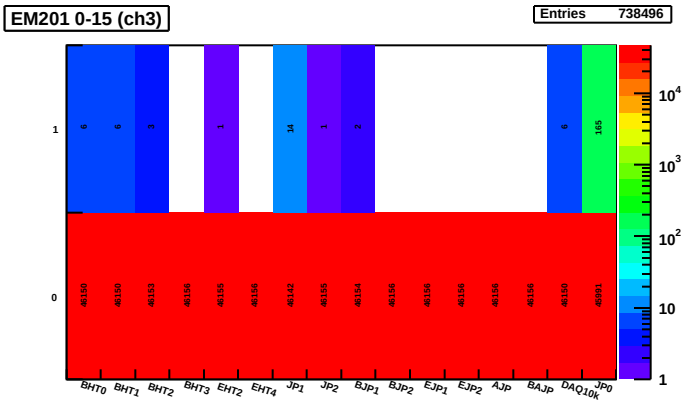
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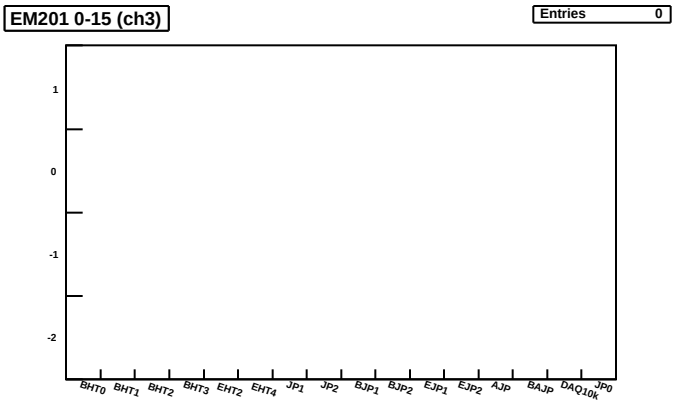
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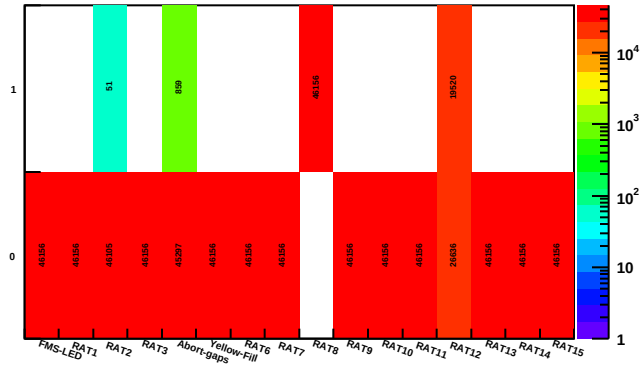
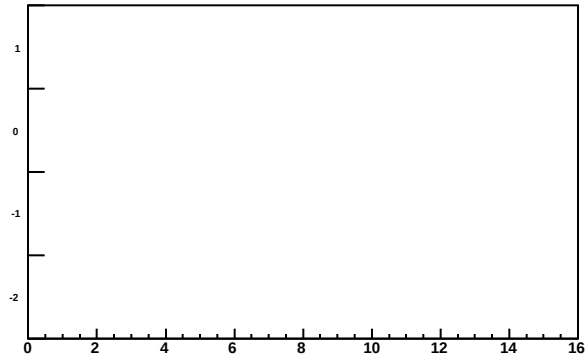
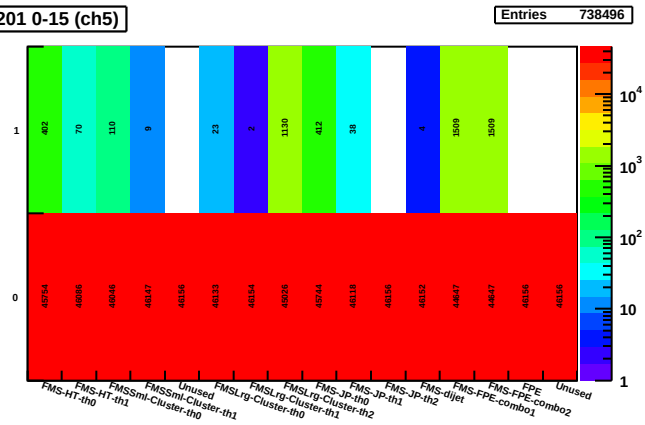
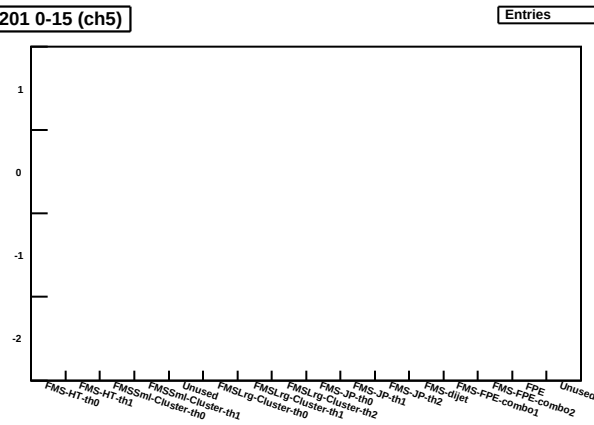
RAT board (ch4)



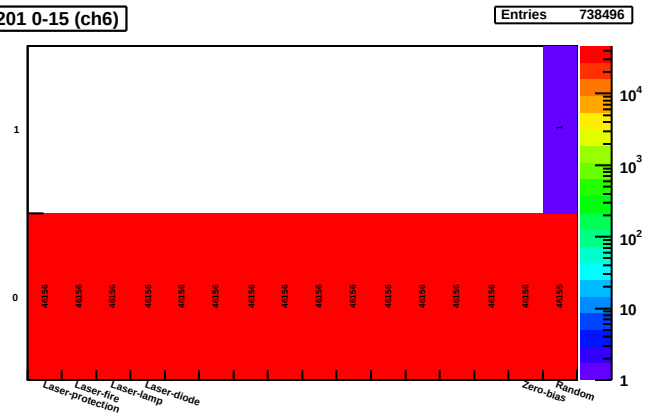


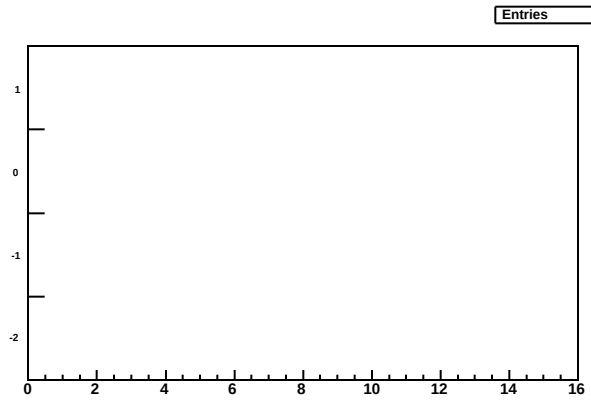
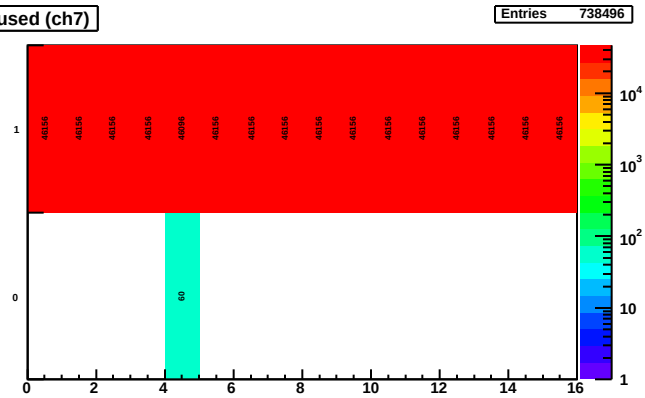
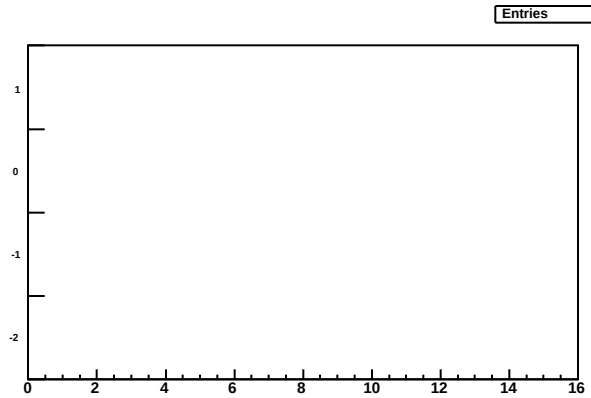
FP201 0-15 (ch5)





ST201 0-15 (ch6)





Unused (ch7)