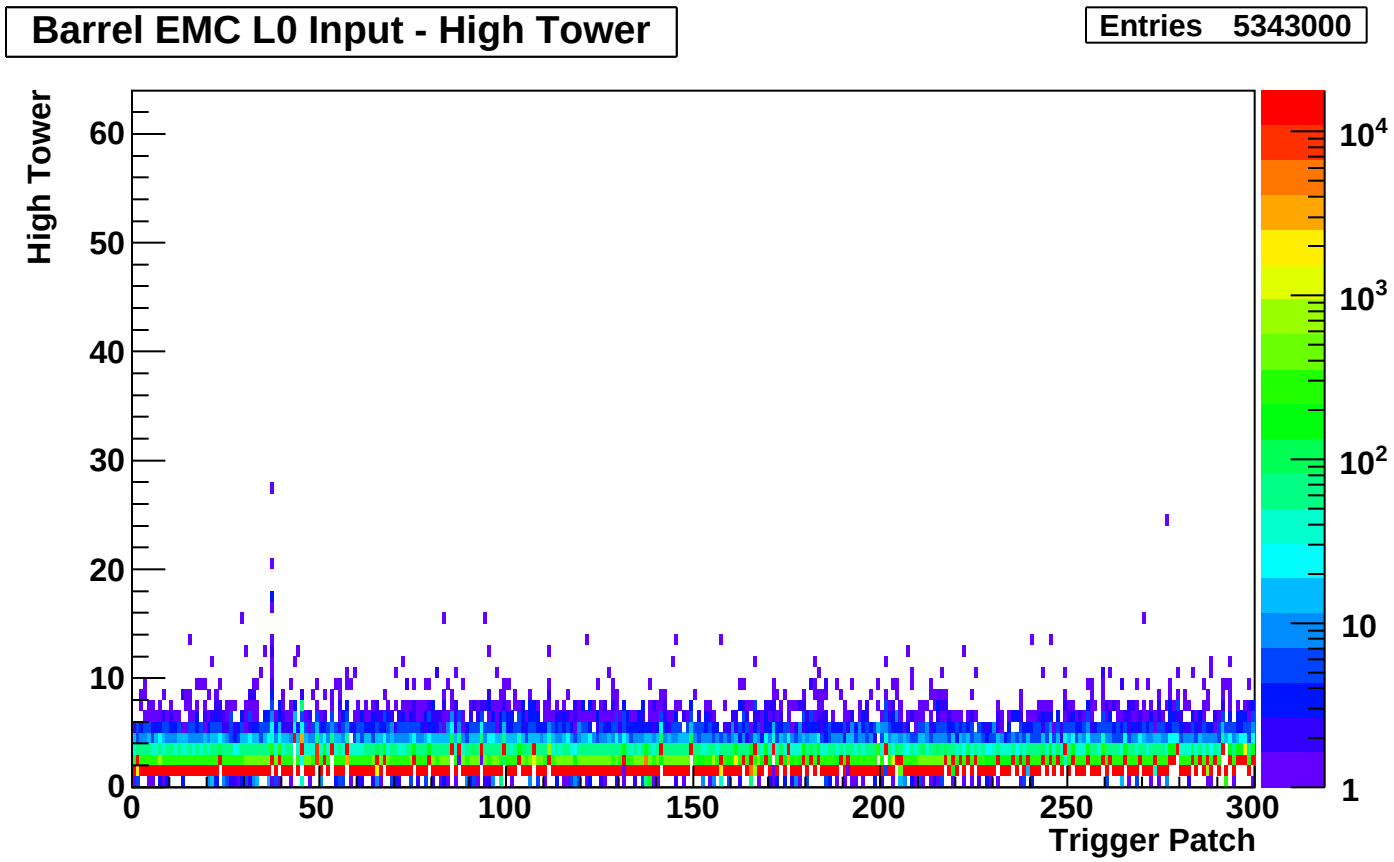
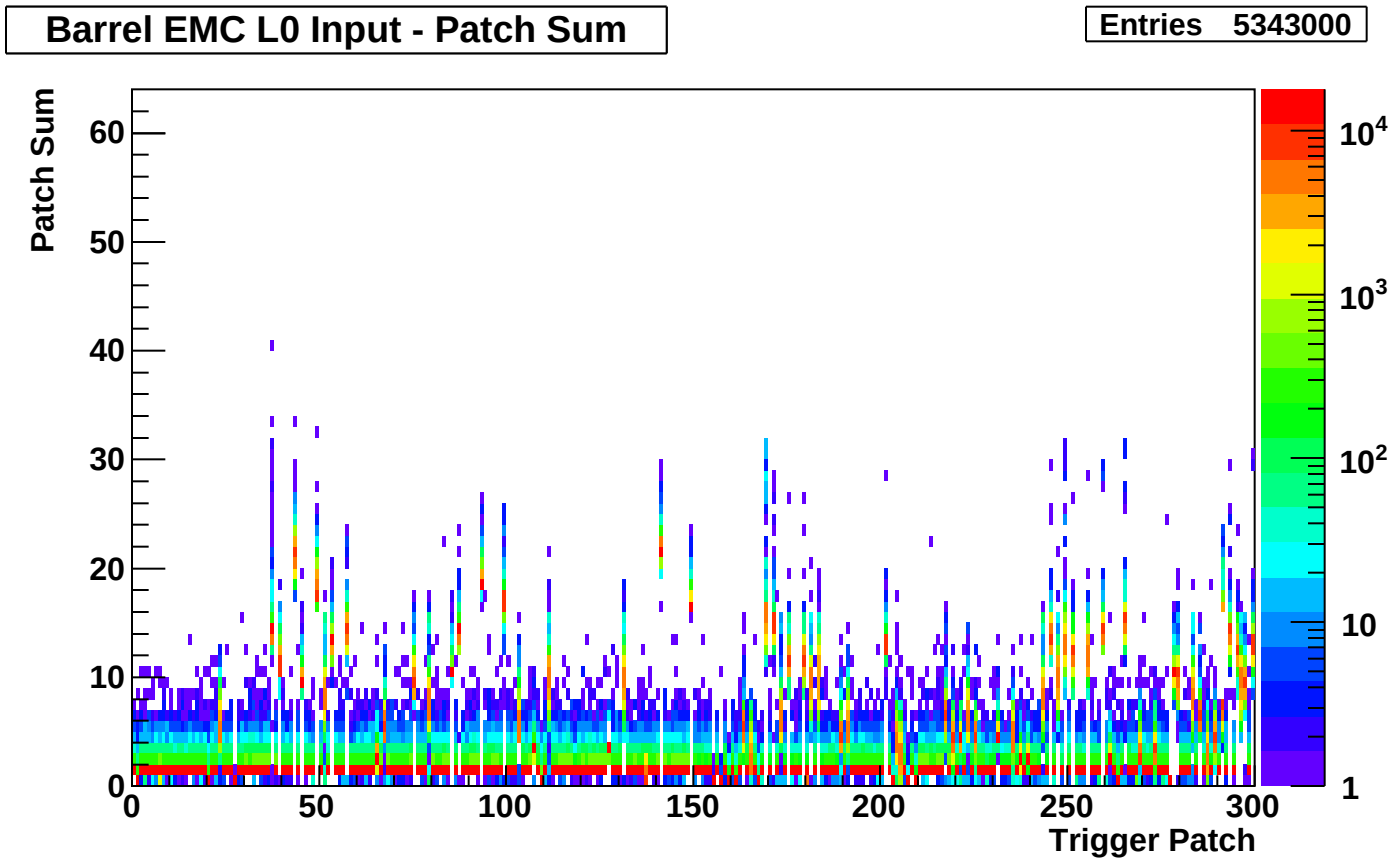
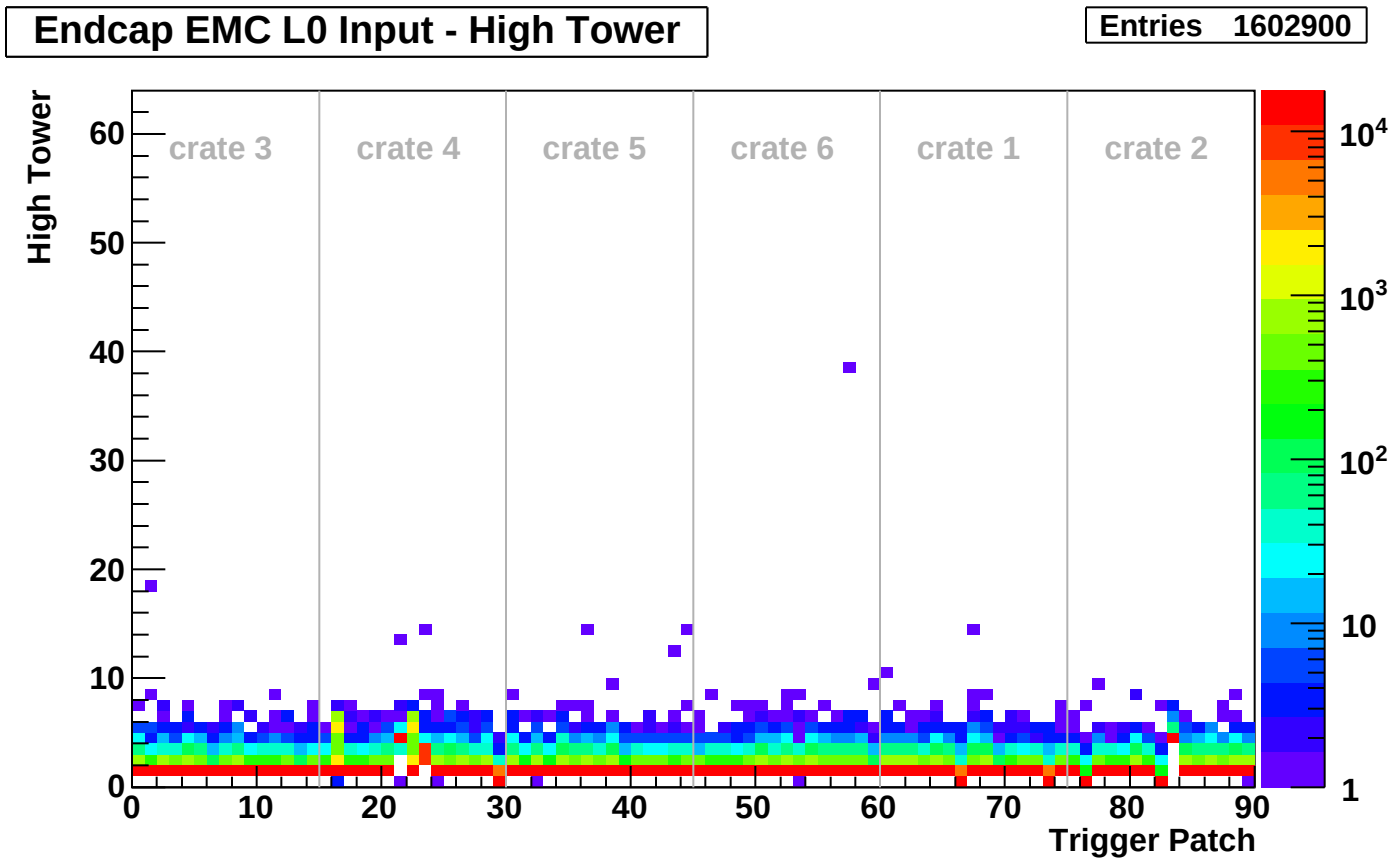
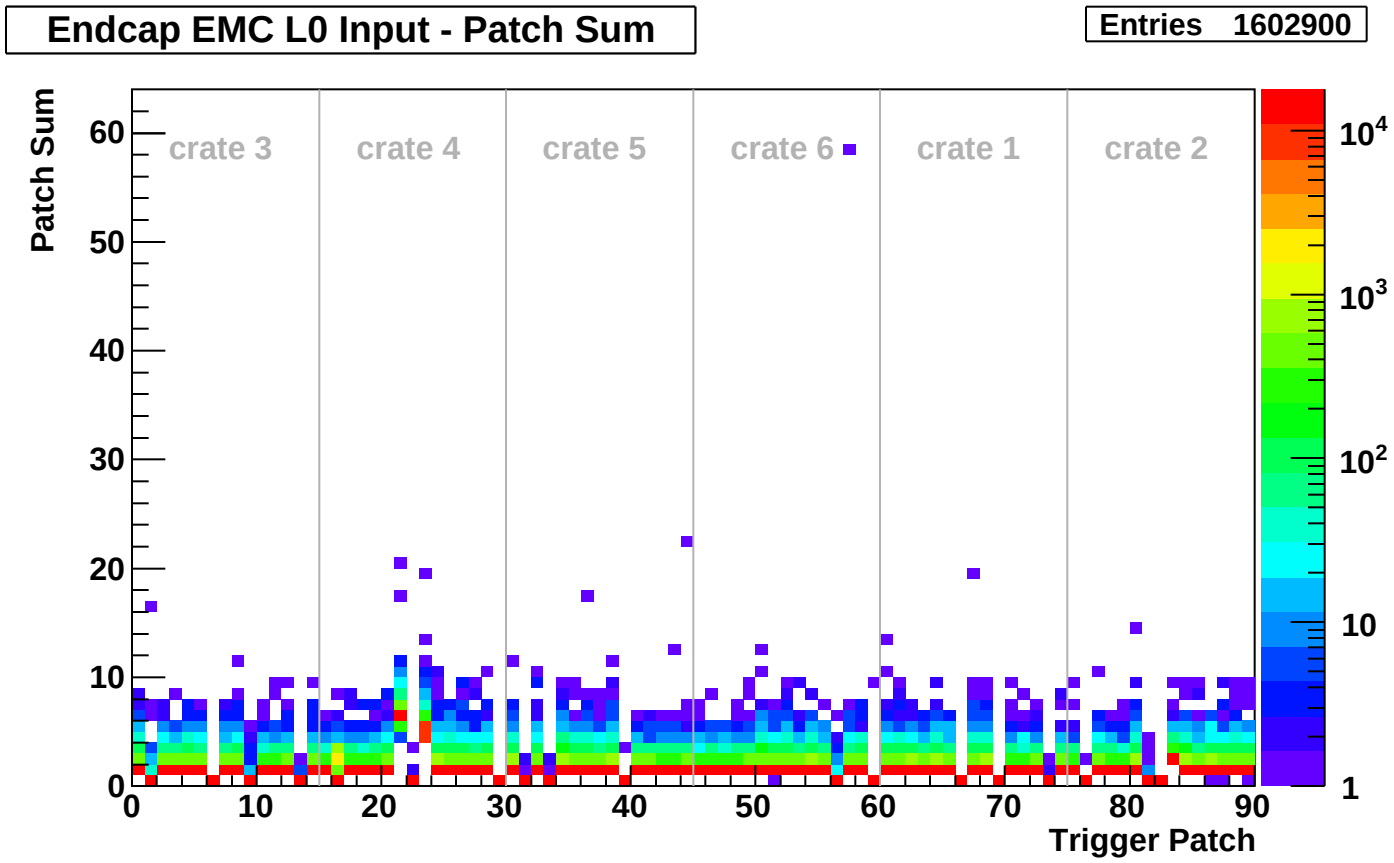
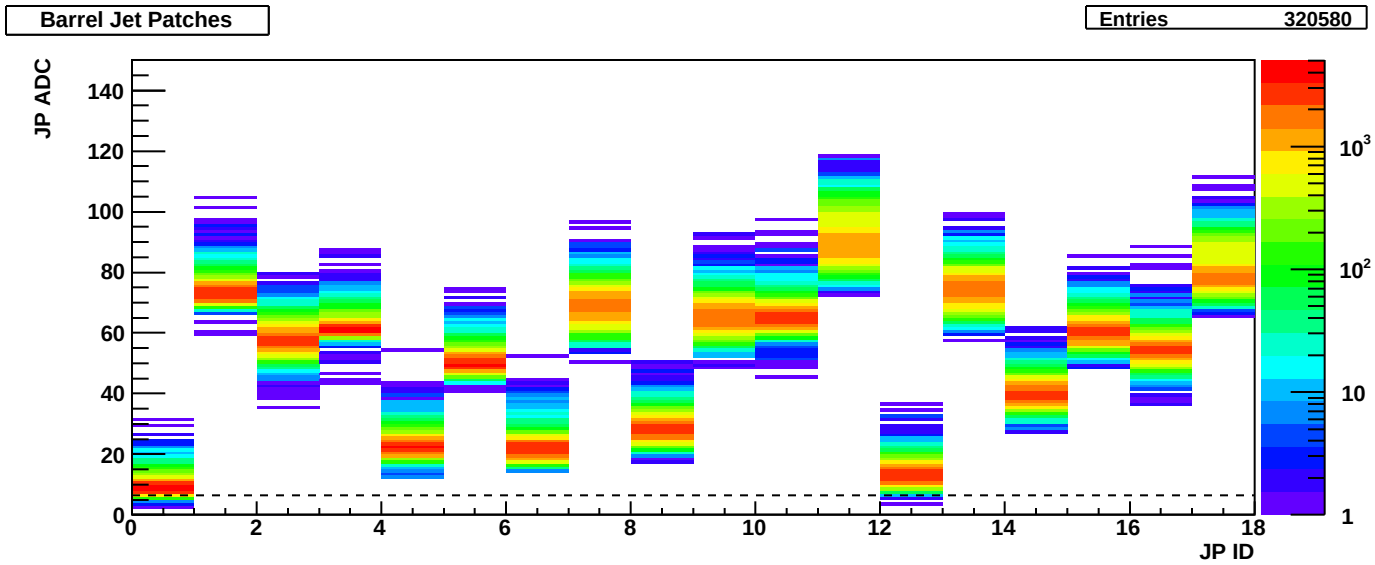
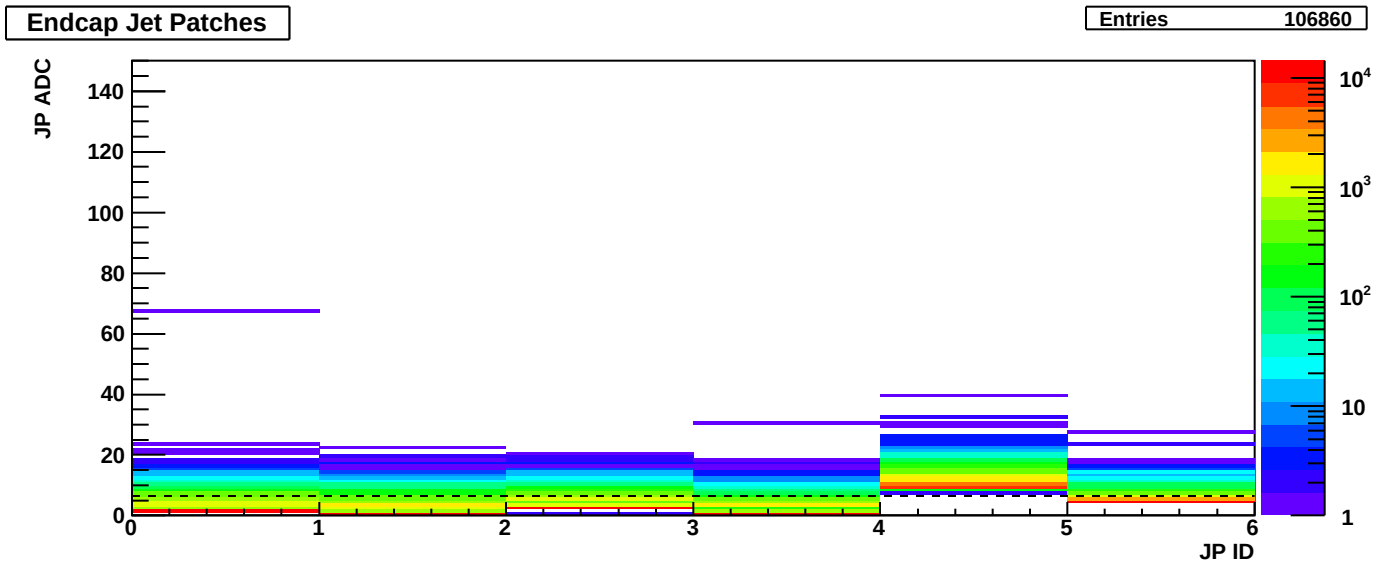

Barrel EMC L0 Input - High Tower
Entries 5343000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

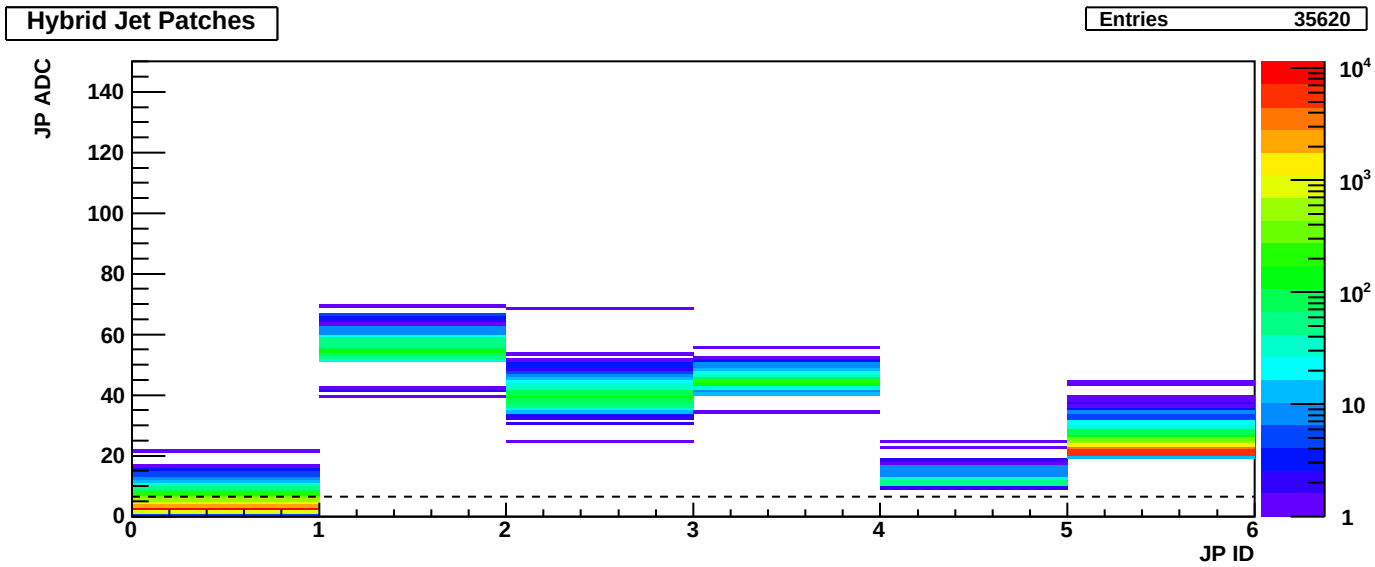

Barrel EMC L0 Input - Patch Sum
Entries 5343000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

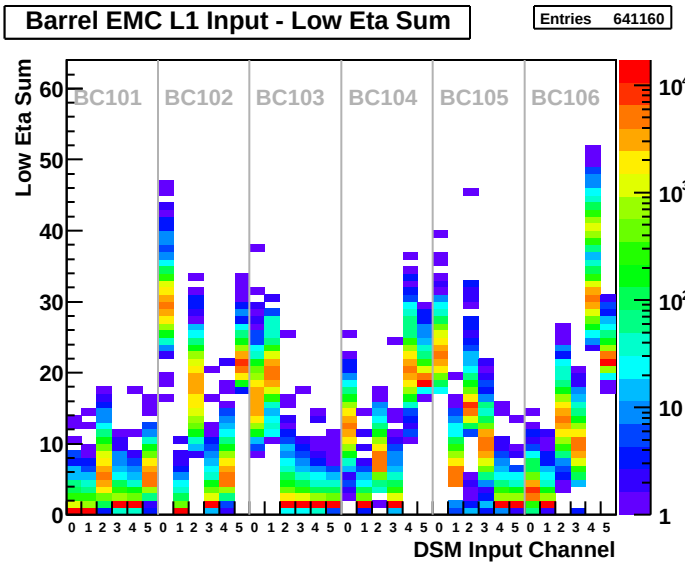

Endcap EMC L0 Input - High Tower
Entries 1602900
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

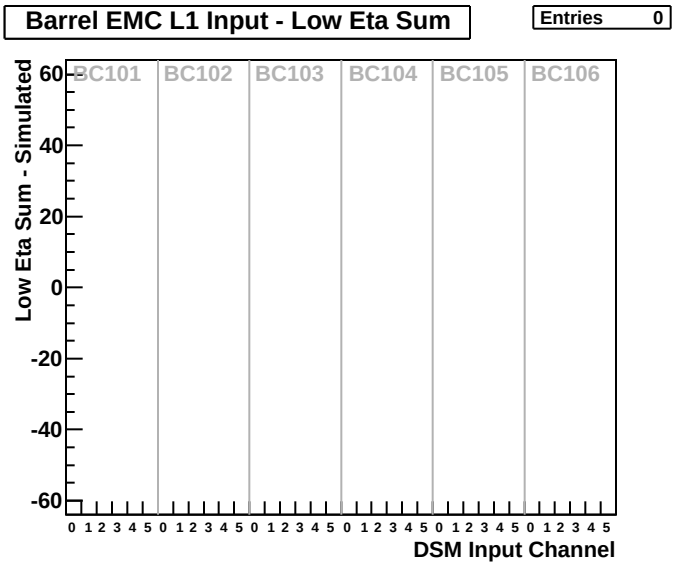

Endcap EMC L0 Input - Patch Sum
Entries 1602900
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

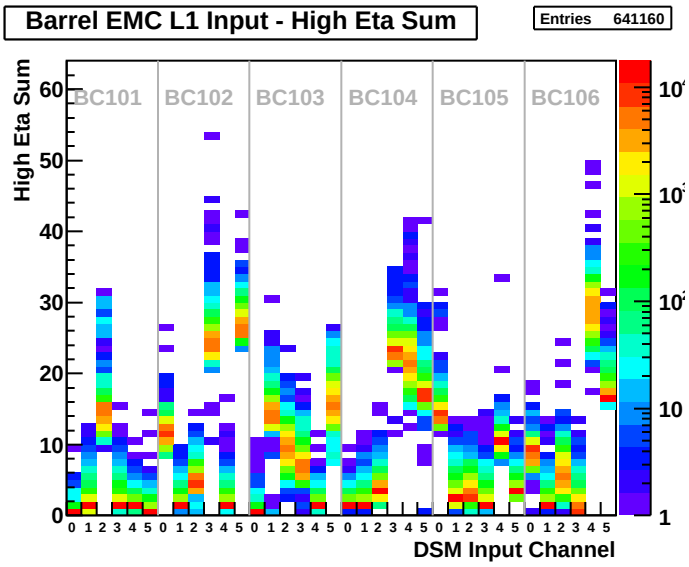

Barrel Jet Patches
Entries 320580
JP ADC
JP ID

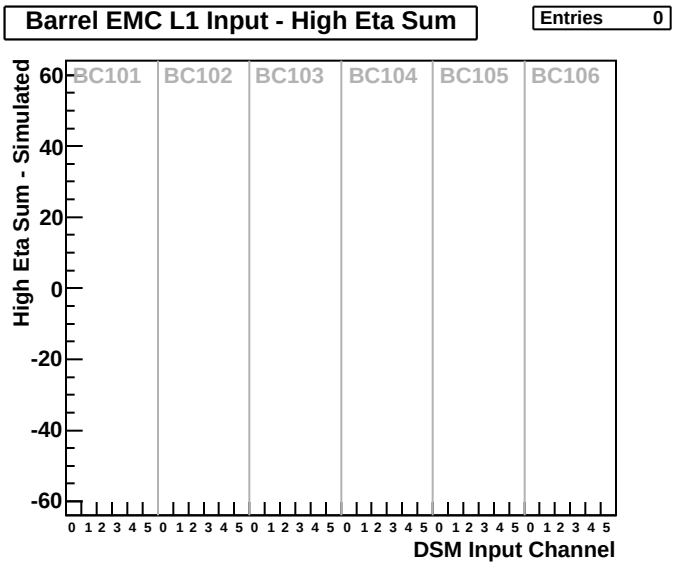

Endcap Jet Patches
Entries 106860
JP ADC
JP ID

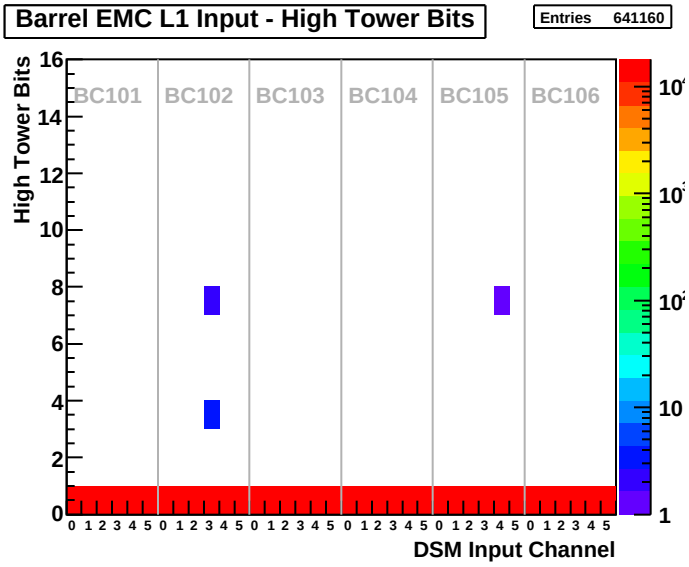

Hybrid Jet Patches
Entries 35620
JP ADC
JP ID

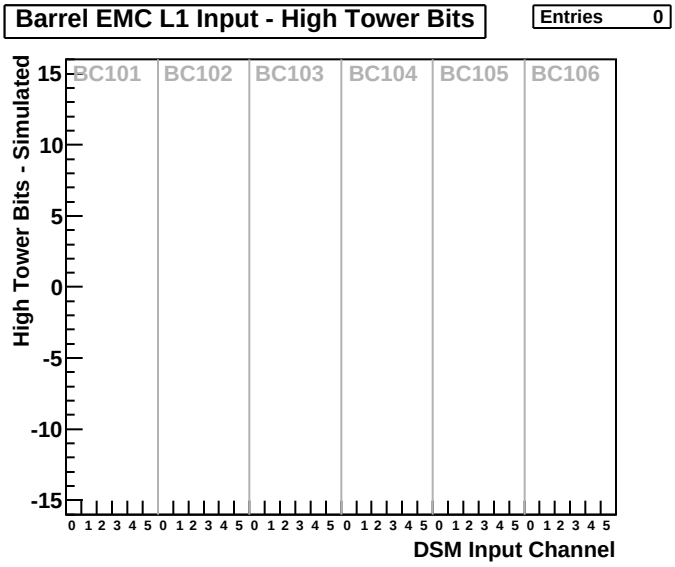

Barrel EMC L1 Input - Low Eta Sum
Entries 641160
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

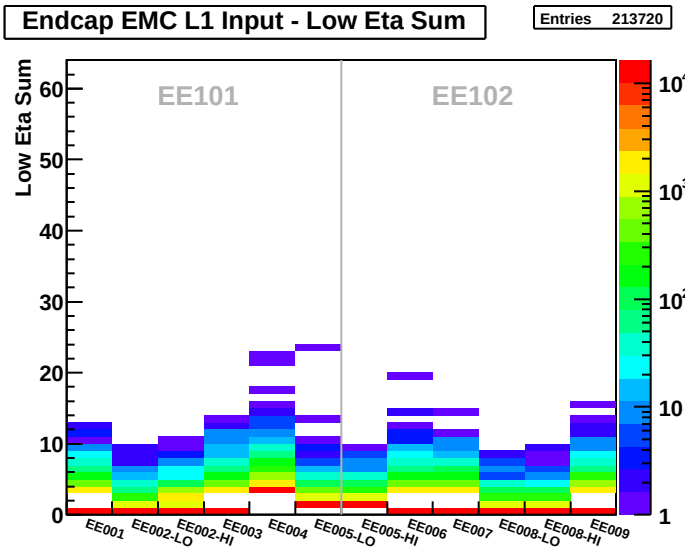

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

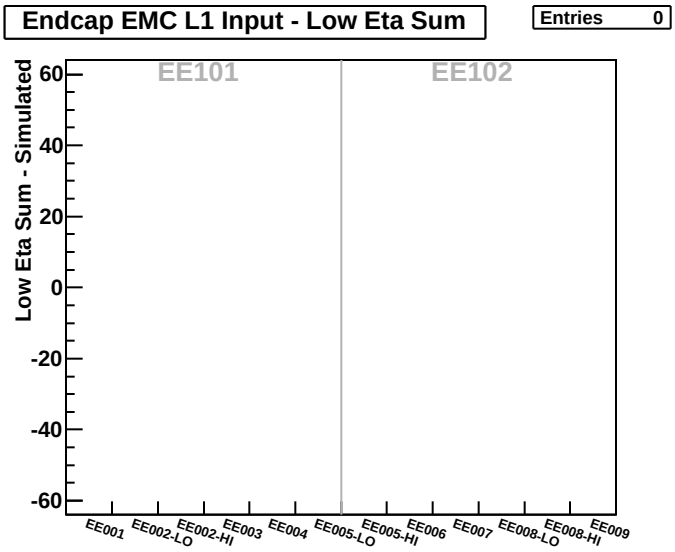

Barrel EMC L1 Input - High Eta Sum
Entries 641160
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

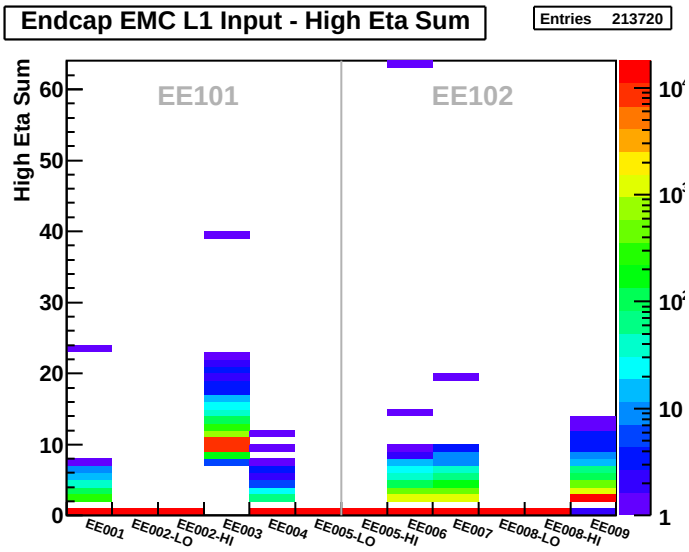

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

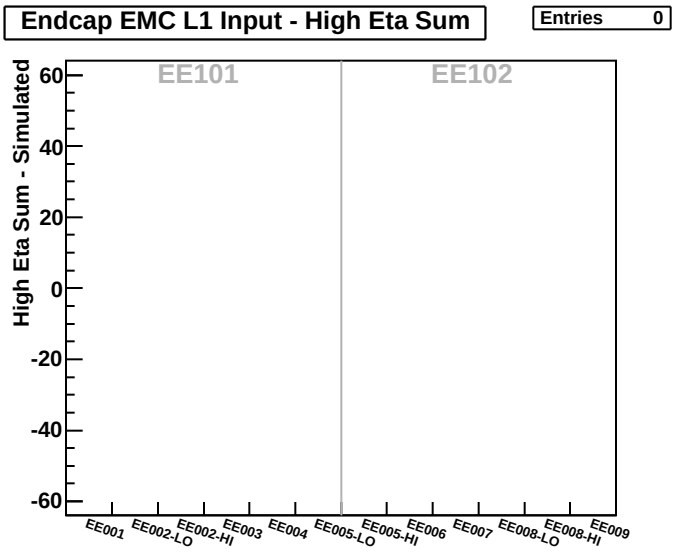

Barrel EMC L1 Input - High Tower Bits
Entries 641160
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

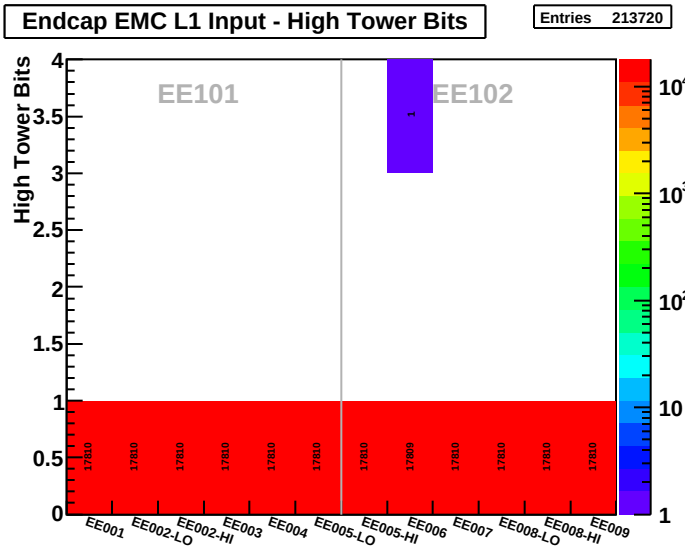

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

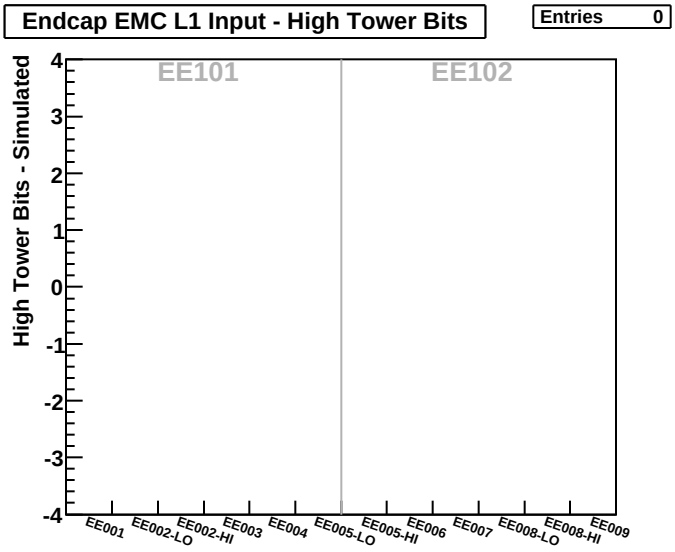

Endcap EMC L1 Input - Low Eta Sum
Entries 213720
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

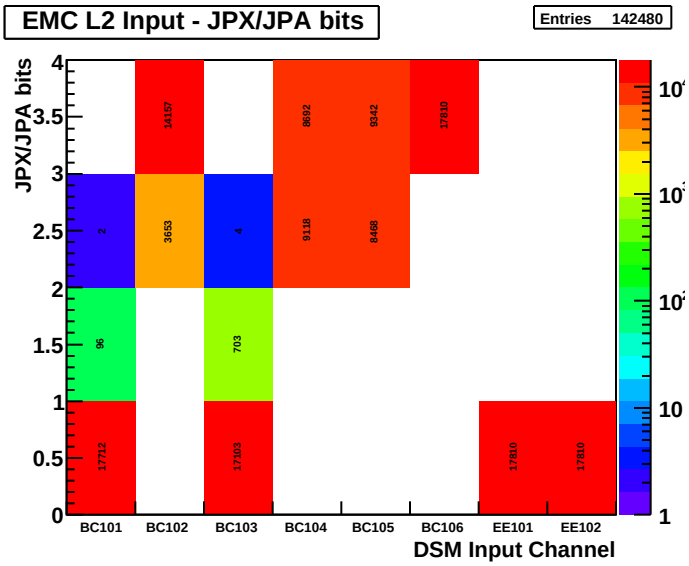

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

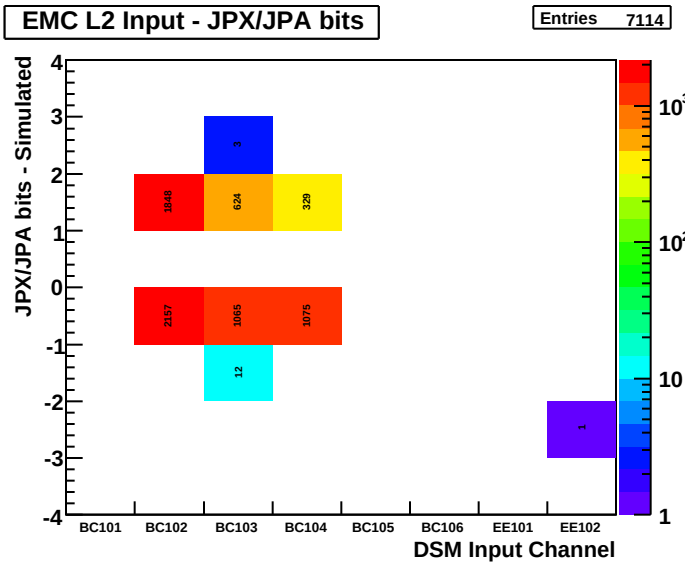

Endcap EMC L1 Input - High Eta Sum
Entries 213720
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

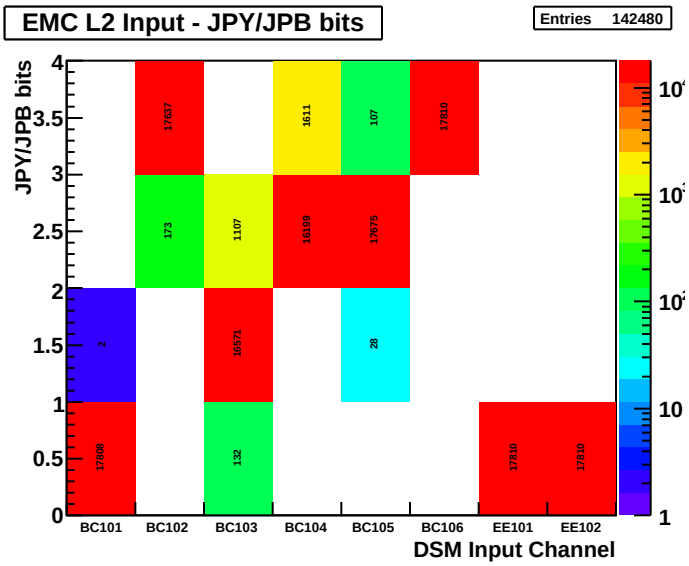

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

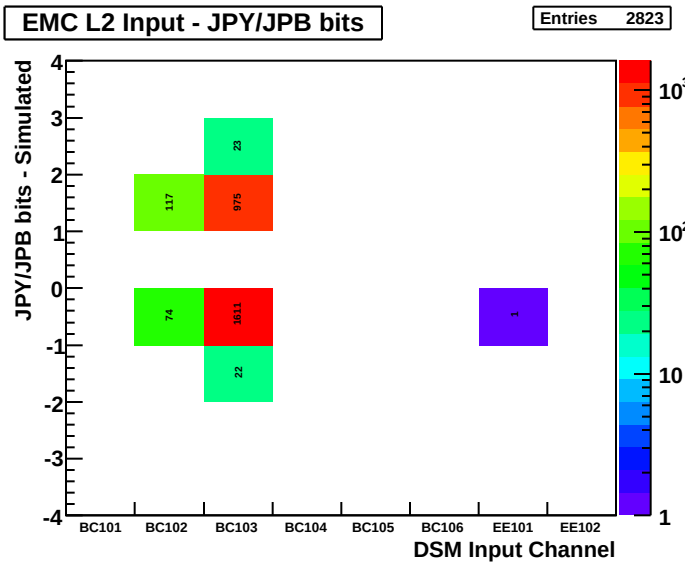

Endcap EMC L1 Input - High Tower Bits
Entries 213720
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

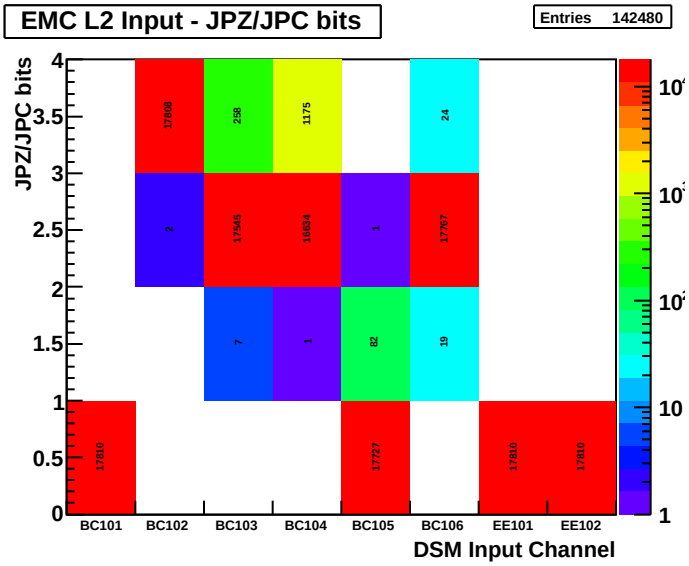

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

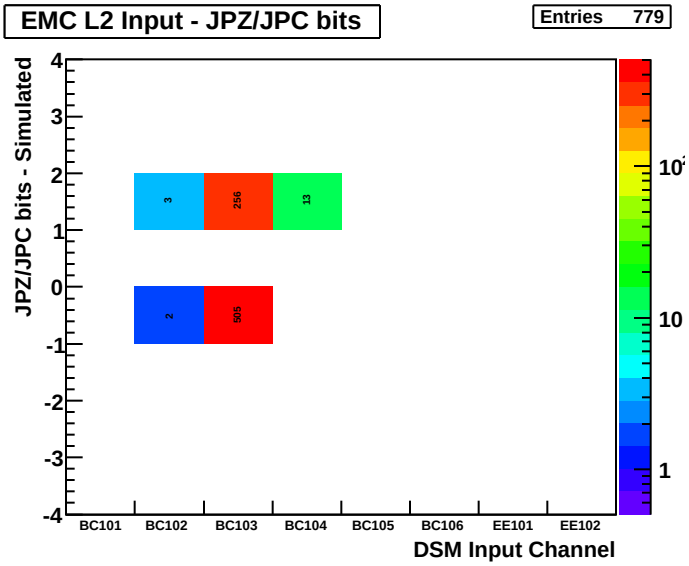

EMC L2 Input - JPX/JPA bits
Entries 142480
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

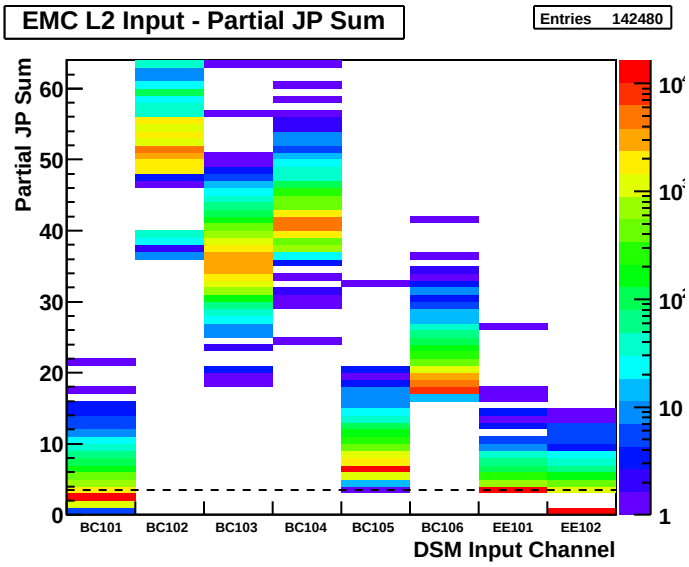

EMC L2 Input - JPX/JPA bits
Entries 7114
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

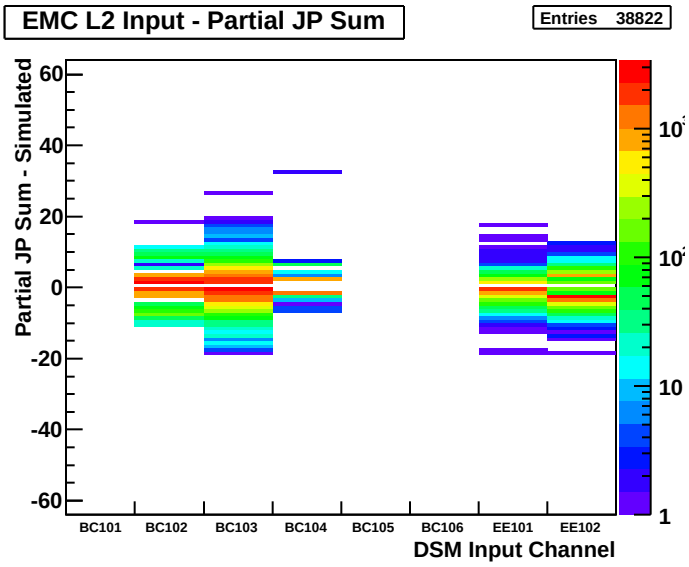

EMC L2 Input - JPY/JPB bits
Entries 142480
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

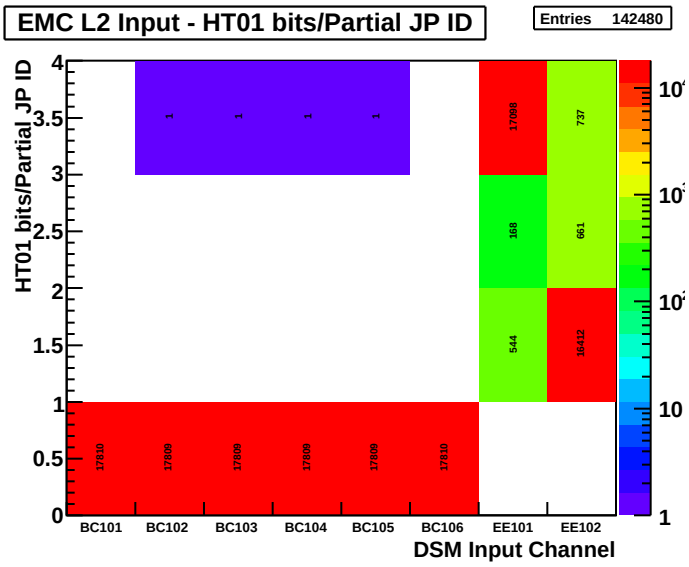

EMC L2 Input - JPY/JPB bits
Entries 2823
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

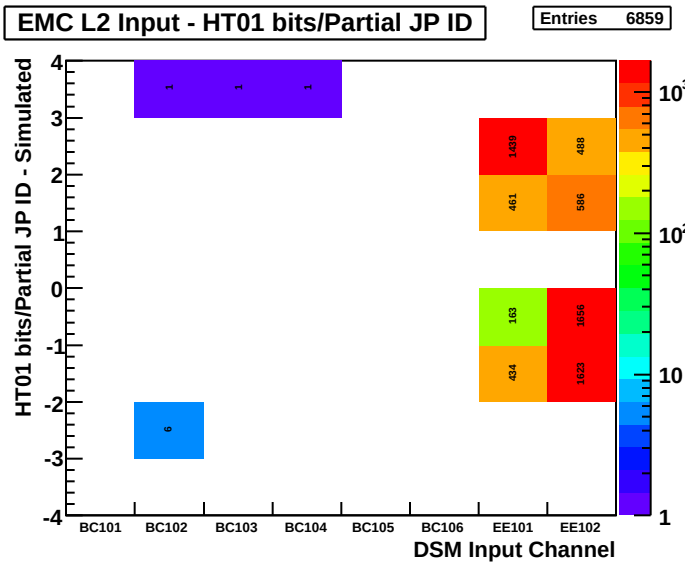

EMC L2 Input - JPZ/JPC bits
Entries 142480
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

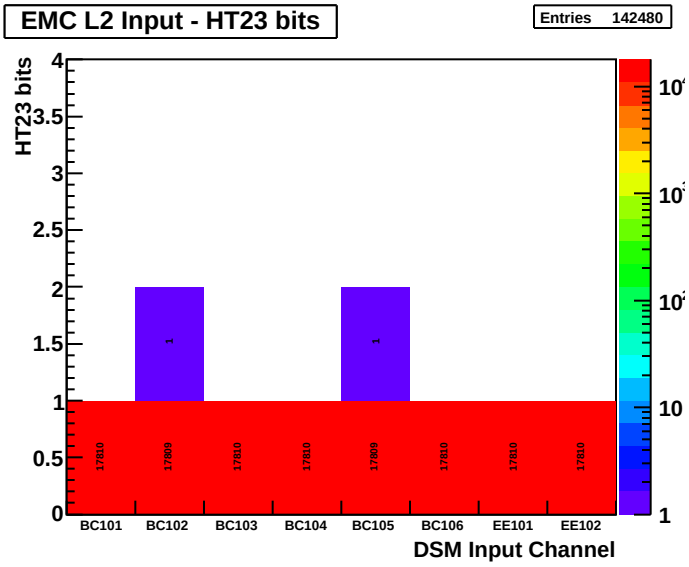

EMC L2 Input - JPZ/JPC bits
Entries 779
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

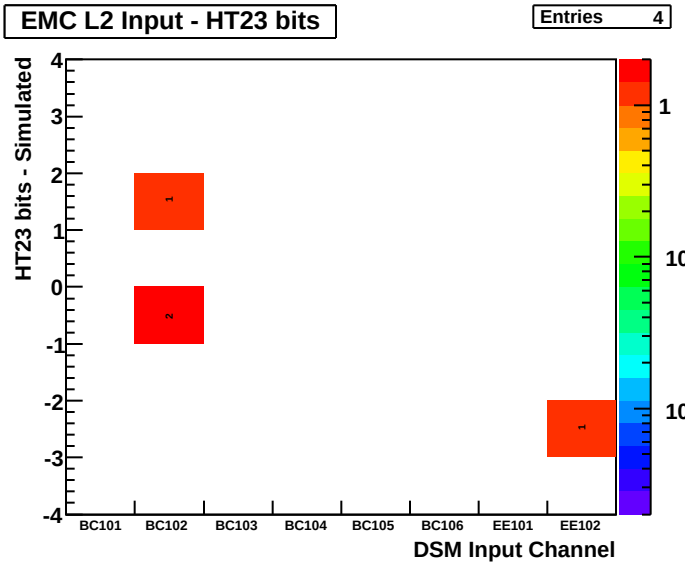

EMC L2 Input - Partial JP Sum
Entries 142480
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

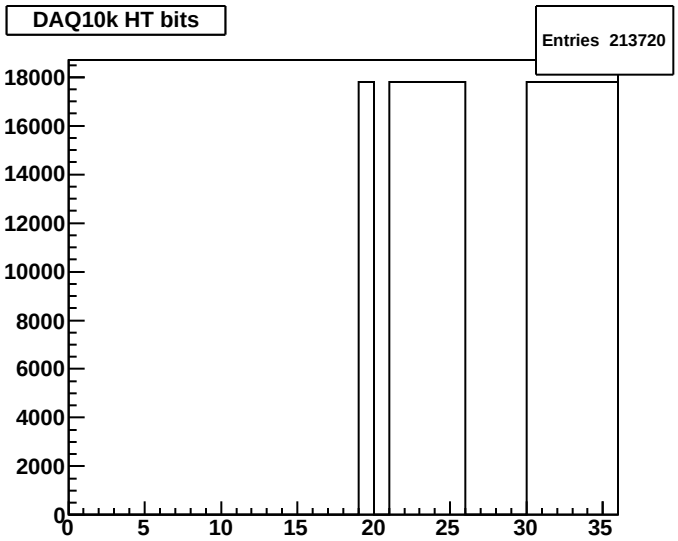

EMC L2 Input - Partial JP Sum
Entries 38822
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

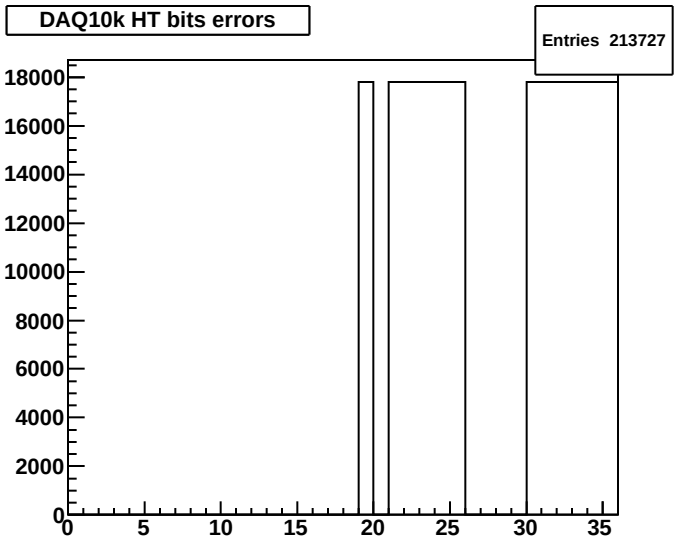

EMC L2 Input - HT01 bits/Partial JP ID
Entries 142480
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

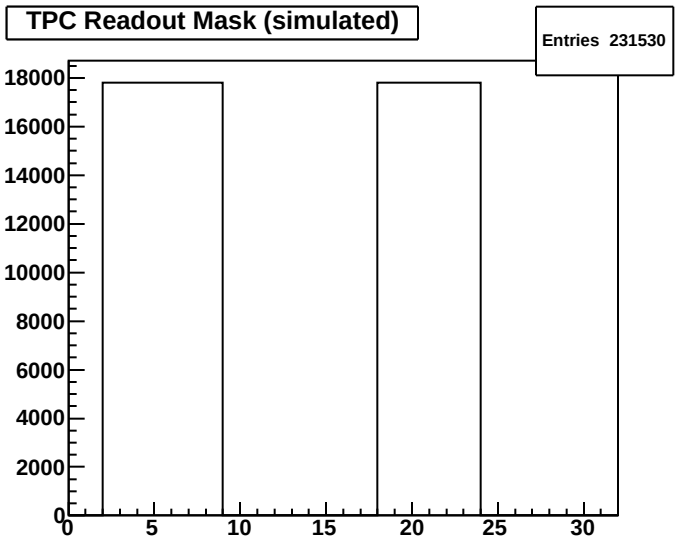

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6859
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

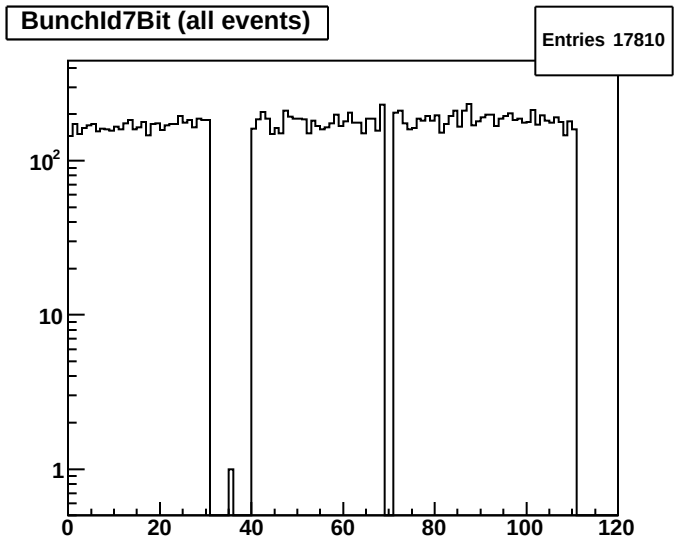

EMC L2 Input - HT23 bits
Entries 142480
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

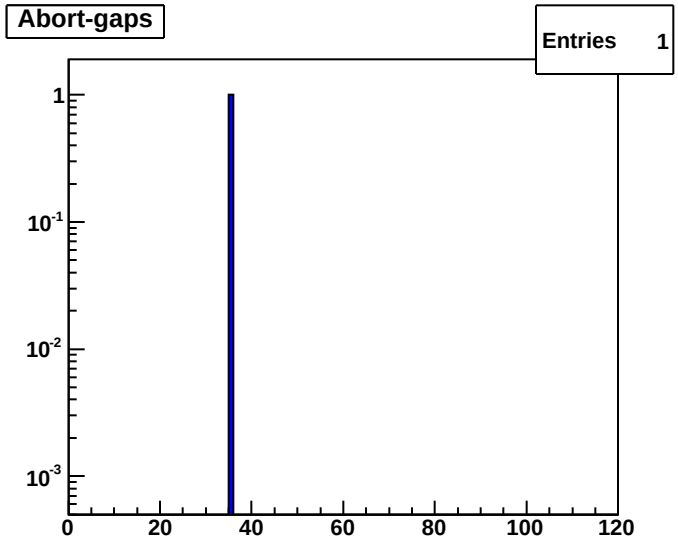

EMC L2 Input - HT23 bits
Entries 4
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

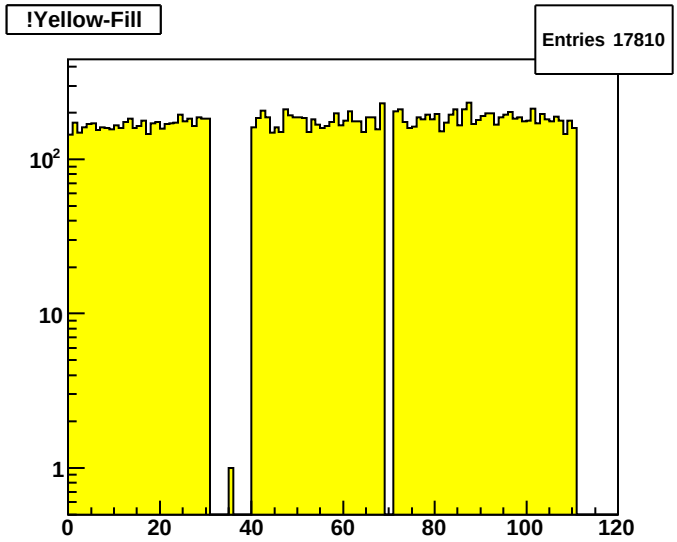

DAQ10k HT bits
Entries 213720

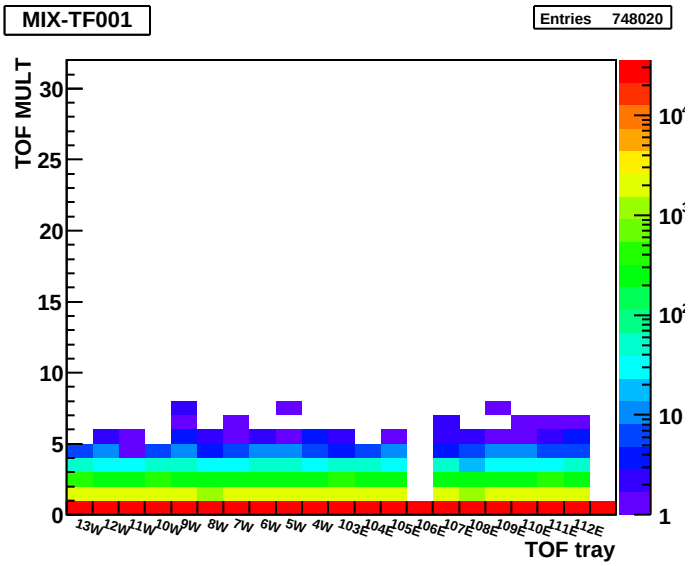

DAQ10k HT bits errors
Entries 213727

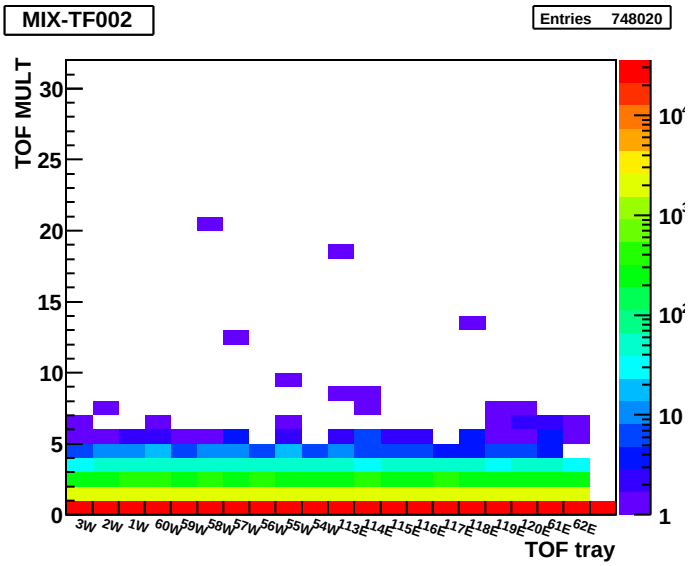

TPC Readout Mask (simulated)
Entries 231530

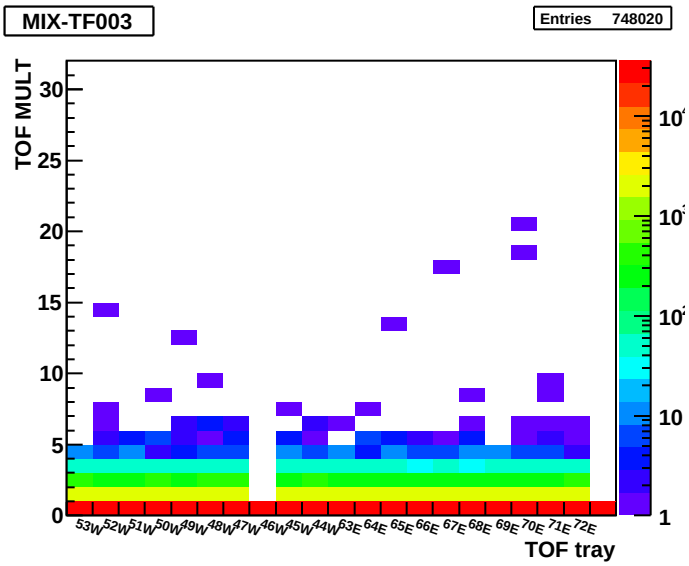

BunchId7Bit (all events)
Entries 17810

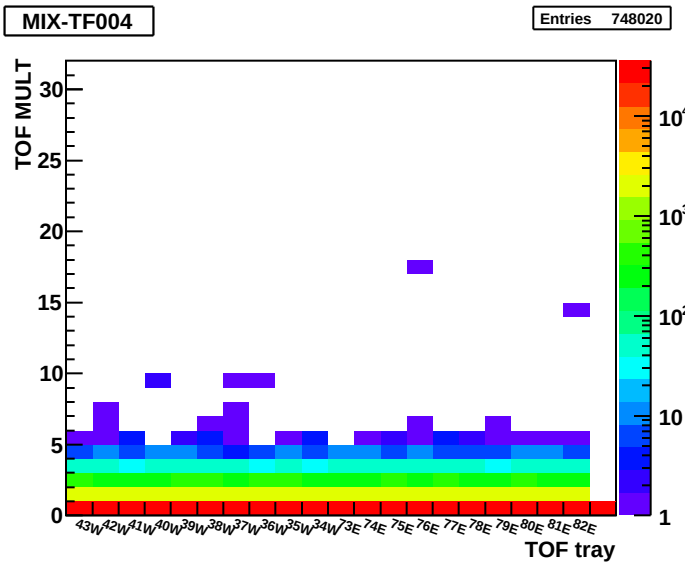

Abort-gaps
Entries 1

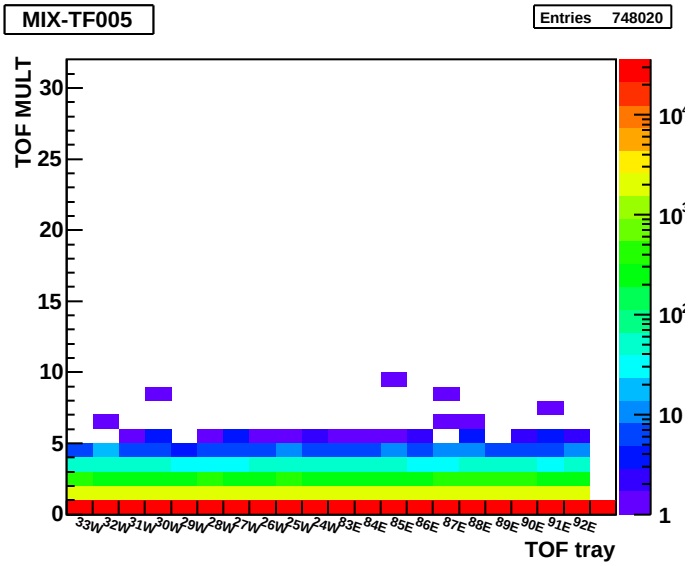

!Yellow-Fill
Entries 17810

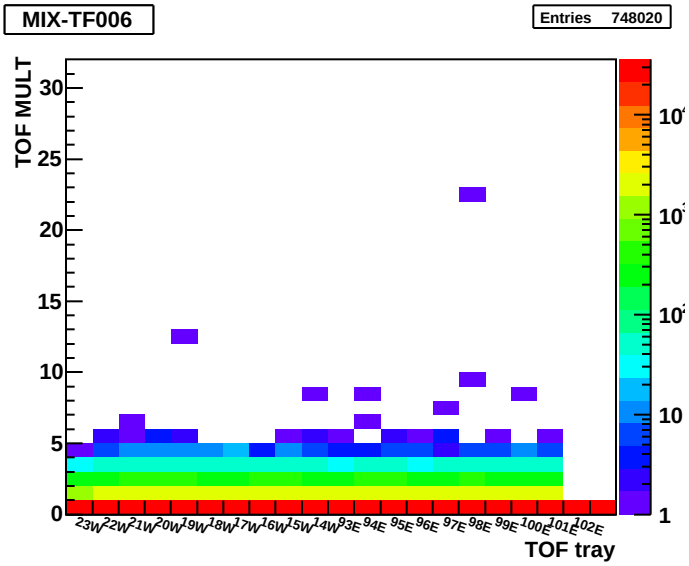

MIX-TF001
Entries 748020
TOF MULT
TOF tray

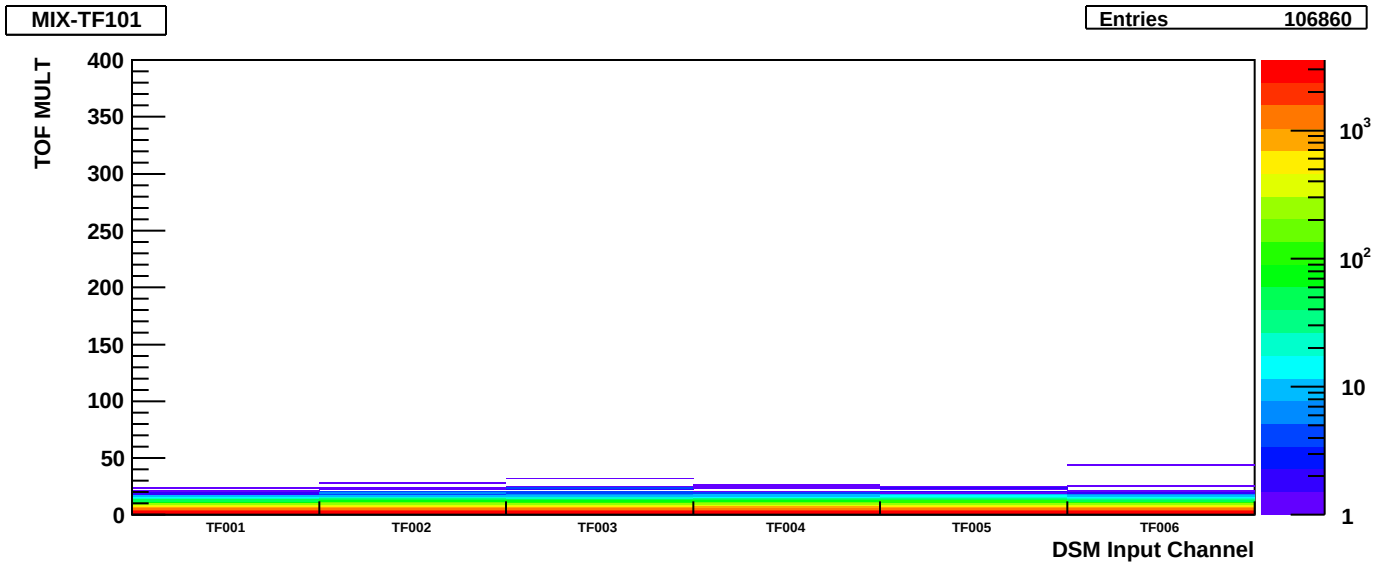

MIX-TF002
Entries 748020
TOF MULT
TOF tray

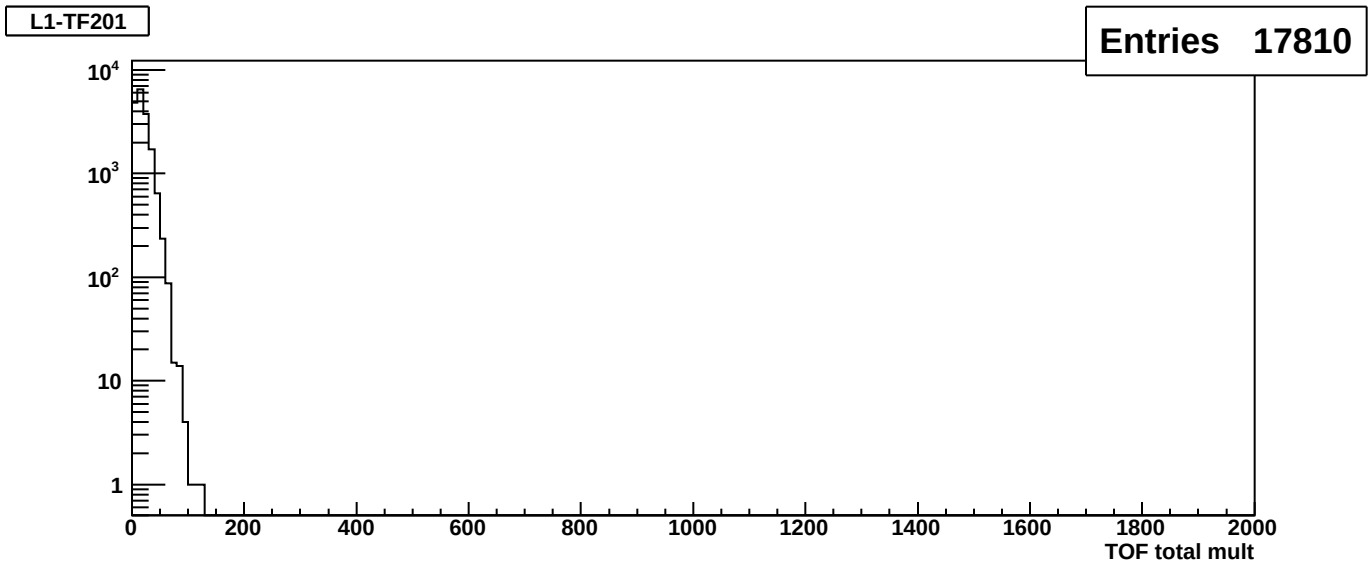

MIX-TF003
Entries 748020
TOF MULT
TOF tray

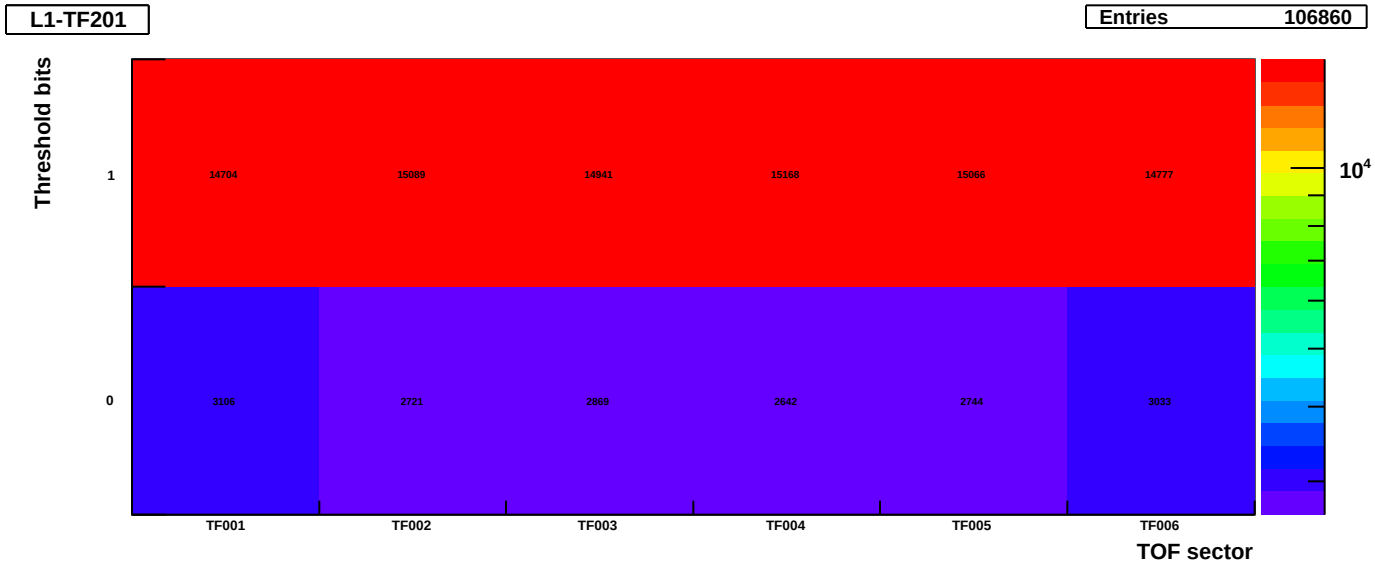

MIX-TF004
Entries 748020
TOF MULT
TOF tray

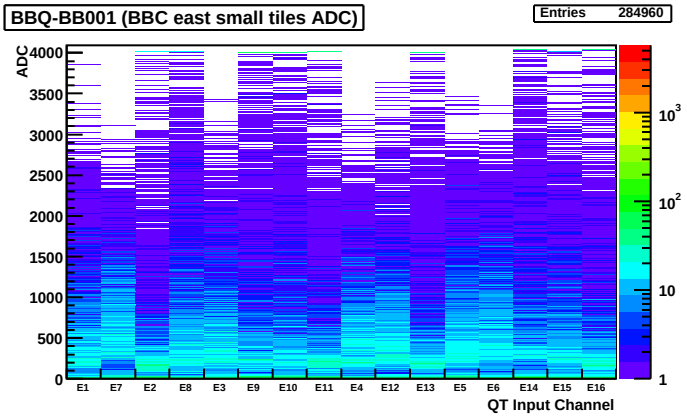

MIX-TF005
Entries 748020
TOF MULT
TOF tray

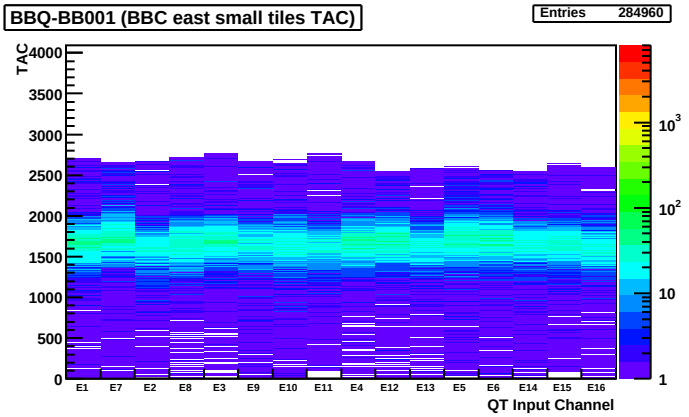

MIX-TF006
Entries 748020
TOF MULT
TOF tray

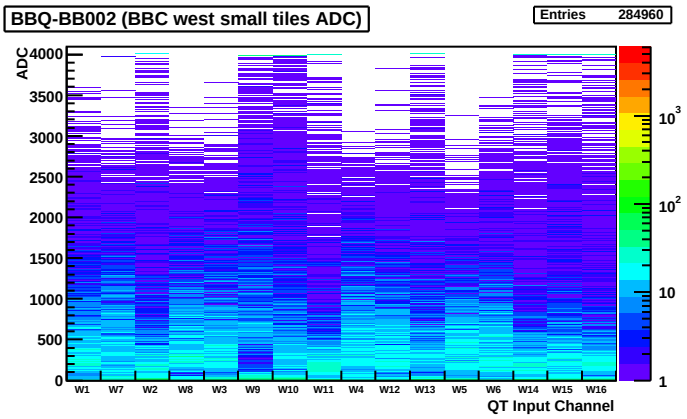

MIX-TF101
Entries 106860
TOF MULT
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

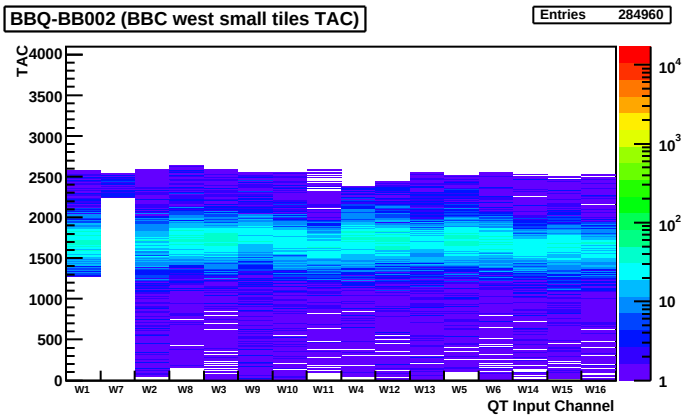

L1-TF201
Entries 17810
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
1600
1800
2000
TOF total mult

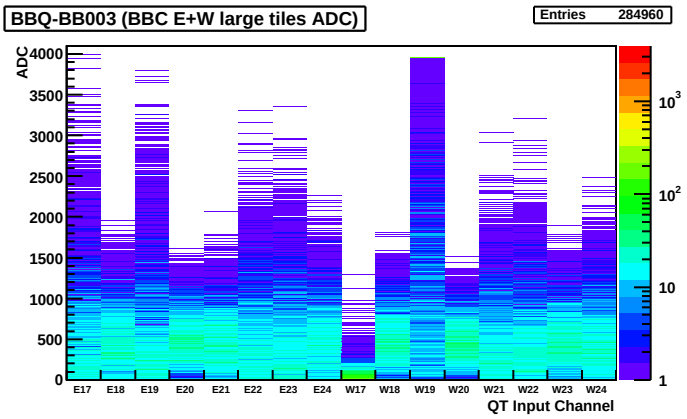

L1-TF201
Entries 106860
Threshold bits
1
0
14704
15089
14941
15168
15066
14777
3106
2721
2869
2642
2744
3033
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

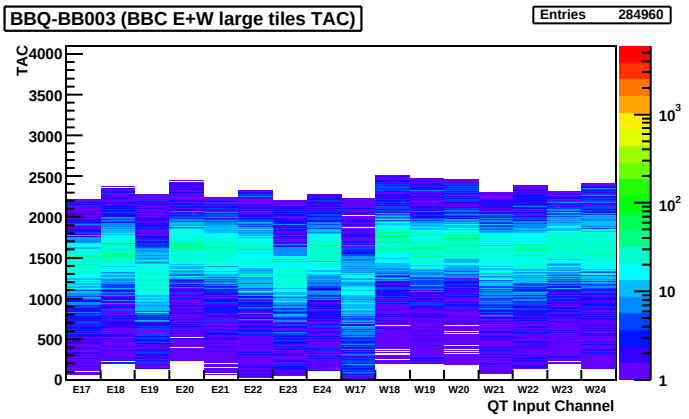

BBQ-BB001 (BBC east small tiles ADC)
Entries 284960
ADC
QT Input Channel

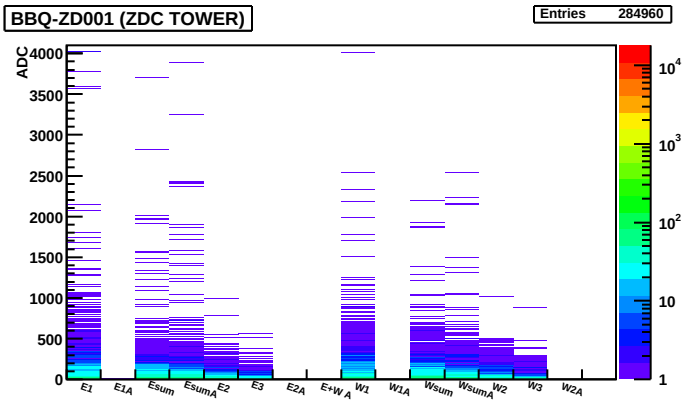

BBQ-BB001 (BBC east small tiles TAC)
Entries 284960
TAC
QT Input Channel

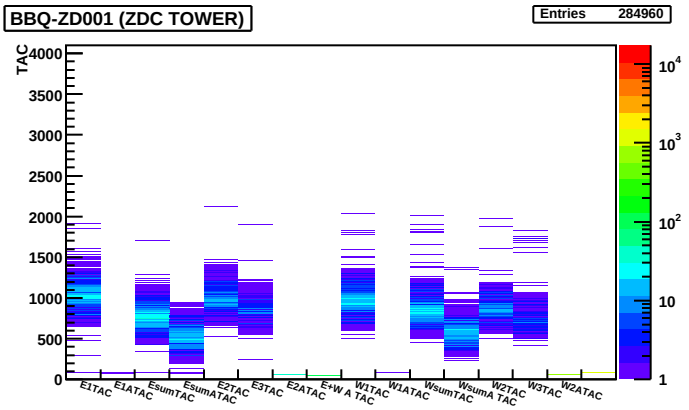

BBQ-BB002 (BBC west small tiles ADC)
Entries 284960
ADC
QT Input Channel

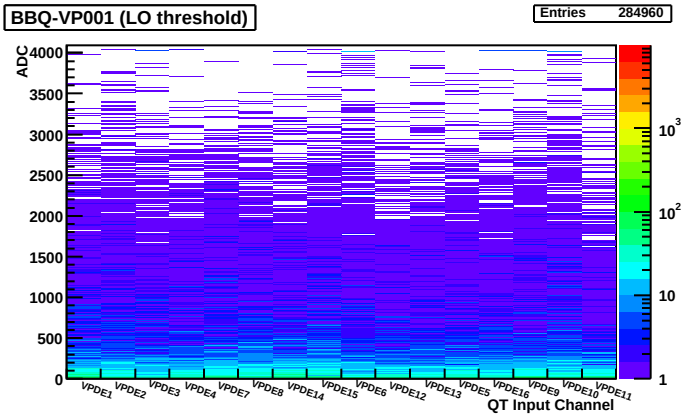

BBQ-BB002 (BBC west small tiles TAC)
Entries 284960
TAC
QT Input Channel

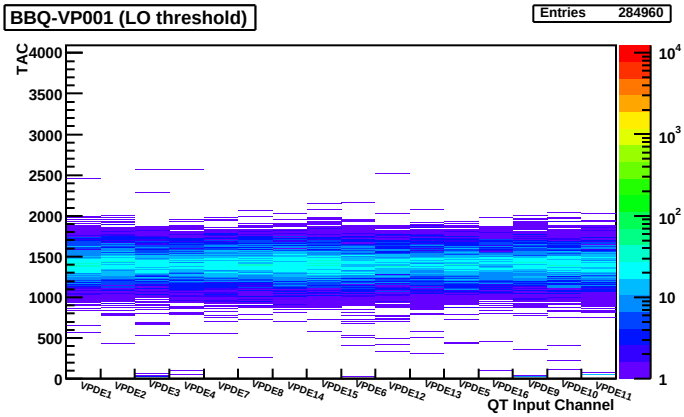

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 284960
ADC
QT Input Channel

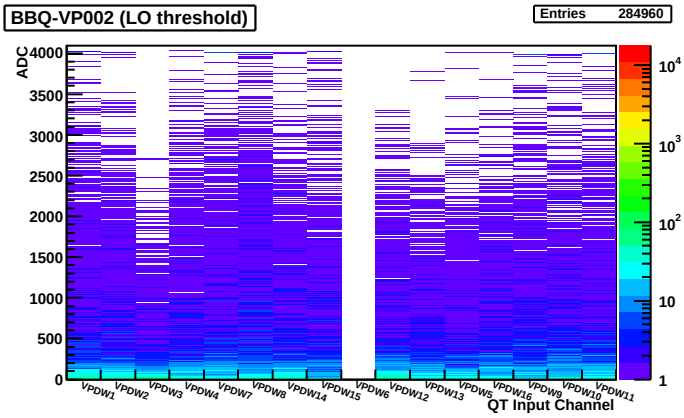

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 284960
TAC
QT Input Channel

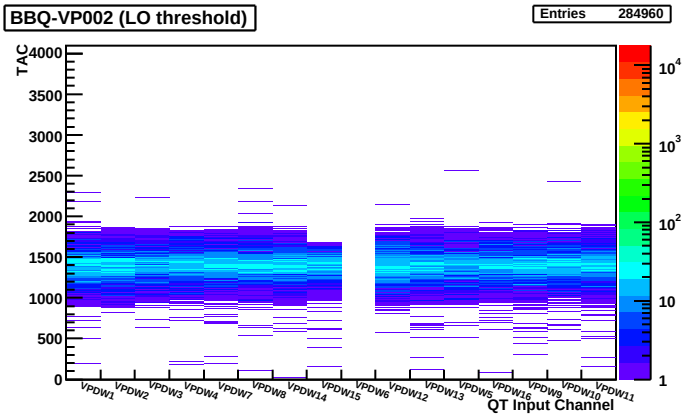

BBQ-ZD001 (ZDC TOWER)
Entries 284960
ADC

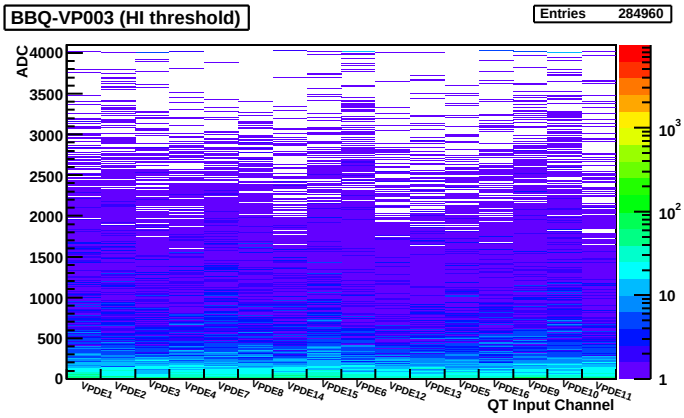

BBQ-ZD001 (ZDC TOWER)
Entries 284960
TAC

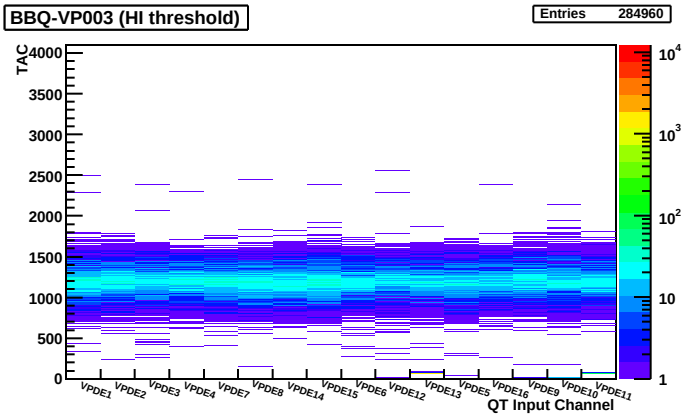

BBQ-VP001 (LO threshold)
Entries 284960
ADC
QT Input Channel

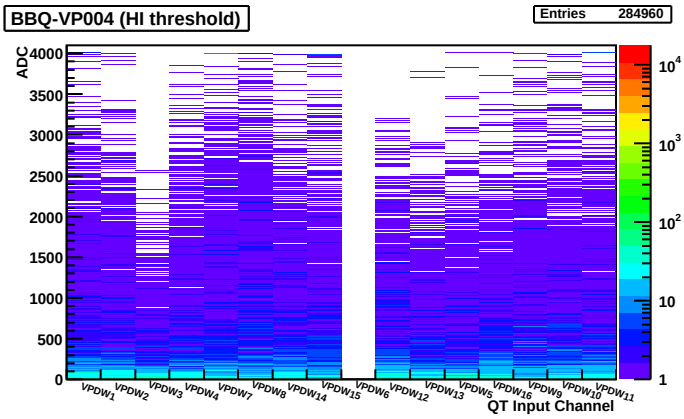

BBQ-VP001 (LO threshold)
Entries 284960
TAC
QT Input Channel

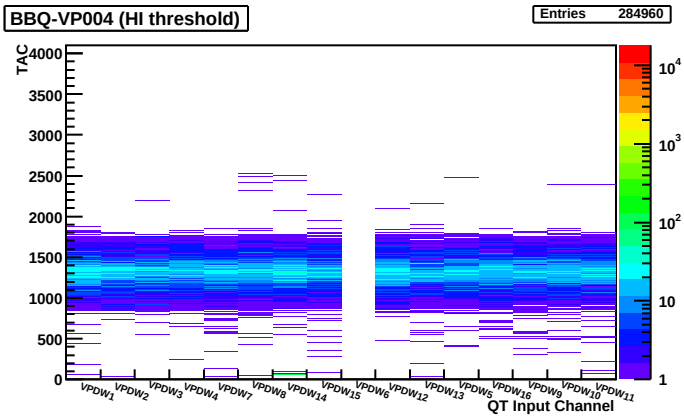

BBQ-VP002 (LO threshold)
Entries 284960
ADC
QT Input Channel

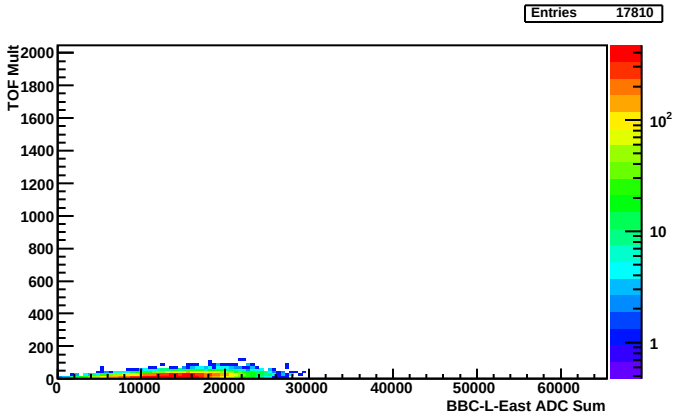

BBQ-VP002 (LO threshold)
Entries 284960
TAC
QT Input Channel

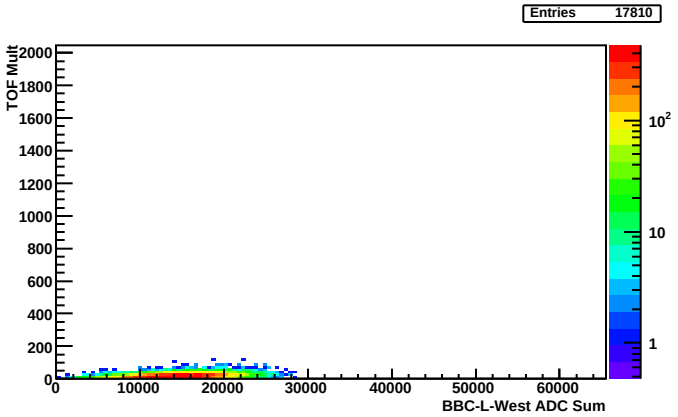

BBQ-VP003 (HI threshold)
Entries 284960
ADC
QT Input Channel

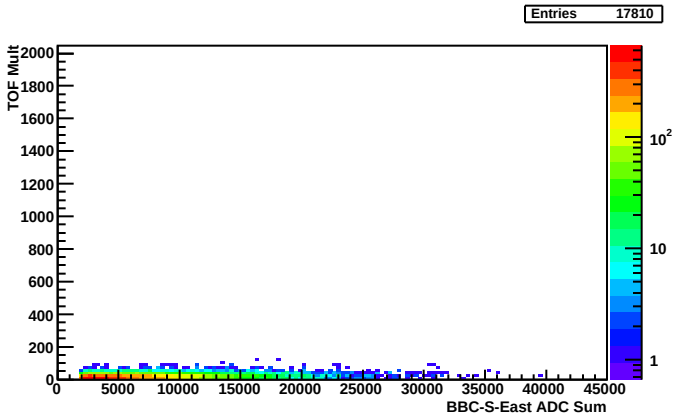

BBQ-VP003 (HI threshold)
Entries 284960
TAC
QT Input Channel

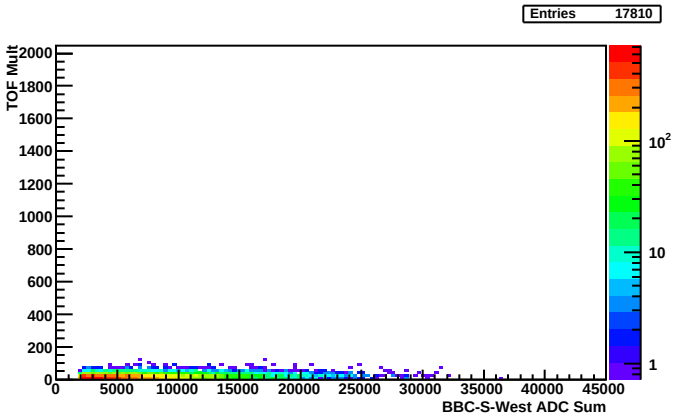

BBQ-VP004 (HI threshold)
Entries 284960
ADC
QT Input Channel

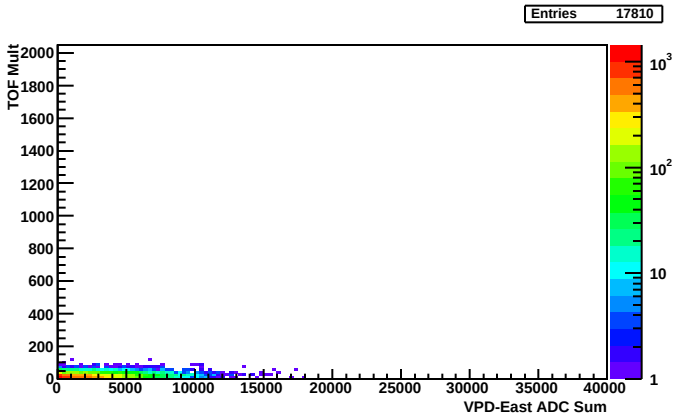

BBQ-VP004 (HI threshold)
Entries 284960
TAC
QT Input Channel

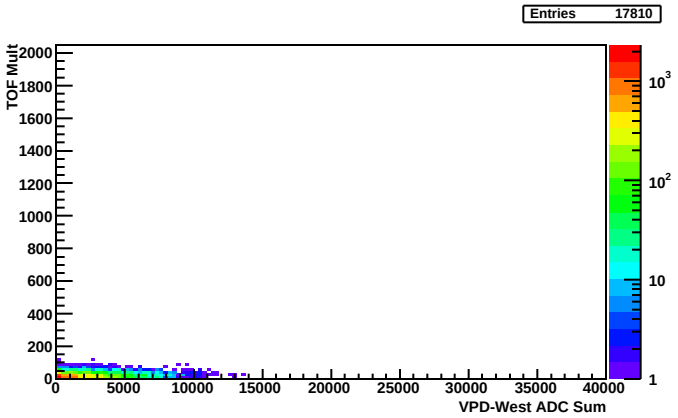

Entries 17810
TOF Mult
BBC-L-East ADC Sum

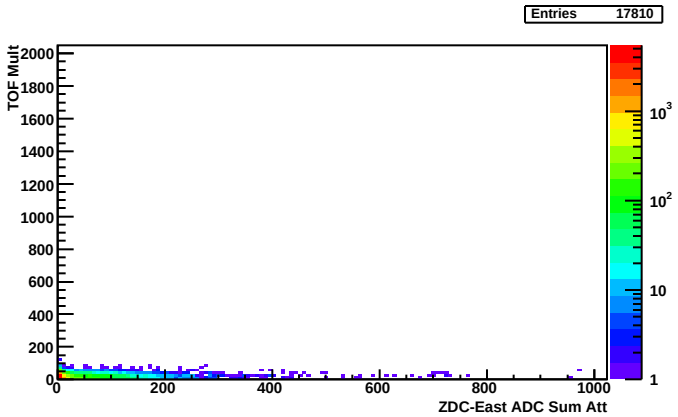

Entries 17810
TOF Mult
BBC-L-West ADC Sum

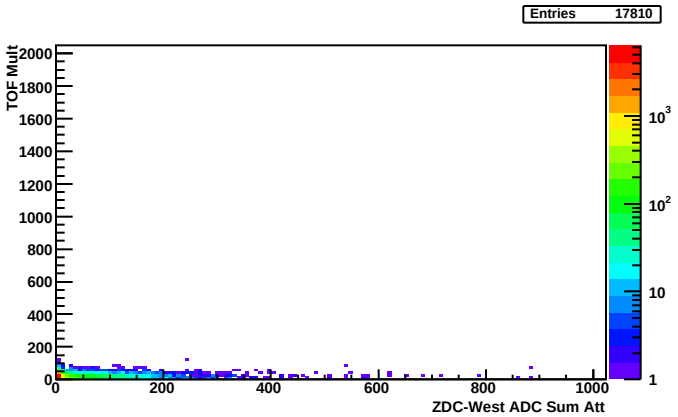

Entries 17810
TOF Mult
BBC-S-East ADC Sum

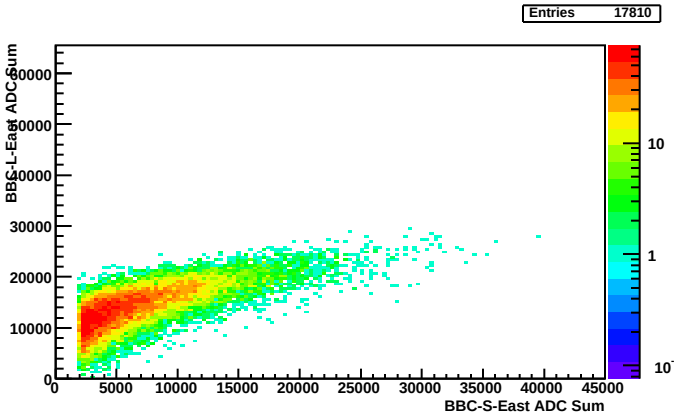

Entries 17810
TOF Mult
BBC-S-West ADC Sum

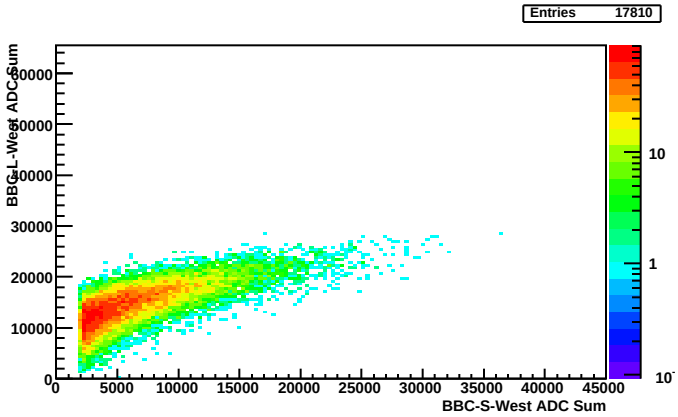

Entries 17810
TOF Mult
VPD-East ADC Sum

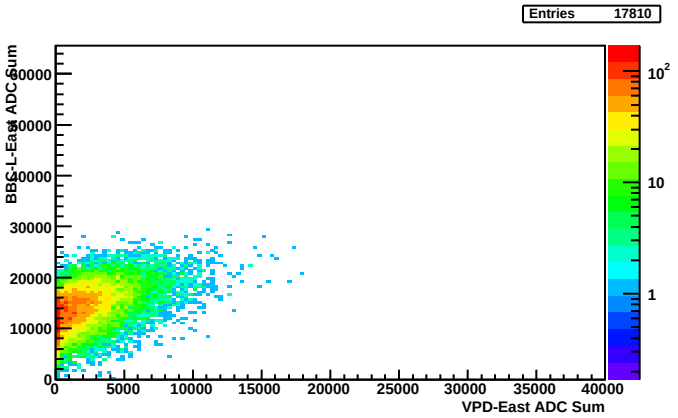

Entries 17810
TOF Mult
VPD-West ADC Sum

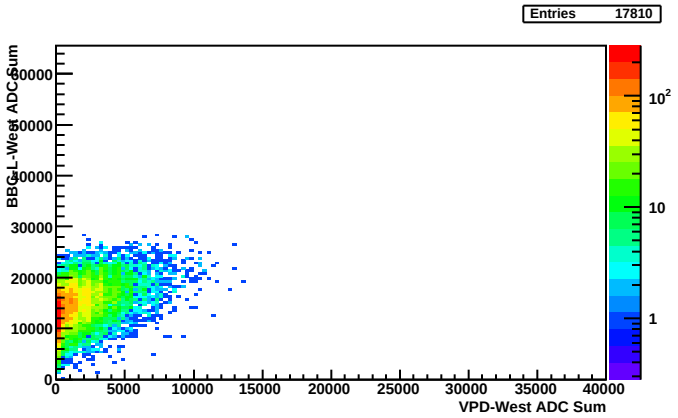

Entries 17810
TOF Mult
ZDC-East ADC Sum Att

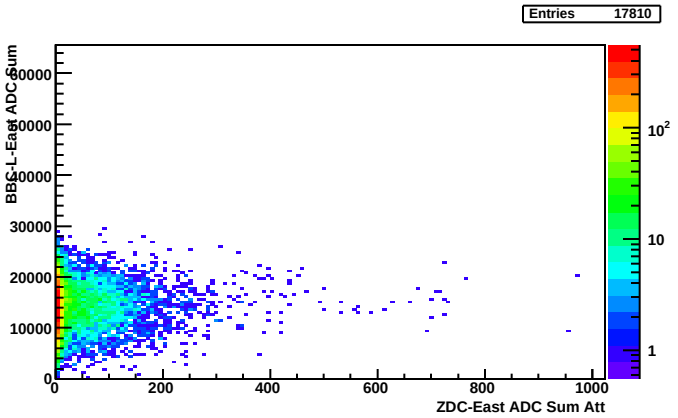

Entries 17810
TOF Mult
ZDC-West ADC Sum Att

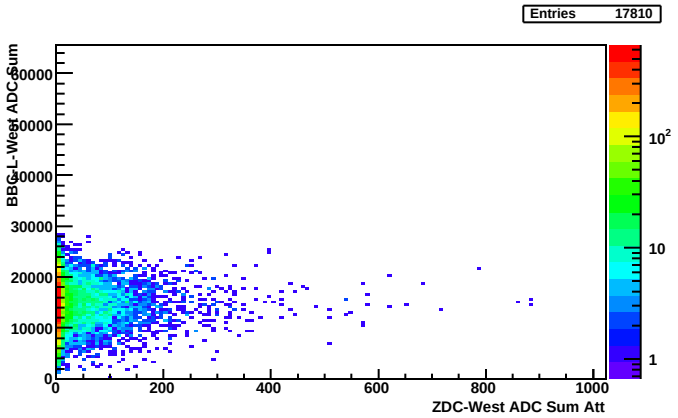

Entries 17810
BBC-L-East ADC Sum
BBC-S-East ADC Sum

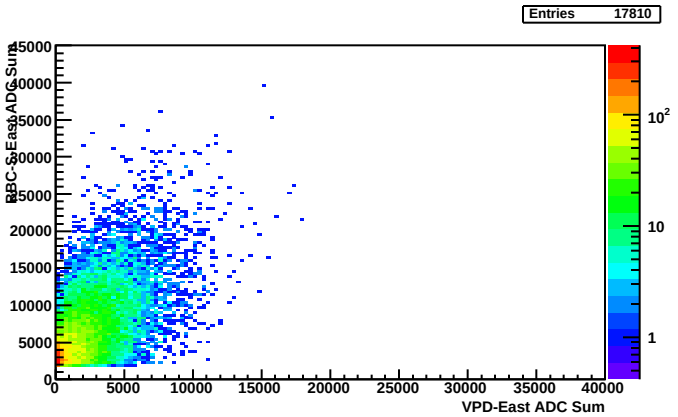

Entries 17810
BBC-L-West ADC Sum
BBC-S-West ADC Sum

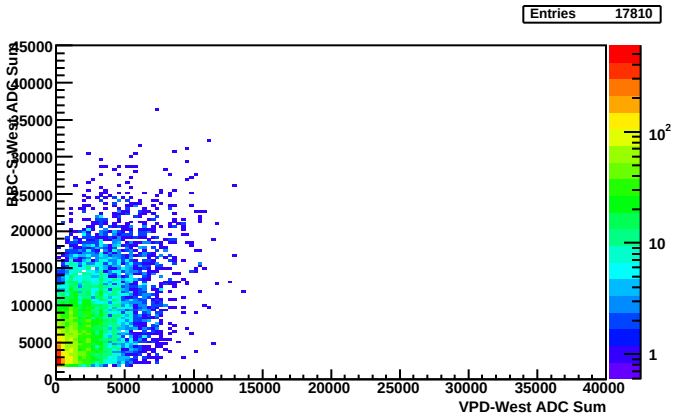

Entries 17810
BBC-L-East ADC Sum
VPD-East ADC Sum

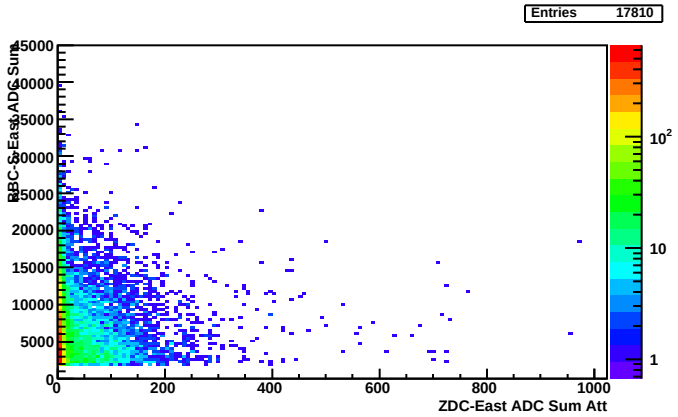

Entries 17810
BBC-L-West ADC Sum
VPD-West ADC Sum

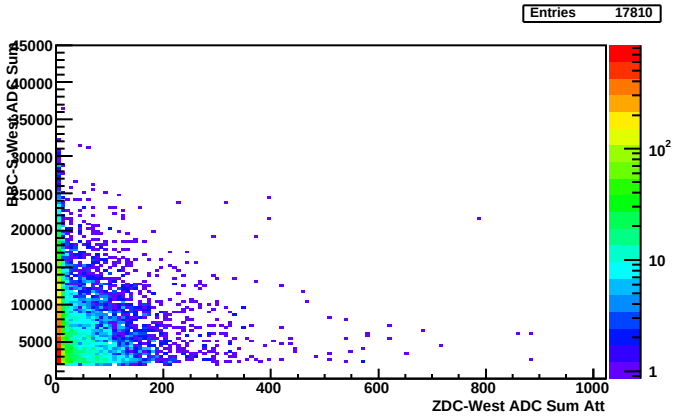

Entries 17810
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

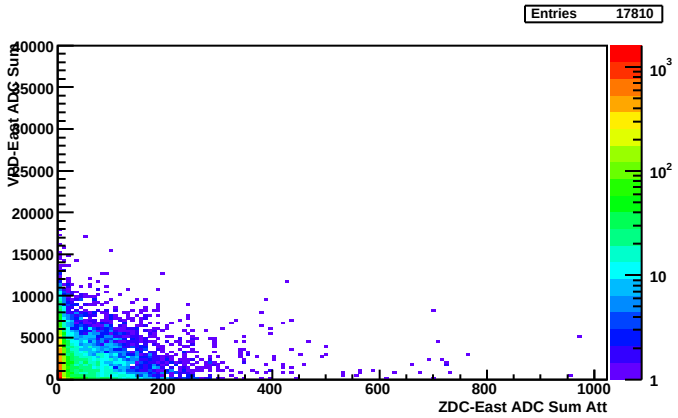

Entries 17810
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

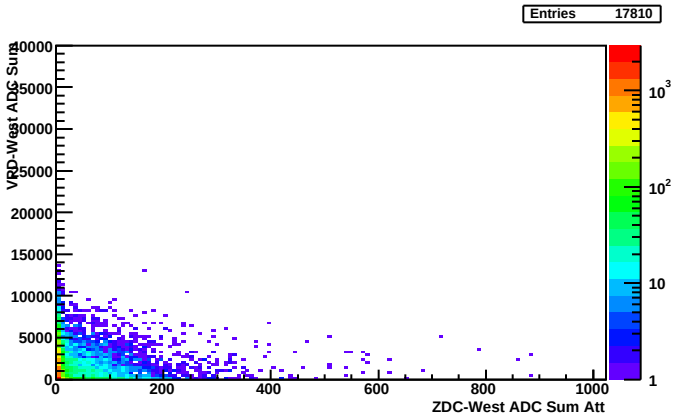

Entries 17810
BBC-S-East ADC Sum
VPD-East ADC Sum

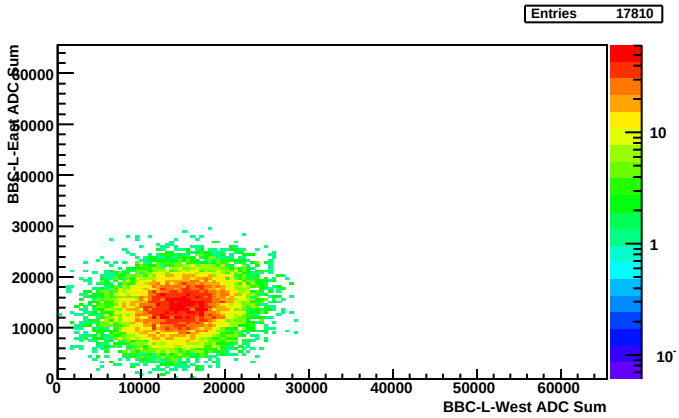

Entries 17810
BBC-S-West ADC Sum
VPD-West ADC Sum

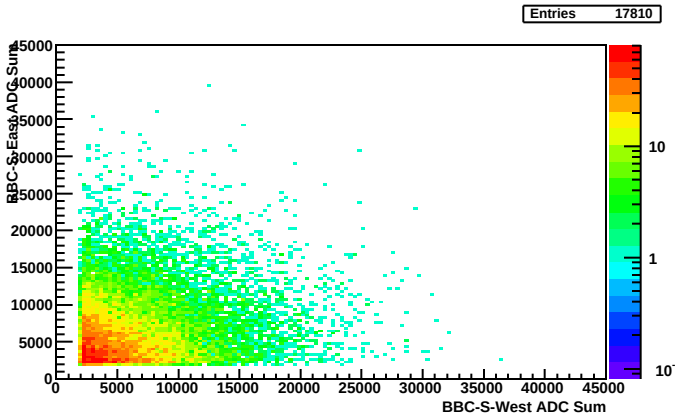

Entries 17810
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

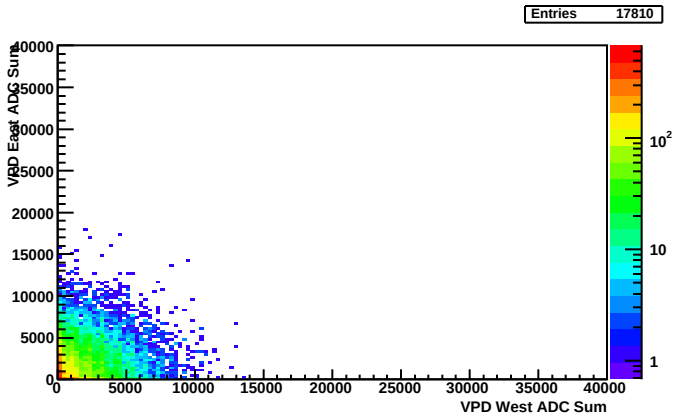

Entries 17810
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

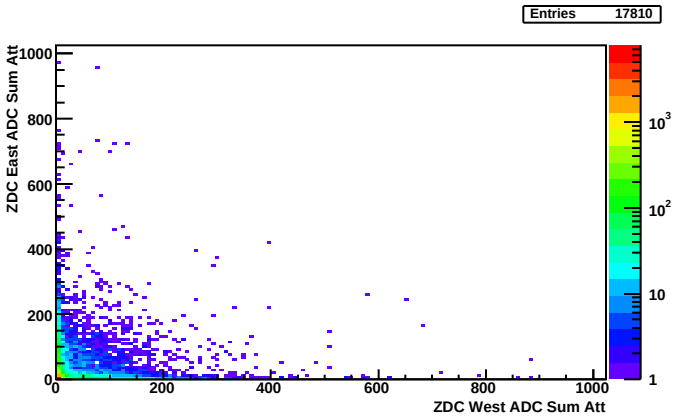

Entries 17810
VPD-East ADC Sum
ZDC-East ADC Sum Att

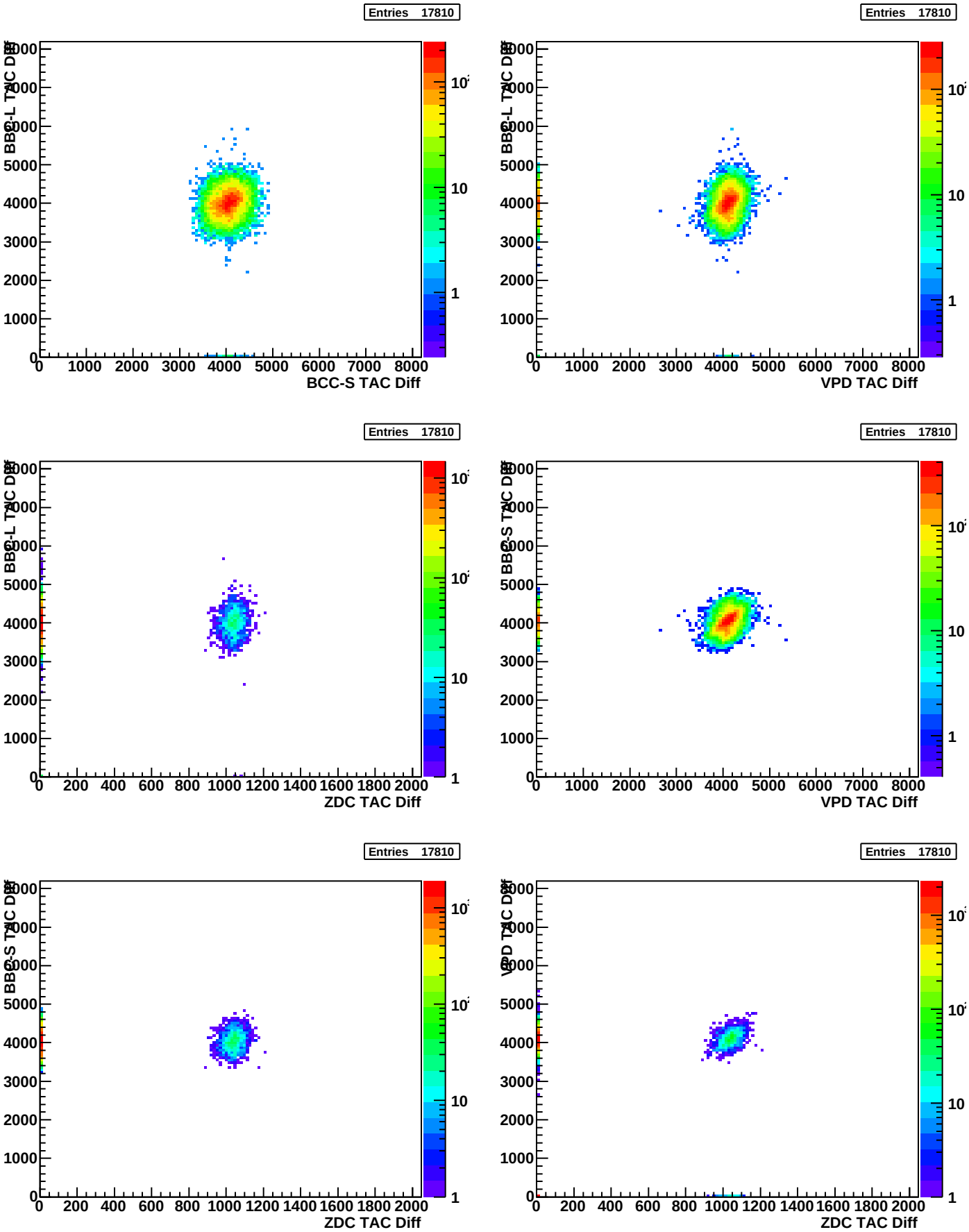

Entries 17810
VPD-West ADC Sum
ZDC-West ADC Sum Att

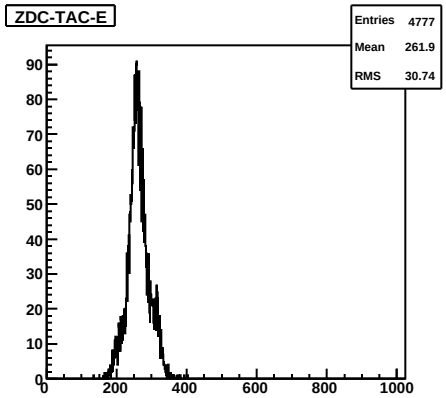

Entries 17810
BBC-L-East ADC Sum
BBC-L-West ADC Sum

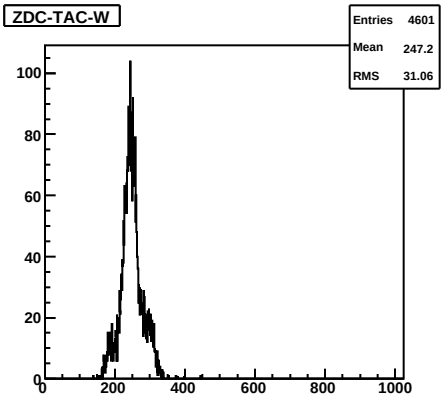

Entries 17810
BBC-S-East ADC Sum
BBC-S-West ADC Sum

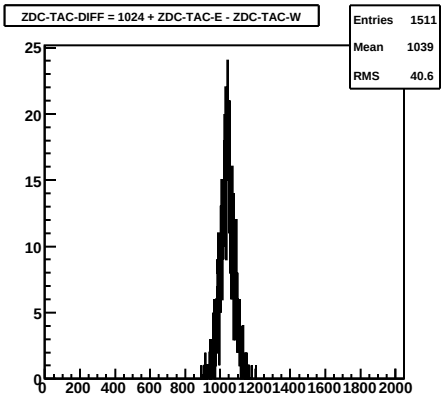

Entries 17810
VPD East ADC Sum
VPD West ADC Sum

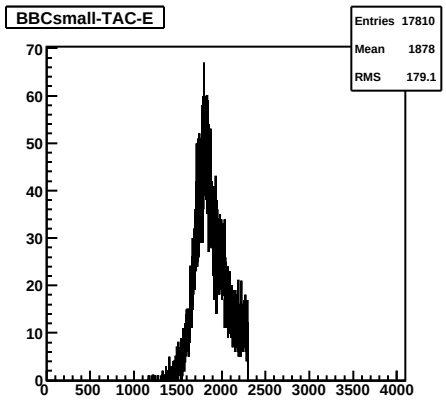

Entries 17810
ZDC East ADC Sum Att
ZDC West ADC Sum Att

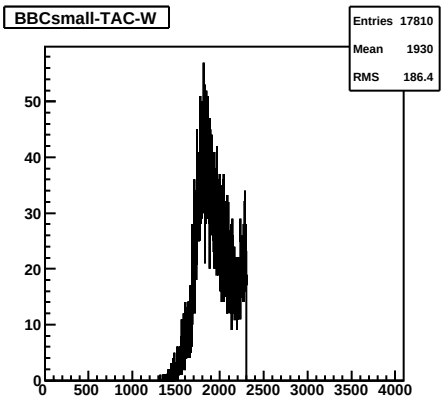

Entries 17810
BBC-L TAC Diff
BCC-S TAC Diff
Entries 17810
BBC-L TAC Diff
VPD TAC Diff
Entries 17810
BBC-L TAC Diff
ZDC TAC Diff
Entries 17810
BBC-S TAC Diff
VPD TAC Diff
Entries 17810
BBC-S TAC Diff
ZDC TAC Diff
Entries 17810
VPD TAC Diff
ZDC TAC Diff

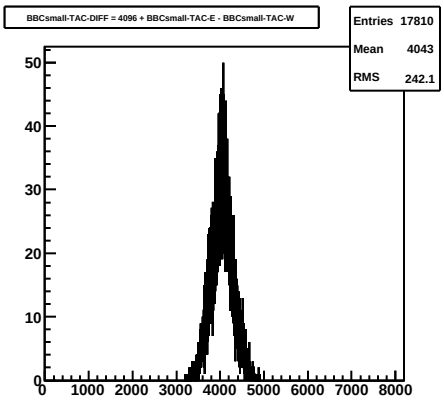

ZDC-TAC-E
Entries 4777
Mean 261.9
RMS 30.74

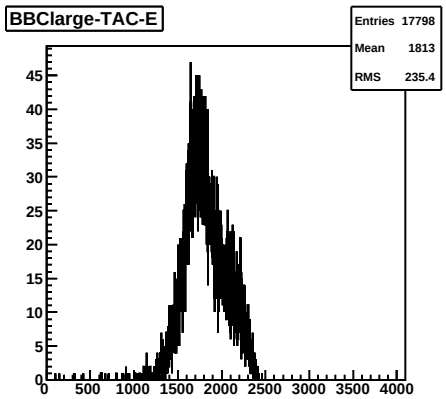

ZDC-TAC-W
Entries 4601
Mean 247.2
RMS 31.06

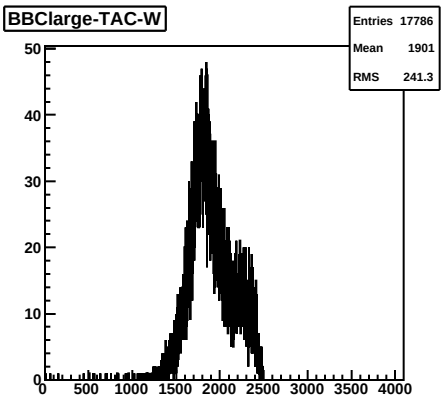

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1511
Mean 1039
RMS 40.6

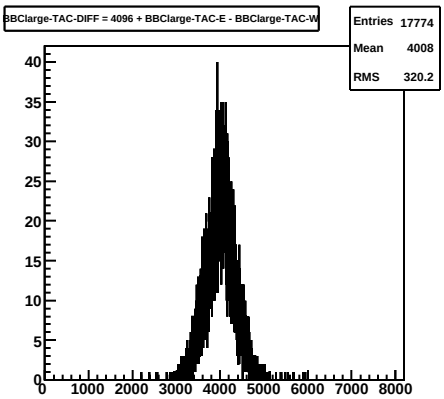

BBCsmall-TAC-E
Entries 17810
Mean 1878
RMS 179.1

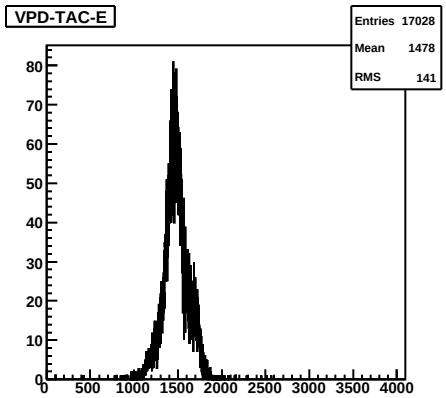

BBCsmall-TAC-W
Entries 17810
Mean 1930
RMS 186.4

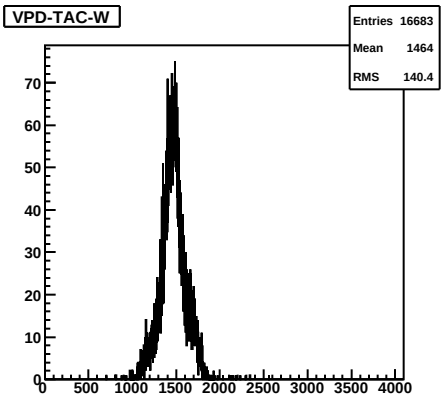

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 17810
Mean 4043
RMS 242.1

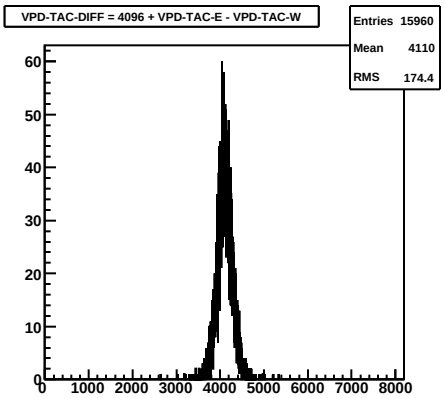

BBClarge-TAC-E
Entries 17798
Mean 1813
RMS 235.4

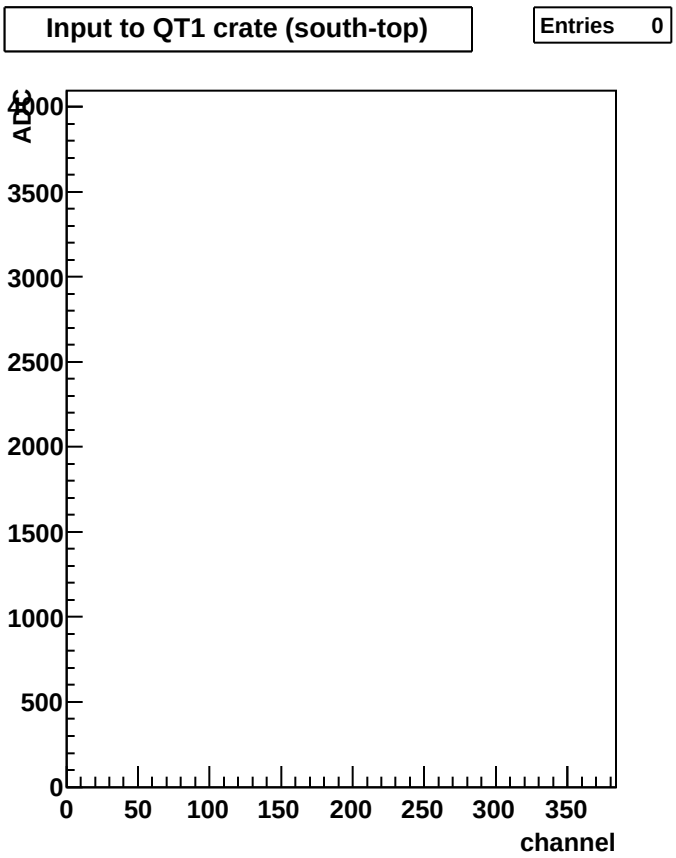

BBClarge-TAC-W
Entries 17786
Mean 1901
RMS 241.3

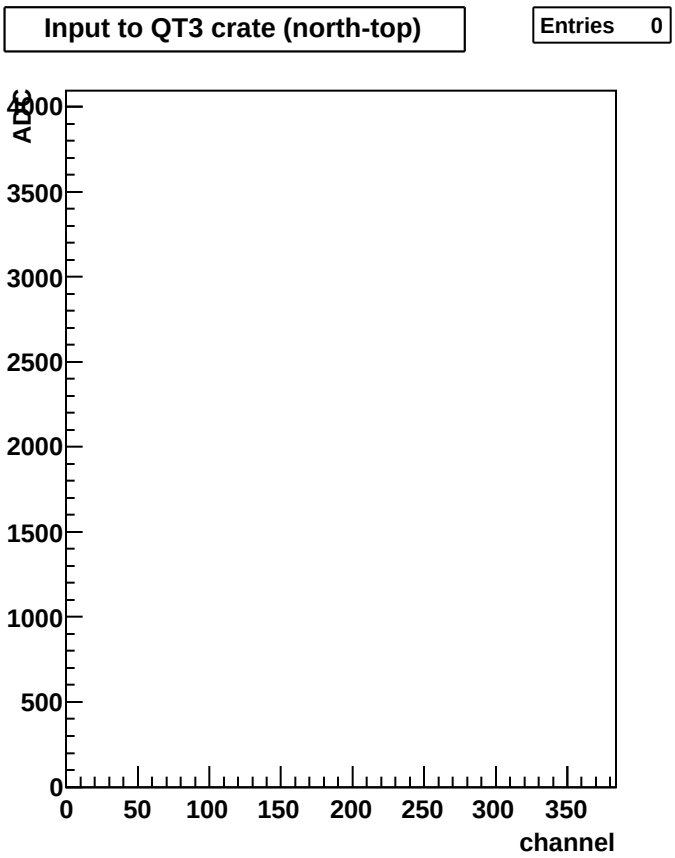

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 17774
Mean 4008
RMS 320.2

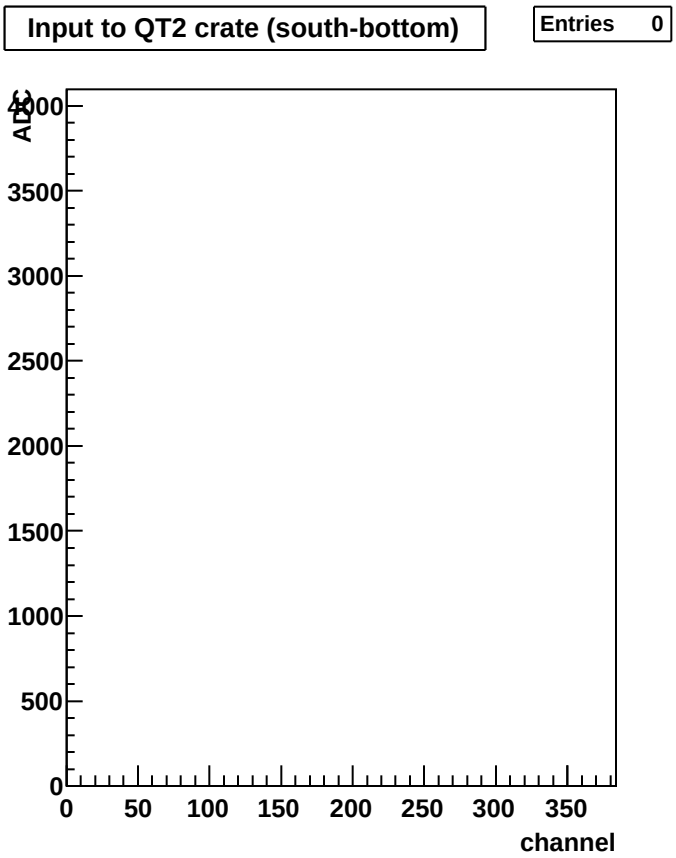

VPD-TAC-E
Entries 17028
Mean 1478
RMS 141

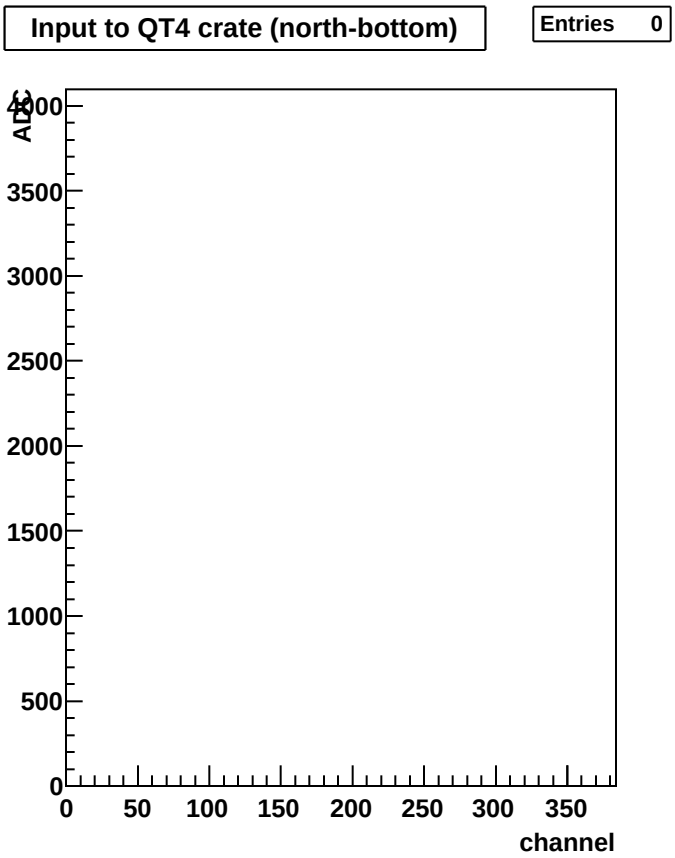

VPD-TAC-W
Entries 16683
Mean 1464
RMS 140.4

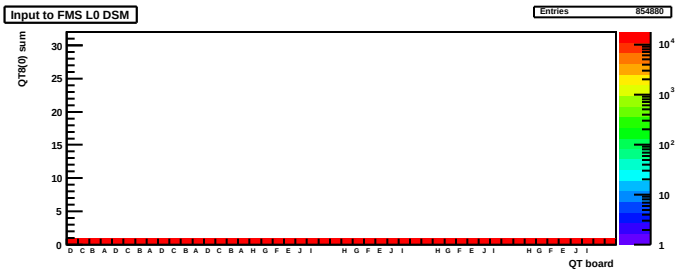

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 15960
Mean 4110
RMS 174.4

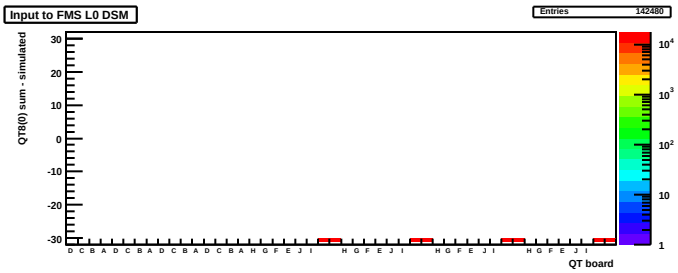

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

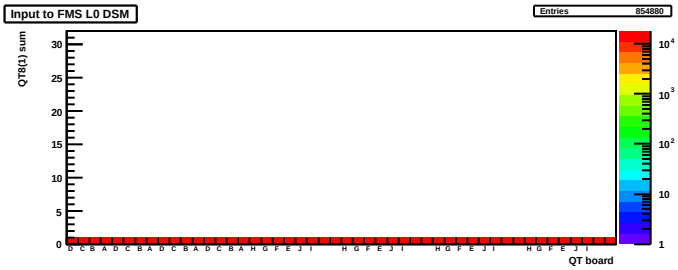

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

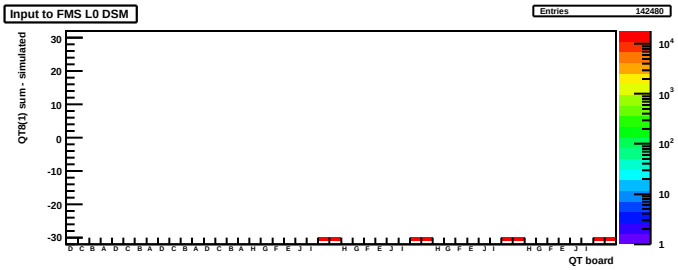

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

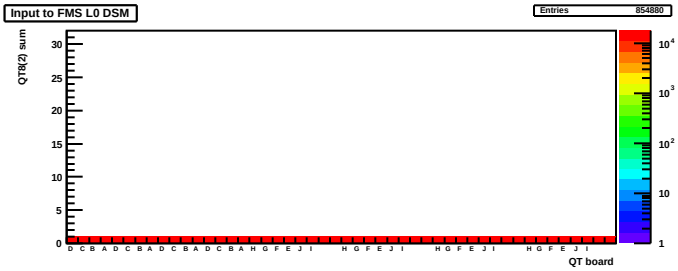

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

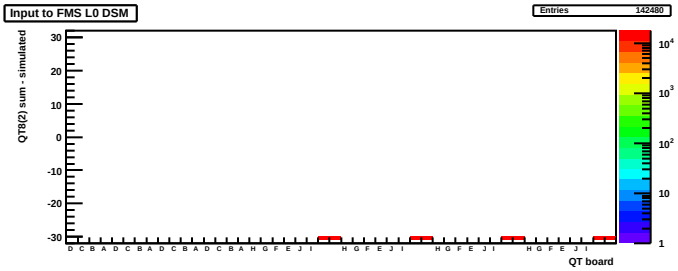

Input to FMS L0 DSM
Entries 854880
QT8(0) sum
QT board

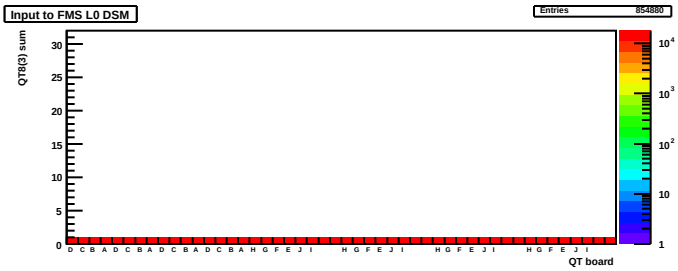

Input to FMS L0 DSM
Entries 142480
QT8(0) sum - simulated
QT board

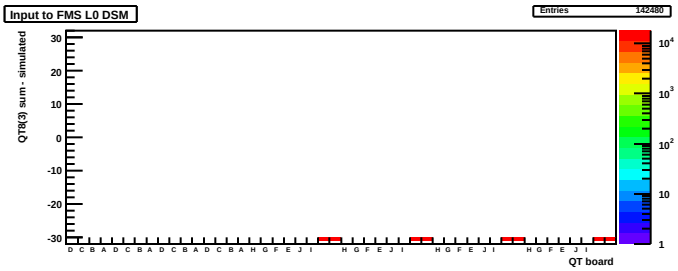

Input to FMS L0 DSM
Entries 854880
QT8(1) sum
QT board

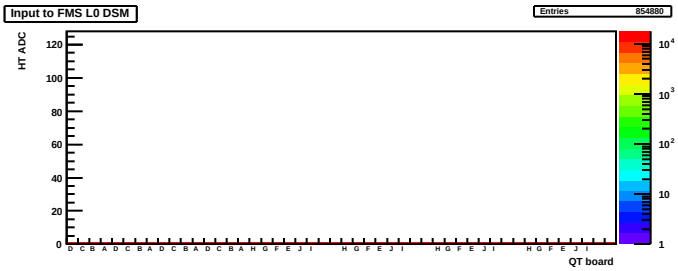

Input to FMS L0 DSM
Entries 142480
QT8(1) sum - simulated
QT board

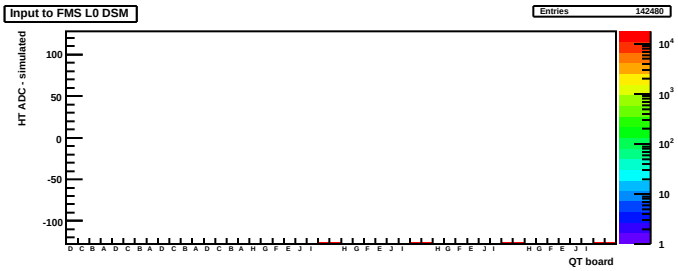

Input to FMS L0 DSM
Entries 854880
QT8(2) sum
QT board

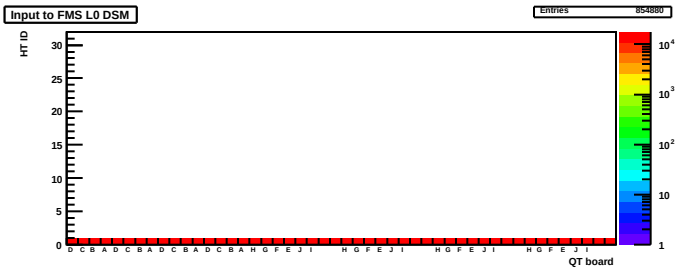

Input to FMS L0 DSM
Entries 142480
QT8(2) sum - simulated
QT board

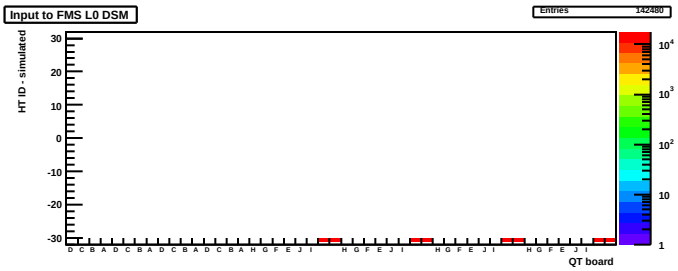

Input to FMS L0 DSM
Entries 854880
QT8(3) sum
QT board

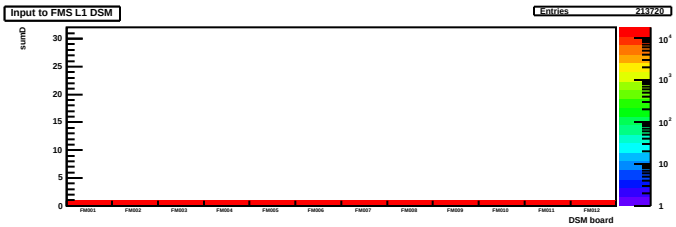

Input to FMS L0 DSM
Entries 142480
QT8(3) sum - simulated
QT board

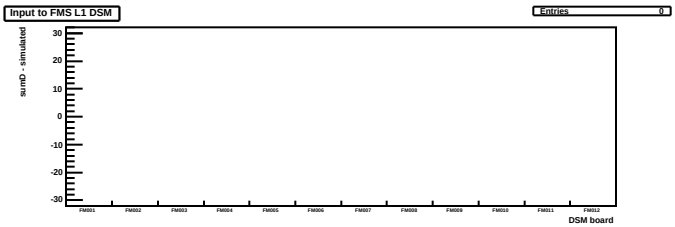

Input to FMS L0 DSM
Entries 854880
HT ADC
QT board

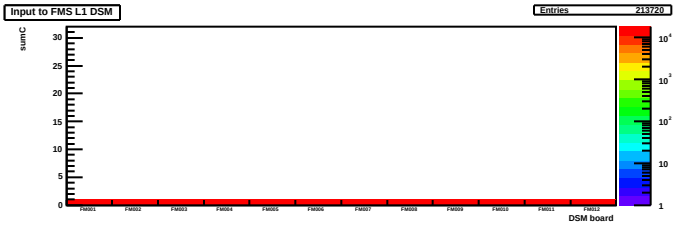

Input to FMS L0 DSM
Entries 142480
HT ADC - simulated
QT board

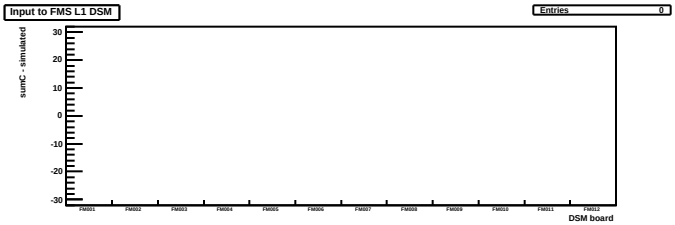

Input to FMS L0 DSM
Entries 854880
HT ID
QT board

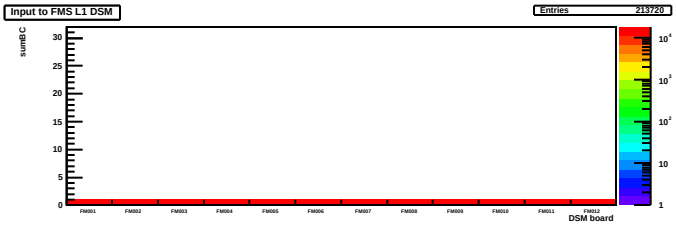

Input to FMS L0 DSM
Entries 142480
HT ID - simulated
QT board

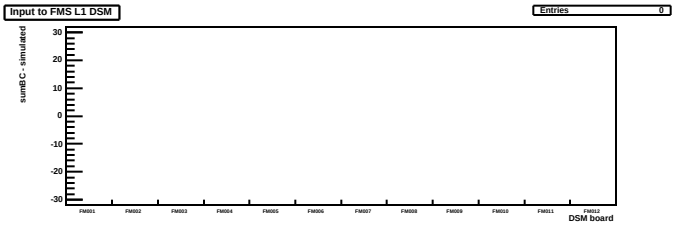

Input to FMS L1 DSM
Entries 213720
sumD
DSM board

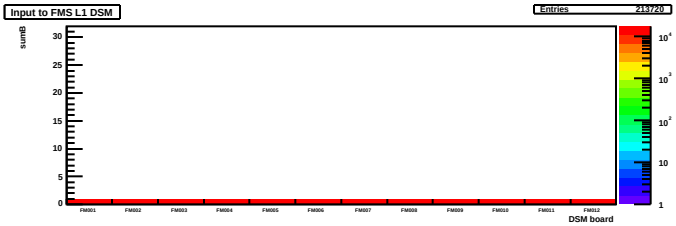

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

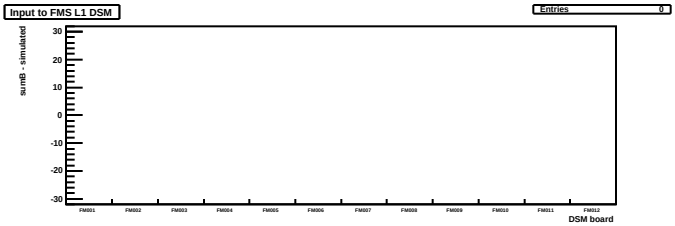

Input to FMS L1 DSM
Entries 213720
sumC
DSM board

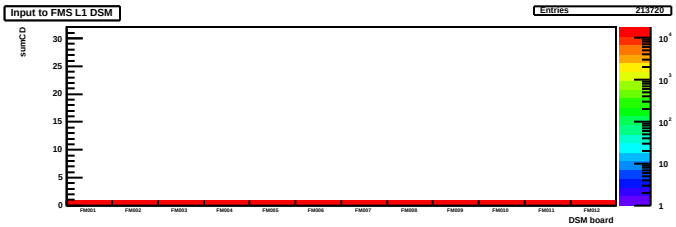

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

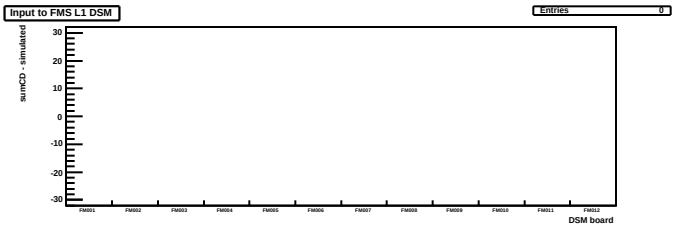

Input to FMS L1 DSM
Entries 213720
sumBC
DSM board

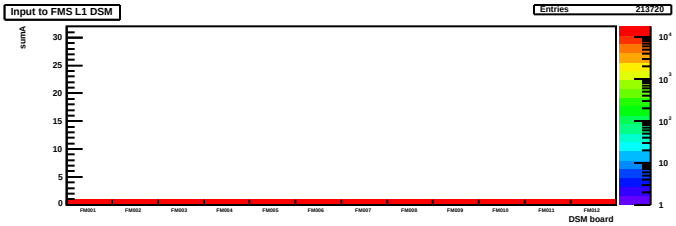

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

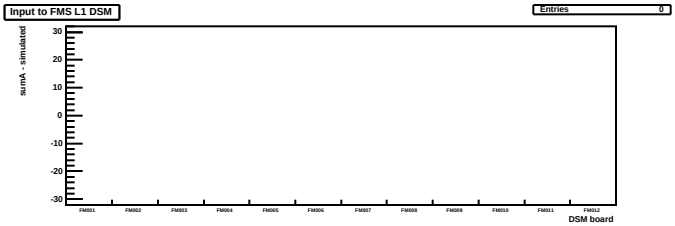

Input to FMS L1 DSM
Entries 213720
sumB
DSM board

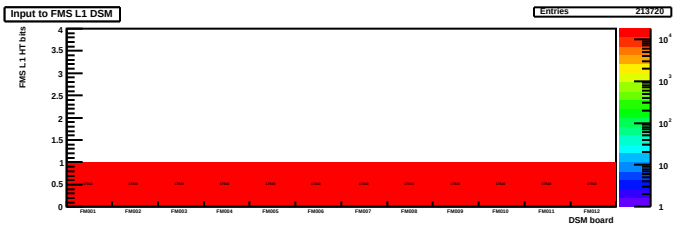

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

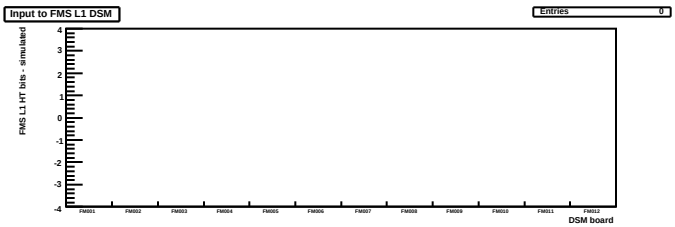

Input to FMS L1 DSM
Entries 213720
sumCD
DSM board

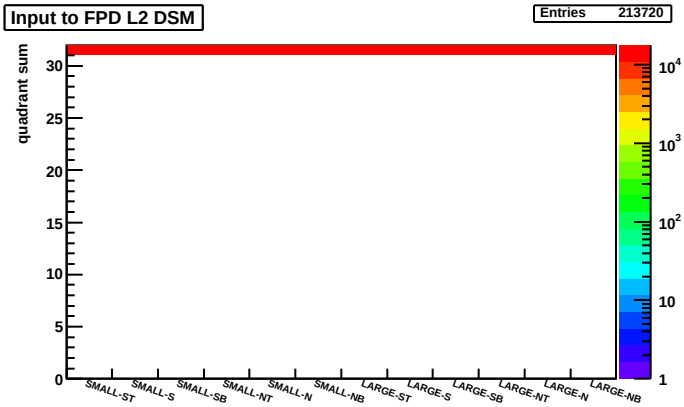

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

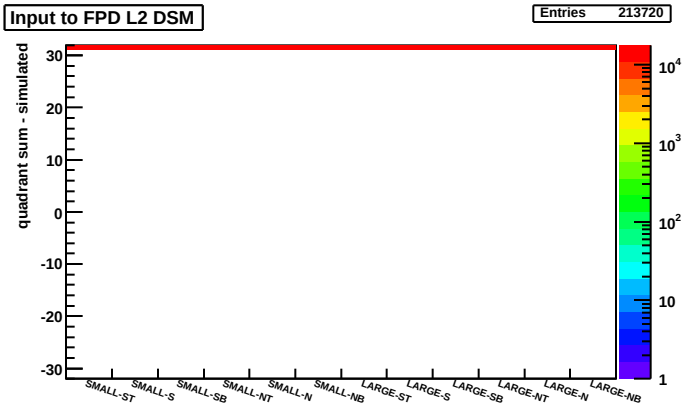

Input to FMS L1 DSM
Entries 213720
sumA
DSM board

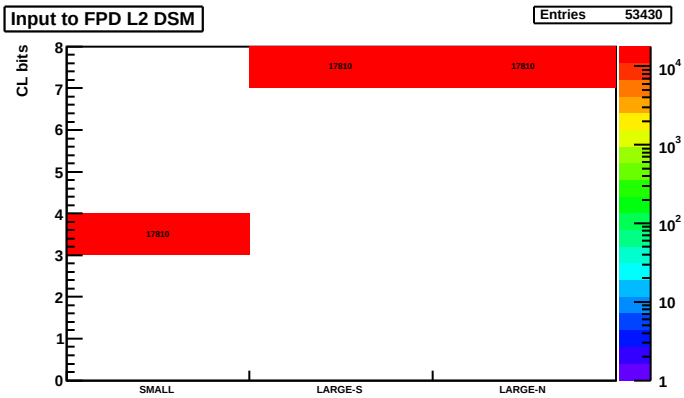

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

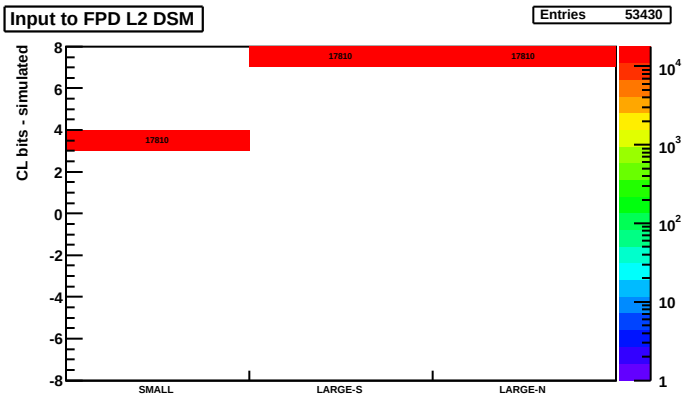

Input to FMS L1 DSM
Entries 213720
FMS L1 HT bits
DSM board

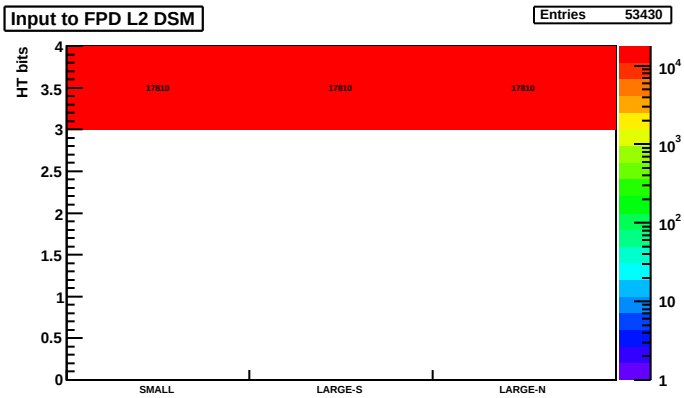

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

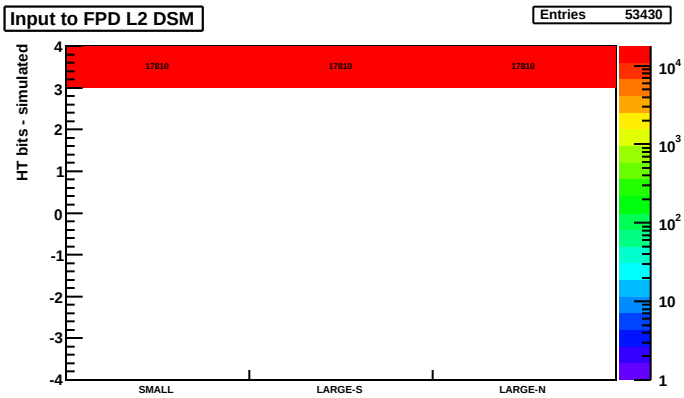

Input to FPD L2 DSM
Entries 213720
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

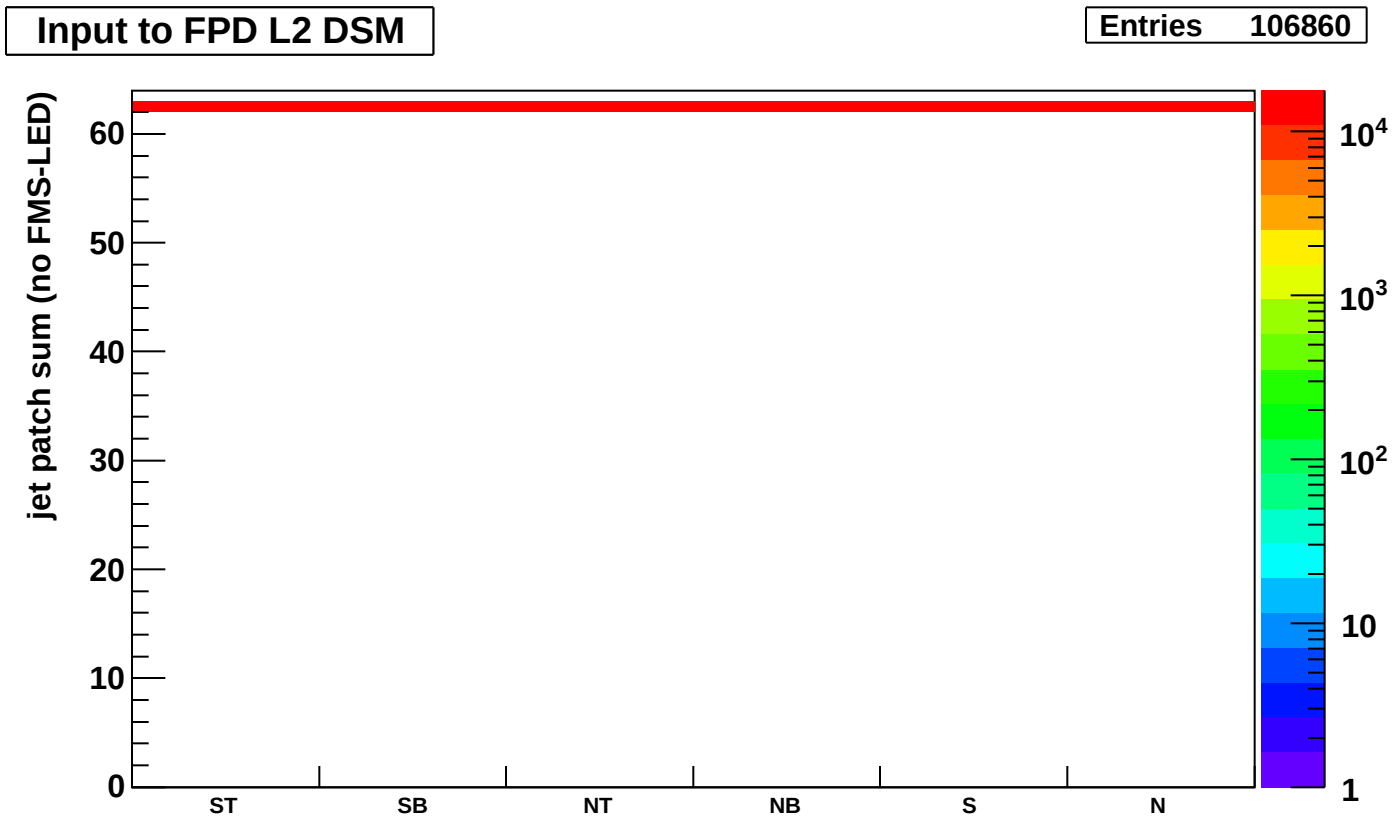

Input to FPD L2 DSM
Entries 213720
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

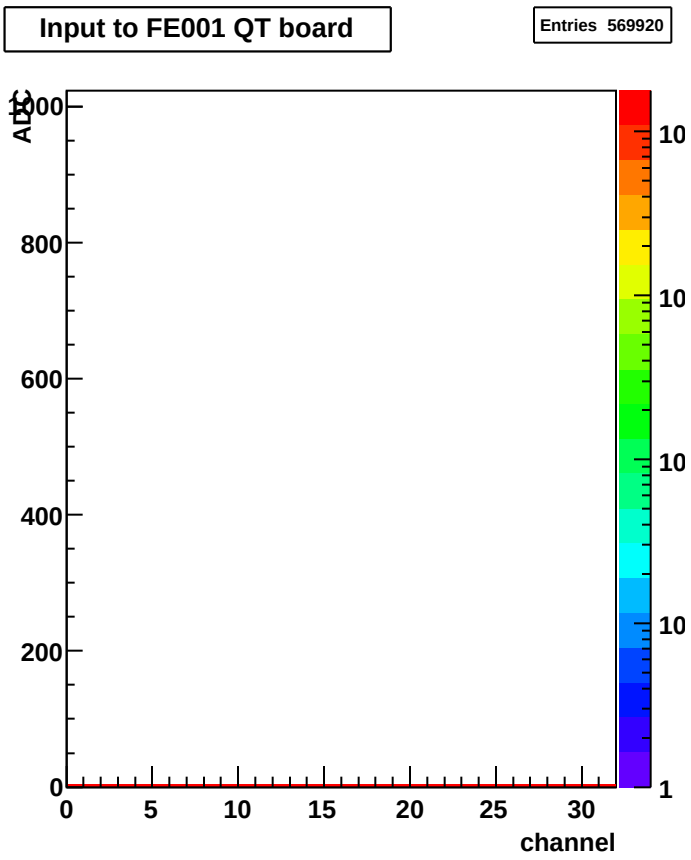

Input to FPD L2 DSM
Entries 53430
CL bits
17810
17810
17810
SMALL
LARGE-S
LARGE-N

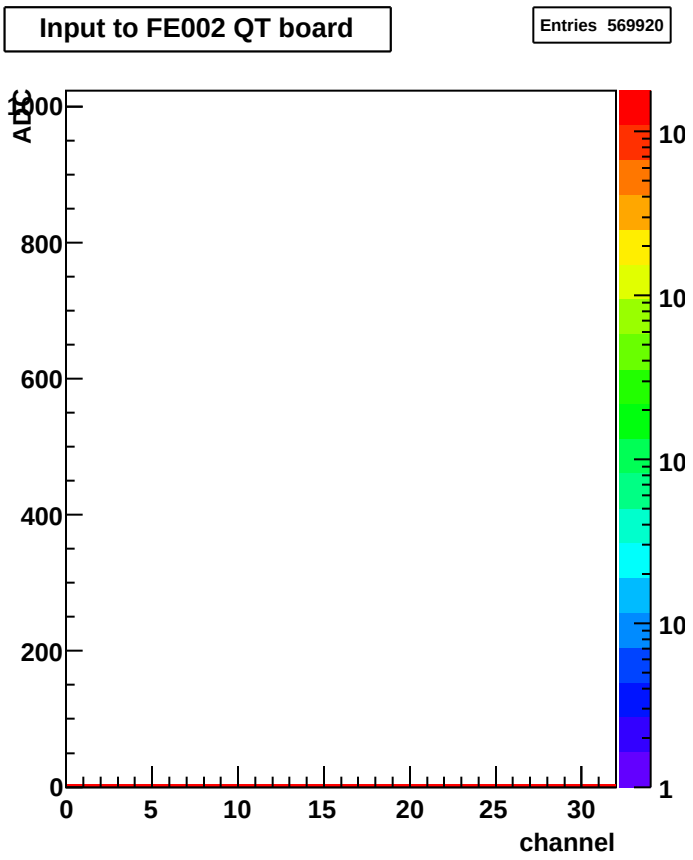

Input to FPD L2 DSM
Entries 53430
CL bits - simulated
17810
17810
17810
SMALL
LARGE-S
LARGE-N

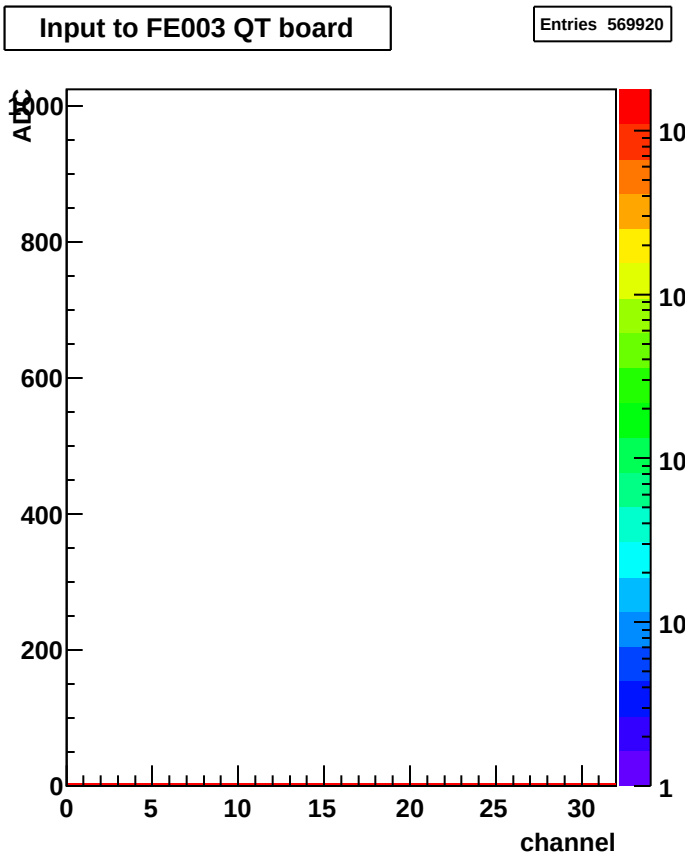

Input to FPD L2 DSM
Entries 53430
HT bits
17810
17810
17810
SMALL
LARGE-S
LARGE-N

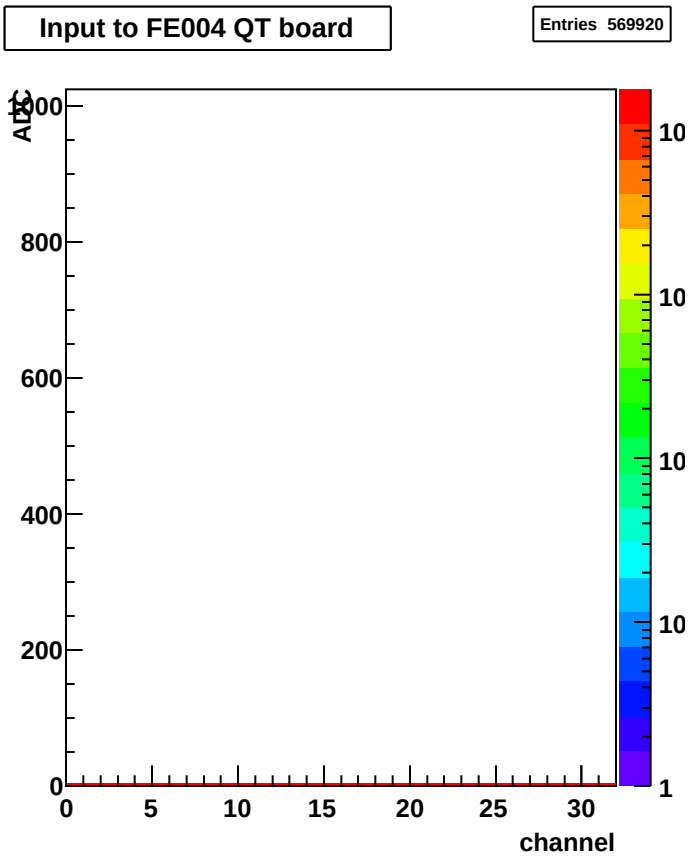

Input to FPD L2 DSM
Entries 53430
HT bits - simulated
17810
17810
17810
SMALL
LARGE-S
LARGE-N

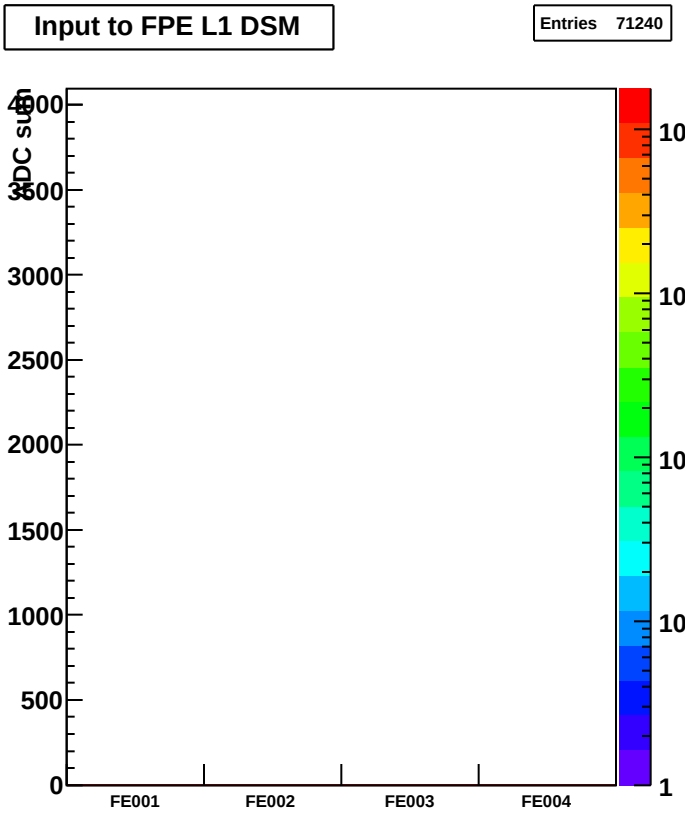

Input to FPD L2 DSM
Entries 106860
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

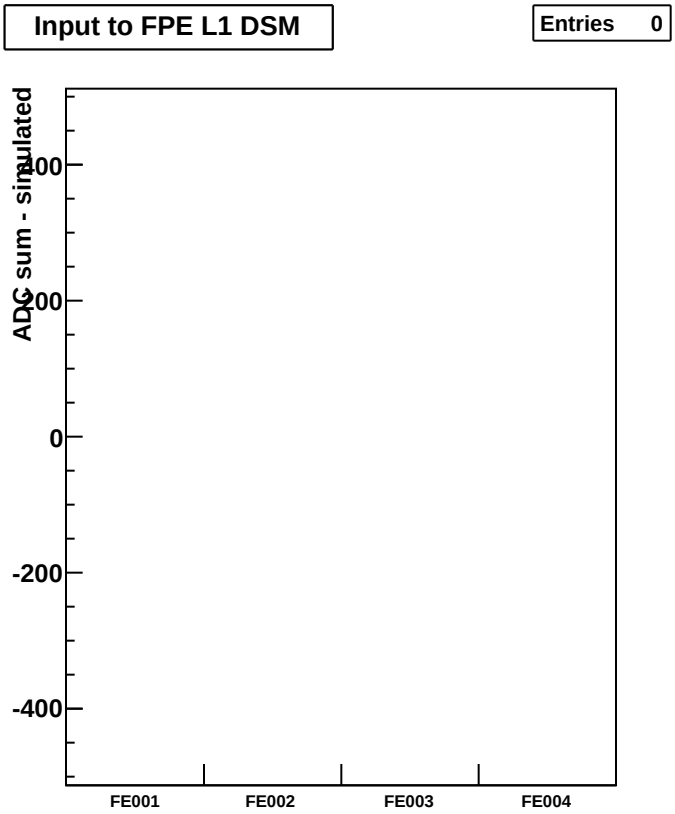

Input to FE001 QT board
Entries 569920
ADC
channel


Input to FE002 QT board
Entries 569920
ADC
channel

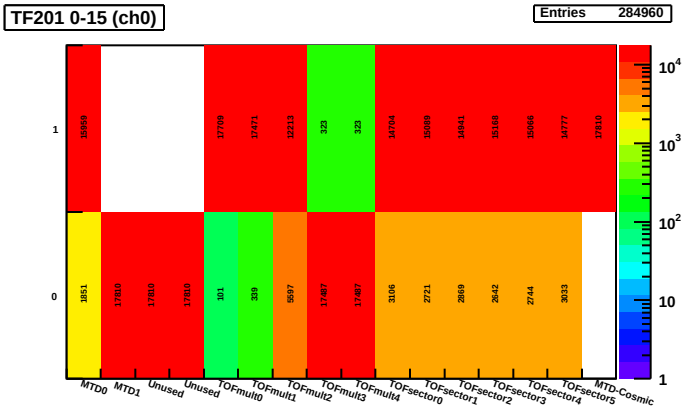

Input to FE003 QT board
Entries 569920
ADC
channel


Input to FE004 QT board
Entries 569920
ADC
channel

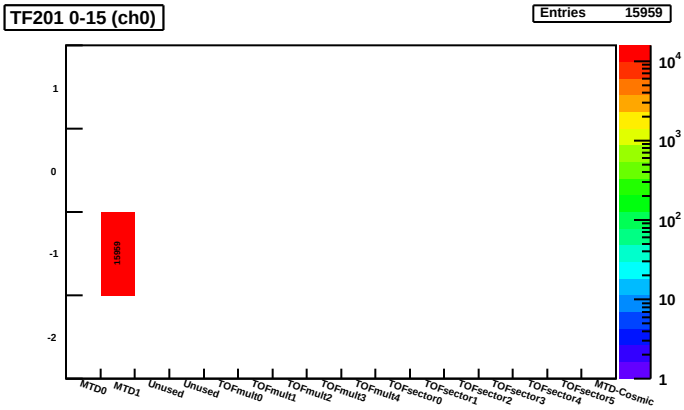

Input to FPE L1 DSM
Entries 71240
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004

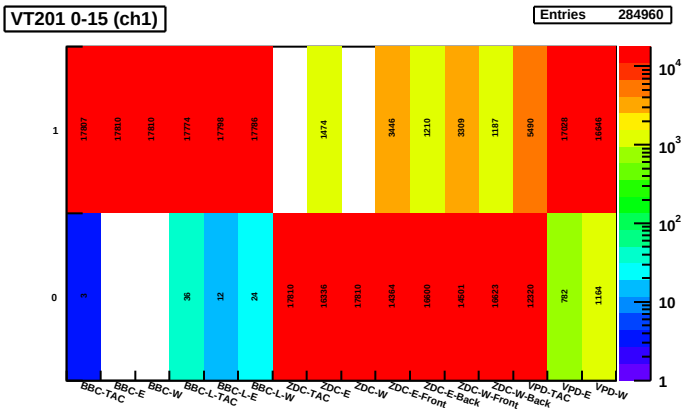
TF201 0-15 (ch0)



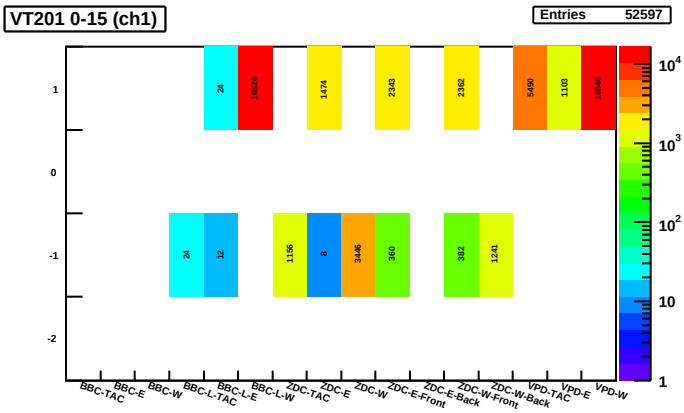
TF201 0-15 (ch0)



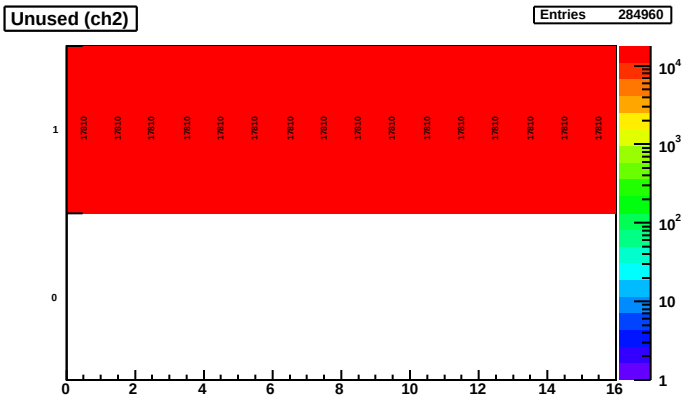
VT201 0-15 (ch1)

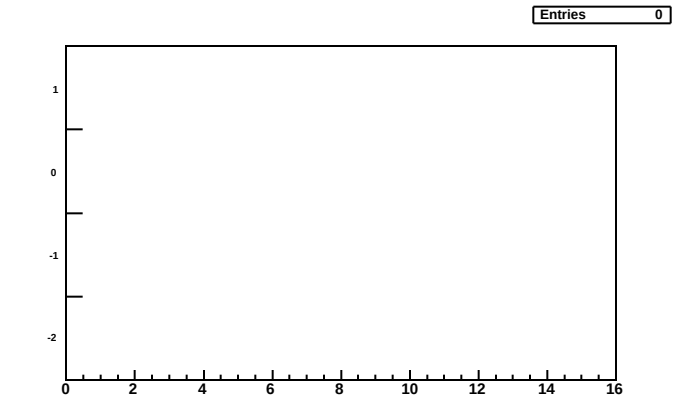


VT201 0-15 (ch1)

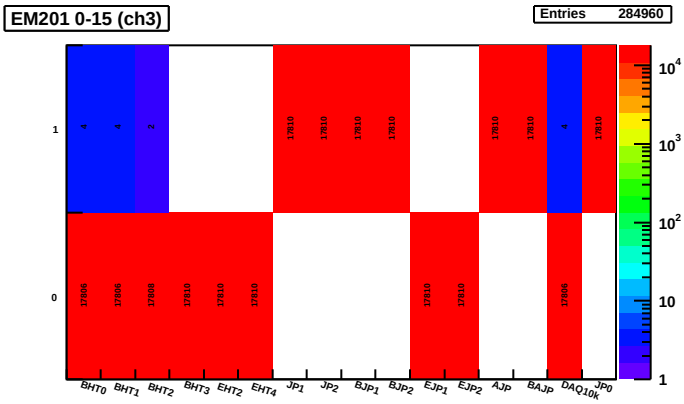


Unused (ch2)





EM201 0-15 (ch3)



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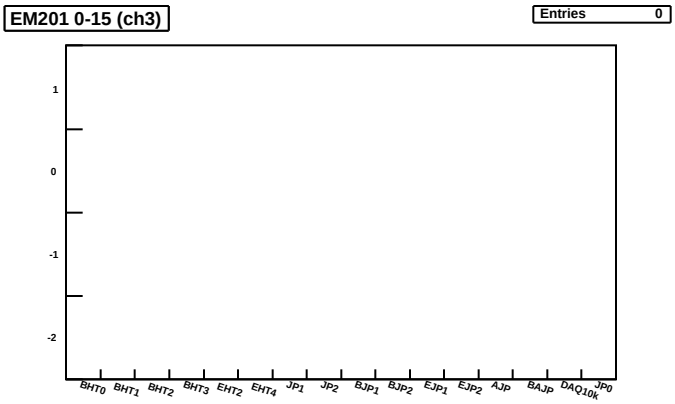
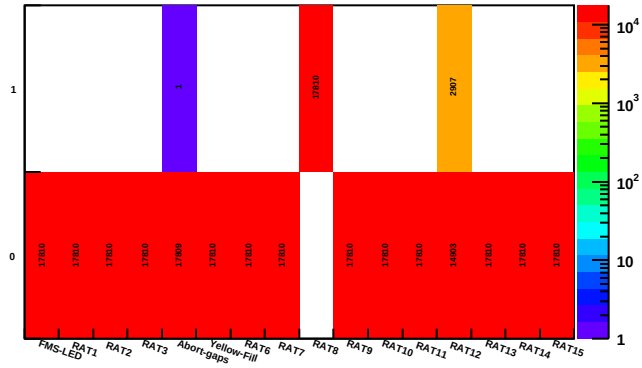
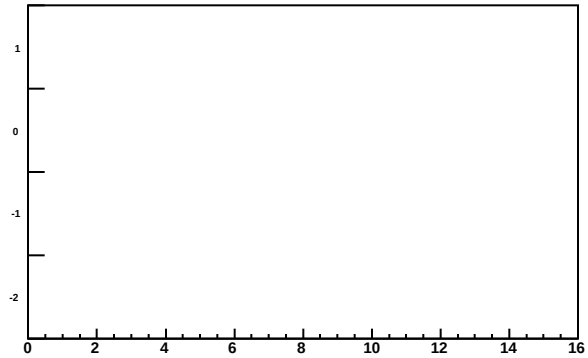
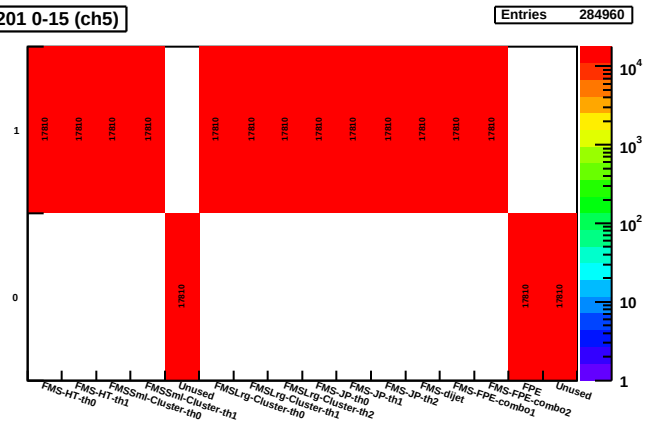


RAT board (ch4)





FP201 0-15 (ch5)



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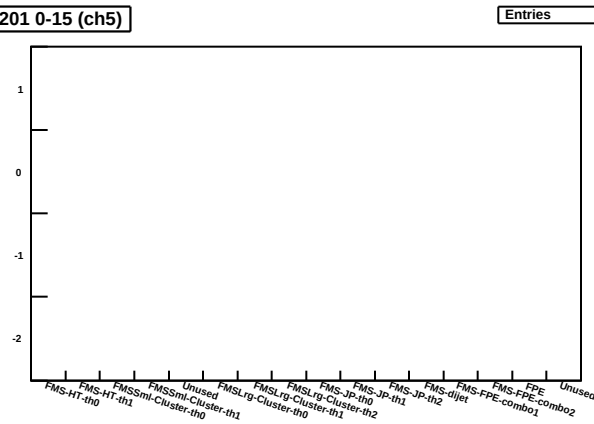

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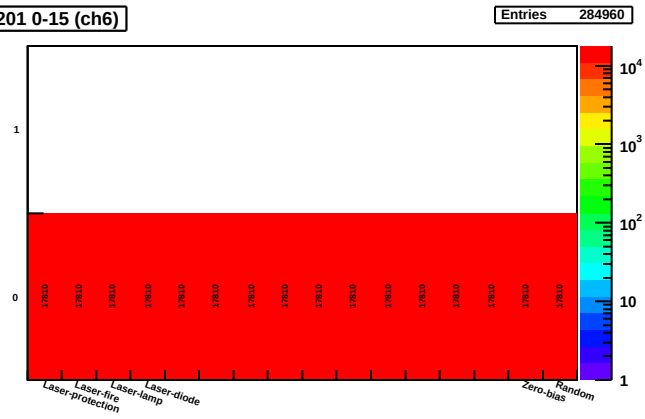

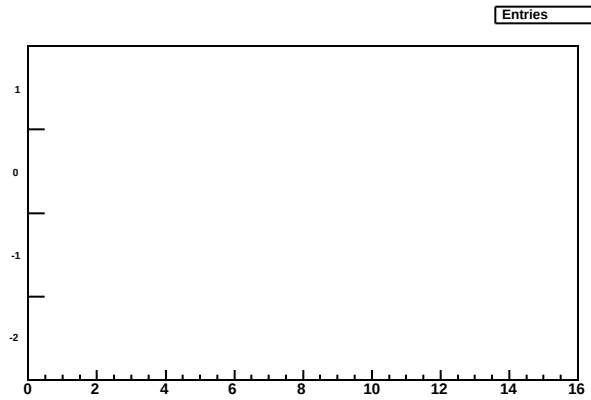
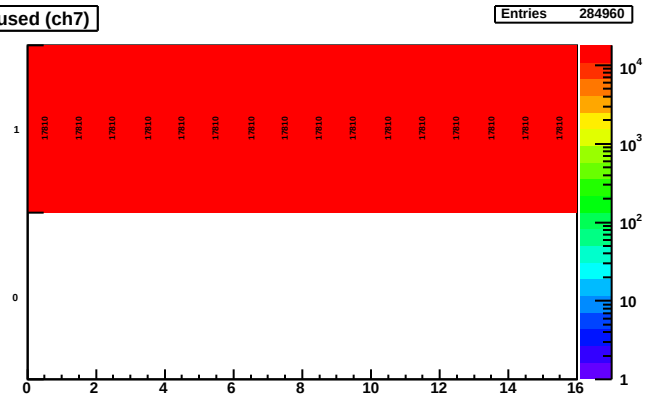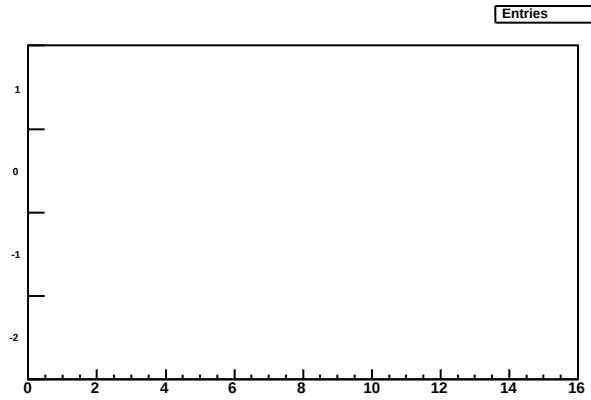
Unused (ch7)