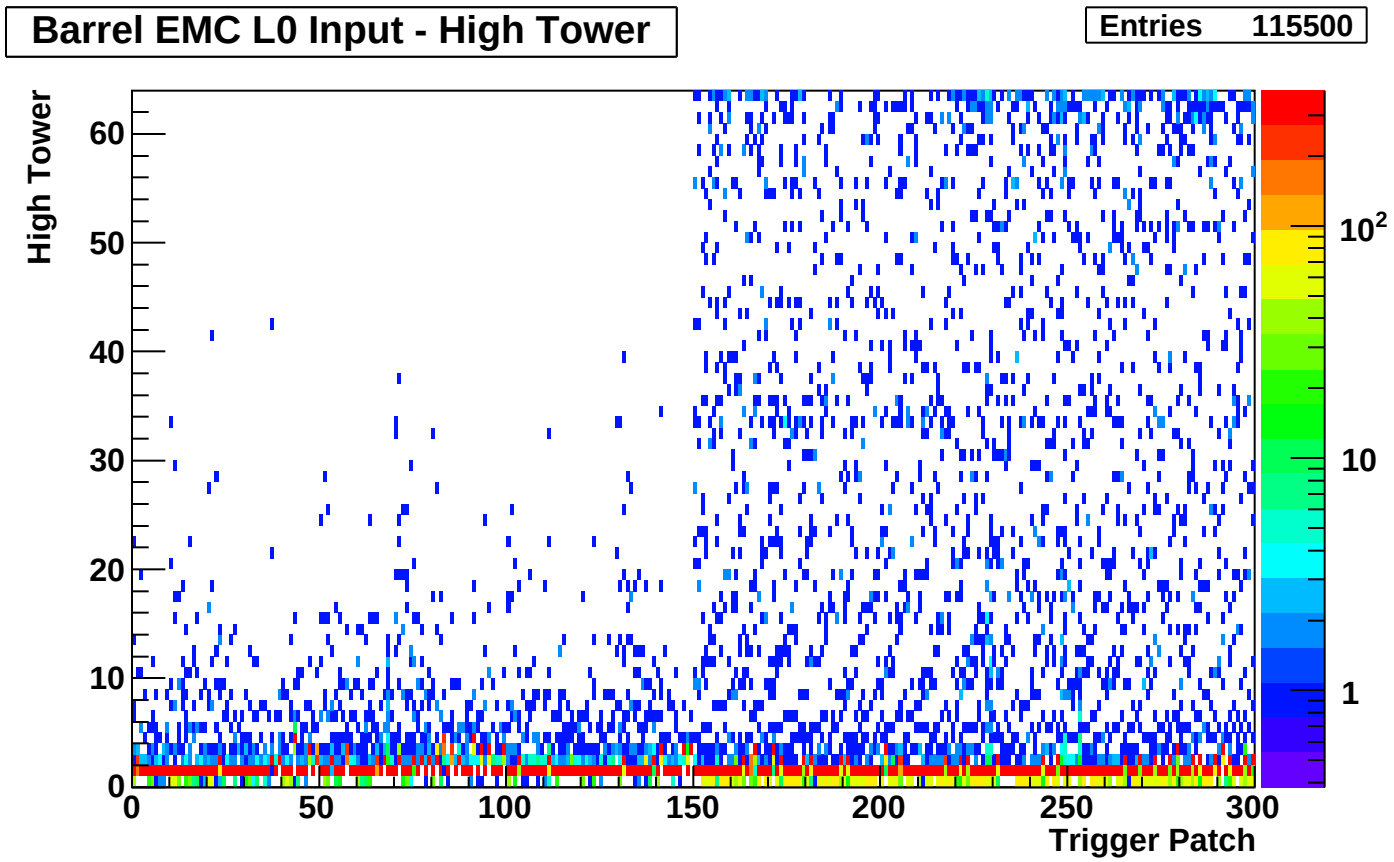
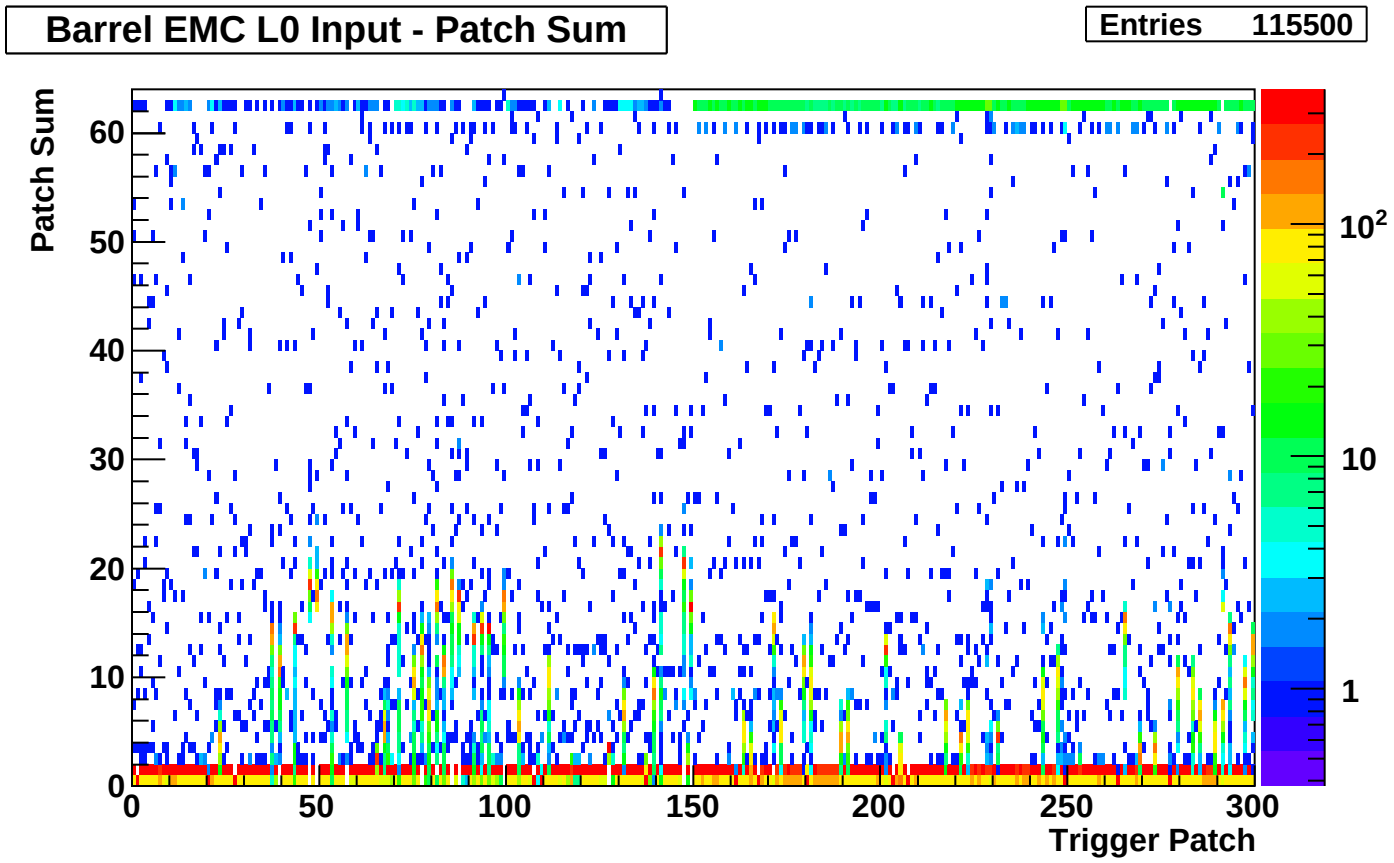
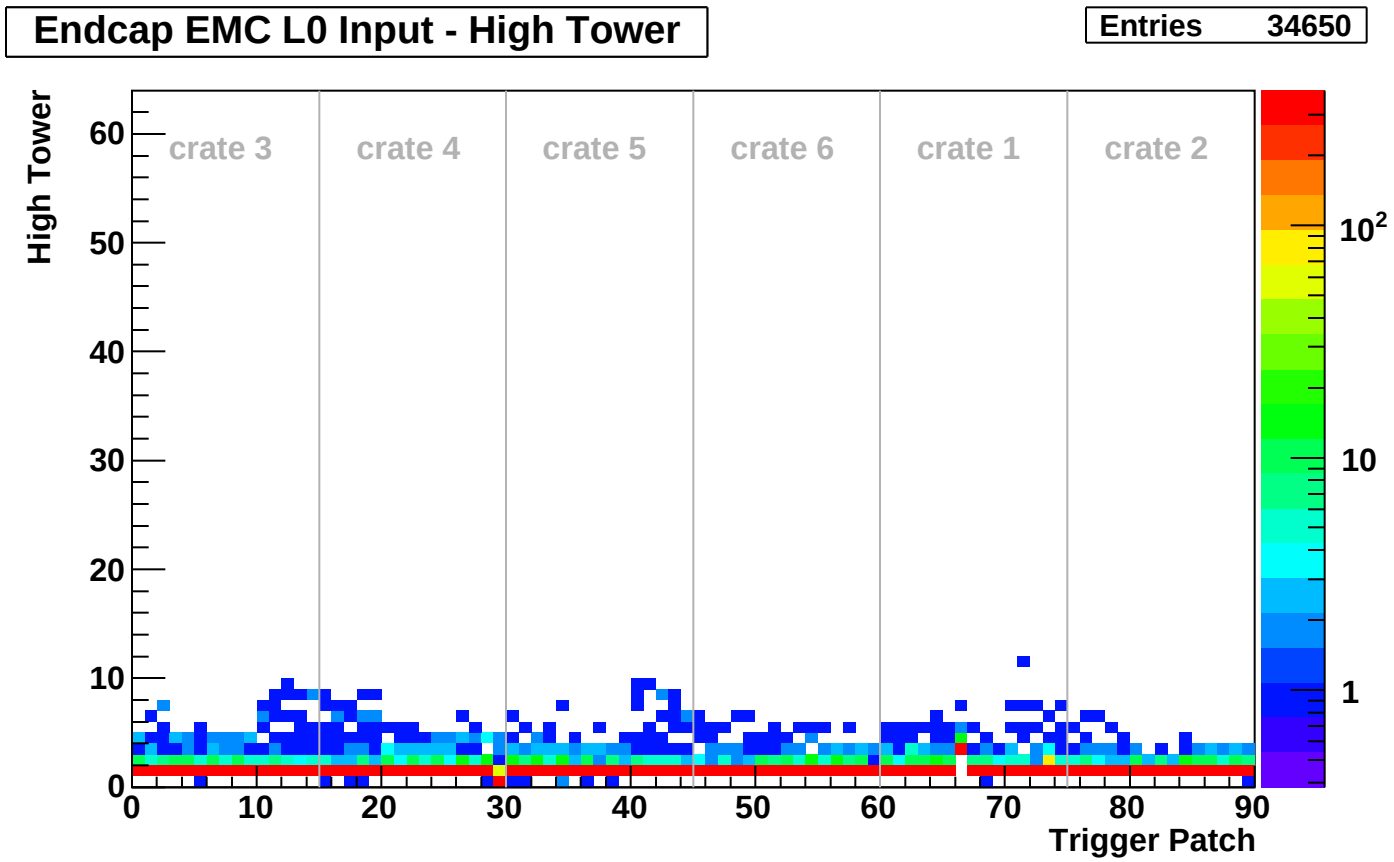
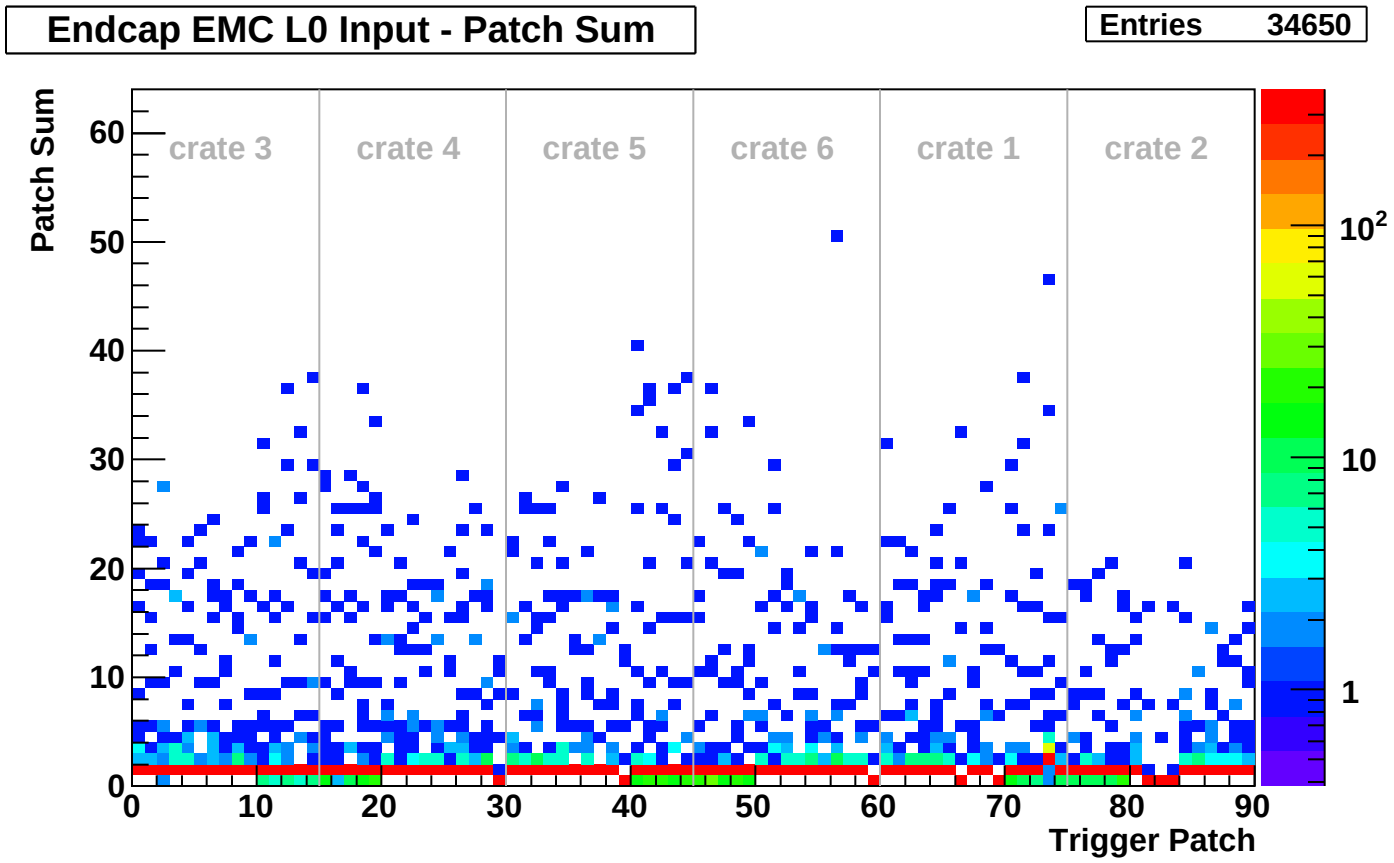
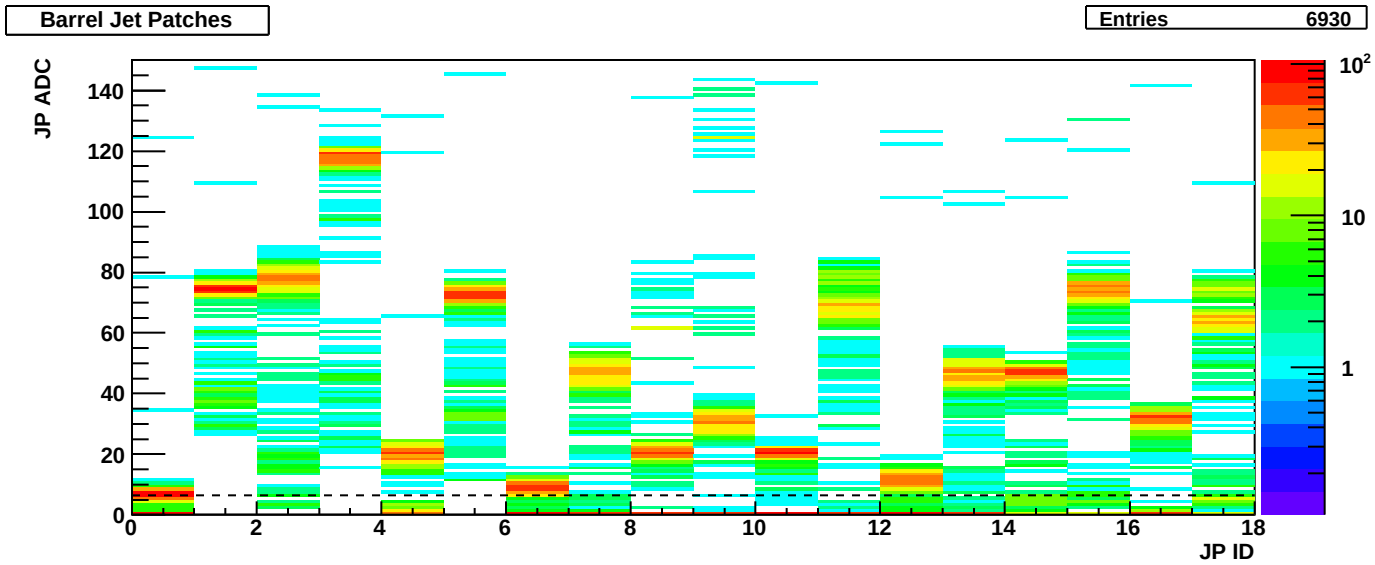
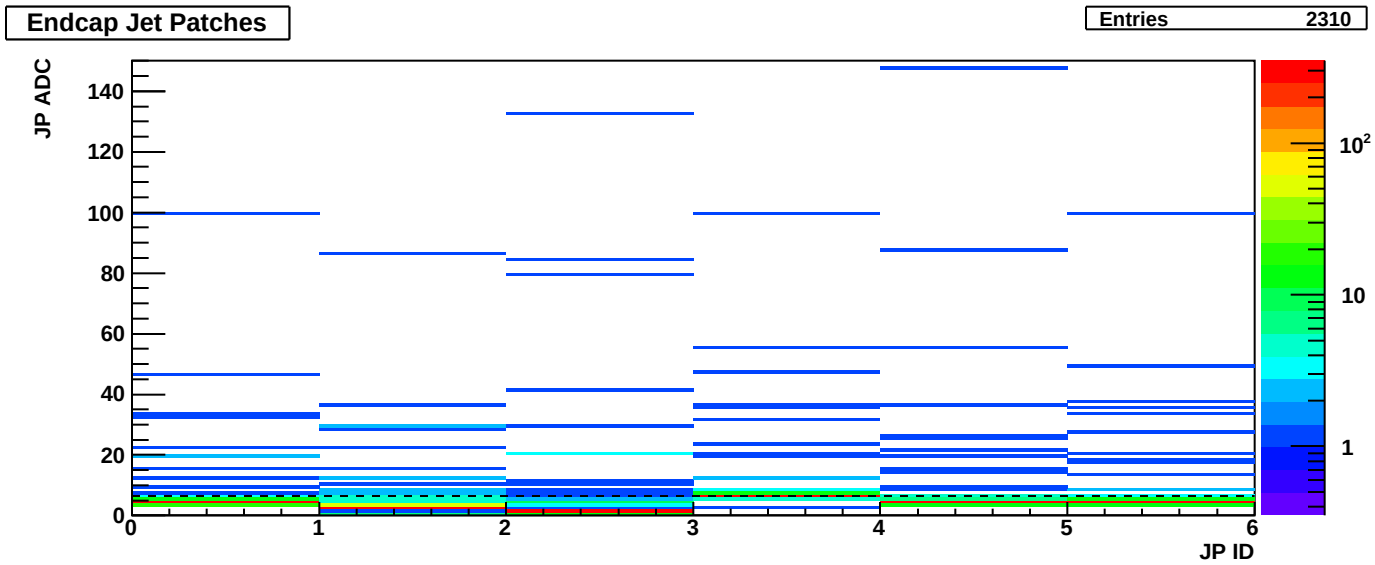

Barrel EMC L0 Input - High Tower
Entries 115500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2

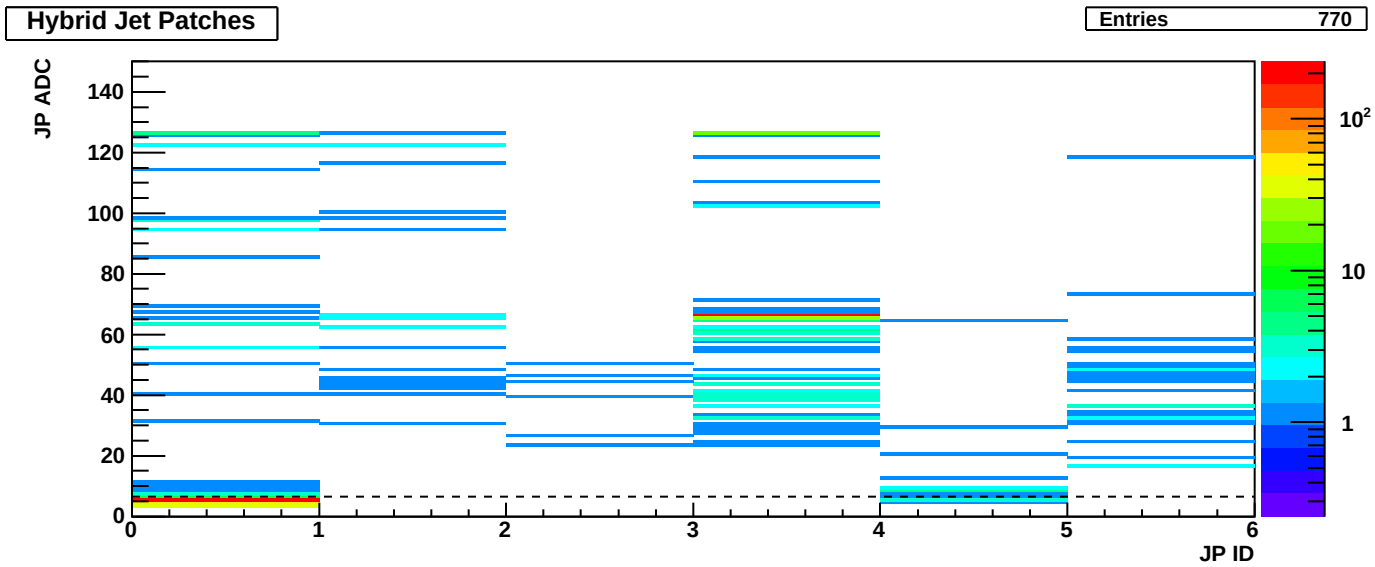

Barrel EMC L0 Input - Patch Sum
Entries 115500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2

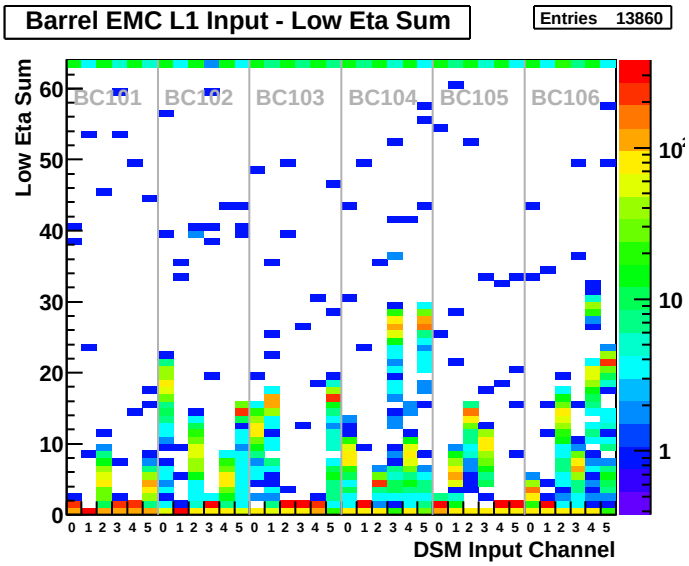

Endcap EMC L0 Input - High Tower
Entries 34650
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

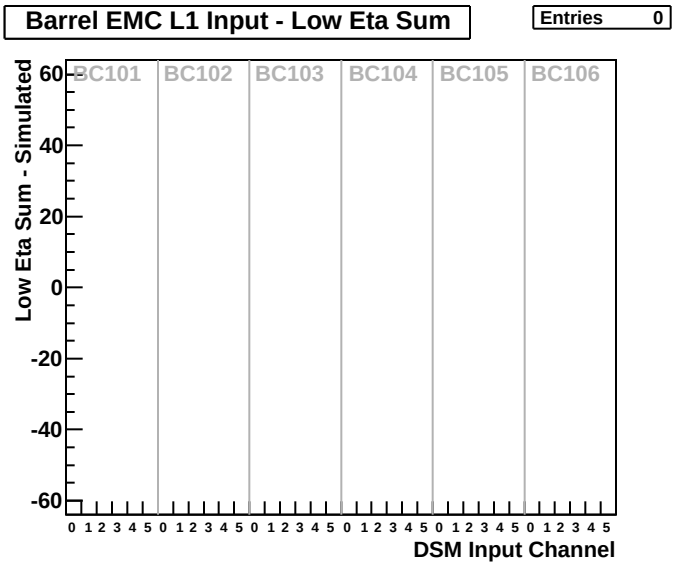

Endcap EMC L0 Input - Patch Sum
Entries 34650
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

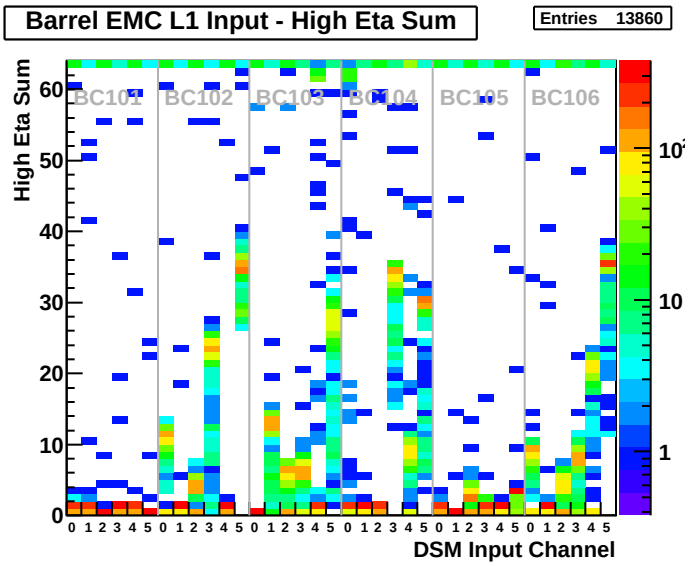

Barrel Jet Patches
Entries 6930
JP ADC
JP ID

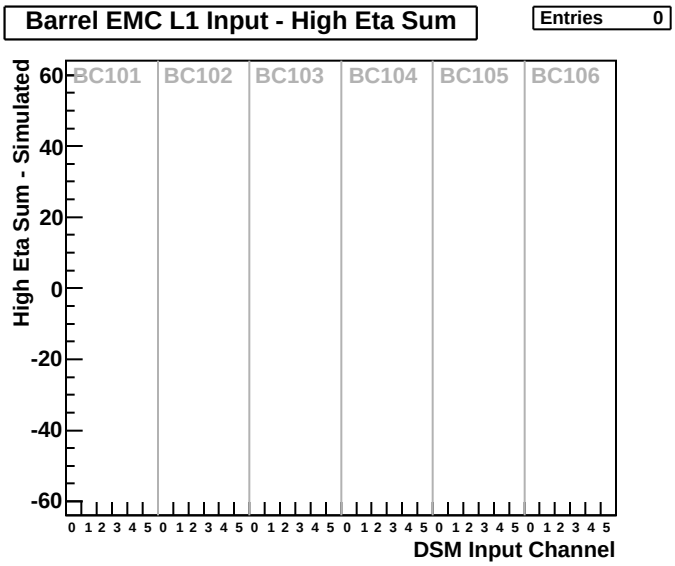

Endcap Jet Patches
Entries 2310
JP ADC
JP ID

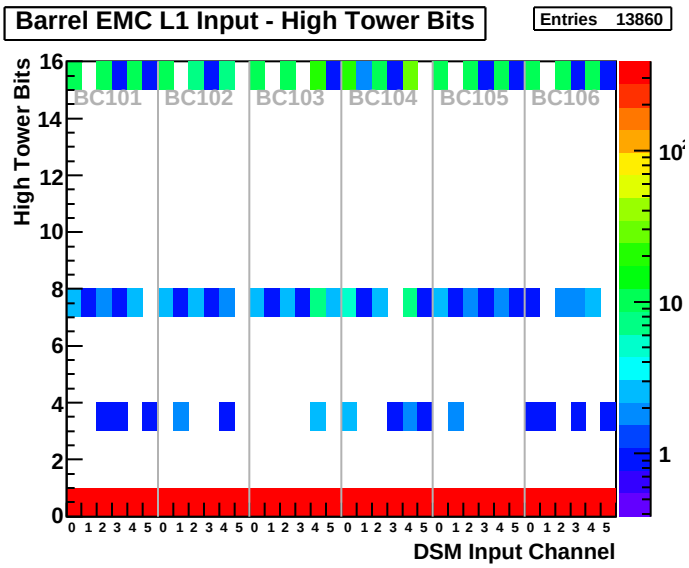

Hybrid Jet Patches
Entries 770
JP ADC
JP ID

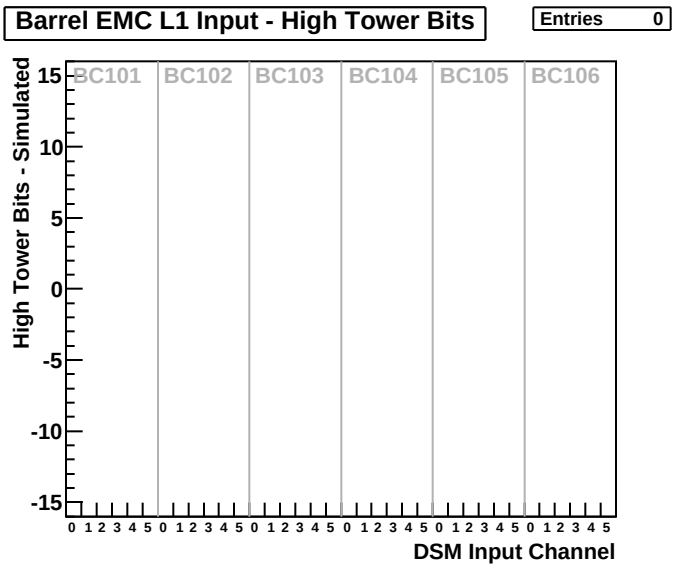

Barrel EMC L1 Input - Low Eta Sum
Entries 13860
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

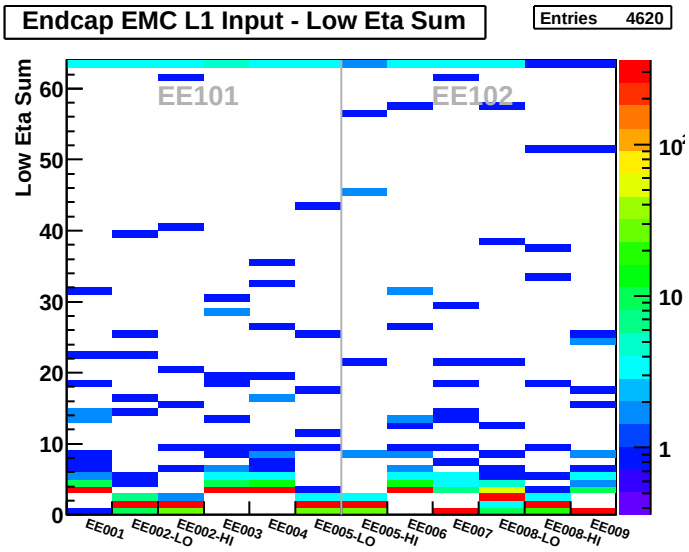

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

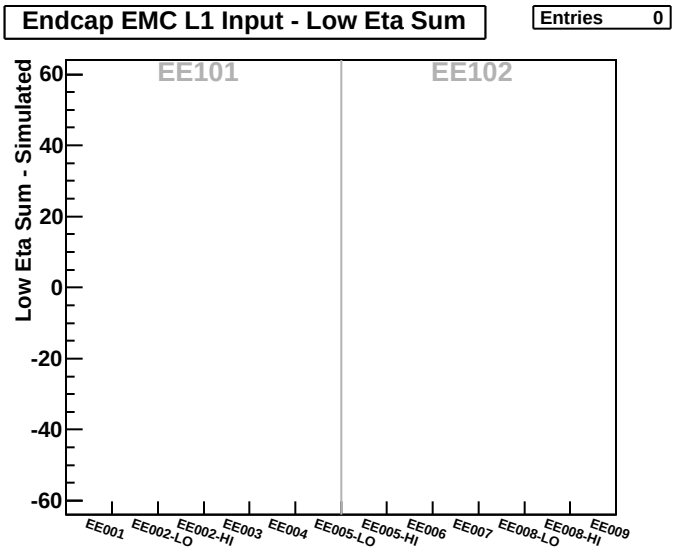

Barrel EMC L1 Input - High Eta Sum
Entries 13860
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

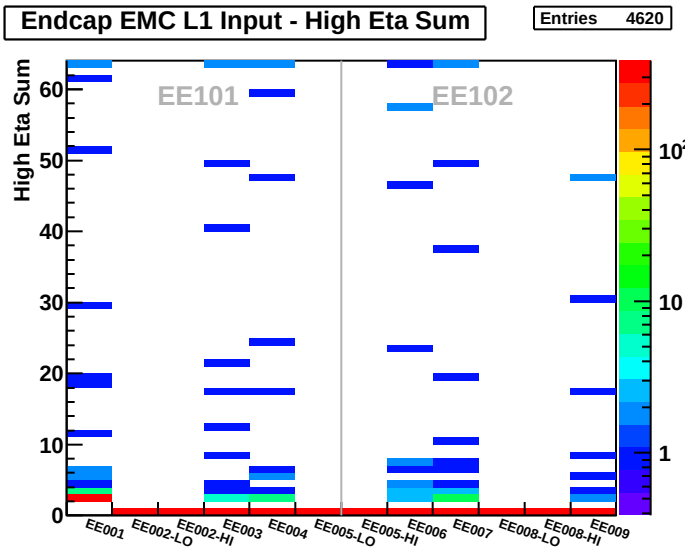

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

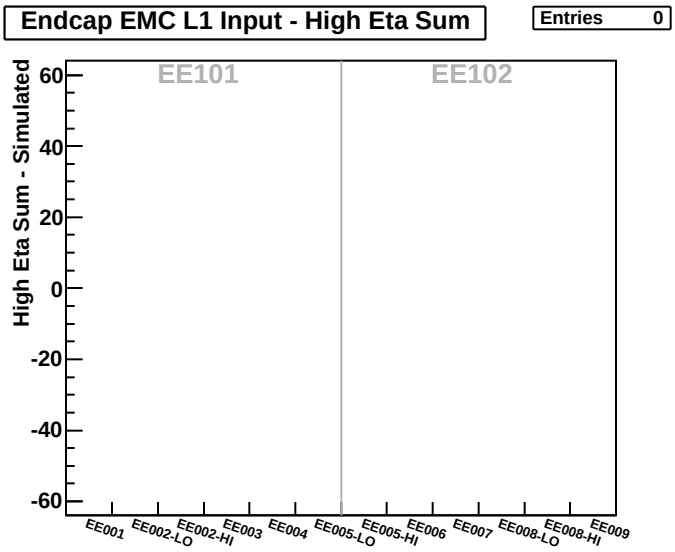

Barrel EMC L1 Input - High Tower Bits
Entries 13860
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

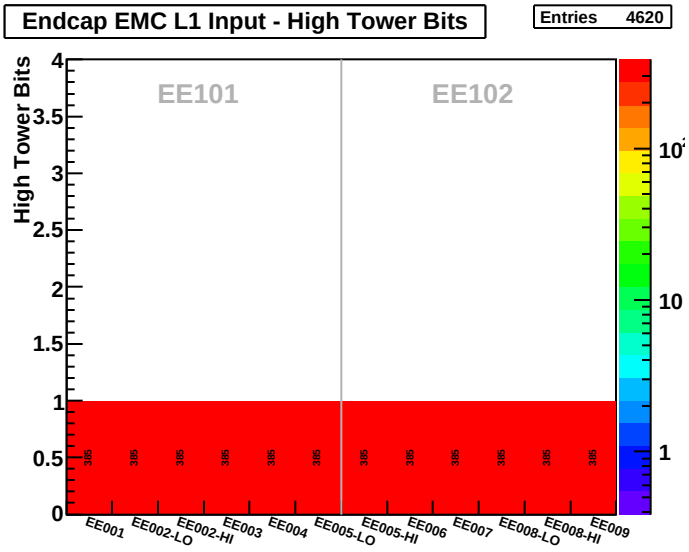

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

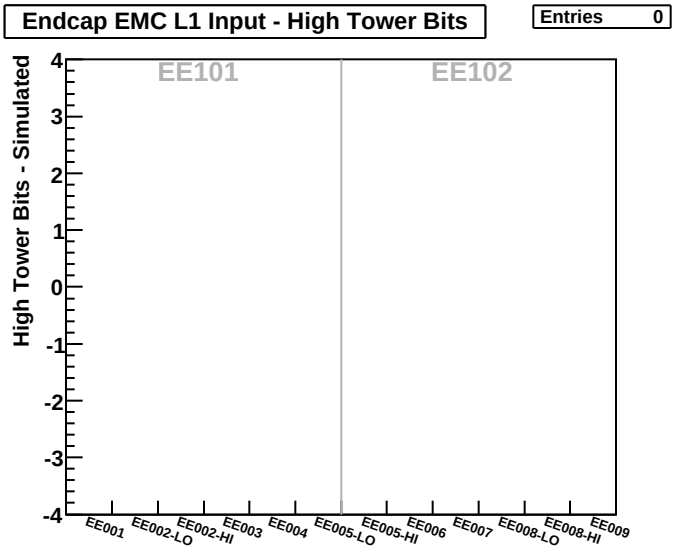

Endcap EMC L1 Input - Low Eta Sum
Entries 4620
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

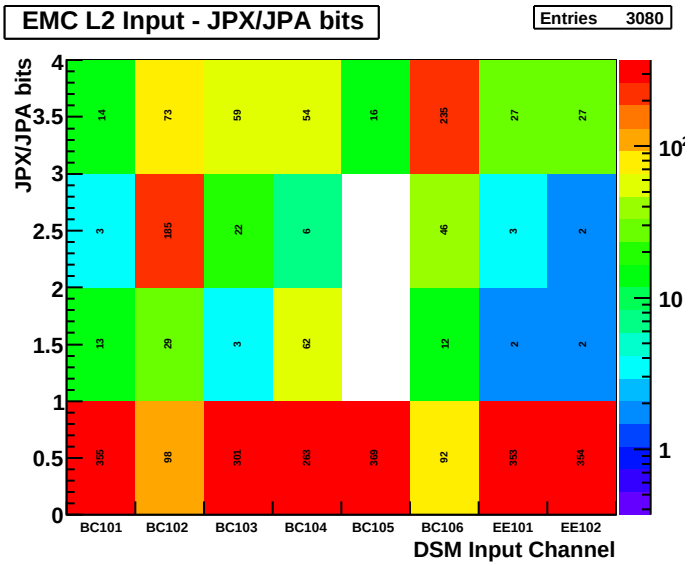

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

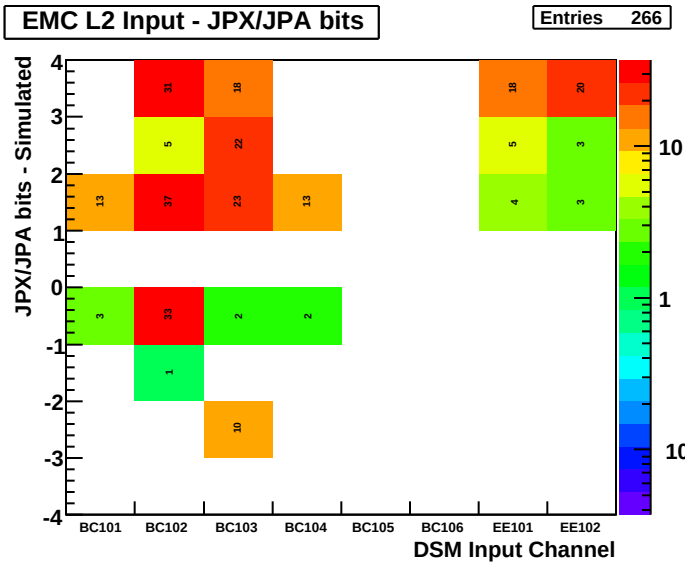

Endcap EMC L1 Input - High Eta Sum
Entries 4620
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

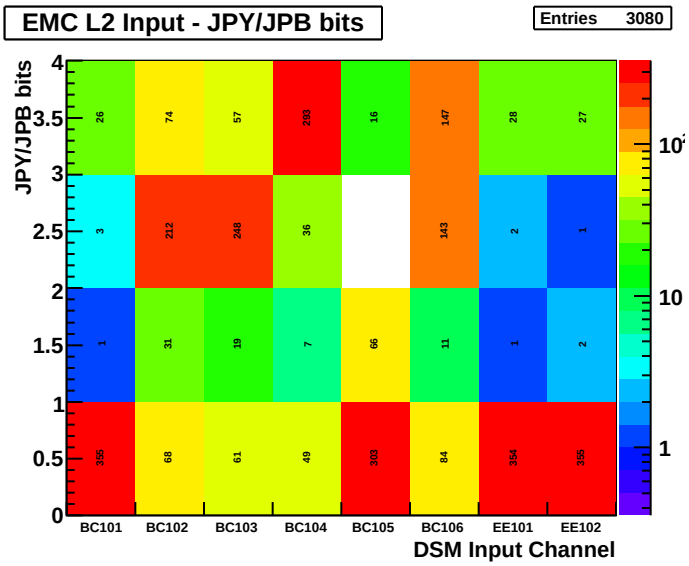

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

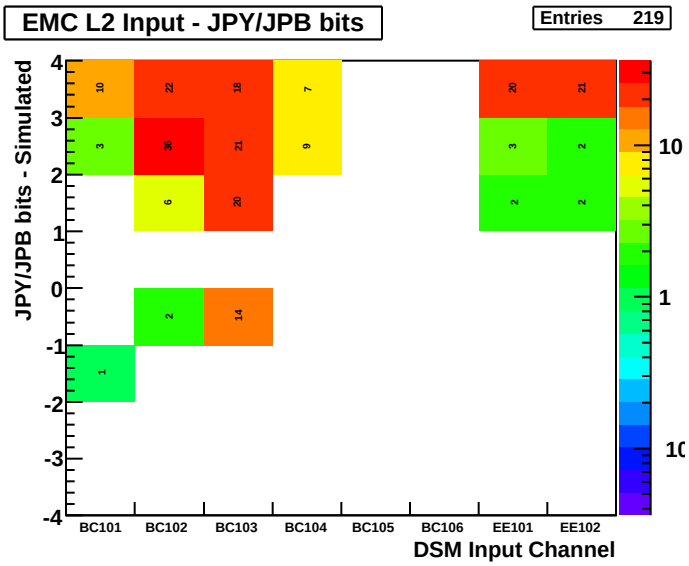

Endcap EMC L1 Input - High Tower Bits
Entries 4620
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

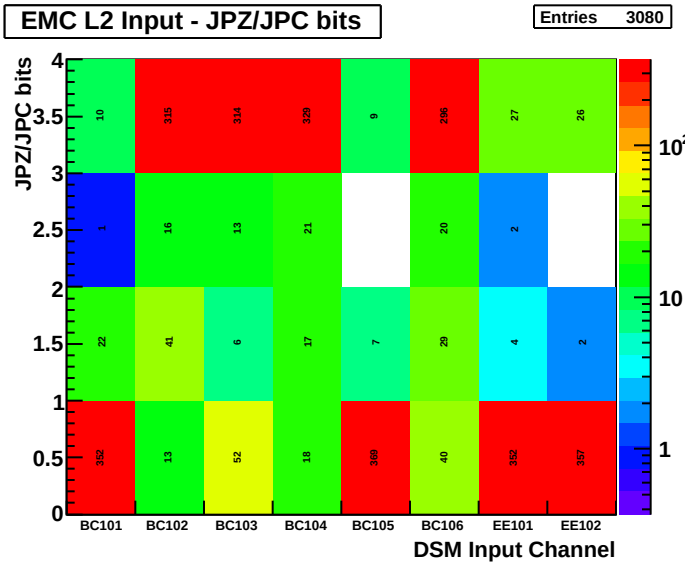

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

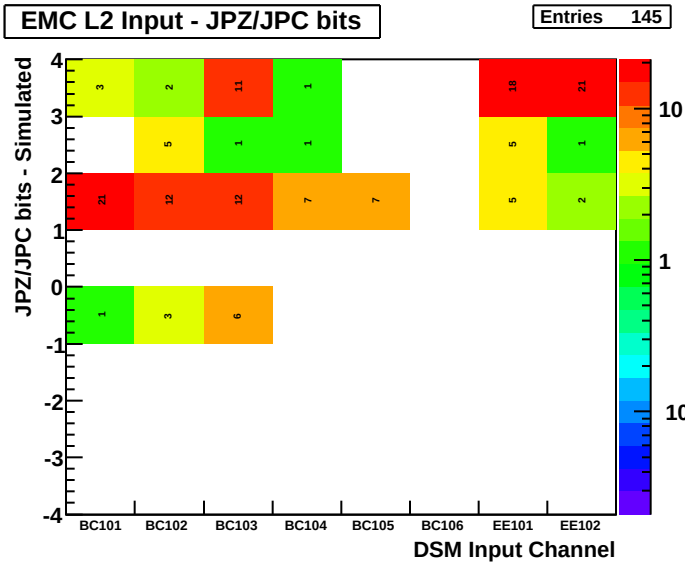

EMC L2 Input - JPX/JPA bits
Entries 3080
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

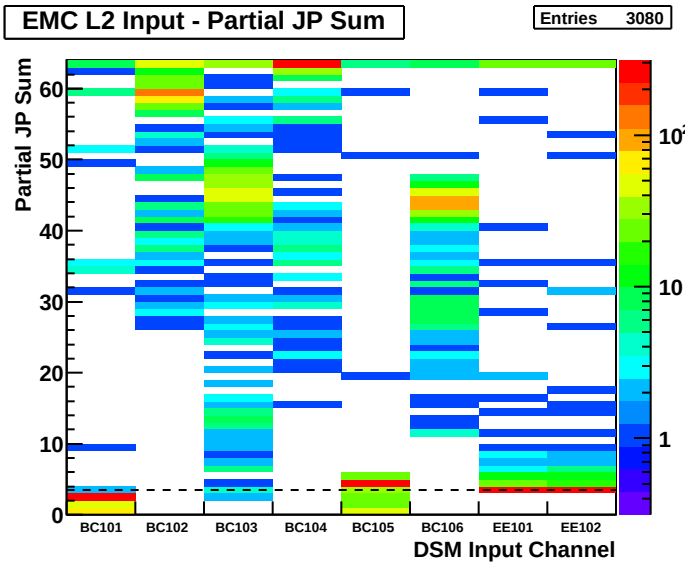

EMC L2 Input - JPX/JPA bits
Entries 266
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

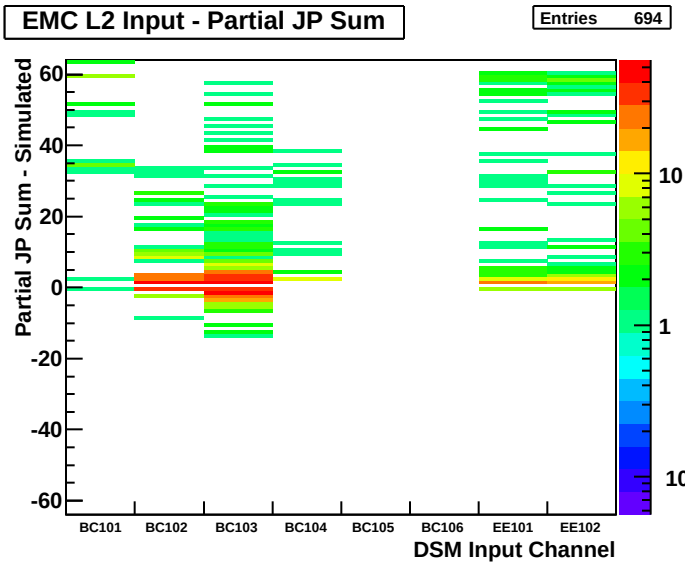

EMC L2 Input - JPY/JPB bits
Entries 3080
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

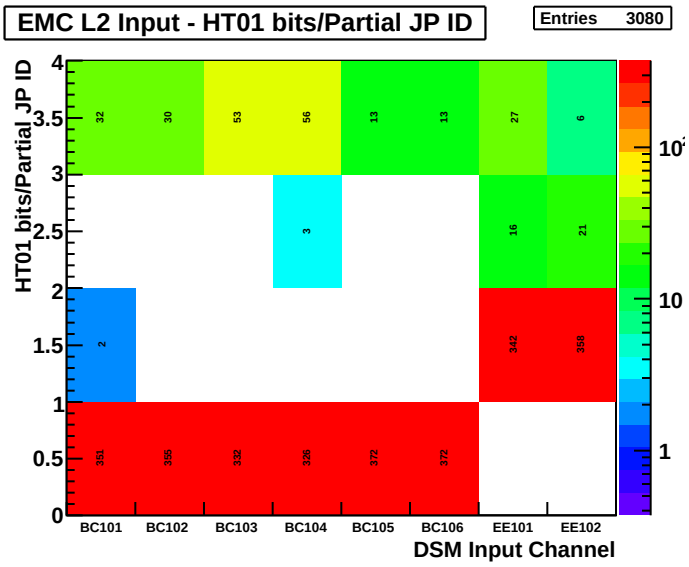

EMC L2 Input - JPY/JPB bits
Entries 219
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

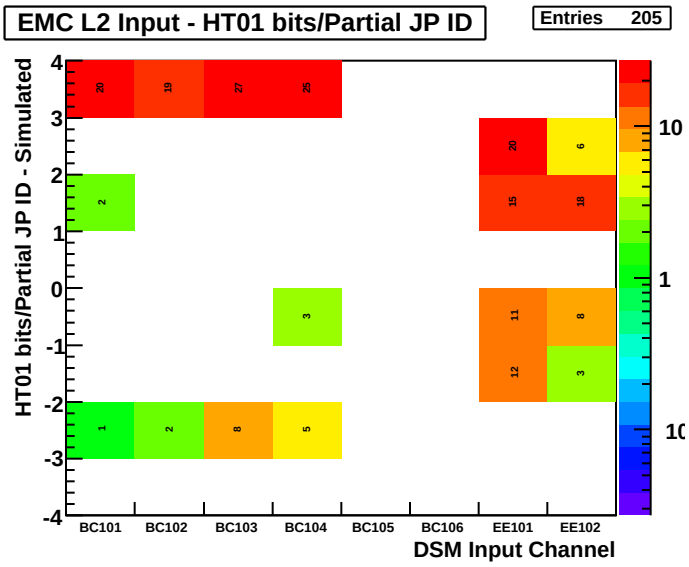

EMC L2 Input - JPZ/JPC bits
Entries 3080
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

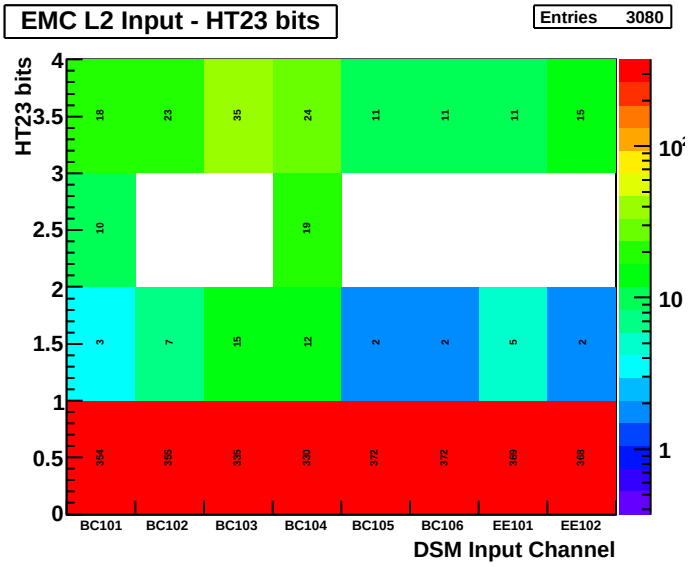

EMC L2 Input - JPZ/JPC bits
Entries 145
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

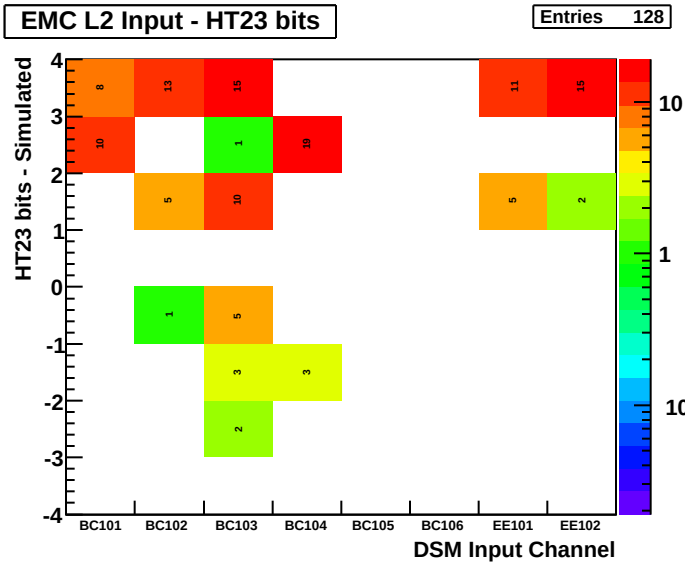

EMC L2 Input - Partial JP Sum
Entries 3080
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

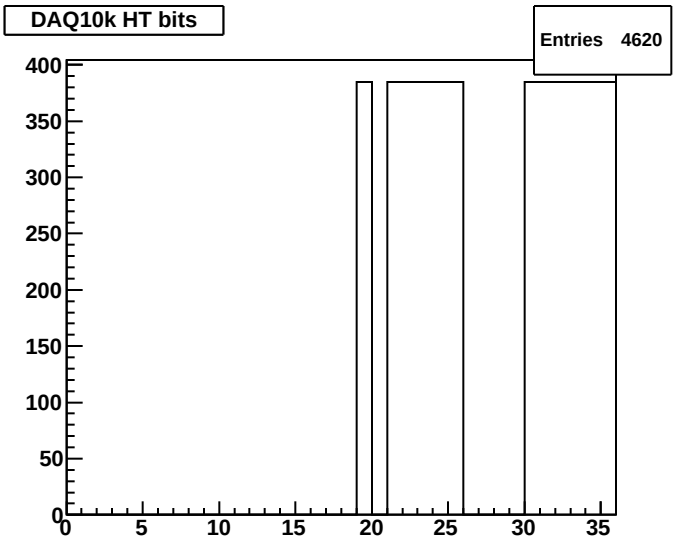

EMC L2 Input - Partial JP Sum
Entries 694
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

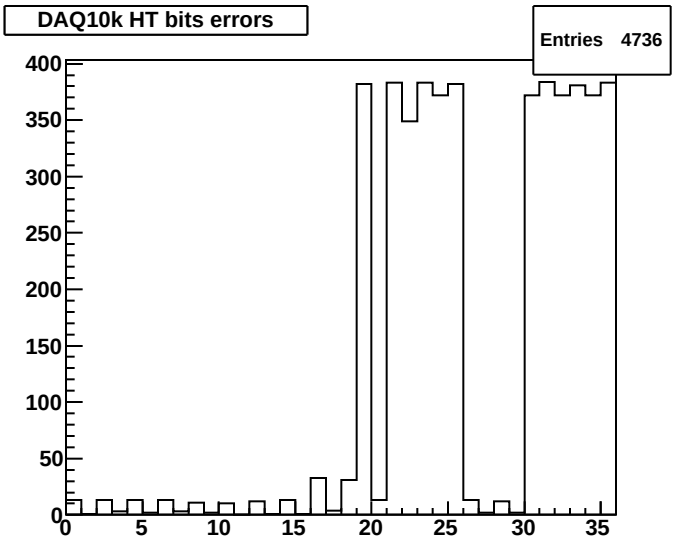

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3080
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

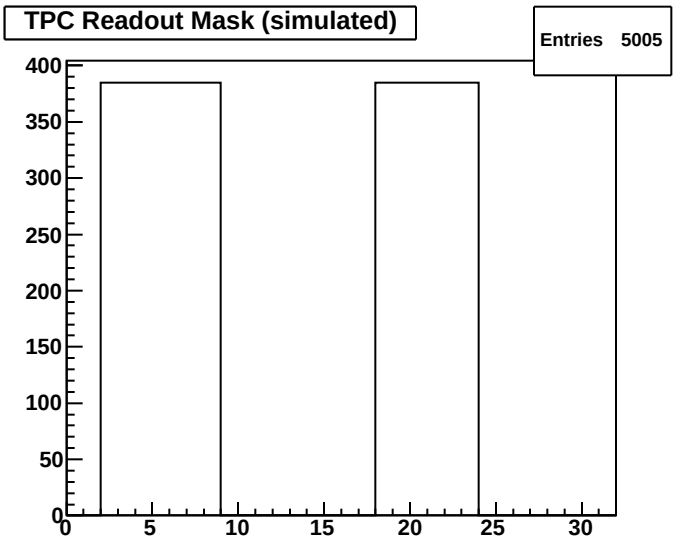

EMC L2 Input - HT01 bits/Partial JP ID
Entries 205
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

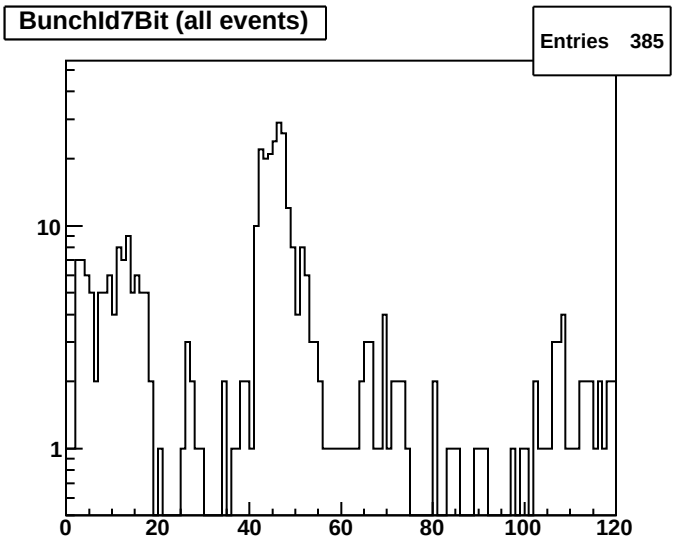

EMC L2 Input - HT23 bits
Entries 3080
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

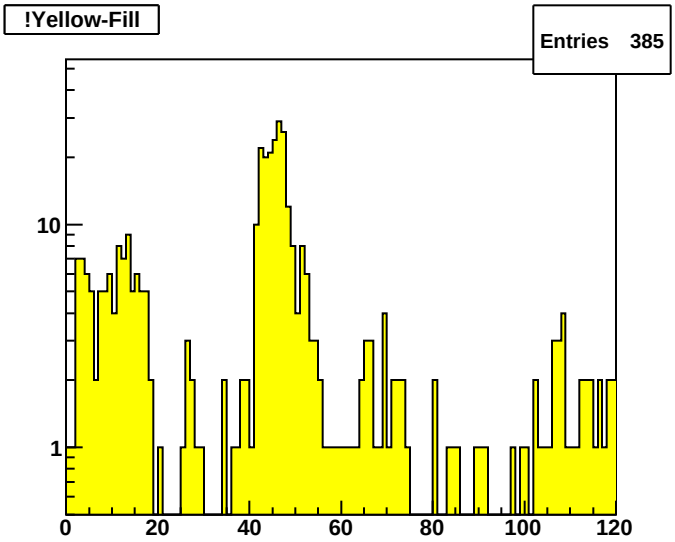

EMC L2 Input - HT23 bits
Entries 128
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

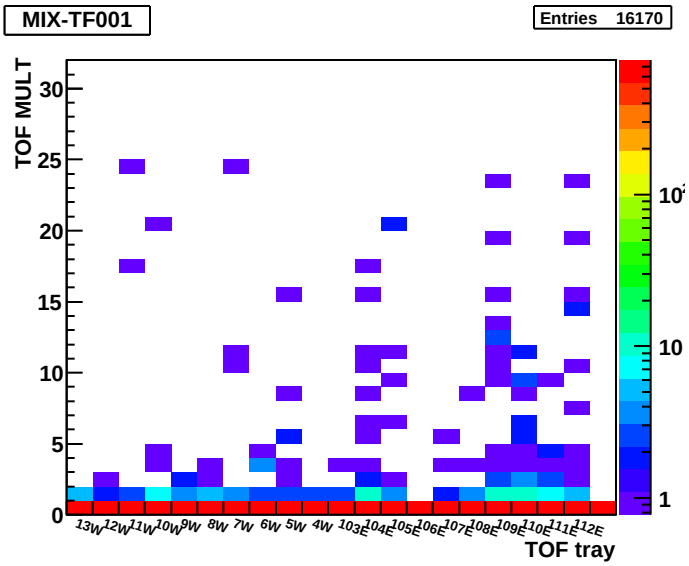

DAQ10k HT bits
Entries 4620
400
350
300
250
200
150
100
50
0
0
5
10
15
20
25
30
35

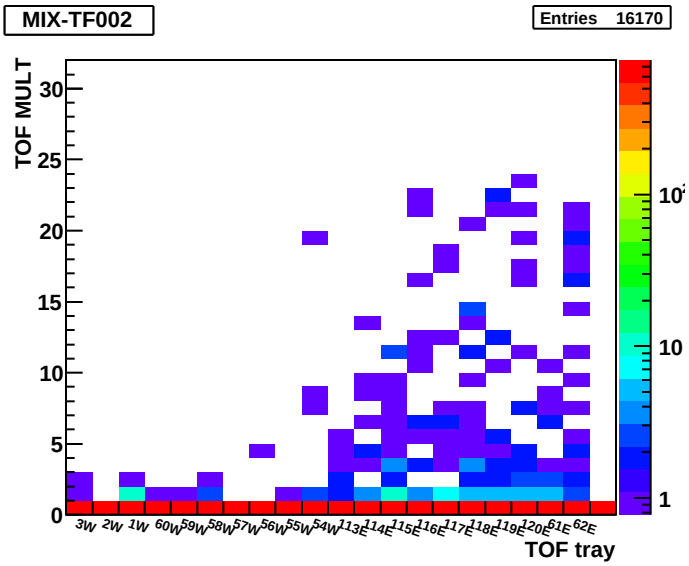

DAQ10k HT bits errors
Entries 4736
400
350
300
250
200
150
100
50
0
0
5
10
15
20
25
30
35

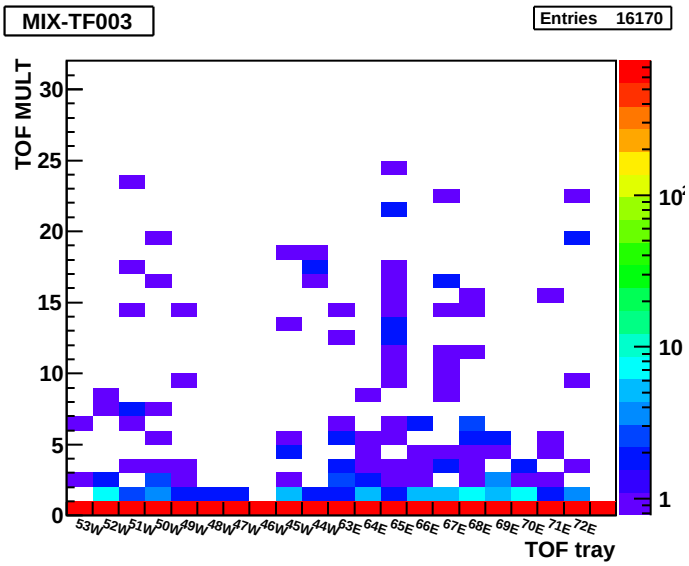

TPC Readout Mask (simulated)
Entries 5005
400
350
300
250
200
150
100
50
0
0
5
10
15
20
25
30

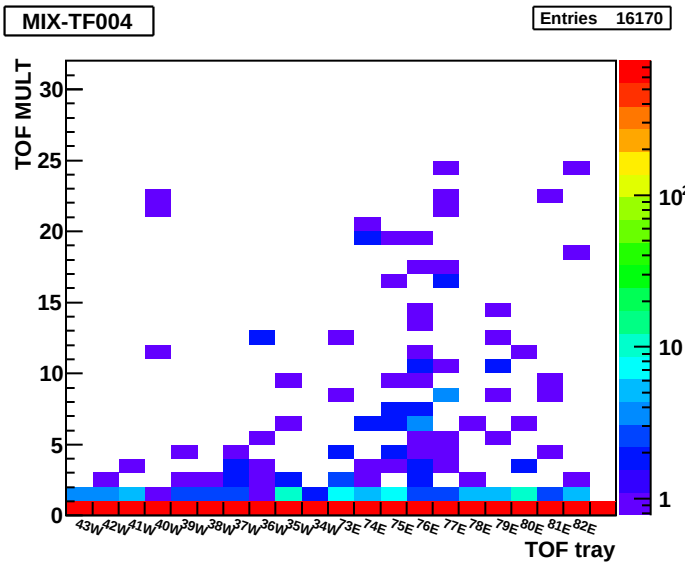

BunchId7Bit (all events)
Entries 385
10
1
0
20
40
60
80
100
120

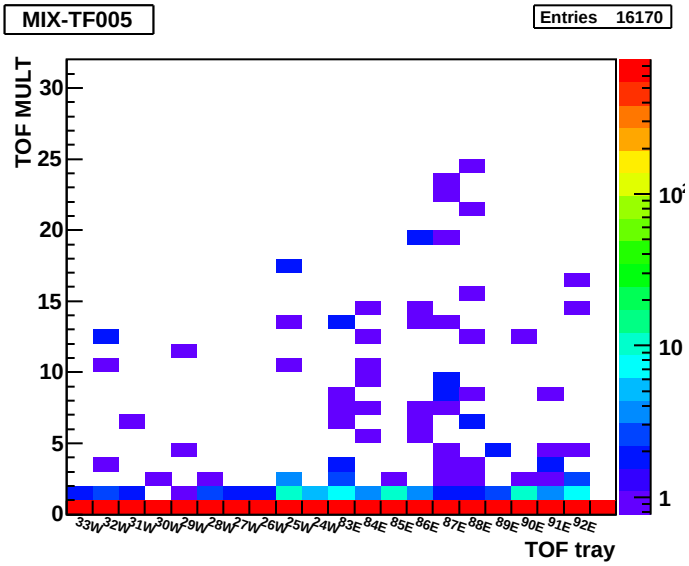

!Yellow-Fill
Entries 385
10
1
0
20
40
60
80
100
120

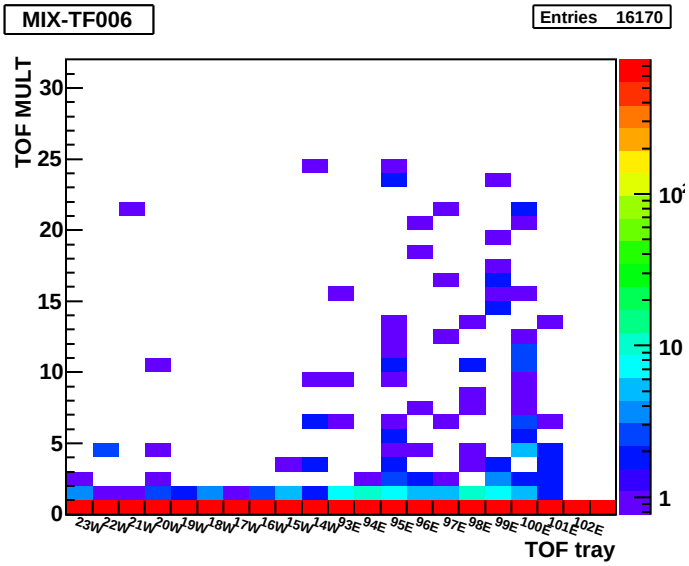

MIX-TF001
Entries 16170
TOF MULT
TOF tray

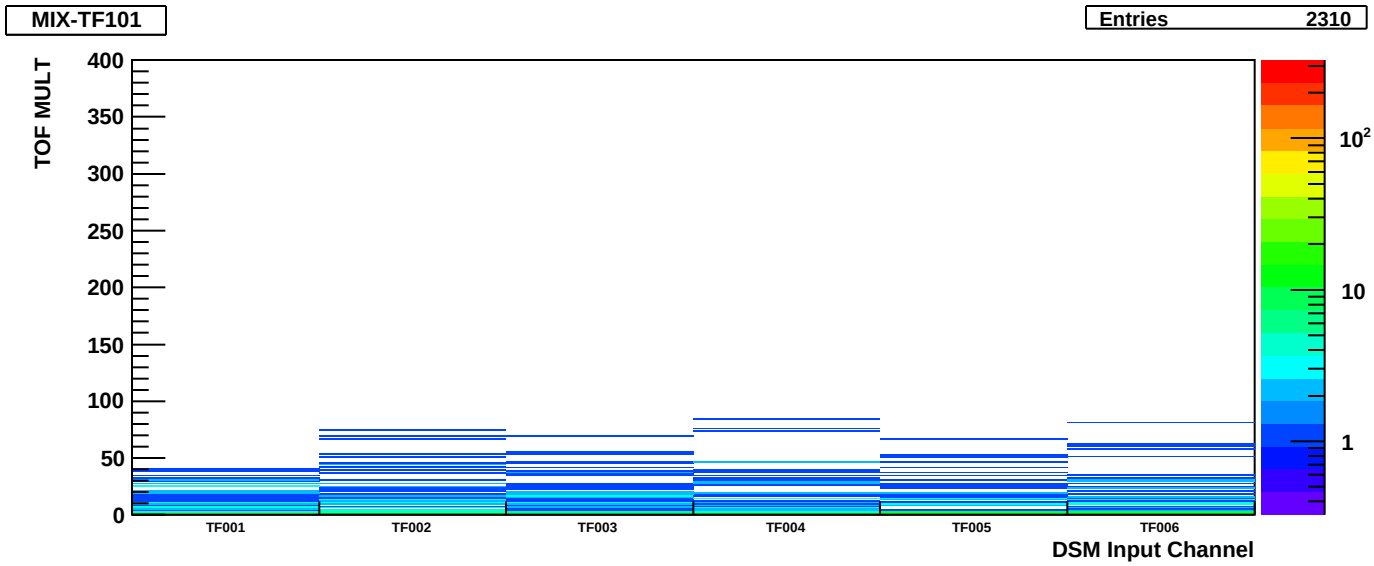

MIX-TF002
Entries 16170
TOF MULT
TOF tray

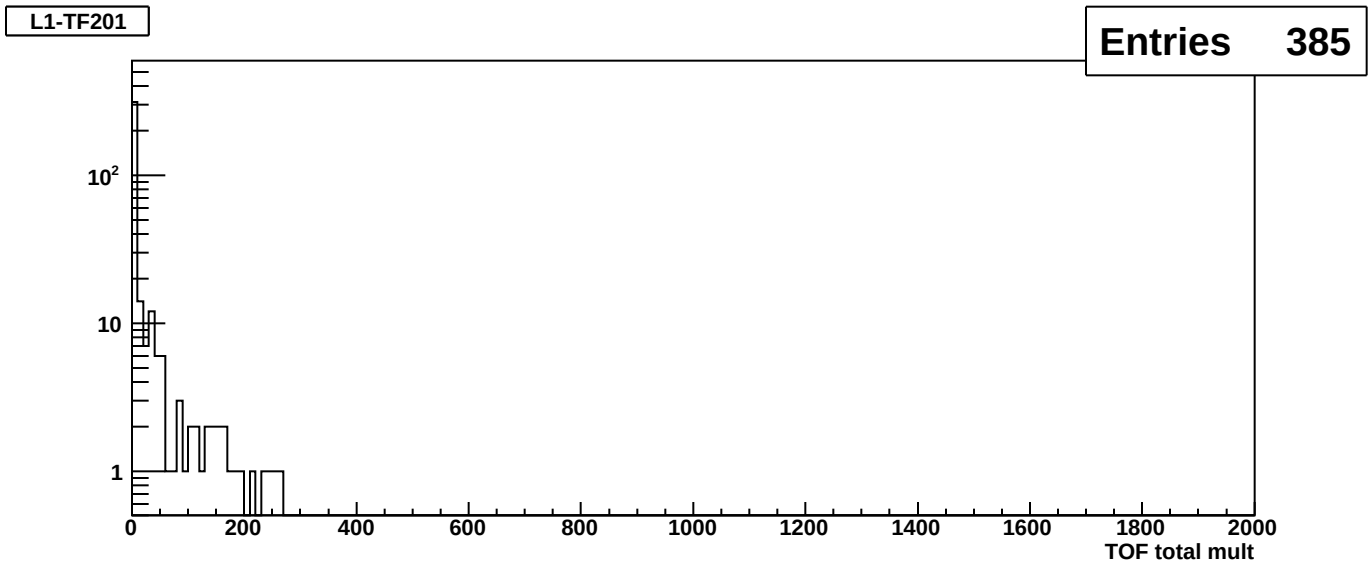

MIX-TF003
Entries 16170
TOF MULT
TOF tray

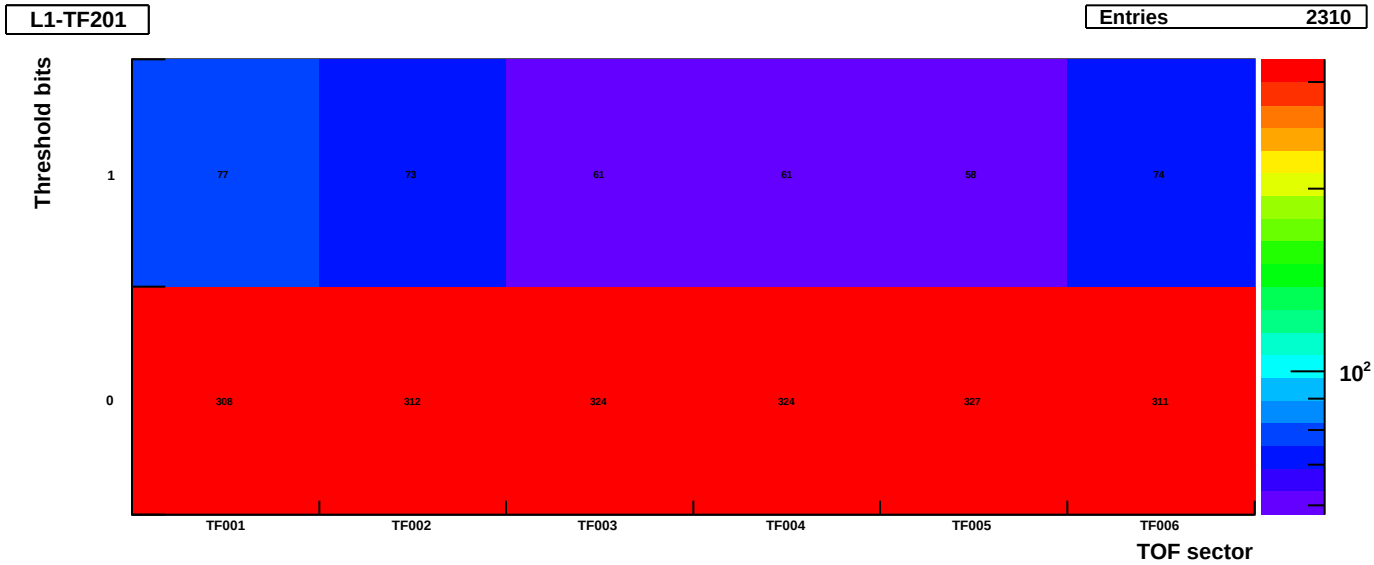

MIX-TF004
Entries 16170
TOF MULT
TOF tray

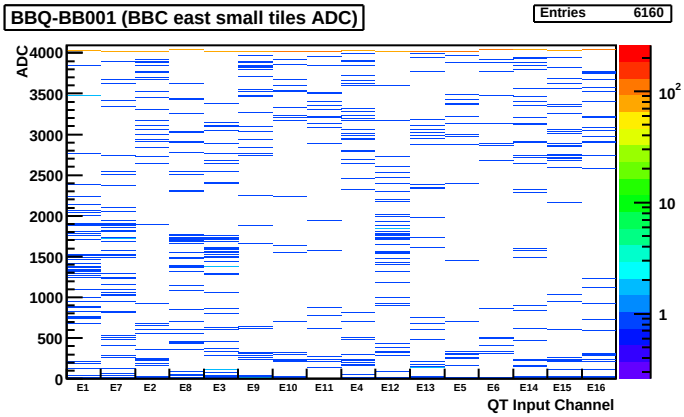

MIX-TF005
Entries 16170
TOF MULT
TOF tray

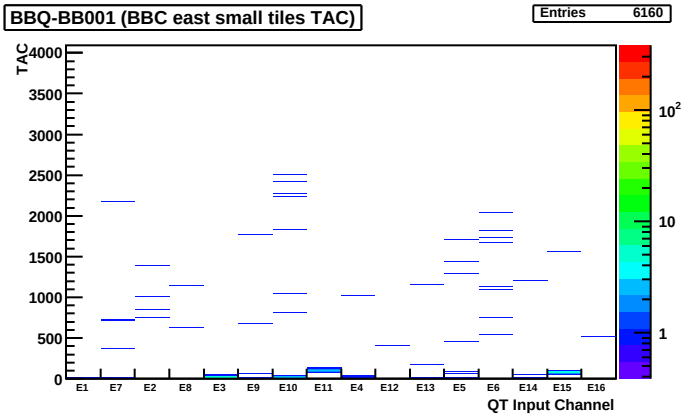

MIX-TF006
Entries 16170
TOF MULT
TOF tray

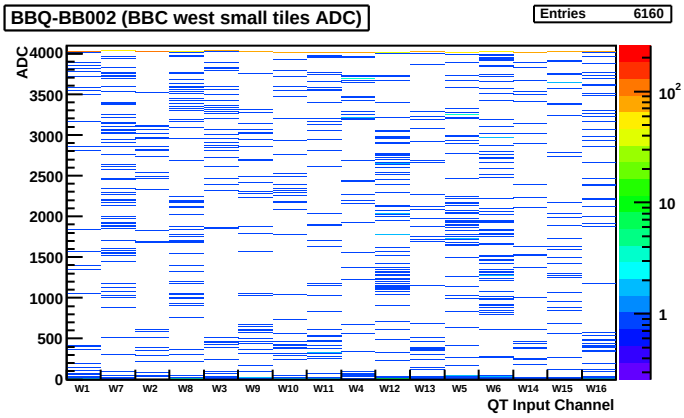

MIX-TF101
Entries 2310
TOF MULT
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

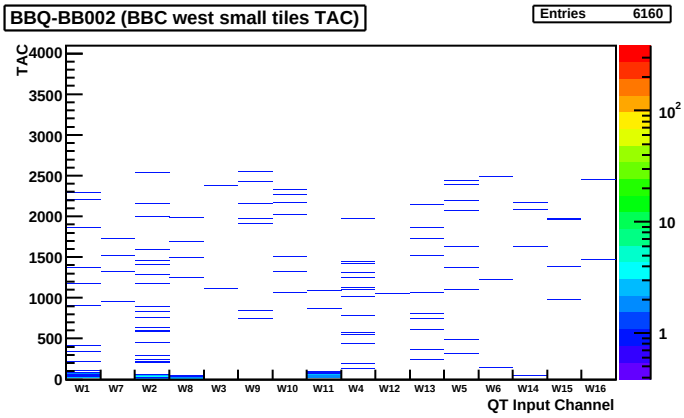

L1-TF201
Entries 385
10^2
10
1
0
200
400
600
800
1000
1200
1400
1600
1800
2000
TOF total mult

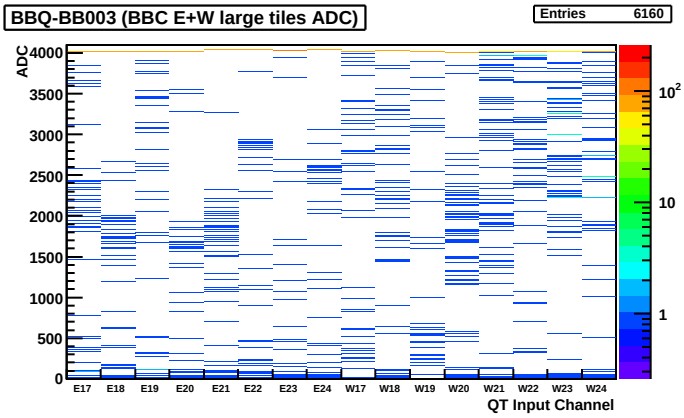

L1-TF201
Entries 2310
Threshold bits
1
0
77
73
61
61
58
74
308
312
324
324
327
311
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^2

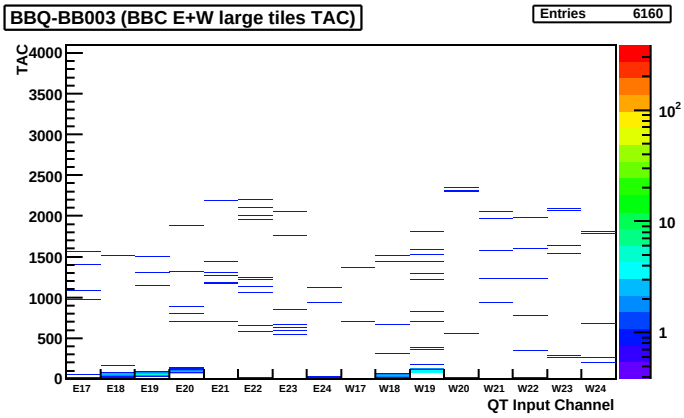

BBQ-BB001 (BBC east small tiles ADC)
Entries 6160
ADC
QT Input Channel

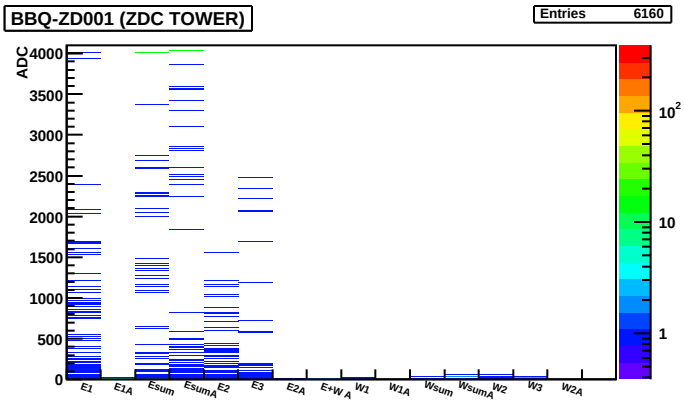

BBQ-BB001 (BBC east small tiles TAC)
Entries 6160
TAC
QT Input Channel

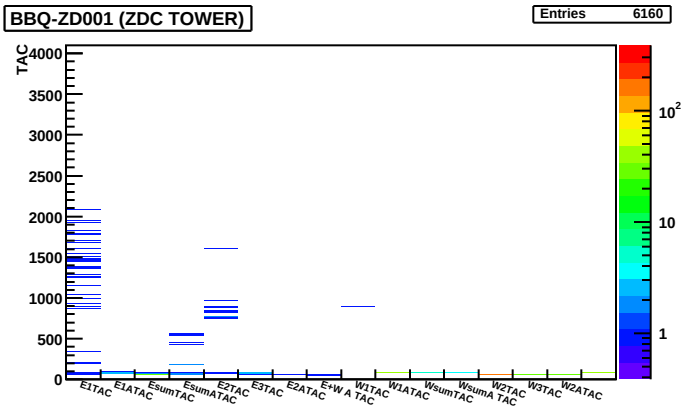

BBQ-BB002 (BBC west small tiles ADC)
Entries 6160
ADC
QT Input Channel

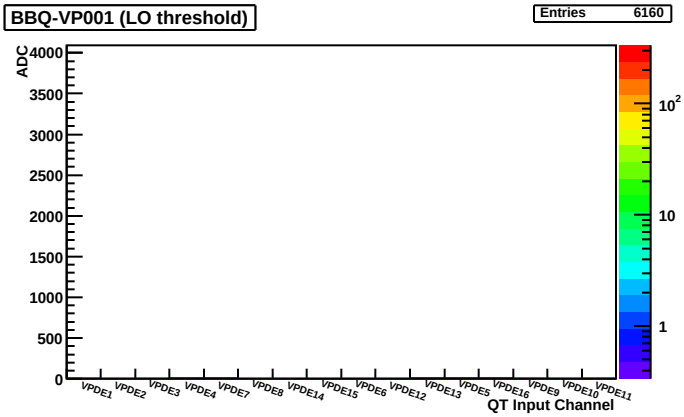

BBQ-BB002 (BBC west small tiles TAC)
Entries 6160
TAC
QT Input Channel

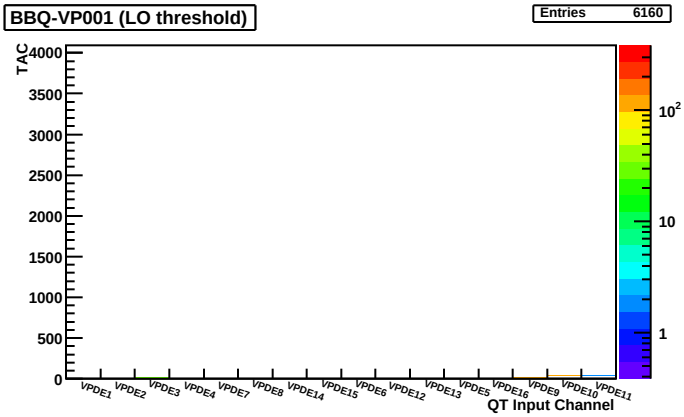

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6160
ADC
QT Input Channel

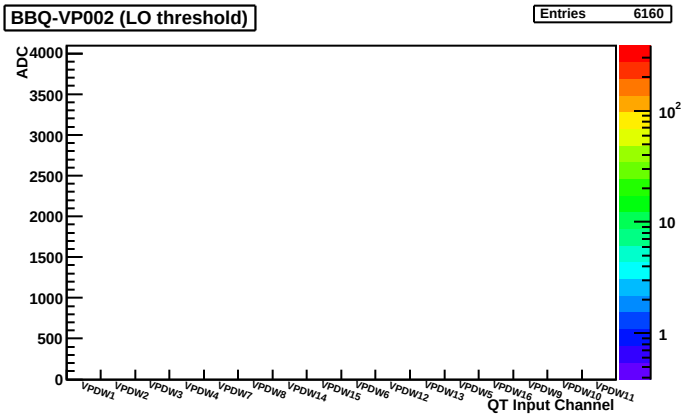

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6160
TAC
QT Input Channel

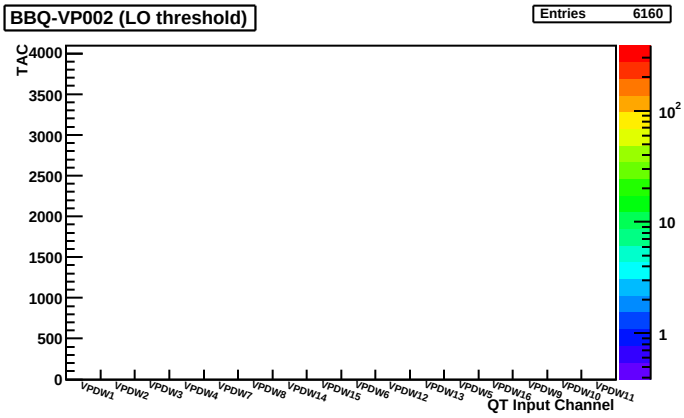

BBQ-ZD001 (ZDC TOWER)
Entries 6160
ADC

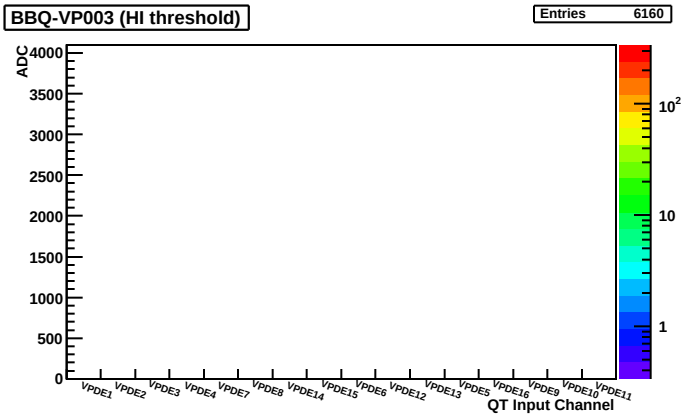

BBQ-ZD001 (ZDC TOWER)
Entries 6160
TAC

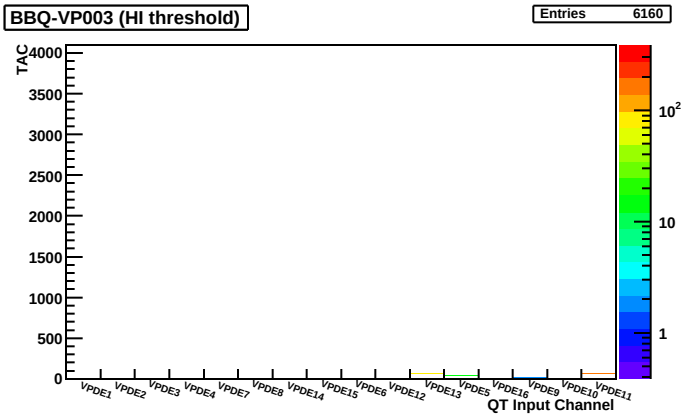

BBQ-VP001 (LO threshold)
Entries 6160
ADC
QT Input Channel

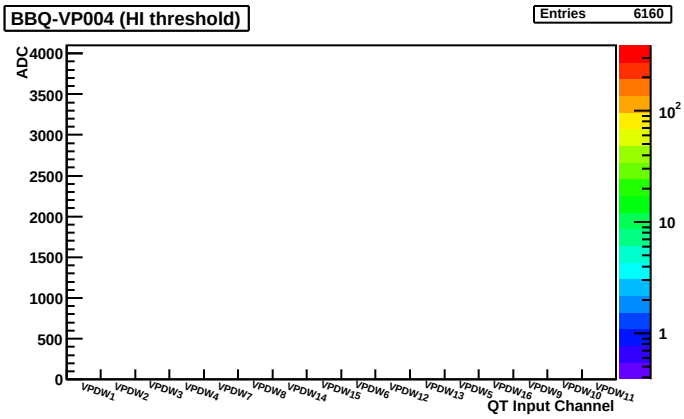

BBQ-VP001 (LO threshold)
Entries 6160
TAC
QT Input Channel

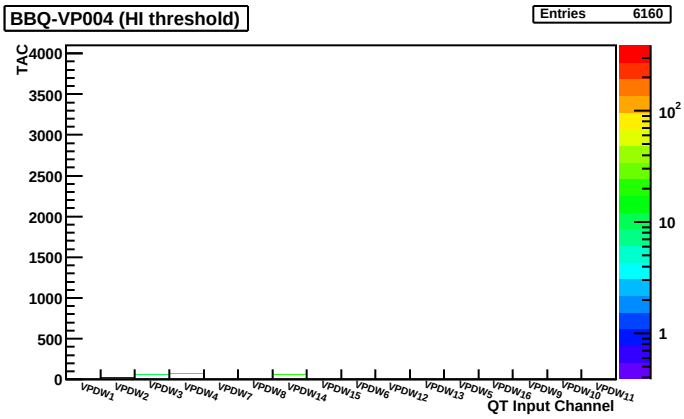

BBQ-VP002 (LO threshold)
Entries 6160
ADC
QT Input Channel

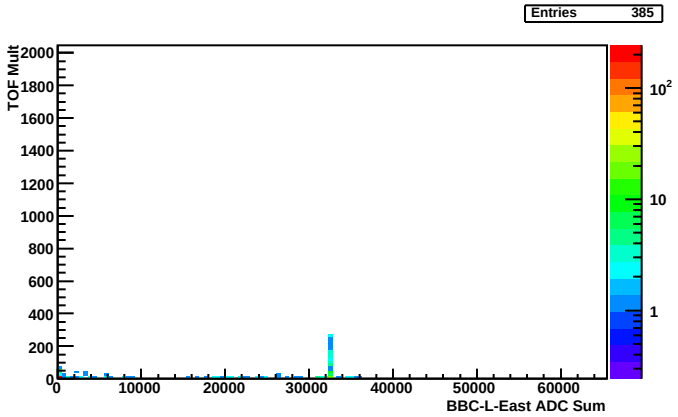

BBQ-VP002 (LO threshold)
Entries 6160
TAC
QT Input Channel

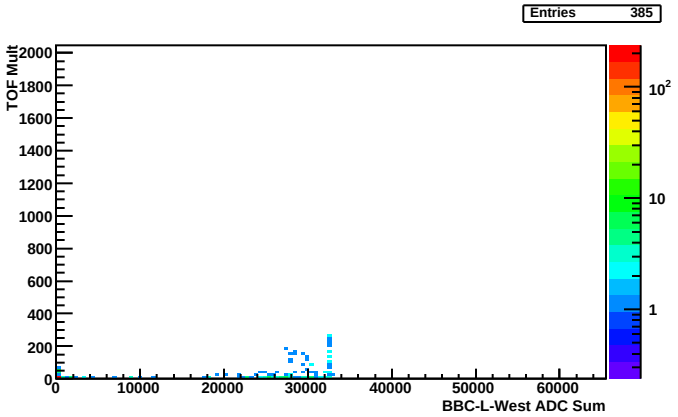

BBQ-VP003 (HI threshold)
Entries 6160
ADC
QT Input Channel

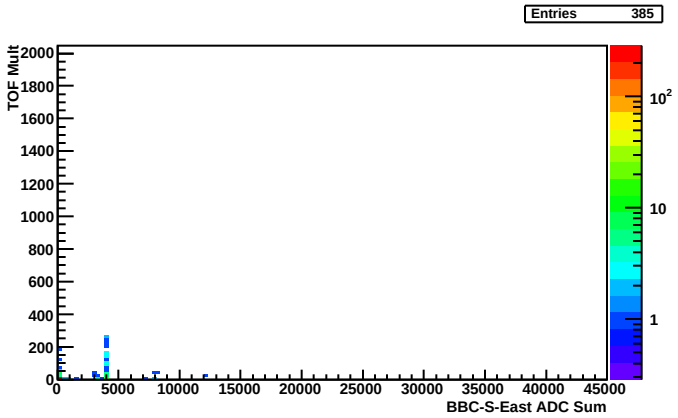

BBQ-VP003 (HI threshold)
Entries 6160
TAC
QT Input Channel

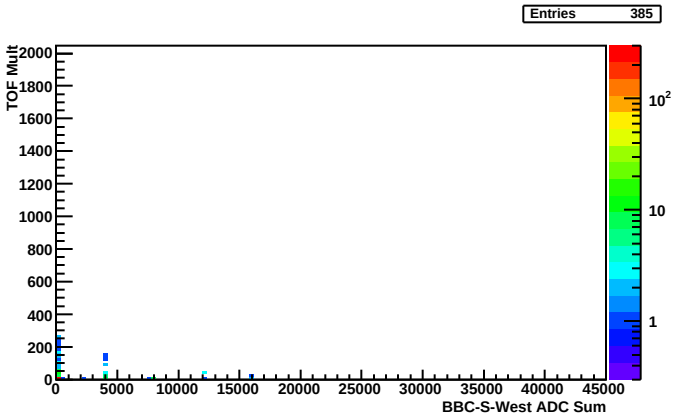

BBQ-VP004 (HI threshold)
Entries 6160
ADC
QT Input Channel

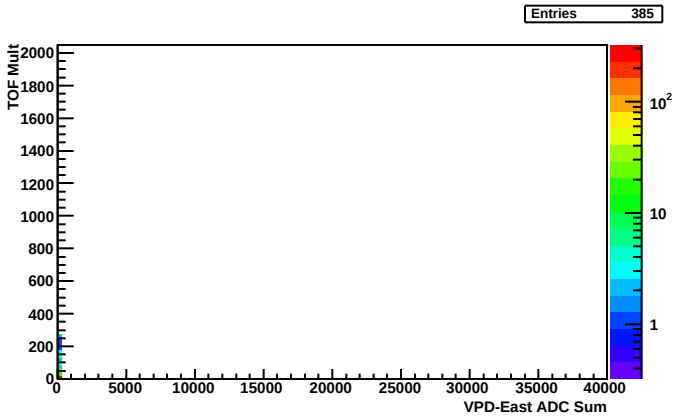

BBQ-VP004 (HI threshold)
Entries 6160
TAC
QT Input Channel

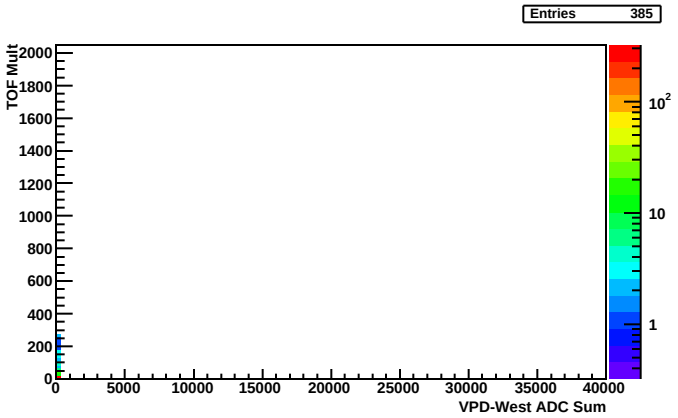

Entries 385
TOF Mult
BBC-L-East ADC Sum

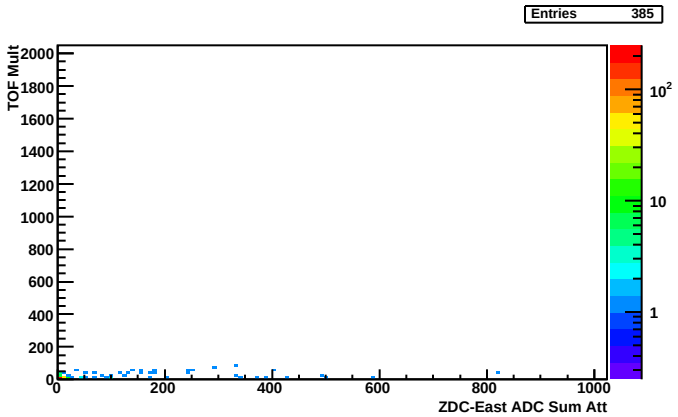

Entries 385
TOF Mult
BBC-L-West ADC Sum

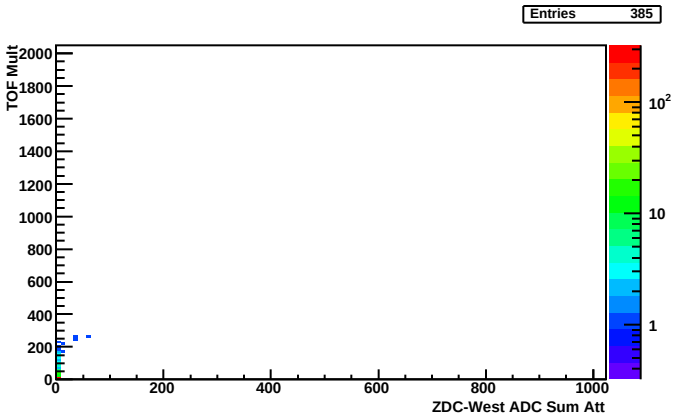

Entries 385
TOF Mult
BBC-S-East ADC Sum

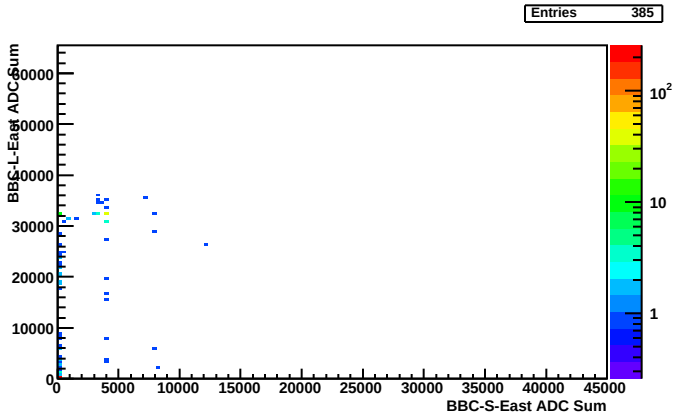

Entries 385
TOF Mult
BBC-S-West ADC Sum

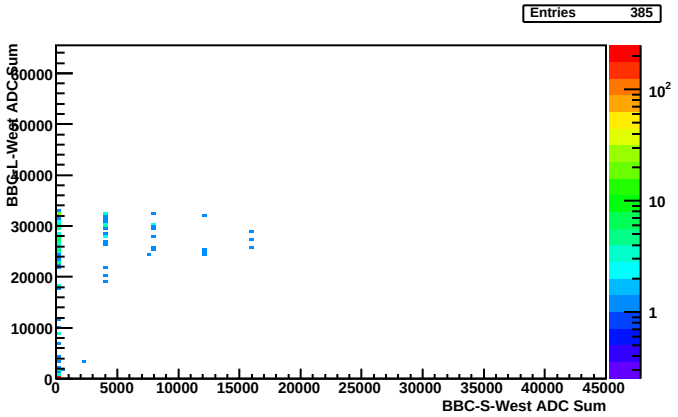

Entries 385
TOF Mult
VPD-East ADC Sum

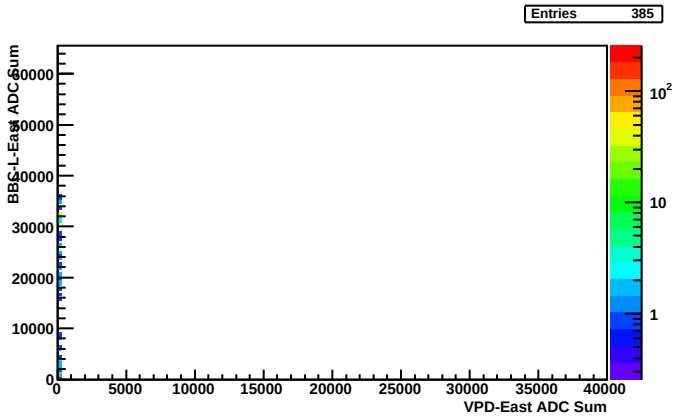

Entries 385
TOF Mult
VPD-West ADC Sum

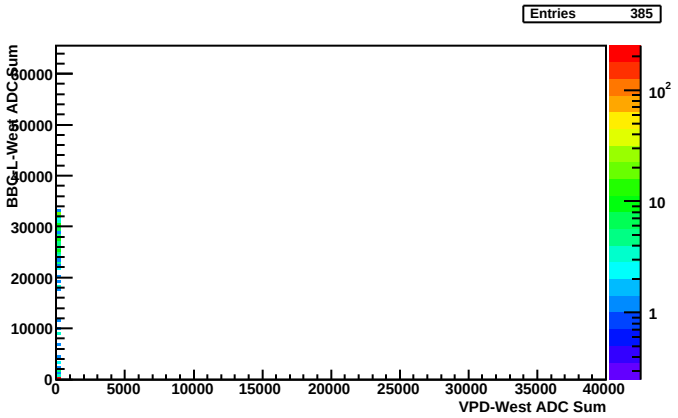

Entries 385
TOF Mult
ZDC-East ADC Sum Att

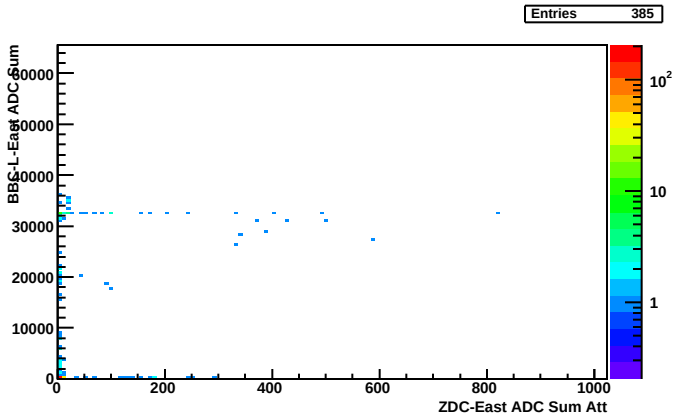

Entries 385
TOF Mult
ZDC-West ADC Sum Att

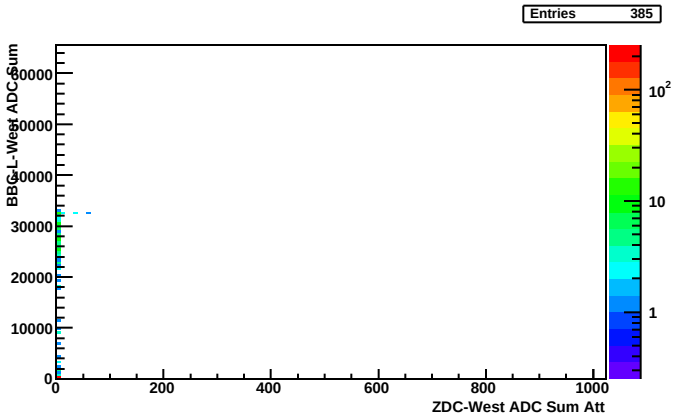

Entries 385
BBC-L-East ADC Sum
BBC-S-East ADC Sum

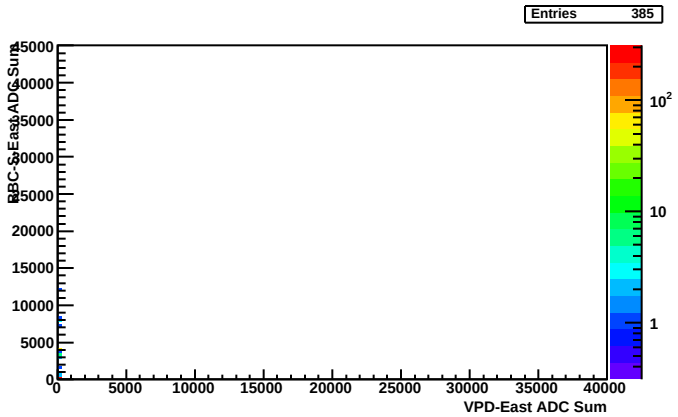

Entries 385
BBC-L-West ADC Sum
BBC-S-West ADC Sum

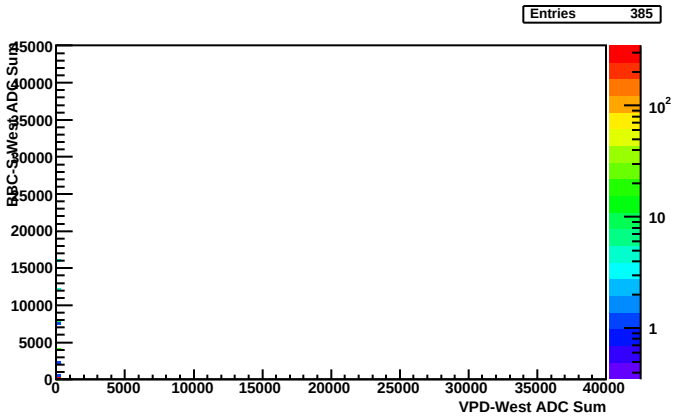

Entries 385
BBC-L-East ADC Sum
VPD-East ADC Sum

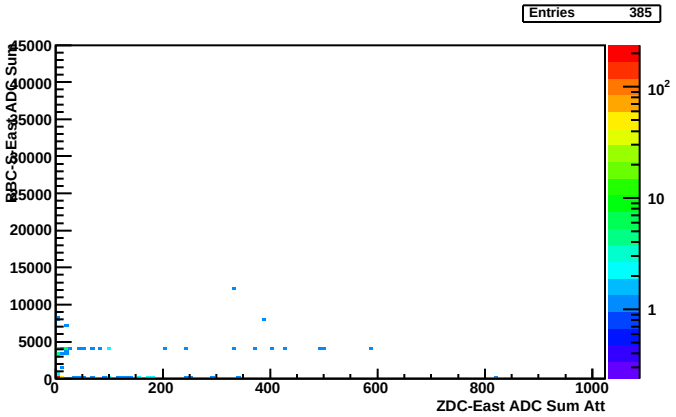

Entries 385
BBC-L-West ADC Sum
VPD-West ADC Sum

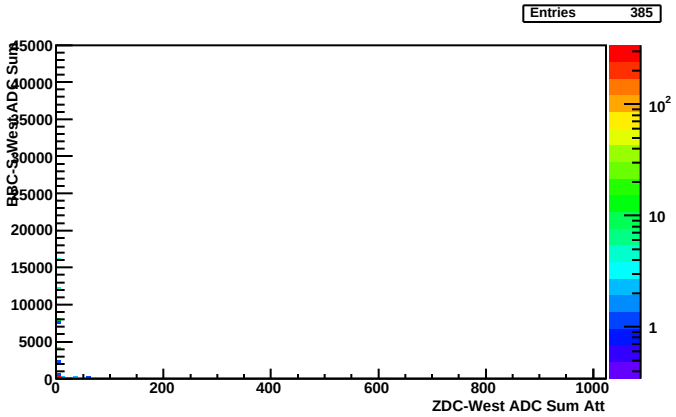

Entries 385
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

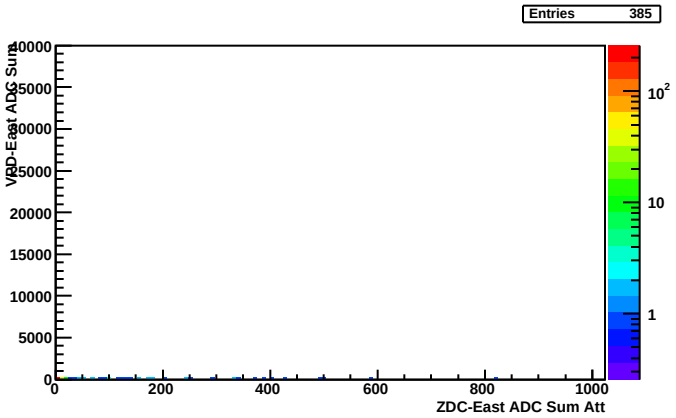

Entries 385
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

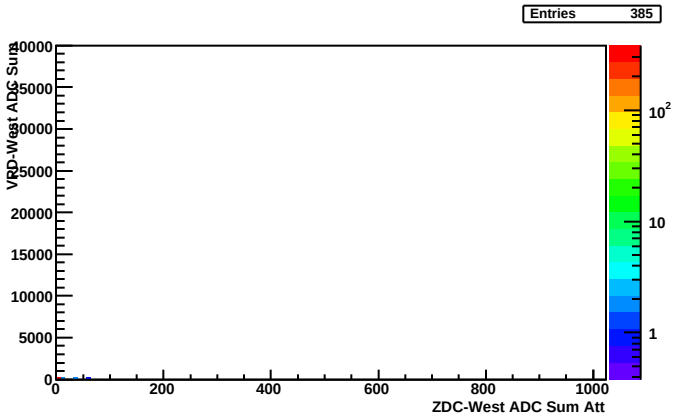

Entries 385
BBC-S-East ADC Sum
VPD-East ADC Sum

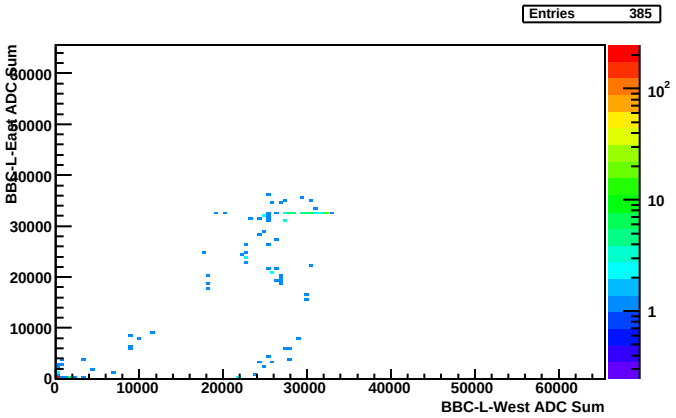

Entries 385
BBC-S-West ADC Sum
VPD-West ADC Sum

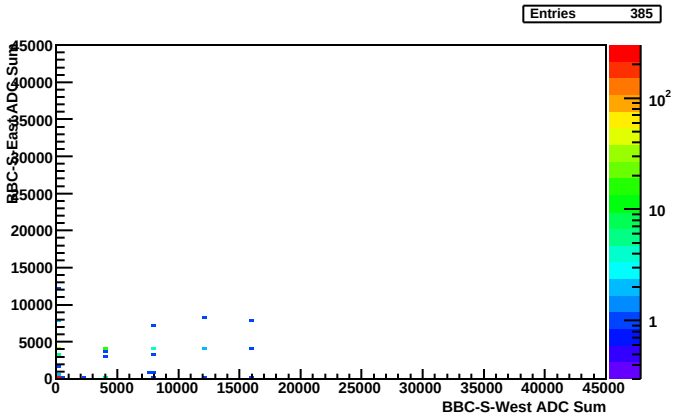

Entries 385
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

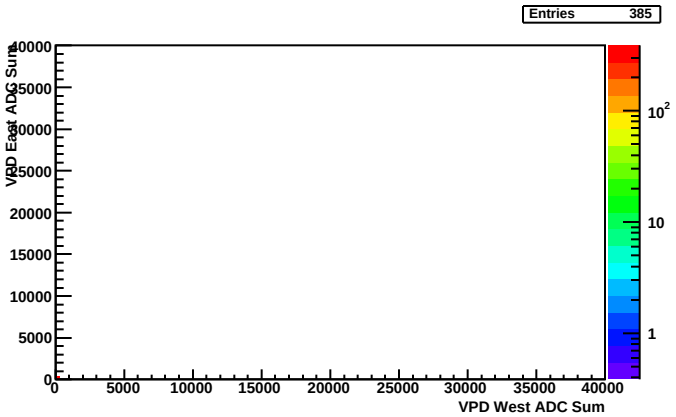

Entries 385
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

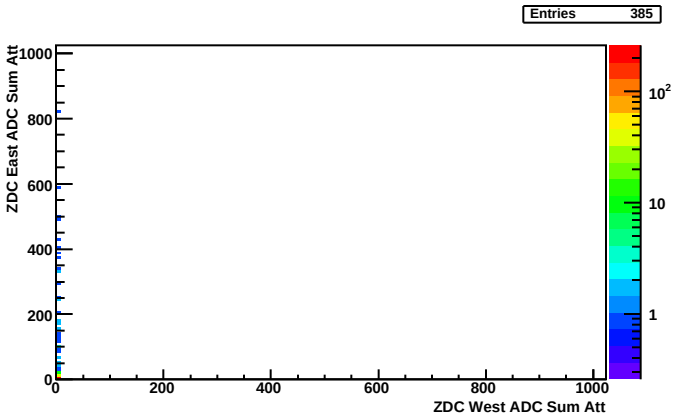

Entries 385
VPD-East ADC Sum
ZDC-East ADC Sum Att

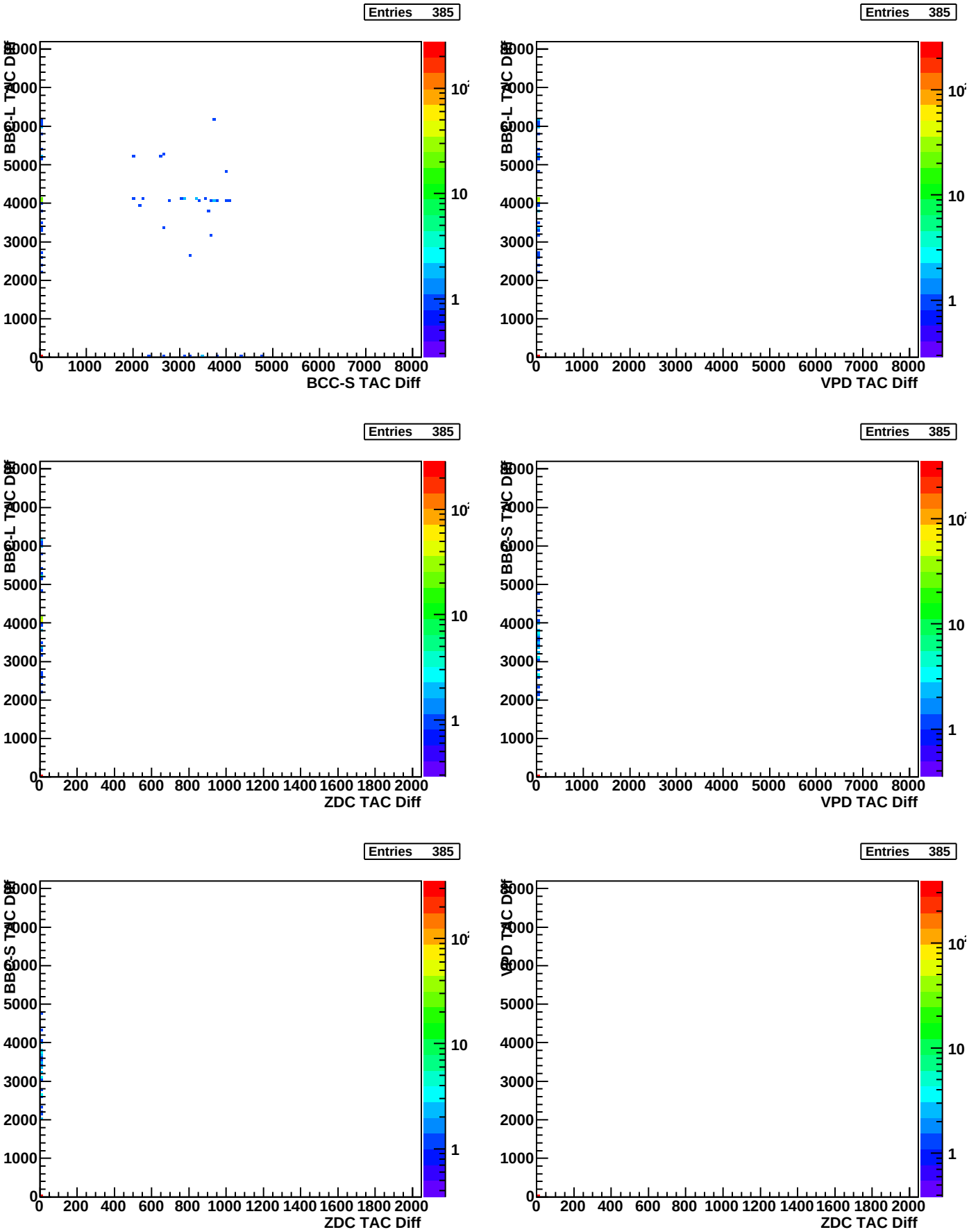

Entries 385
VPD-West ADC Sum
ZDC-West ADC Sum Att

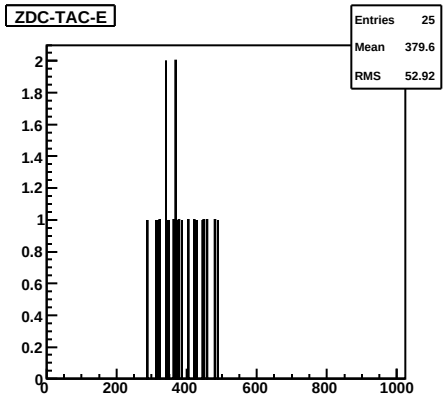

Entries 385
BBC-L-East ADC Sum
BBC-L-West ADC Sum

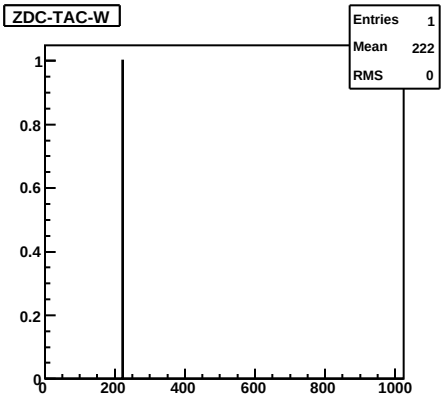

Entries 385
BBC-S-East ADC Sum
BBC-S-West ADC Sum

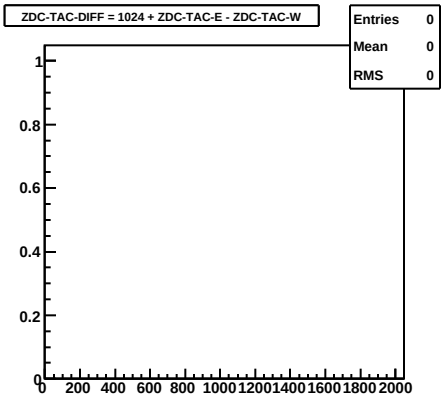

Entries 385
VPD East ADC Sum
VPD West ADC Sum

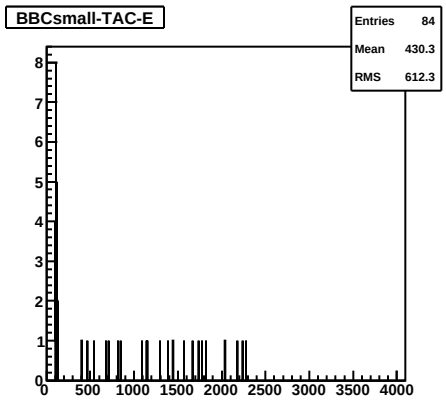

Entries 385
ZDC East ADC Sum Att
ZDC West ADC Sum Att

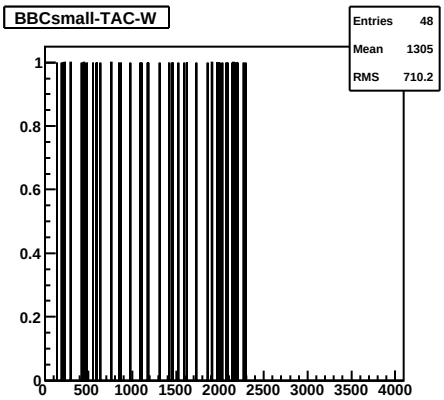

Entries 385
BBC-L TAC Diff
BCC-S TAC Diff
Entries 385
BBC-L TAC Diff
VPD TAC Diff
Entries 385
BBC-L TAC Diff
ZDC TAC Diff
Entries 385
BBC-S TAC Diff
VPD TAC Diff
Entries 385
BBC-S TAC Diff
ZDC TAC Diff
Entries 385
VPD TAC Diff
ZDC TAC Diff

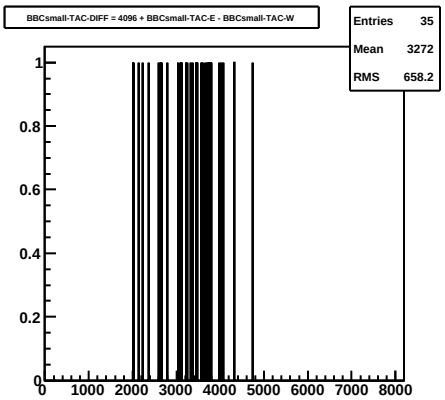

ZDC-TAC-E
Entries 25
Mean 379.6
RMS 52.92

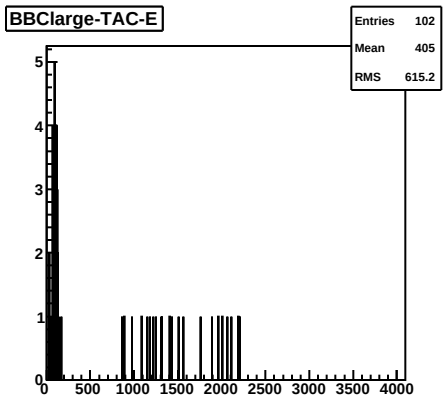

ZDC-TAC-W
Entries 1
Mean 222
RMS 0

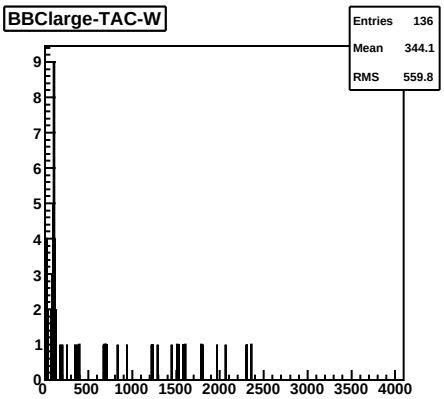

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 0
Mean 0
RMS 0

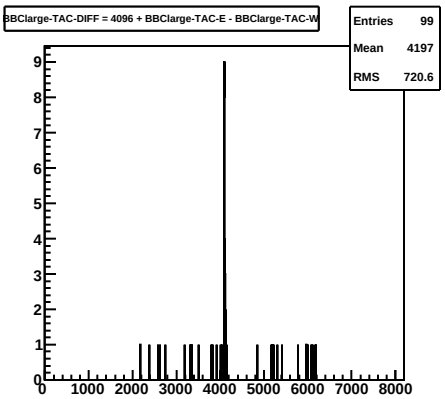

BBCsmall-TAC-E
Entries 84
Mean 430.3
RMS 612.3

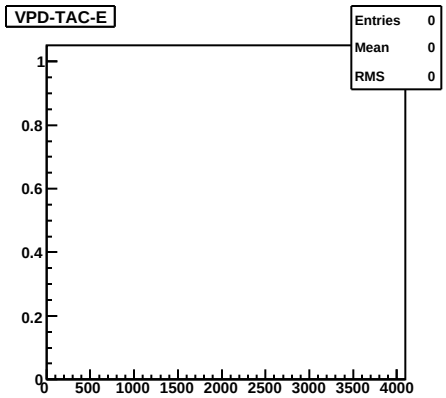

BBCsmall-TAC-W
Entries 48
Mean 1305
RMS 710.2

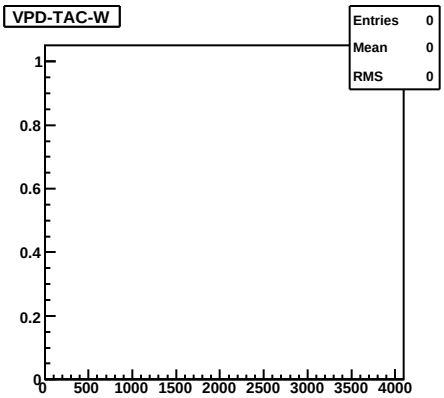

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 35
Mean 3272
RMS 658.2

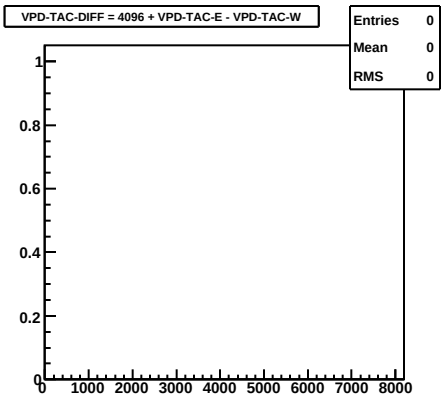

BBClarge-TAC-E
Entries 102
Mean 405
RMS 615.2

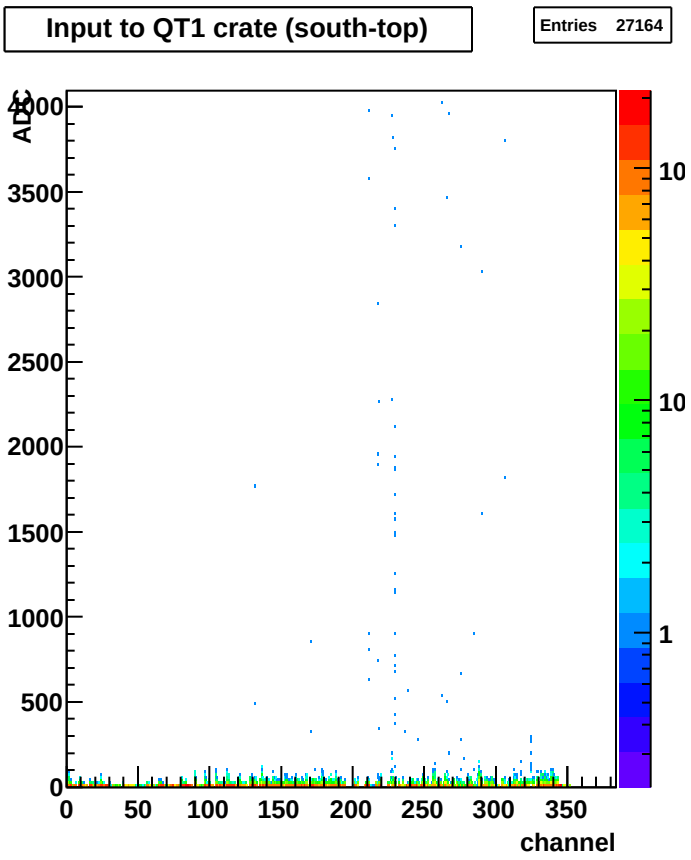

BBClarge-TAC-W
Entries 136
Mean 344.1
RMS 559.8

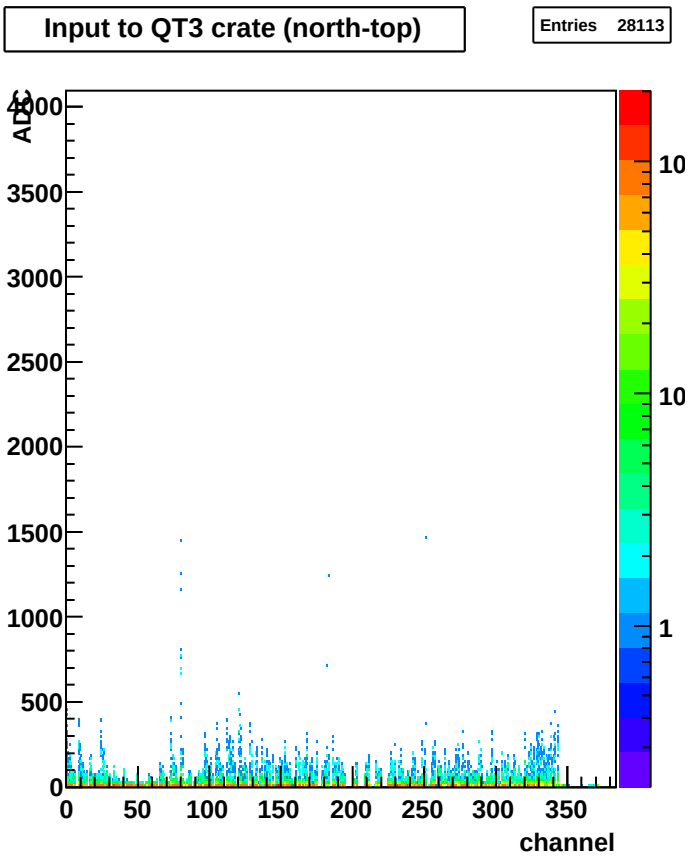

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 99
Mean 4197
RMS 720.6

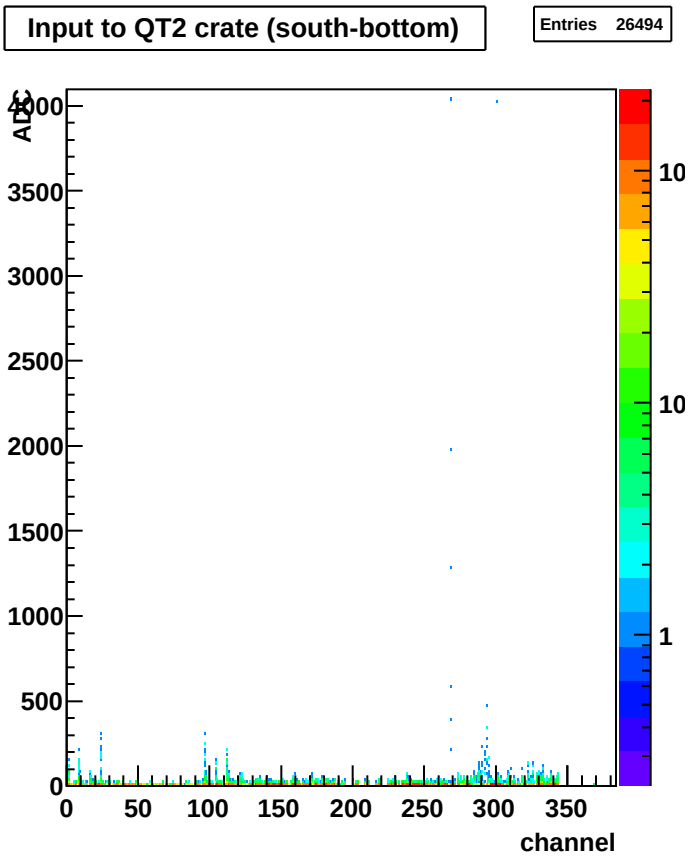

VPD-TAC-E
Entries 0
Mean 0
RMS 0

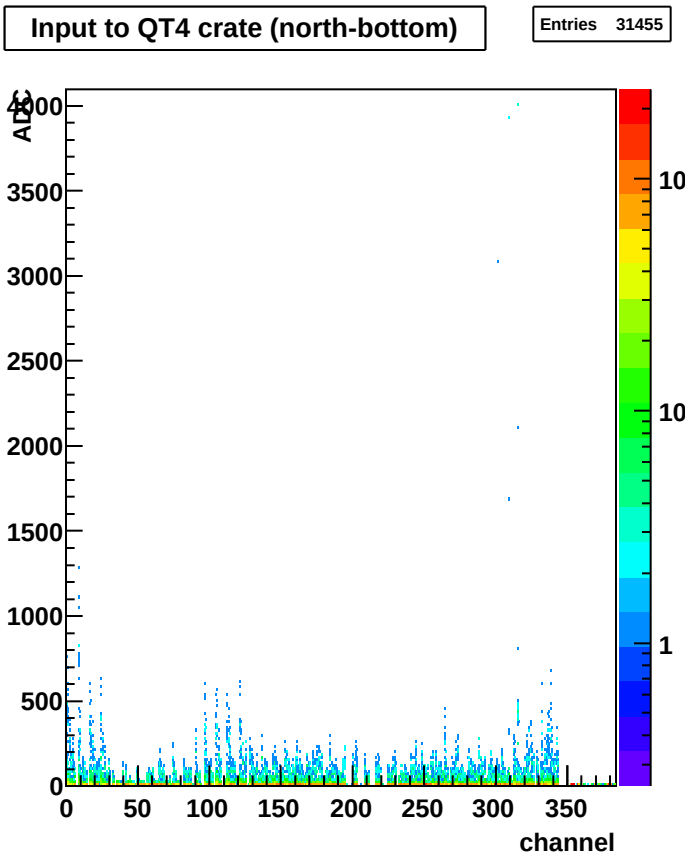

VPD-TAC-W
Entries 0
Mean 0
RMS 0

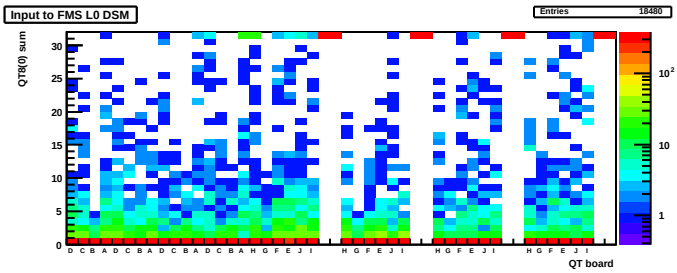

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

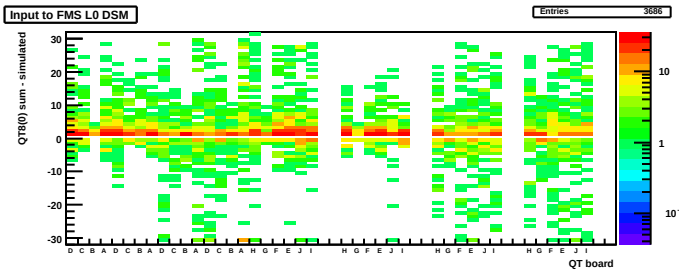

Input to QT1 crate (south-top)
Entries 27164
ADC
channel

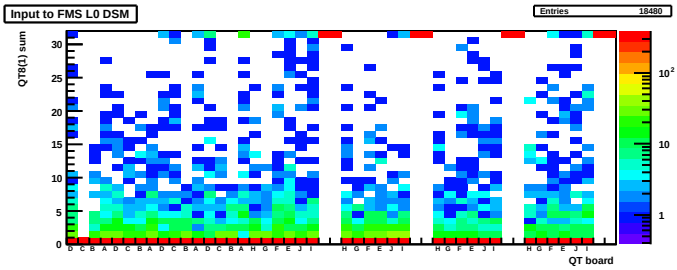

Input to QT3 crate (north-top)
Entries 28113
ADC
channel

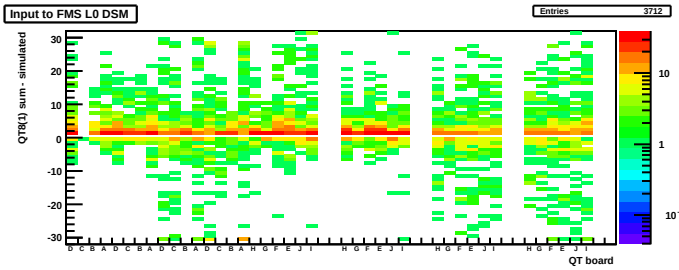

Input to QT2 crate (south-bottom)
Entries 26494
ADC
channel

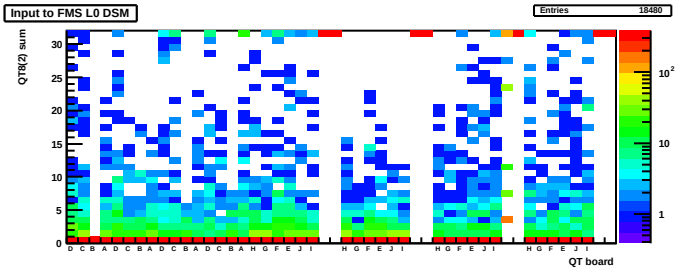

Input to QT4 crate (north-bottom)
Entries 31455
ADC
channel

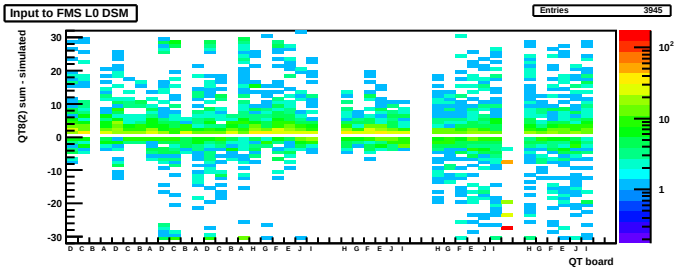

Input to FMS L0 DSM
Entries 18480
QT8(0) sum
QT board

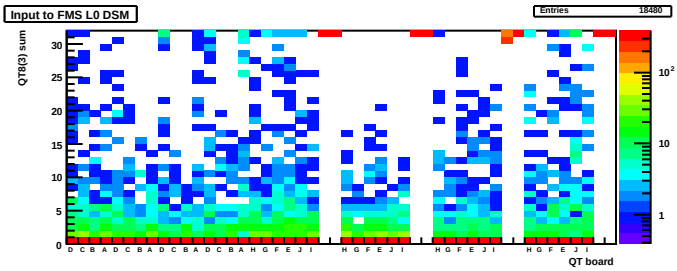

Input to FMS L0 DSM
Entries 3686
QT8(0) sum - simulated
QT board

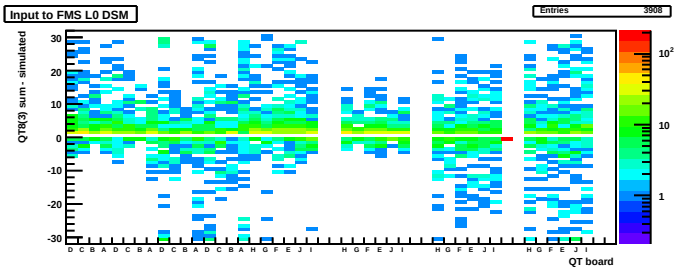

Input to FMS L0 DSM
Entries 18480
QT8(1) sum
QT board

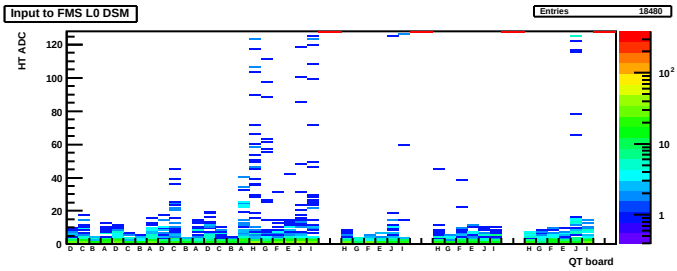

Input to FMS L0 DSM
Entries 3712
QT8(1) sum - simulated
QT board

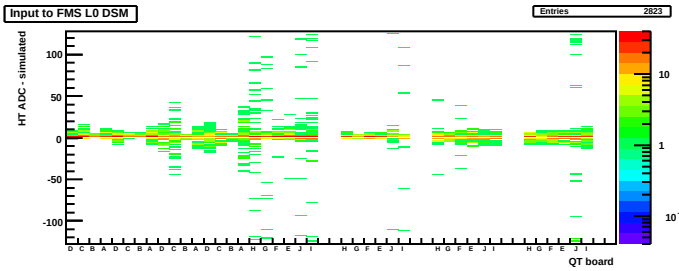

Input to FMS L0 DSM
Entries 18480
QT8(2) sum
QT board

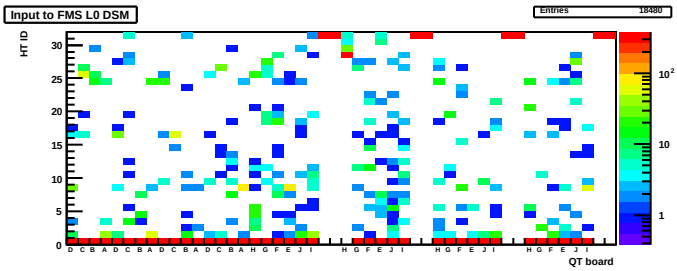

Input to FMS L0 DSM
Entries 3945
QT8(2) sum - simulated
QT board

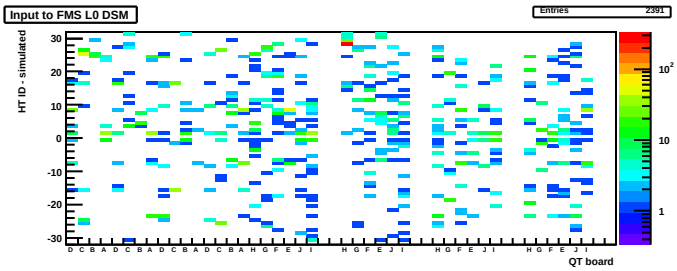

Input to FMS L0 DSM
Entries 18480
QT8(3) sum
QT board

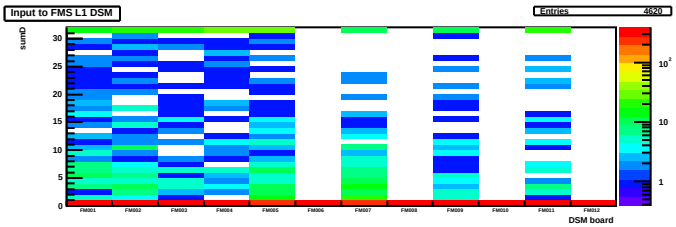

Input to FMS L0 DSM
Entries 3908
QT8(3) sum - simulated
QT board

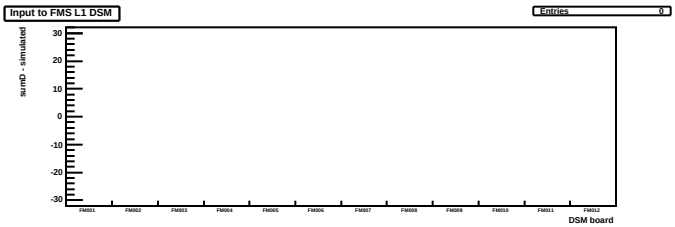

Input to FMS L0 DSM
Entries 18480
HT ADC
QT board

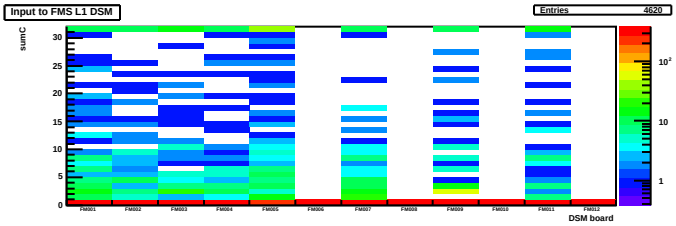

Input to FMS L0 DSM
Entries 2823
HT ADC - simulated
QT board

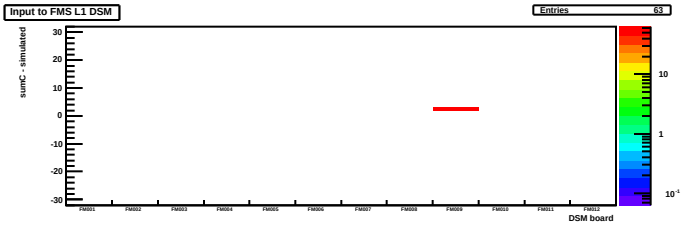

Input to FMS L0 DSM
Entries 18480
HT ID
QT board

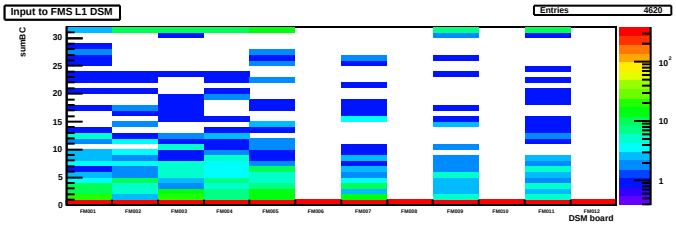

Input to FMS L0 DSM
Entries 2391
HT ID - simulated
QT board

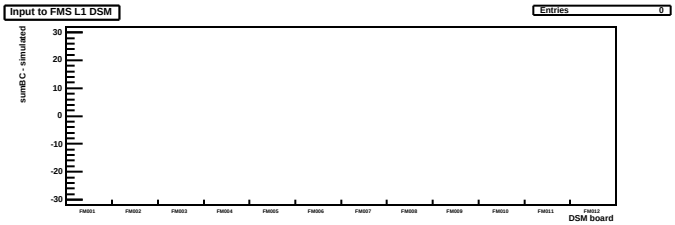

Input to FMS L1 DSM
Entries 4620
sumD
DSM board

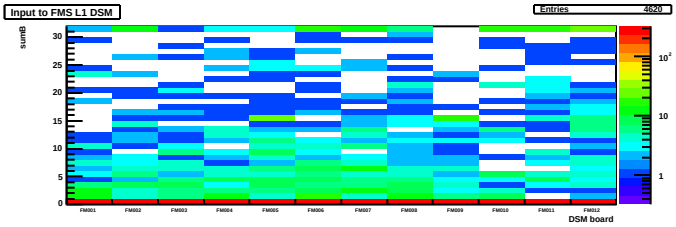

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

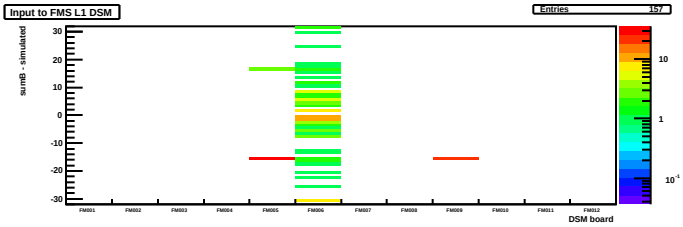

Input to FMS L1 DSM
Entries 4620
sumC
DSM board

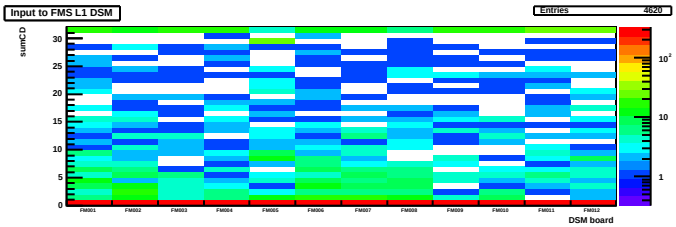

Input to FMS L1 DSM
Entries 63
sumC - simulated
DSM board

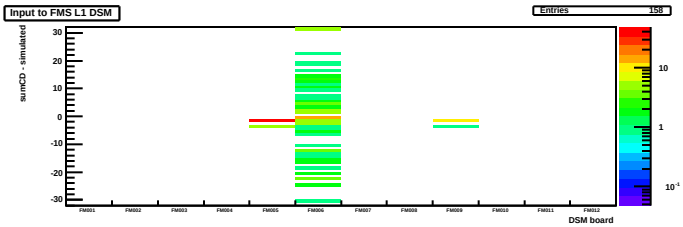

Input to FMS L1 DSM
Entries 4620
sumBC
DSM board

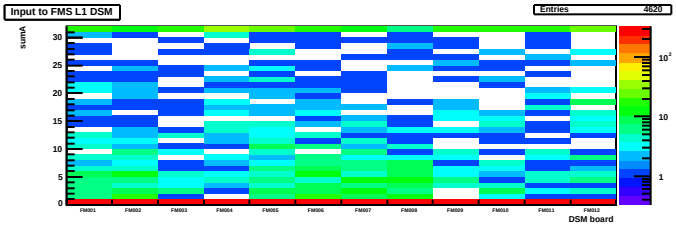

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

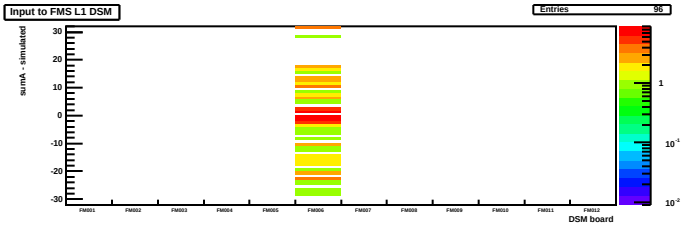

Input to FMS L1 DSM
Entries 4620
sumB
DSM board

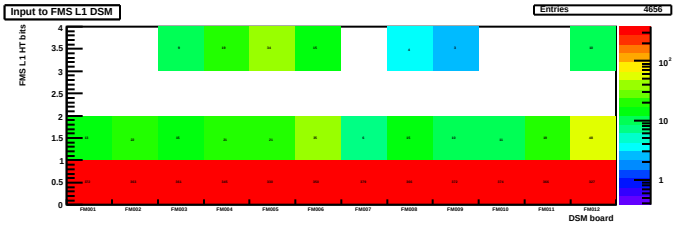

Input to FMS L1 DSM
Entries 157
sumB - simulated
DSM board

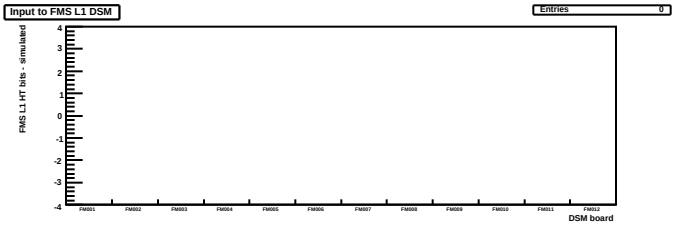

Input to FMS L1 DSM
Entries 4620
sumCD
DSM board

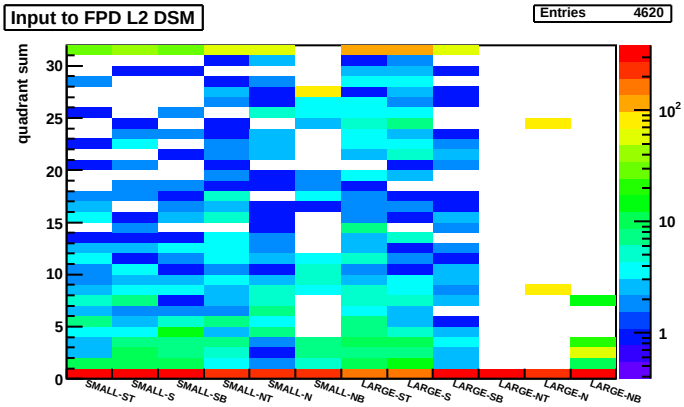

Input to FMS L1 DSM
Entries 158
sumCD - simulated
DSM board

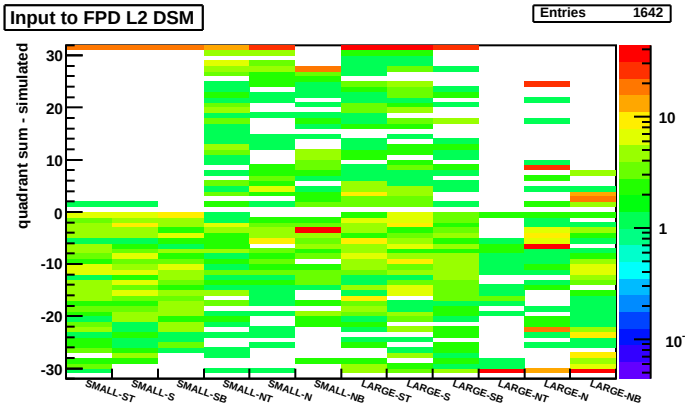

Input to FMS L1 DSM
Entries 4620
sumA
DSM board

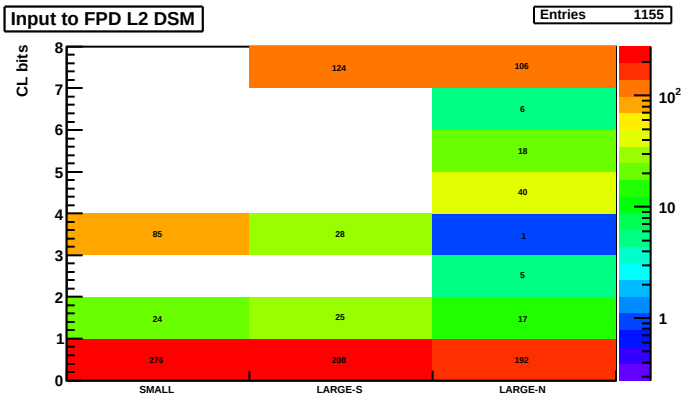

Input to FMS L1 DSM
Entries 96
sumA - simulated
DSM board

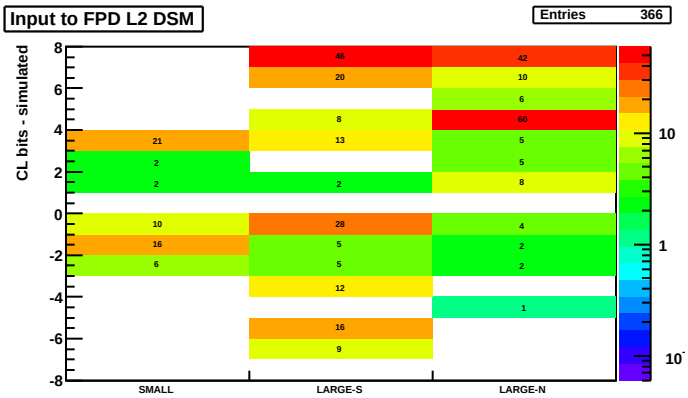

Input to FMS L1 DSM
Entries 4656
FMS L1 HT bits
DSM board

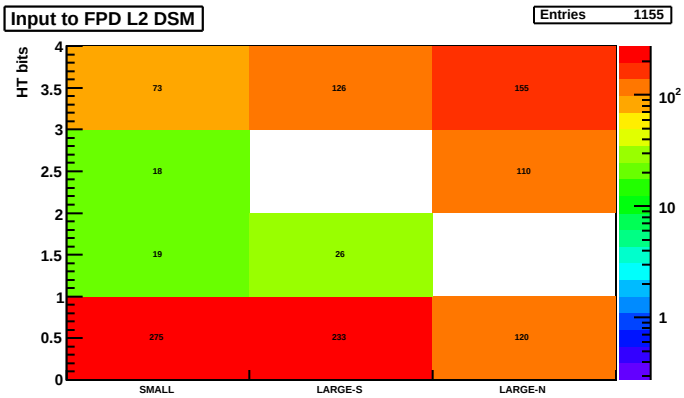

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

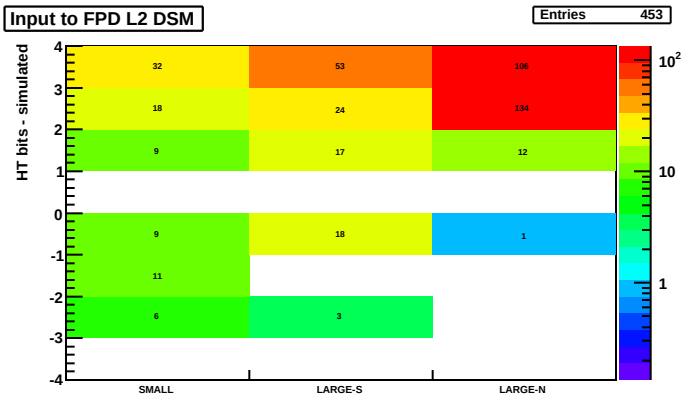

Input to FPD L2 DSM
Entries 4620
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

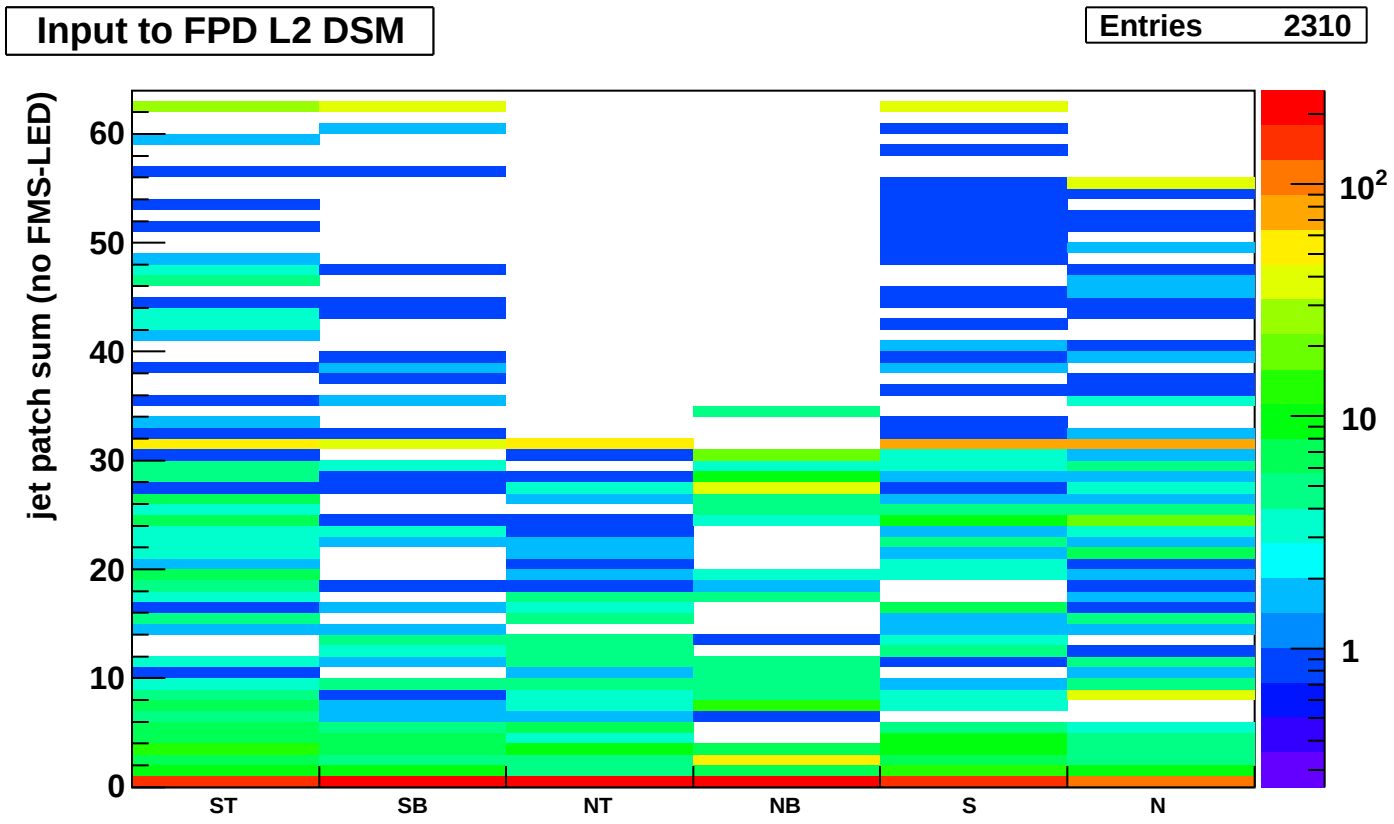

Input to FPD L2 DSM
Entries 1642
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

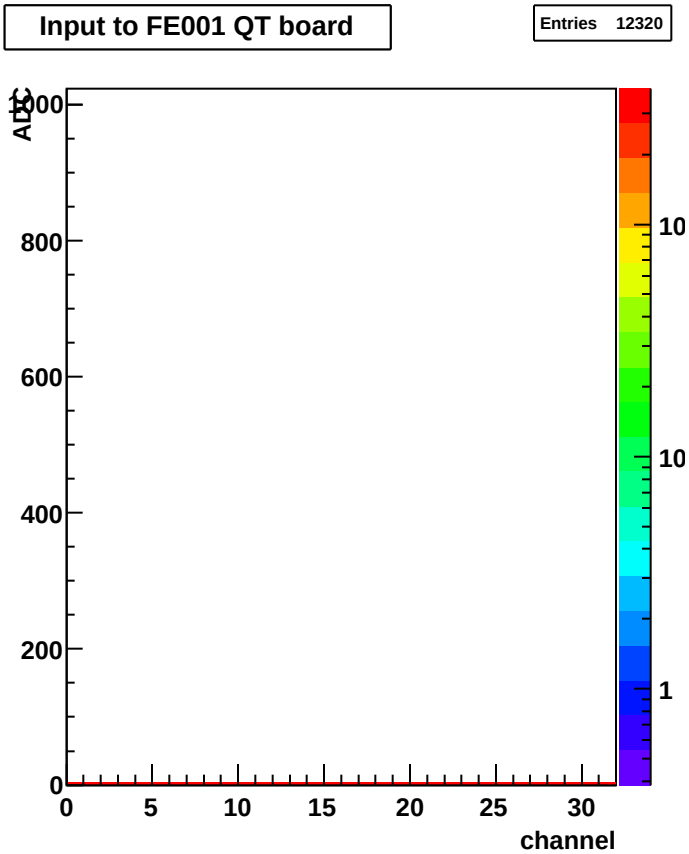

Input to FPD L2 DSM
Entries 1155
CL bits
SMALL
LARGE-S
LARGE-N

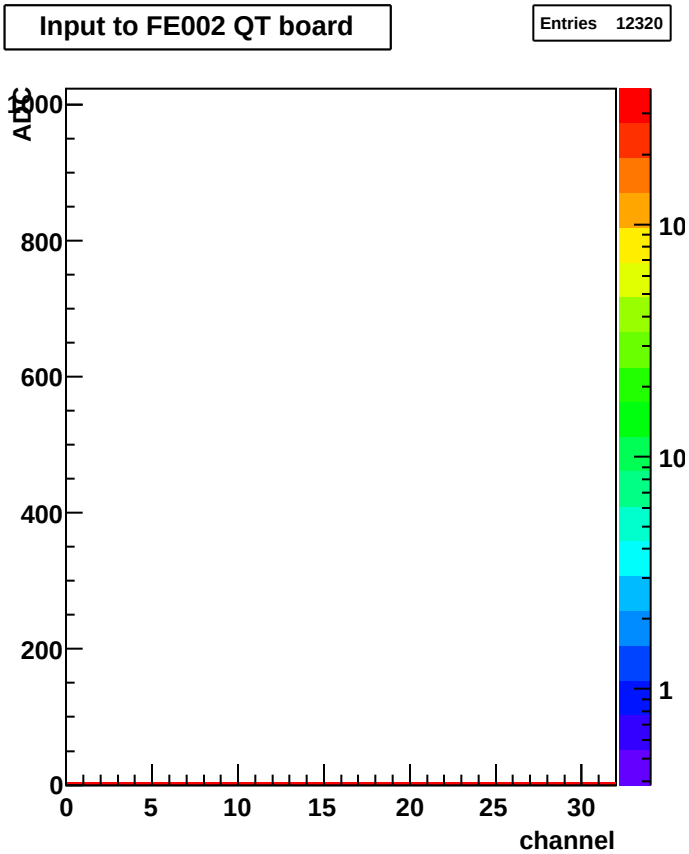

Input to FPD L2 DSM
Entries 366
CL bits - simulated
SMALL
LARGE-S
LARGE-N

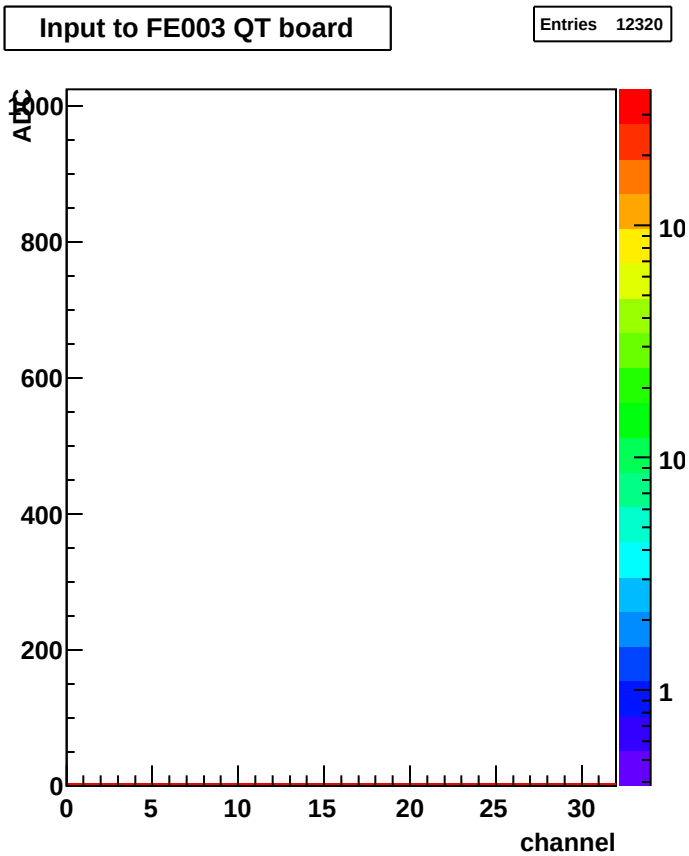

Input to FPD L2 DSM
Entries 1155
HT bits
SMALL
LARGE-S
LARGE-N

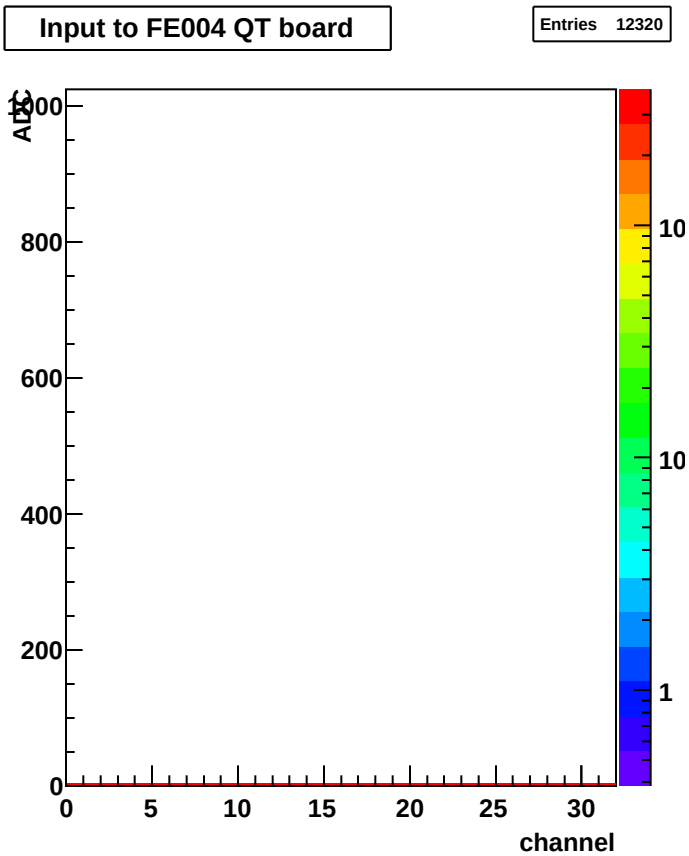

Input to FPD L2 DSM
Entries 453
HT bits - simulated
SMALL
LARGE-S
LARGE-N

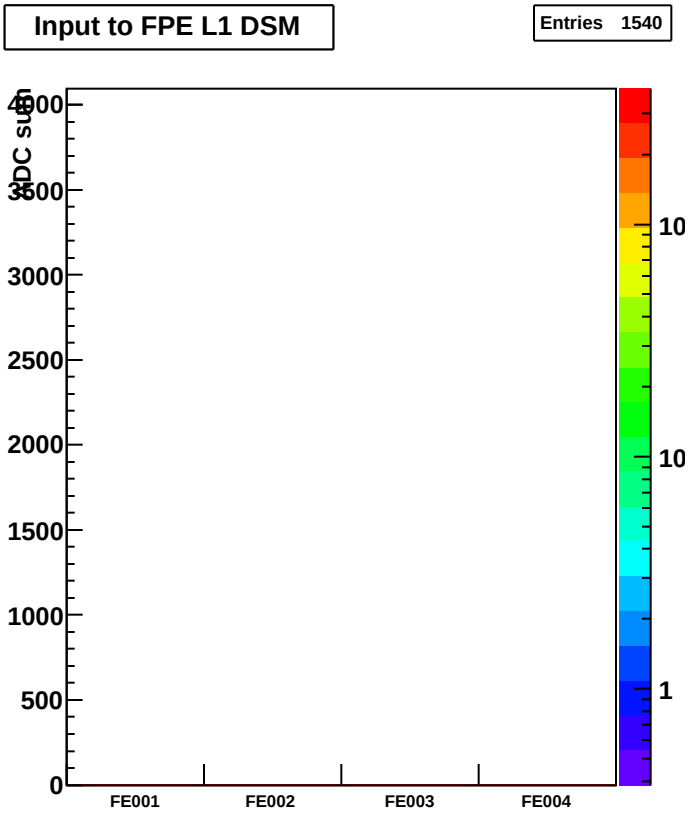

Input to FPD L2 DSM
Entries 2310
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
10^2
10
1

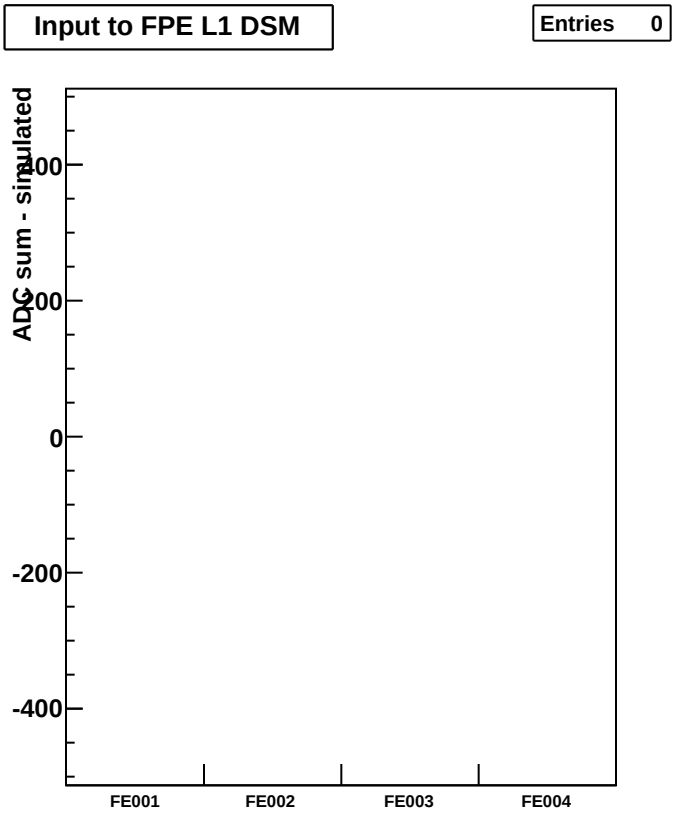

Input to FE001 QT board
Entries 12320
ADC
channel

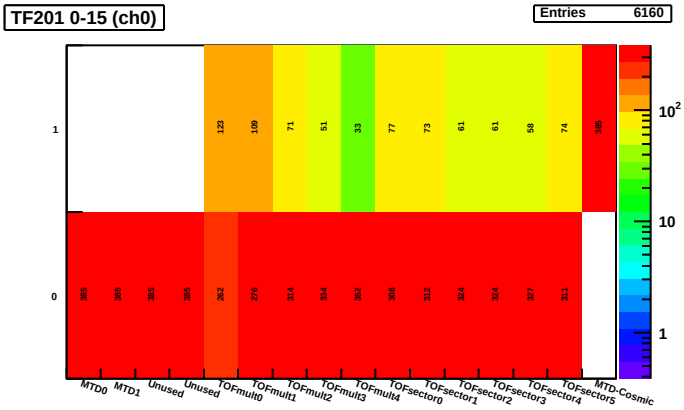

Input to FE002 QT board
Entries 12320
ADC
channel

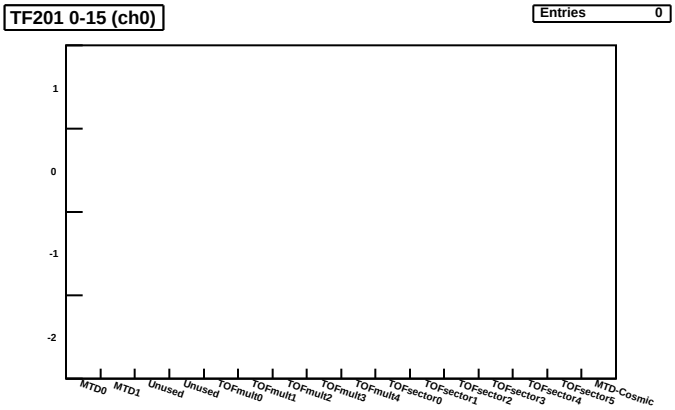

Input to FE003 QT board
Entries 12320
ADC
channel

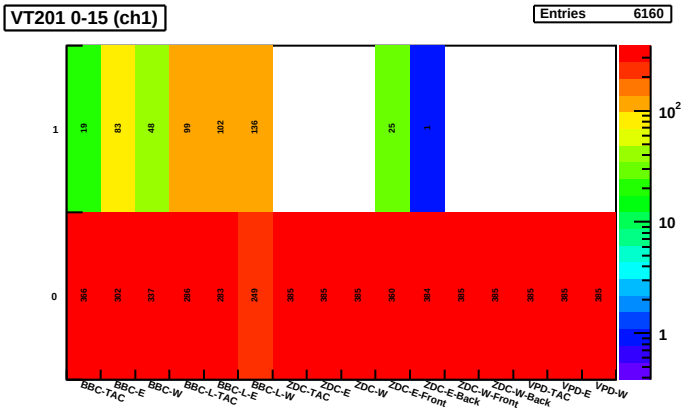

Input to FE004 QT board
Entries 12320
ADC
channel

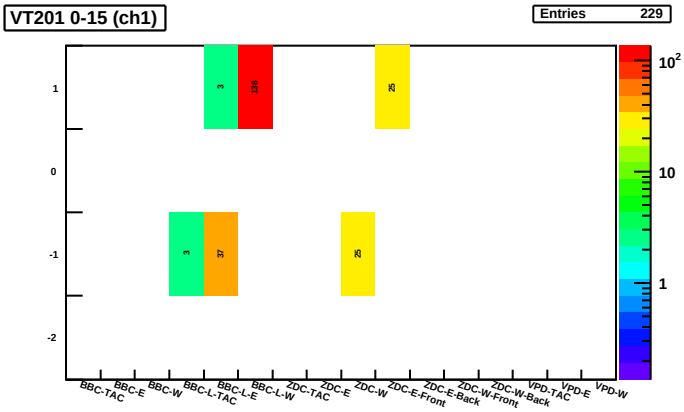

Input to FPE L1 DSM
Entries 1540
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
1

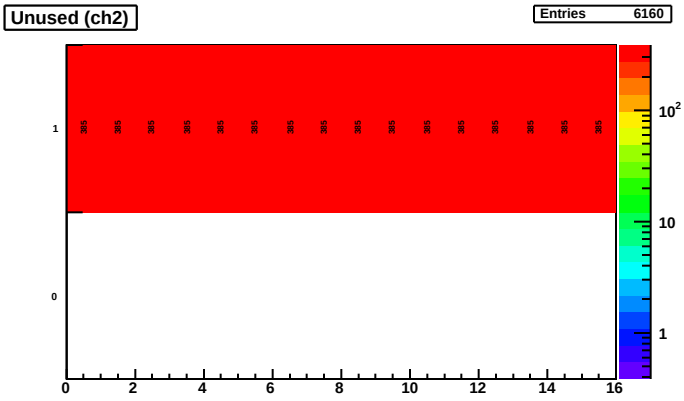

Input to FPE L1 DSM
Entries 0
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004

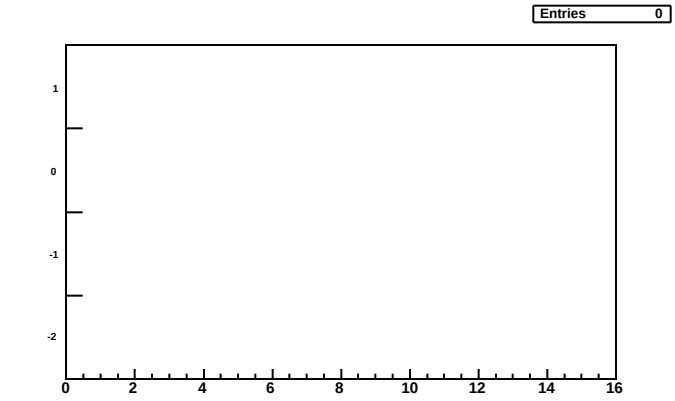

TF201 0-15 (ch0)
Entries 6160
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

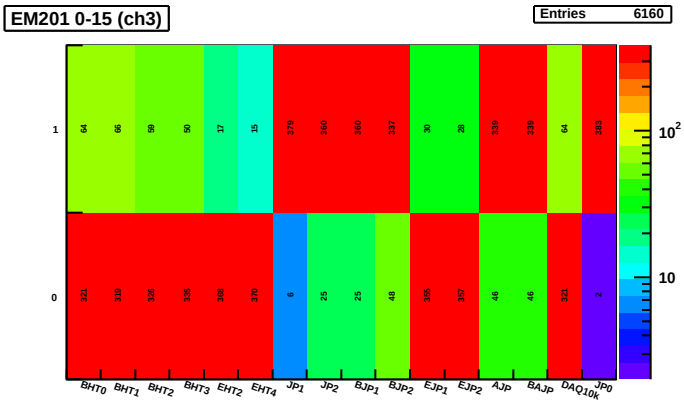

TF201 0-15 (ch0)
Entries 0
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

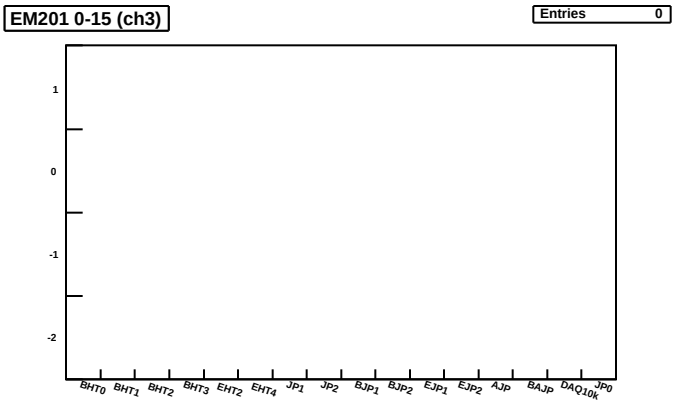

VT201 0-15 (ch1)
Entries 6160
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 229
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

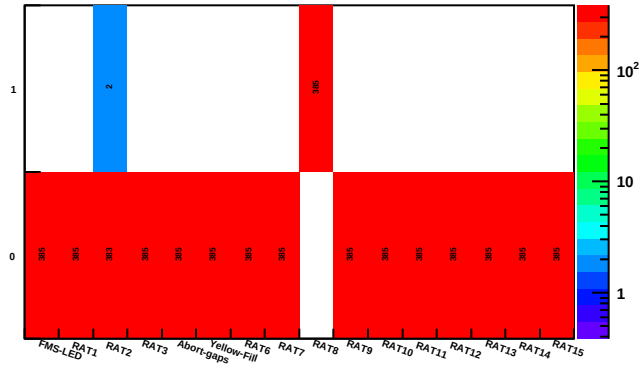

Unused (ch2)
Entries 6160

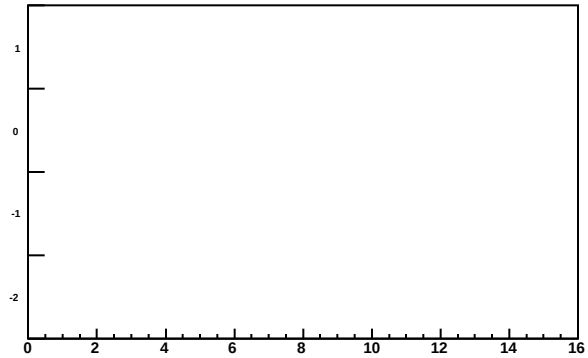

Entries 0


EM201 0-15 (ch3)
Entries 6160
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

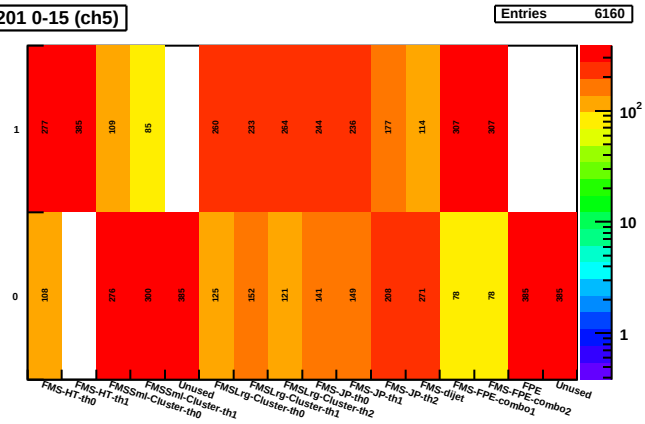

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

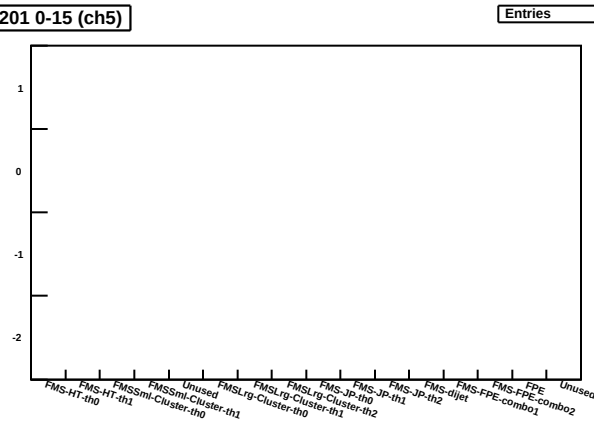




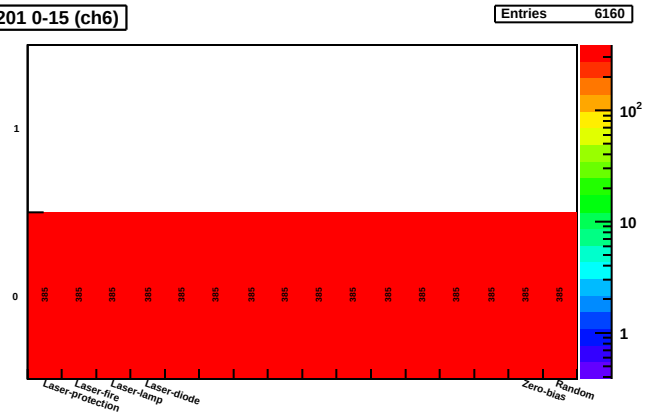
FP201 0-15 (ch5)

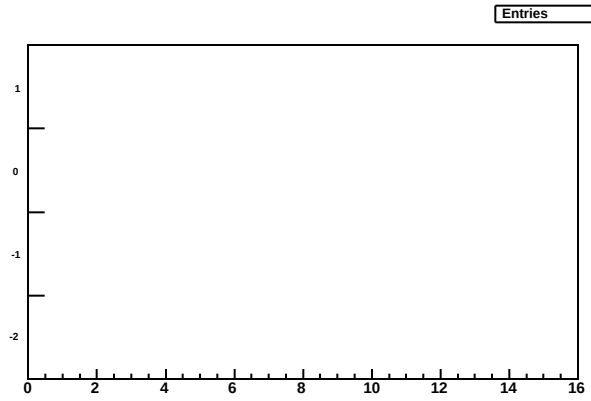
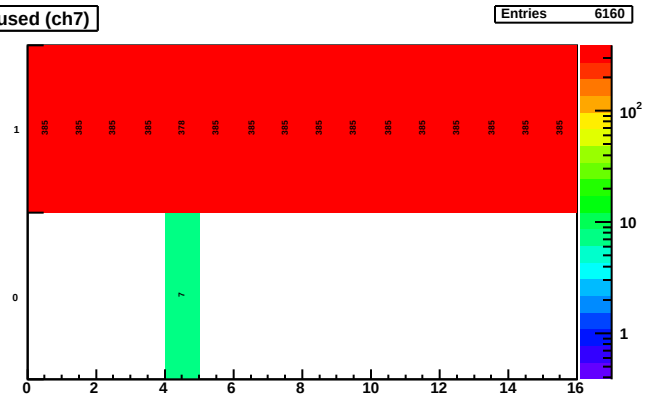
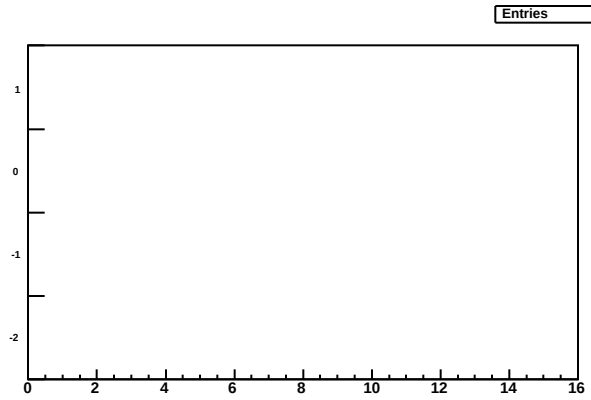


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)