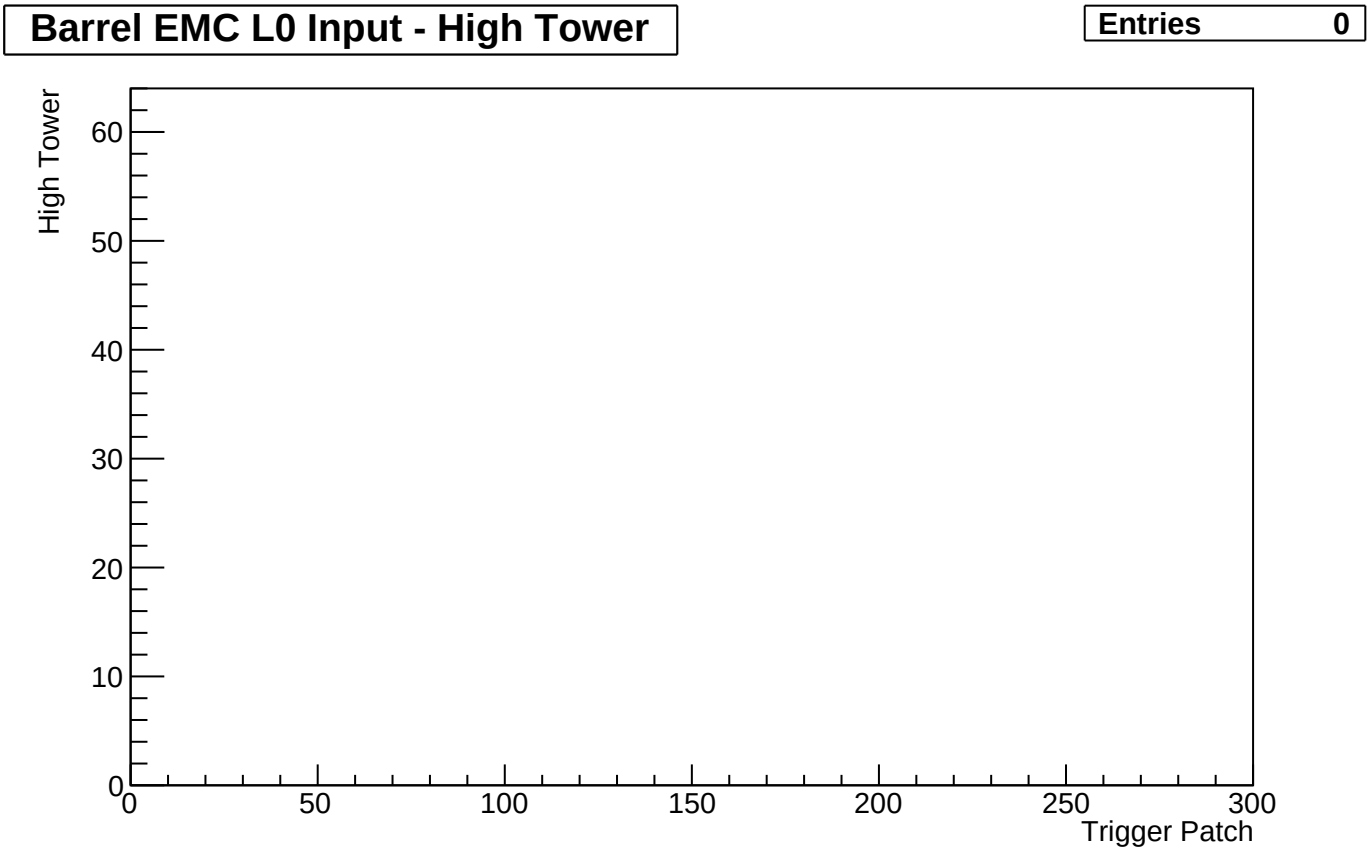
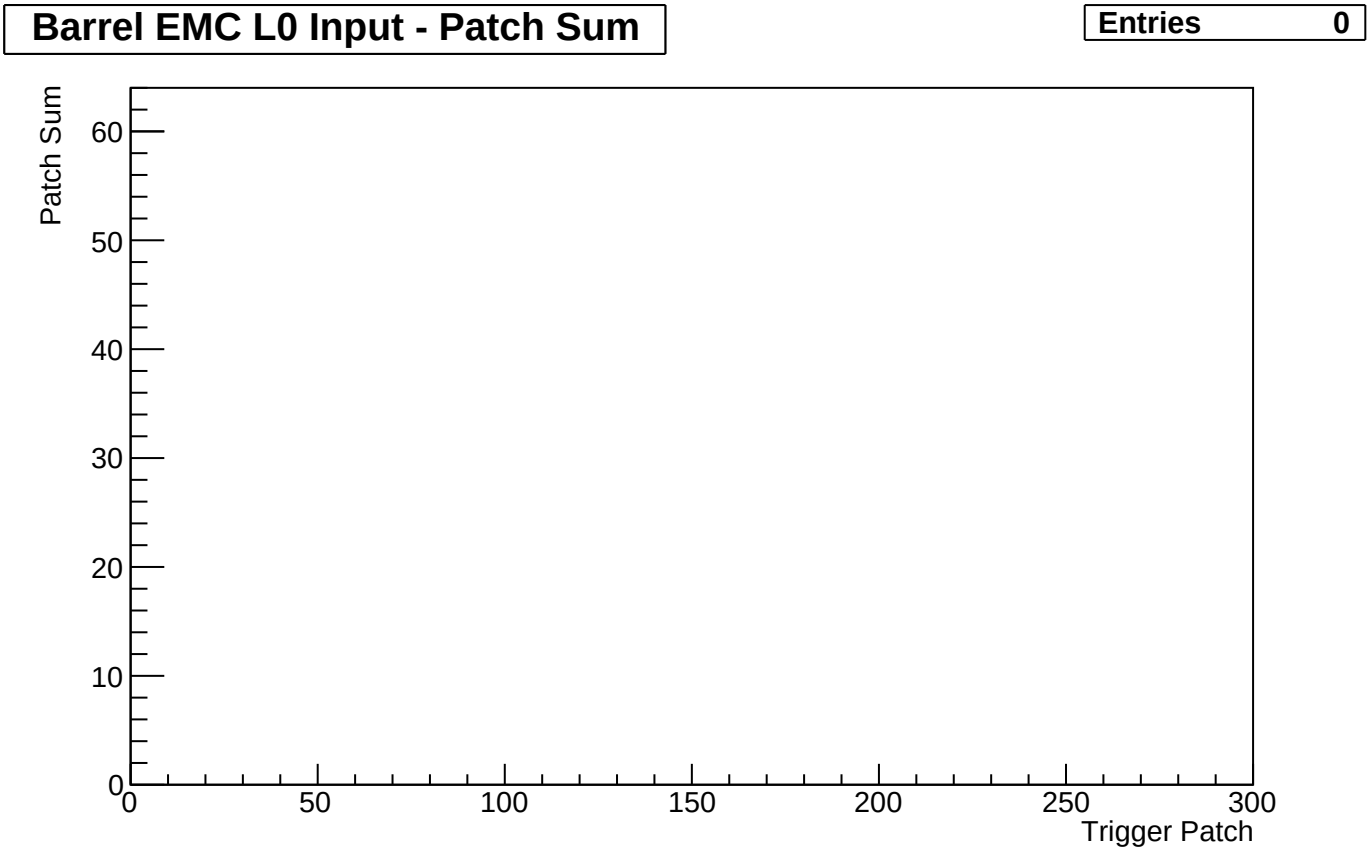
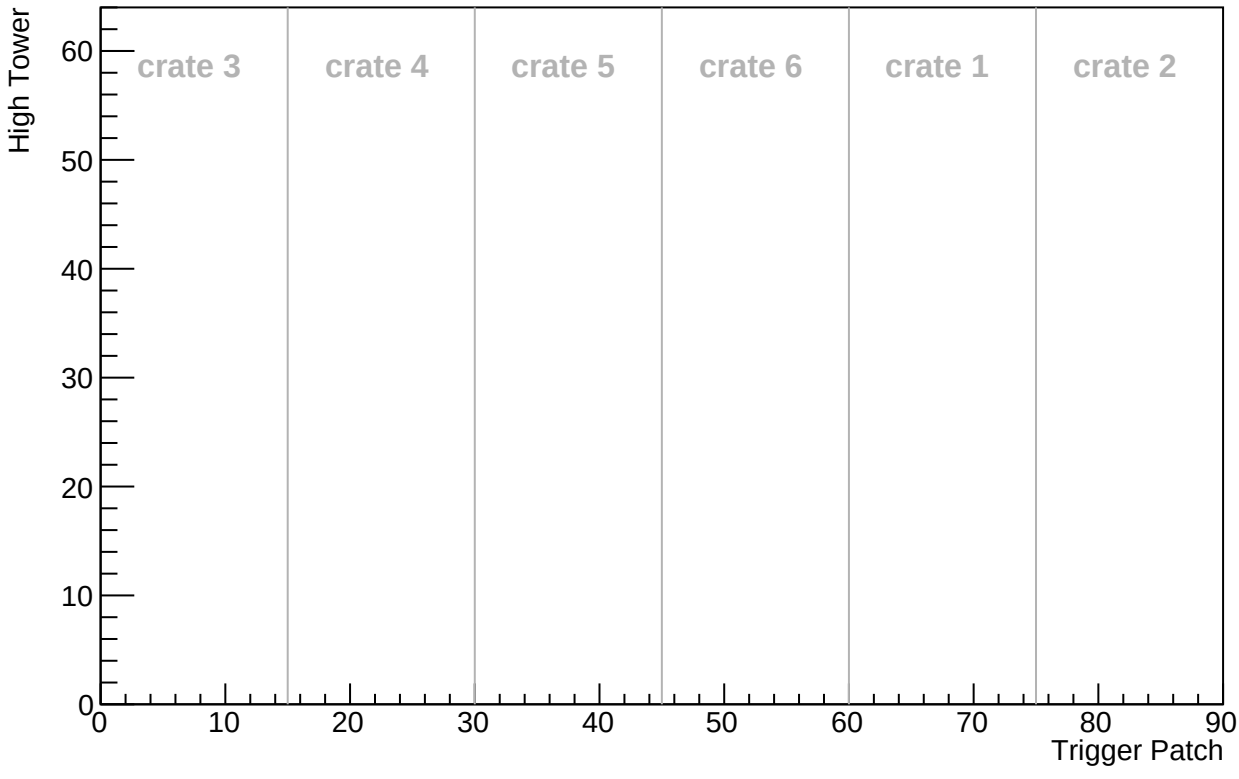

Barrel EMC L0 Input - High Tower
Entries 0
High Tower
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch


Barrel EMC L0 Input - Patch Sum
Entries 0
Patch Sum
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch

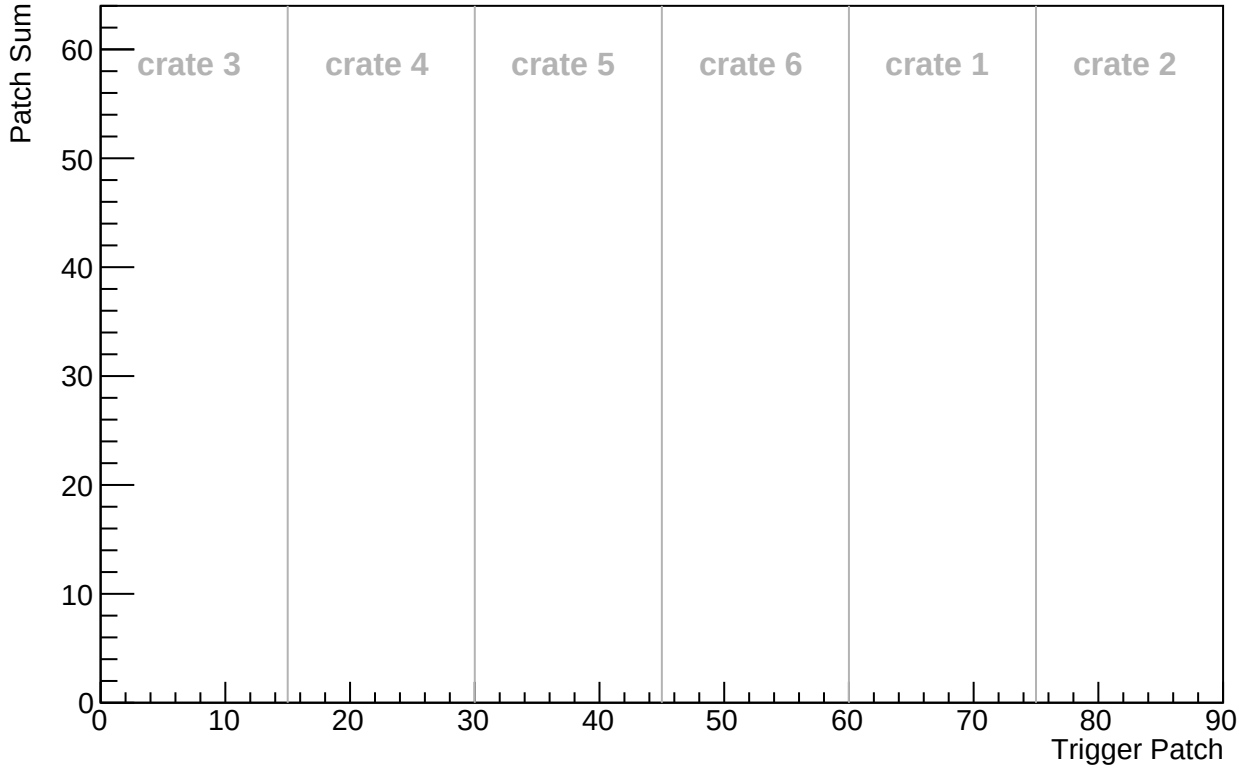
Endcap EMC L0 Input - High Tower

Entries 0



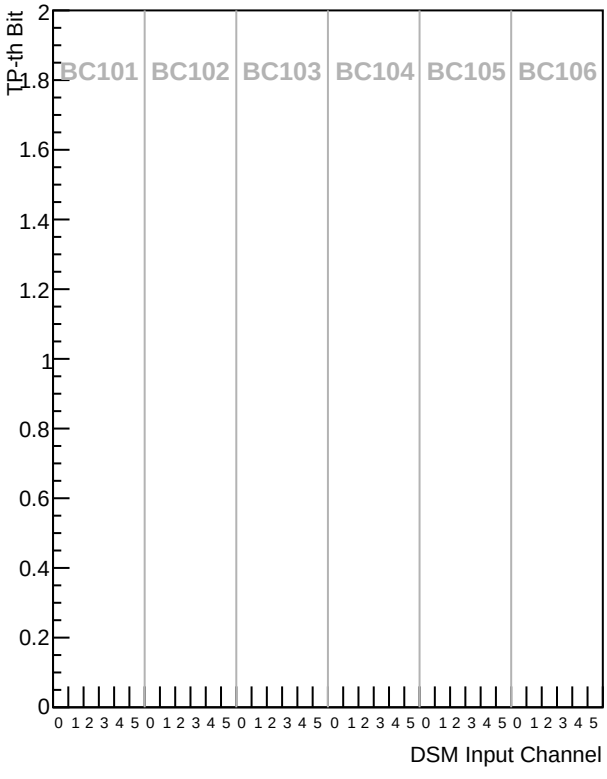
Endcap EMC L0 Input - Patch Sum

Entries 0



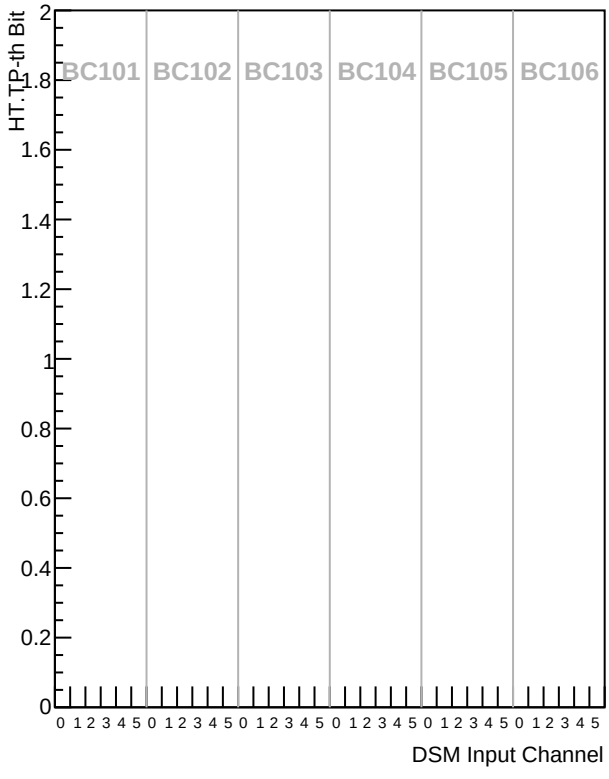
Barrel EMC L1 Input - TP Bit

Entries 0



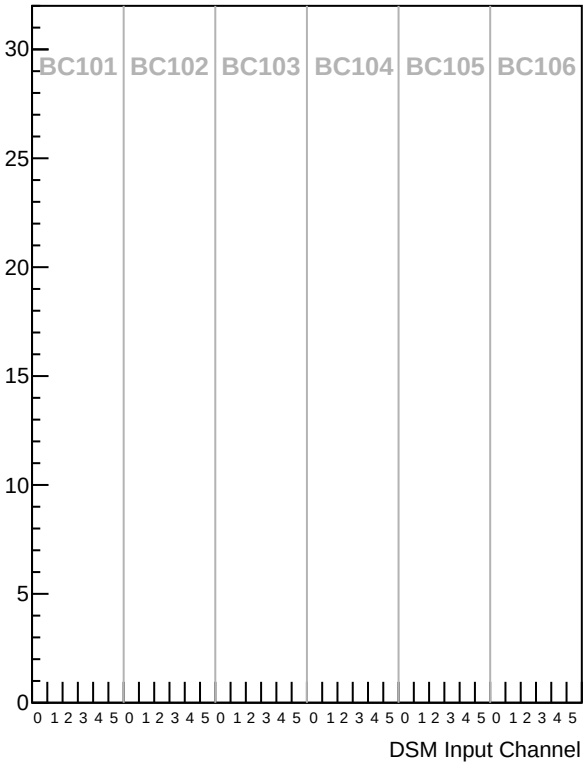
Barrel EMC L1 Input - HT.TP Bit

Entries 0



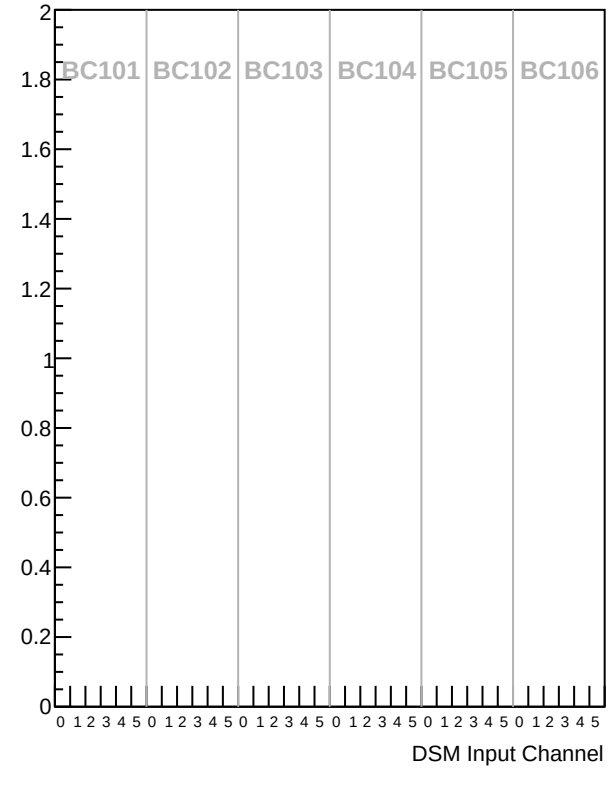
Barrel EMC L1 Input - HT Bits (th1-4)

Entries 0



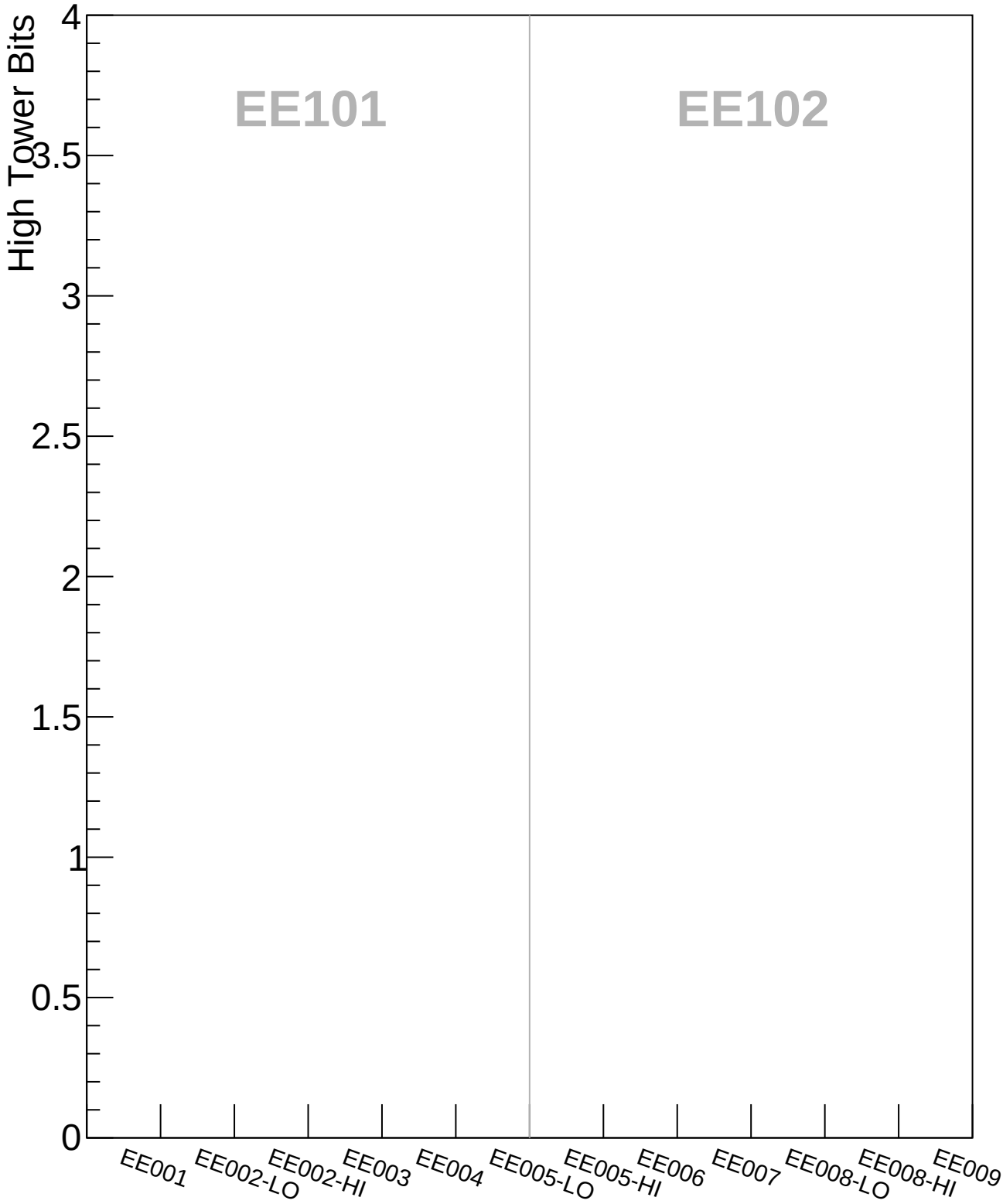
Barrel EMC L1 Input - HT Bit (UPC)

Entries 0



Endcap EMC L1 Input - High Tower Bits

Entries 0



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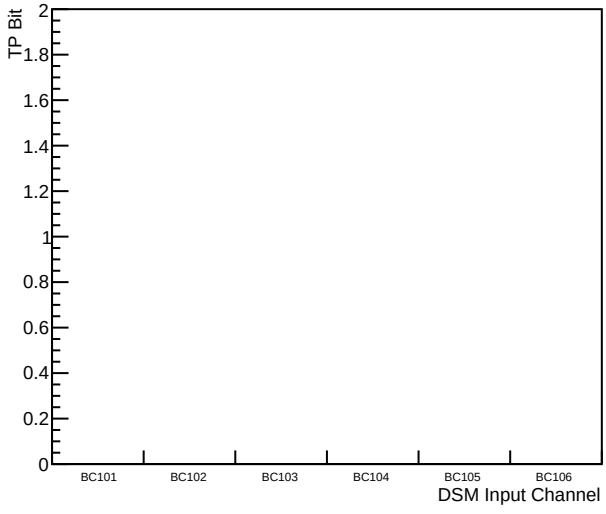
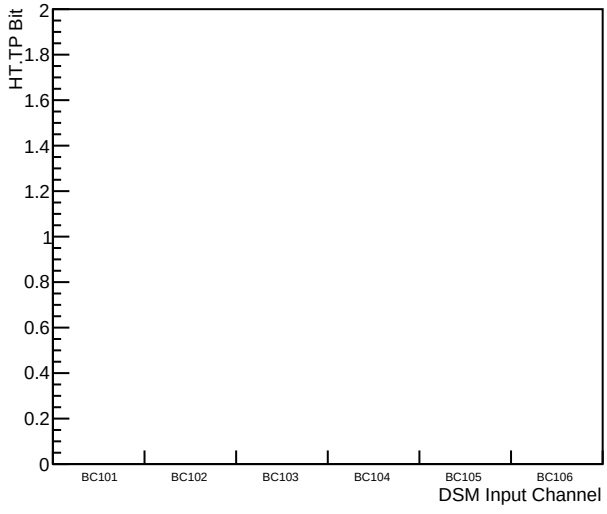
EMC L2 Input - TP Bit

Entries 0



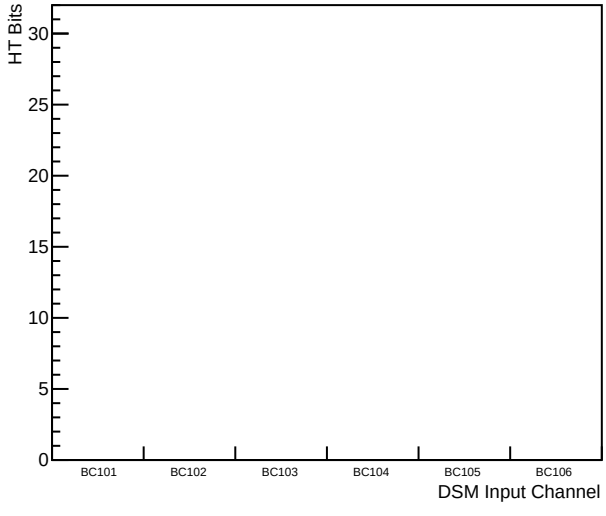
EMC L2 Input - HT.TP Bit

Entries 0



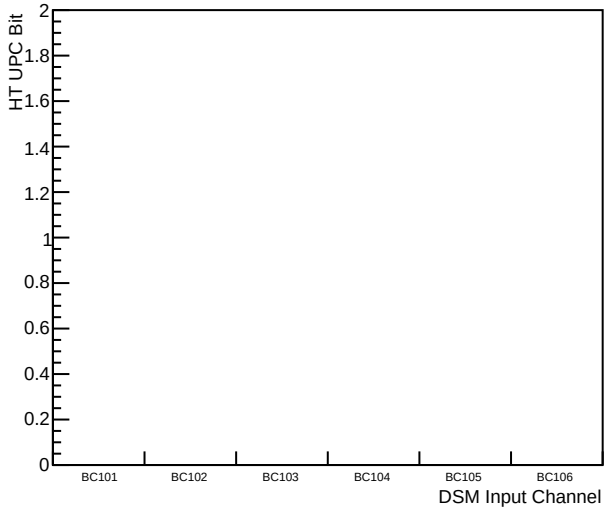
EMC L2 Input - HT Bits (th1-4)

Entries 0



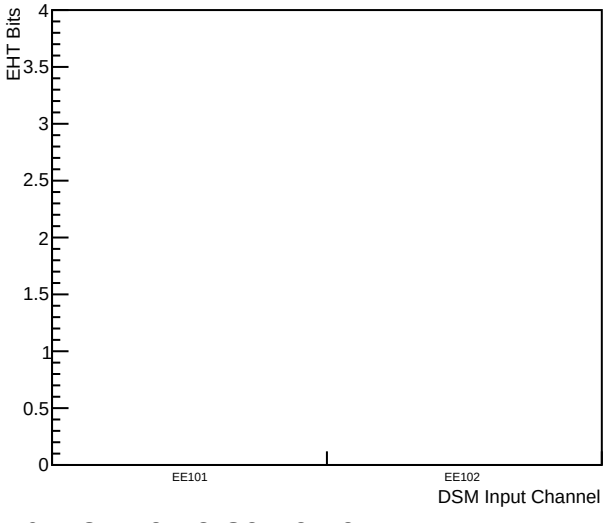
EMC L2 Input - HT Bit (UPC)

Entries 0



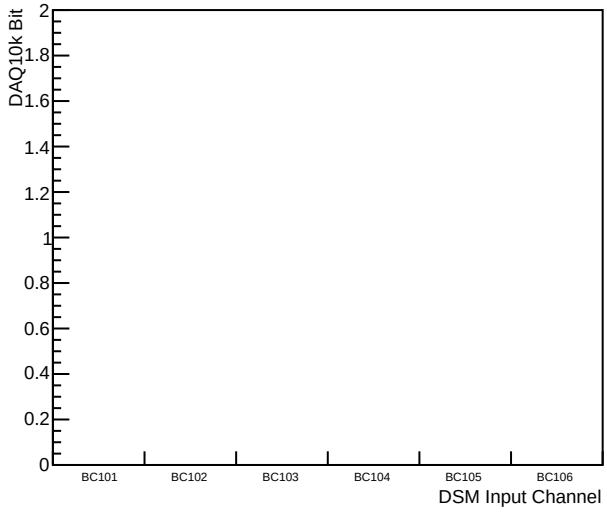
EMC L2 Input - EHT Bits

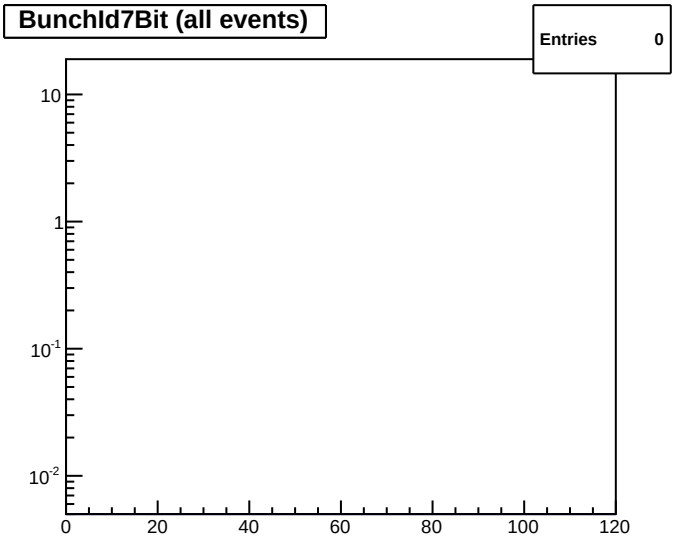
Entries 0

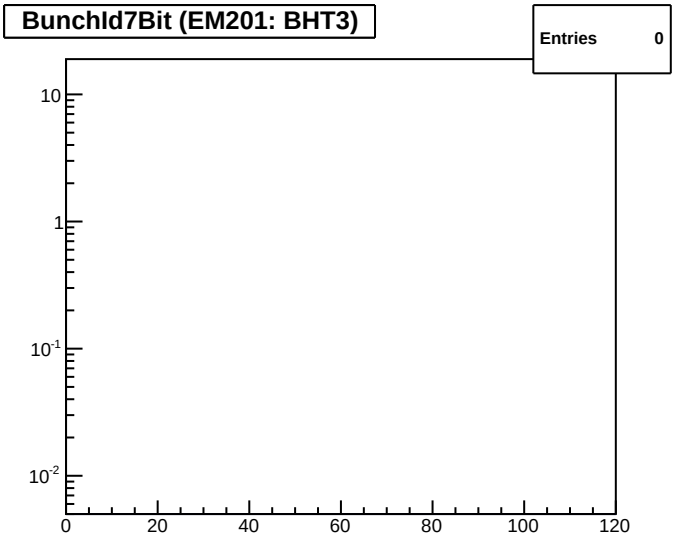


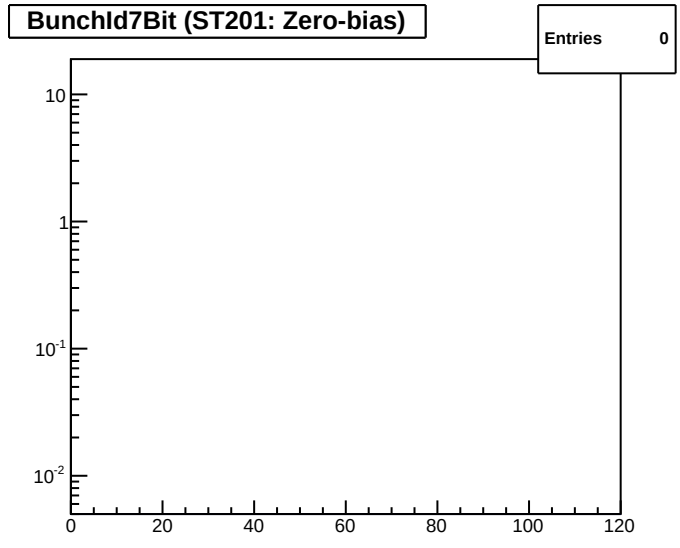
EMC L2 Input - DAQ10k Bit

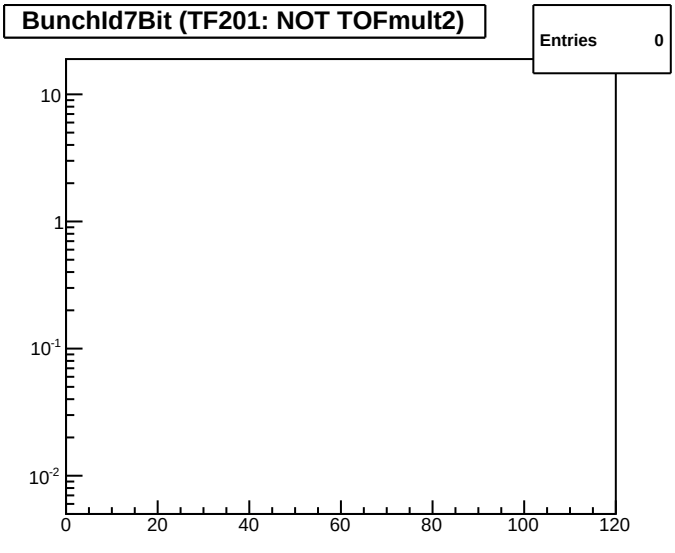
Entries 0

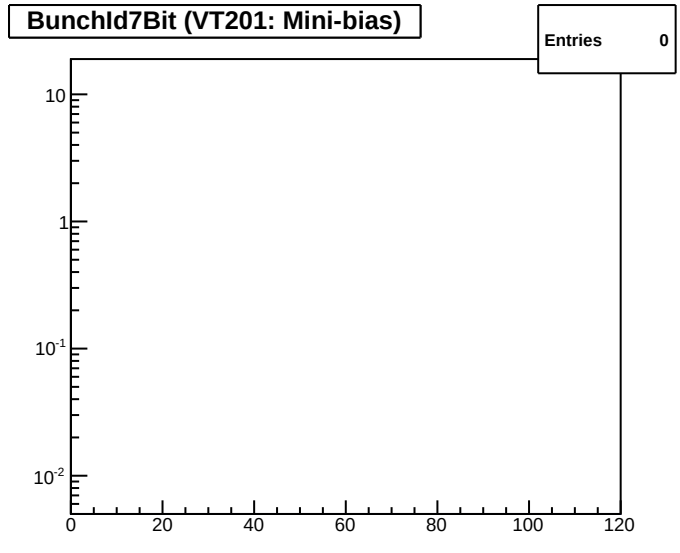


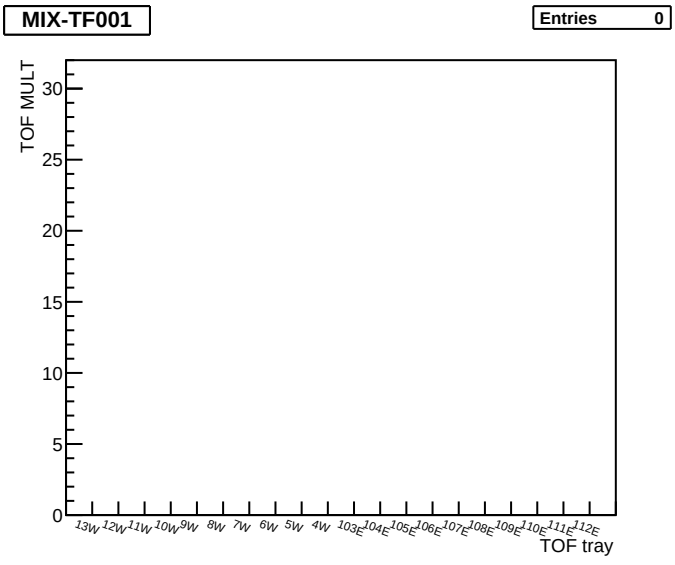

BunchId7Bit (all events)
Entries 0
10
1
10-1
10-2
0
20
40
60
80
100
120

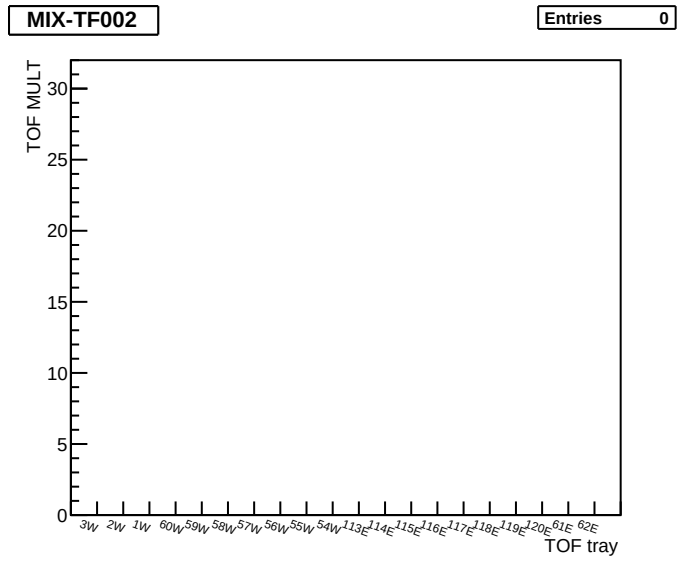

BunchId7Bit (EM201: BHT3)
Entries 0
10
1
10-1
10-2
0
20
40
60
80
100
120

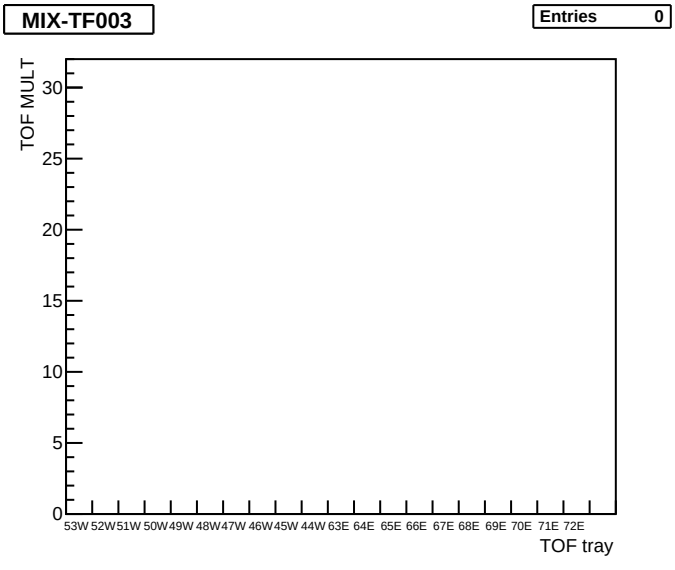

BunchId7Bit (ST201: Zero-bias)
Entries 0
10
1
10-1
10-2
0
20
40
60
80
100
120

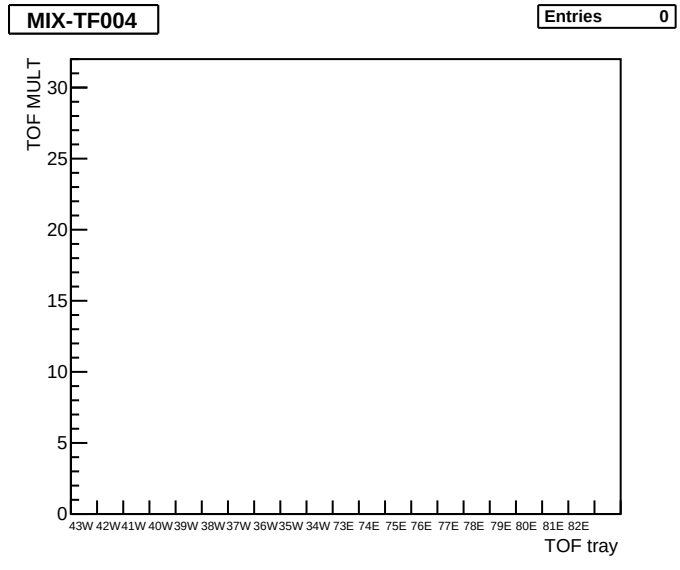

BunchId7Bit (TF201: NOT TOFmult2)
Entries 0
10
1
10-1
10-2
0
20
40
60
80
100
120

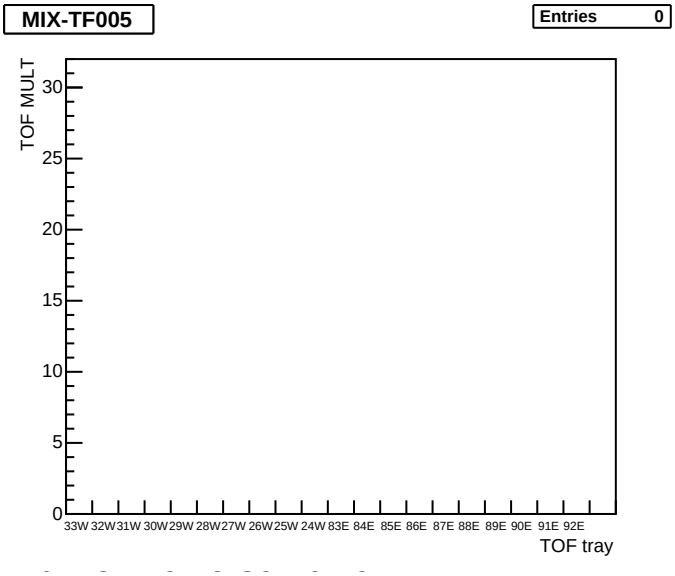

BunchId7Bit (VT201: Mini-bias)
Entries 0
10
1
10-1
10-2
0
20
40
60
80
100
120

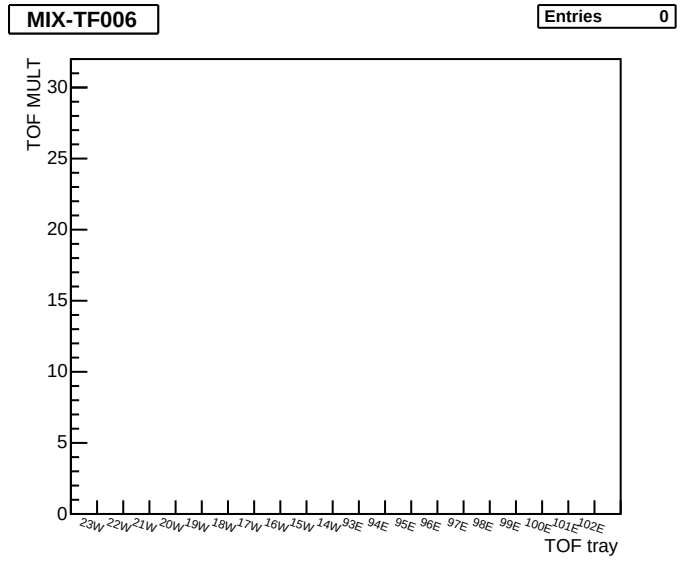

MIX-TF001
Entries 0
TOF MULT
0 5 10 15 20 25 30
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E
TOF tray

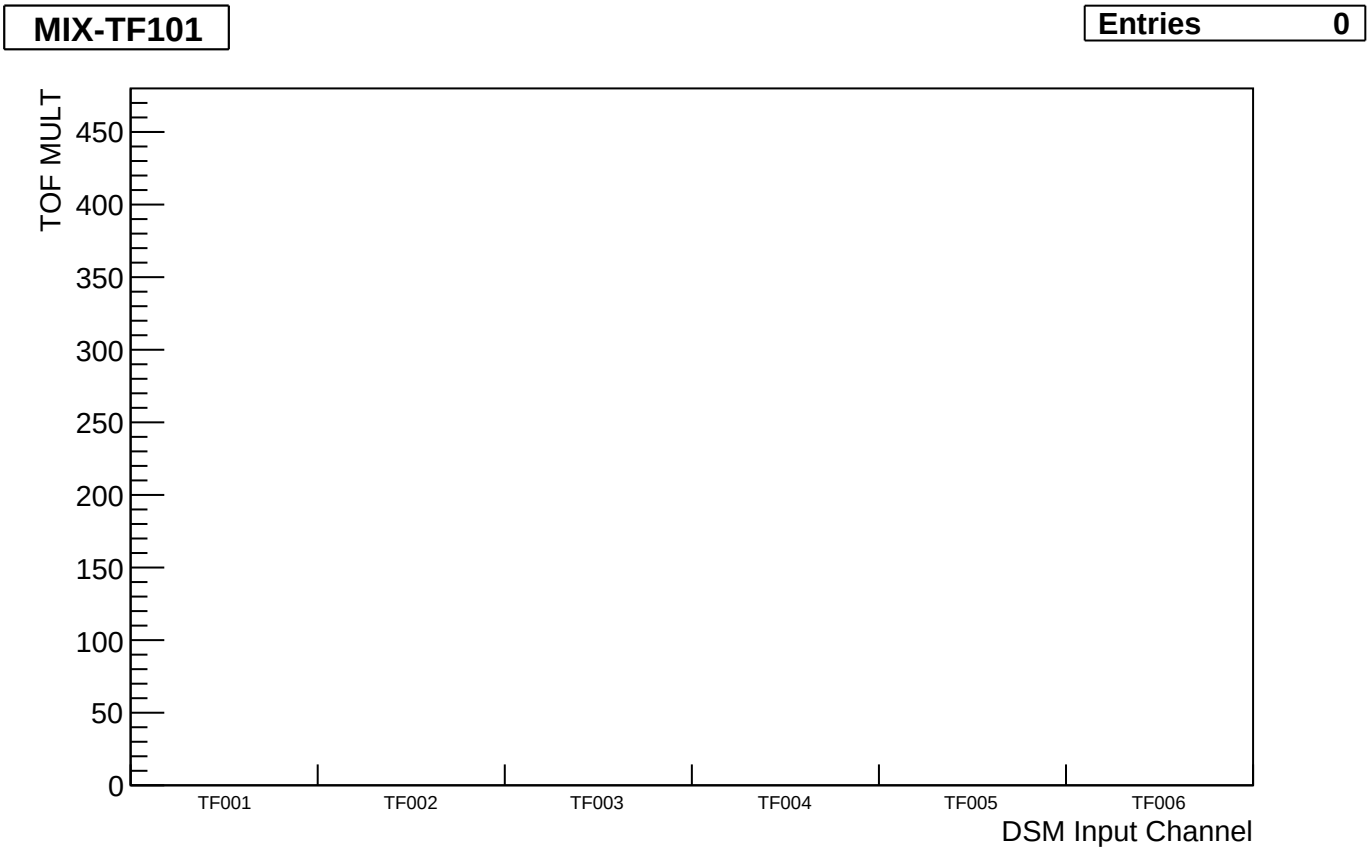

MIX-TF002
Entries 0
TOF MULT
0 5 10 15 20 25 30
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E
TOF tray

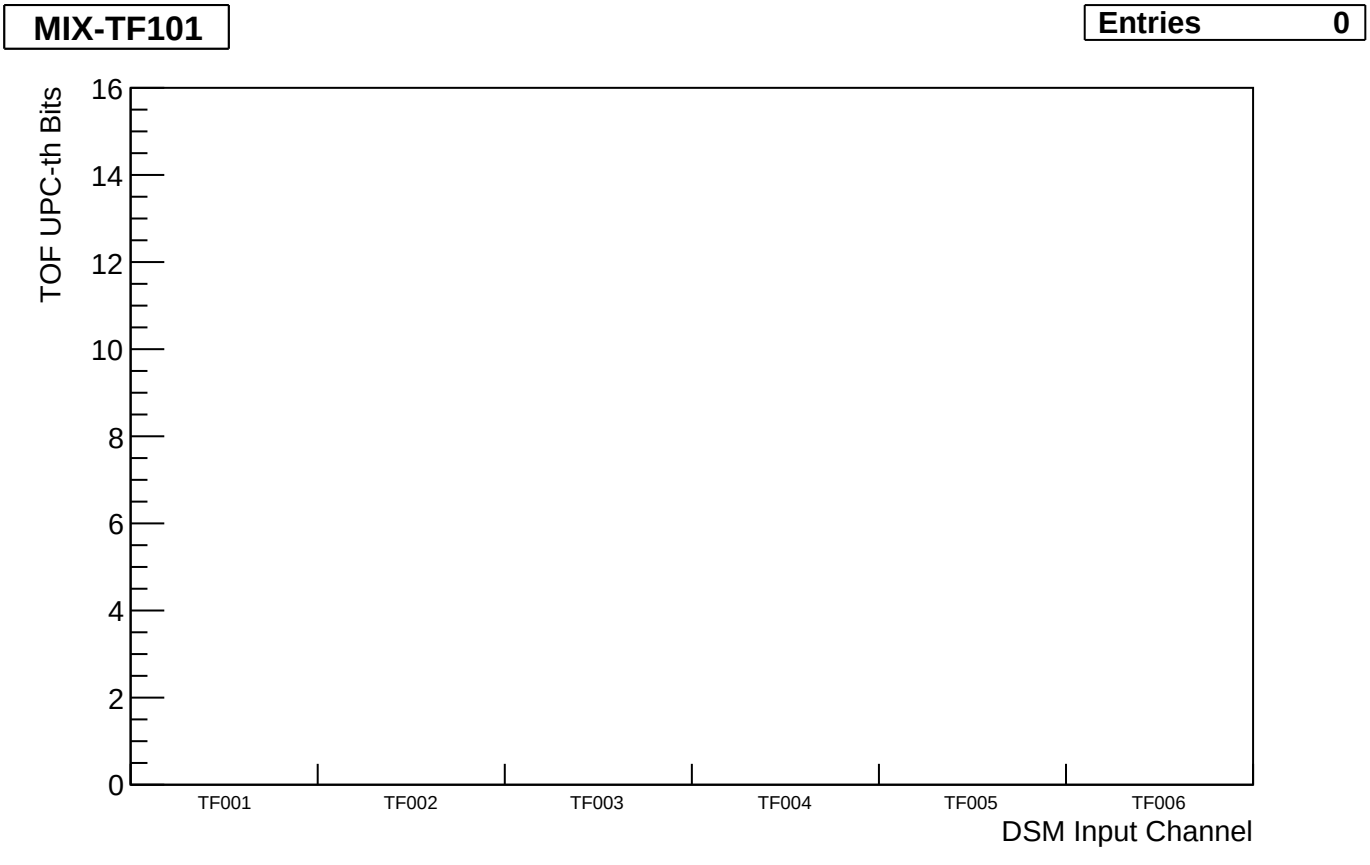

MIX-TF003
Entries 0
TOF MULT
0 5 10 15 20 25 30
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E
TOF tray

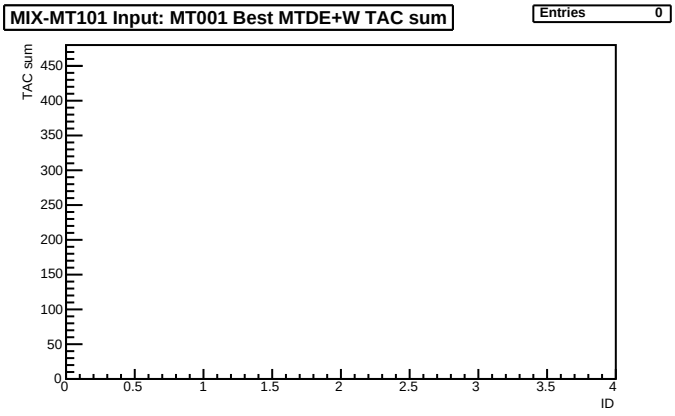

MIX-TF004
Entries 0
TOF MULT
0 5 10 15 20 25 30
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E
TOF tray

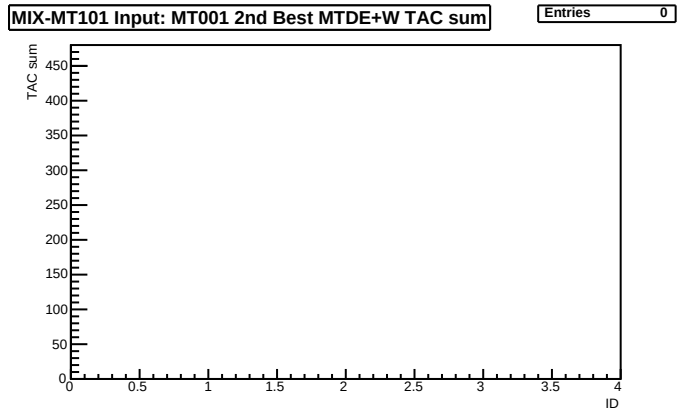

MIX-TF005
Entries 0
TOF MULT
0 5 10 15 20 25 30
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E
TOF tray

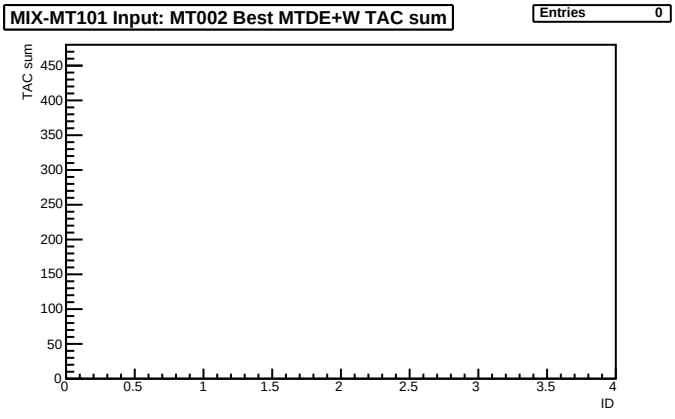

MIX-TF006
Entries 0
TOF MULT
0 5 10 15 20 25 30
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E
TOF tray

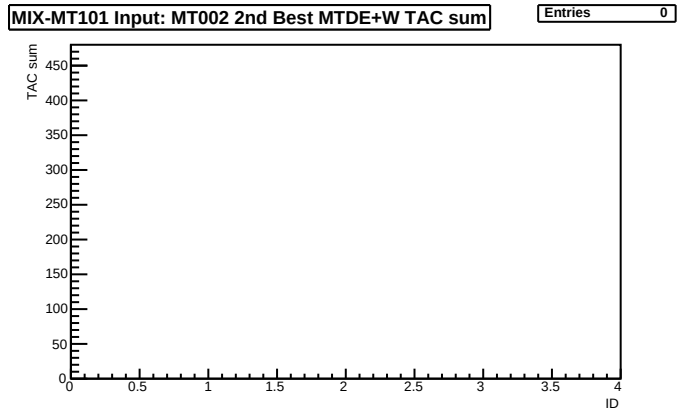

MIX-TF101
Entries 0
TOF MULT
450
400
350
300
250
200
150
100
50
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel

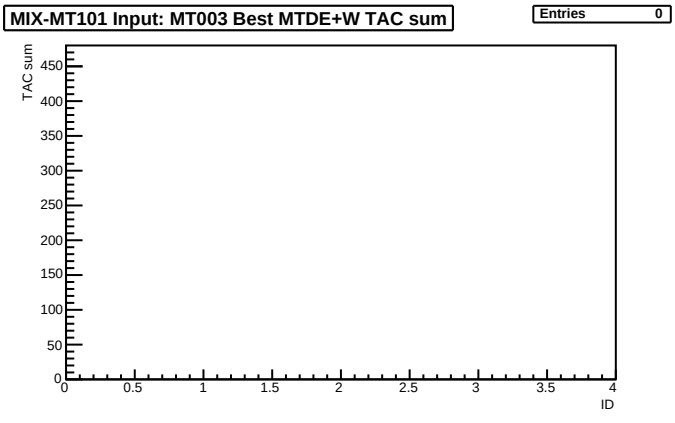

MIX-TF101
Entries 0
TOF UPC-th Bits
16
14
12
10
8
6
4
2
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel

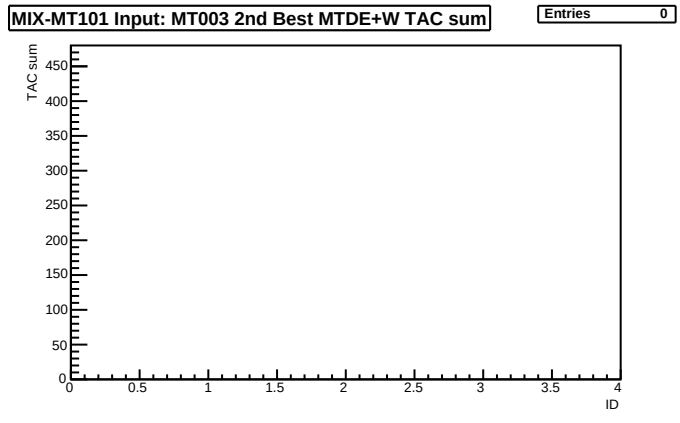

MIX-MT101 Input: MT001 Best MTDE+W TAC sum
Entries 0
TAC sum
ID

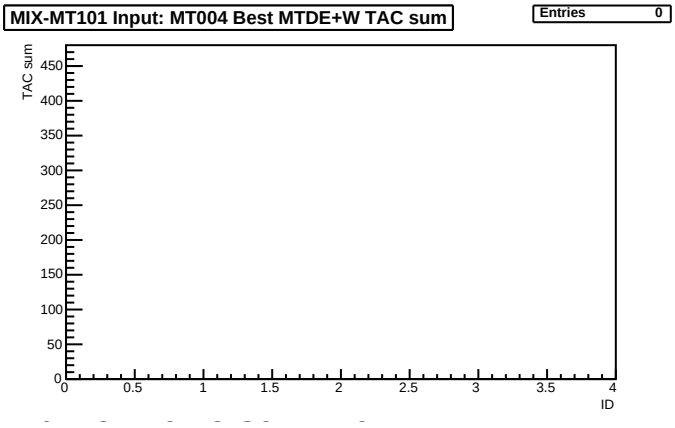

MIX-MT101 Input: MT001 2nd Best MTDE+W TAC sum
Entries 0
TAC sum
ID

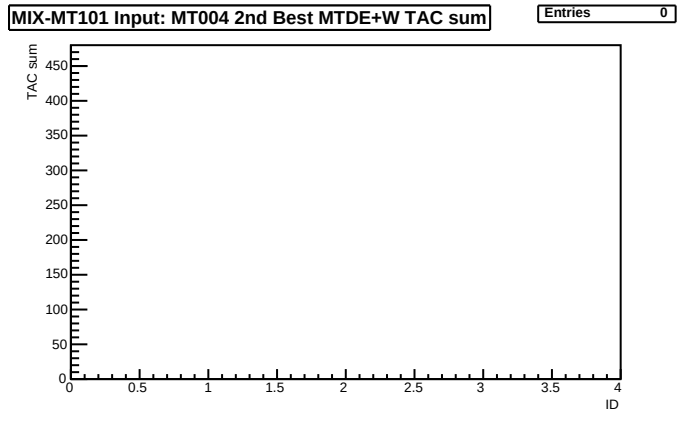

MIX-MT101 Input: MT002 Best MTDE+W TAC sum
Entries 0
TAC sum
ID

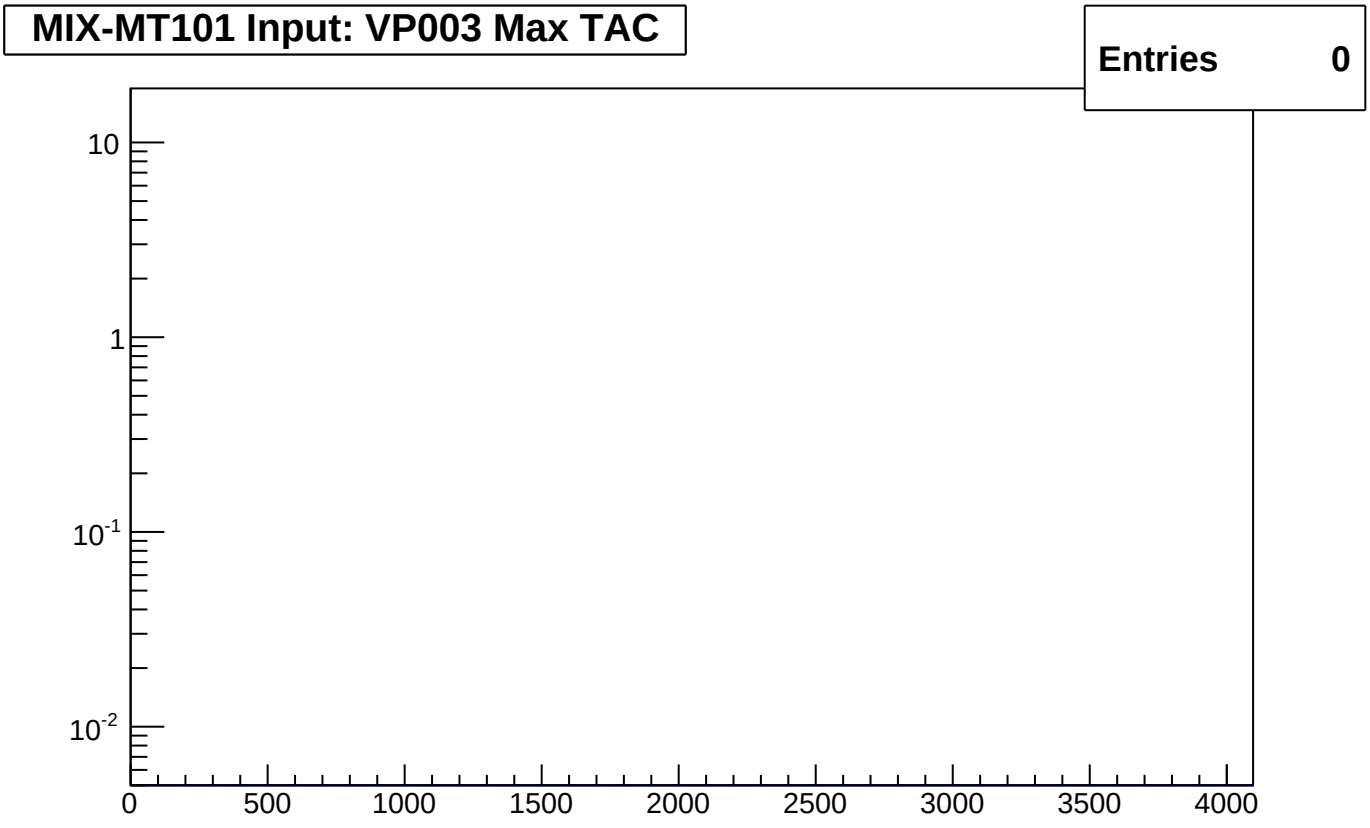

MIX-MT101 Input: MT002 2nd Best MTDE+W TAC sum
Entries 0
TAC sum
ID

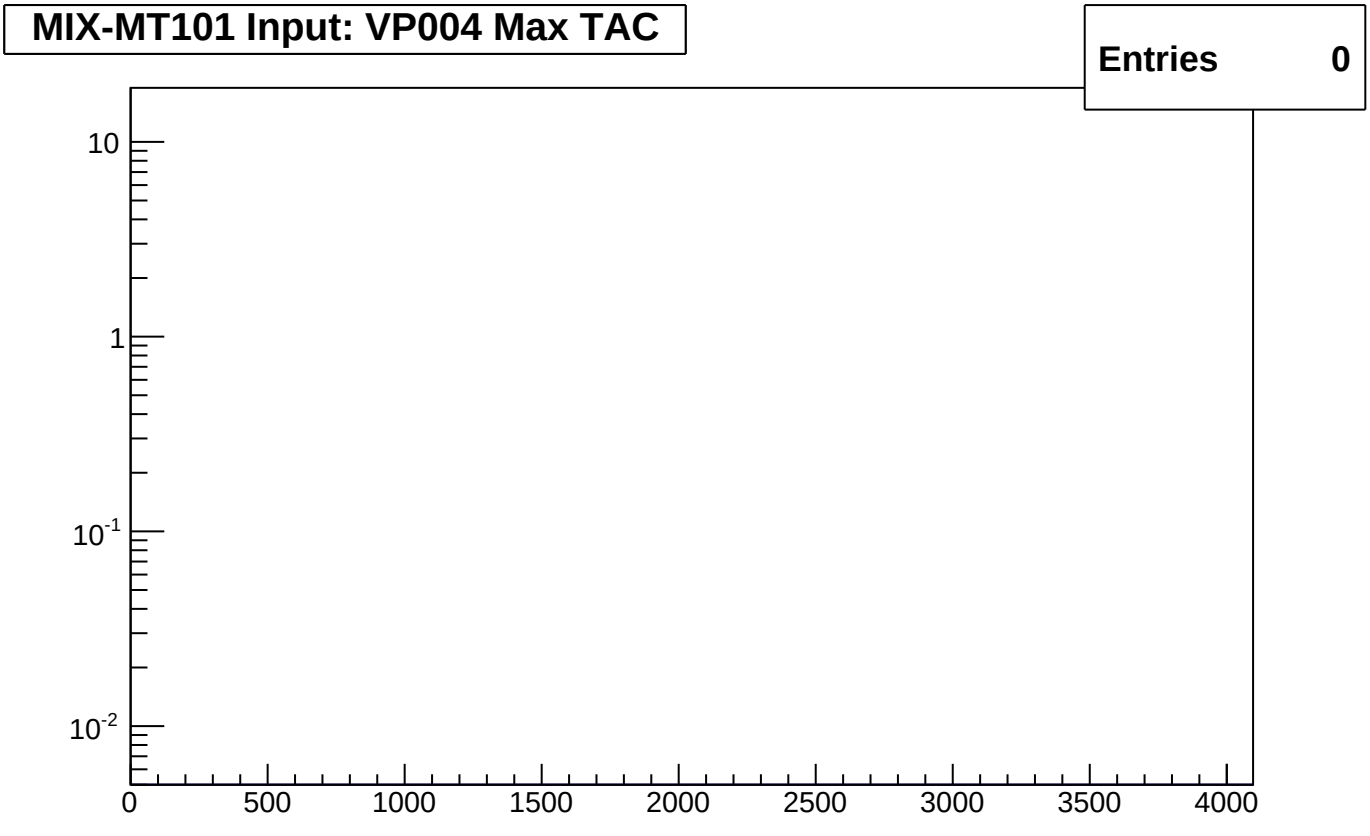

MIX-MT101 Input: MT003 Best MTDE+W TAC sum
Entries 0
TAC sum
ID

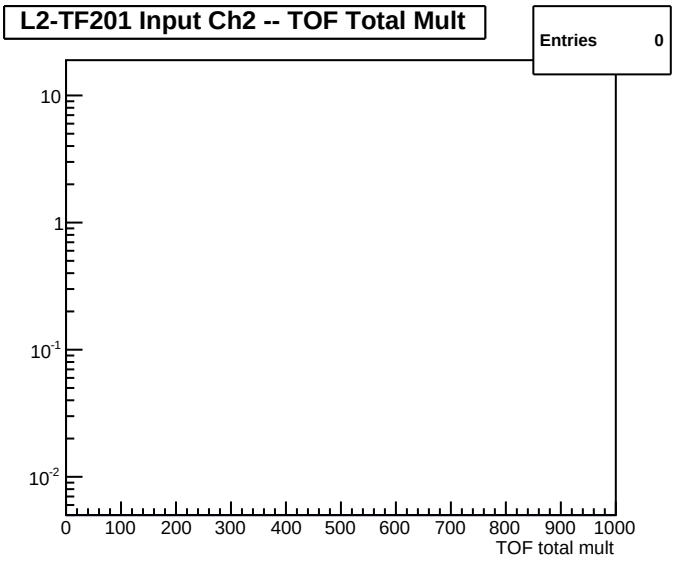

MIX-MT101 Input: MT003 2nd Best MTDE+W TAC sum
Entries 0
TAC sum
ID

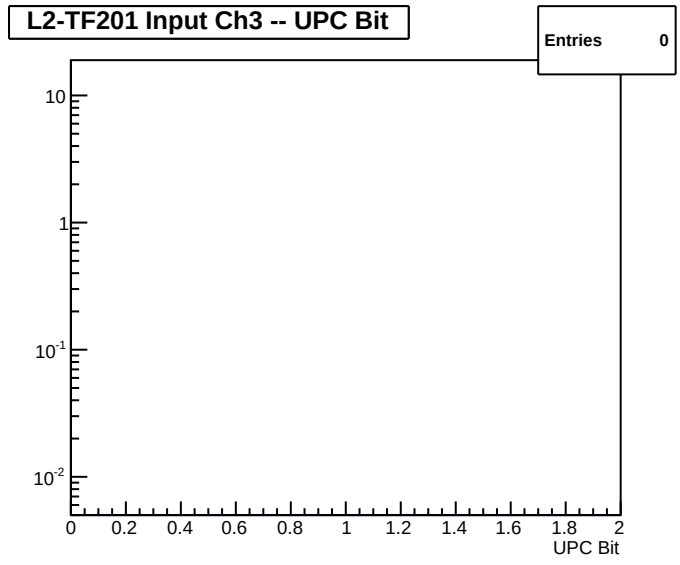

MIX-MT101 Input: MT004 Best MTDE+W TAC sum
Entries 0
TAC sum
ID

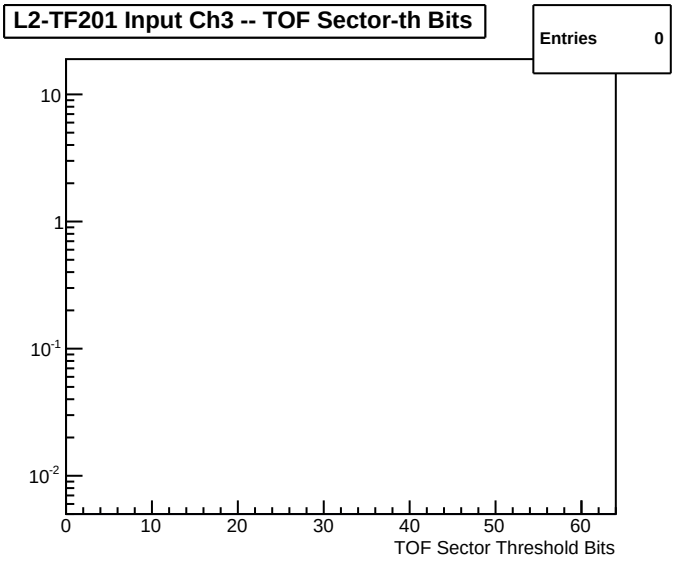

MIX-MT101 Input: MT004 2nd Best MTDE+W TAC sum
Entries 0
TAC sum
ID

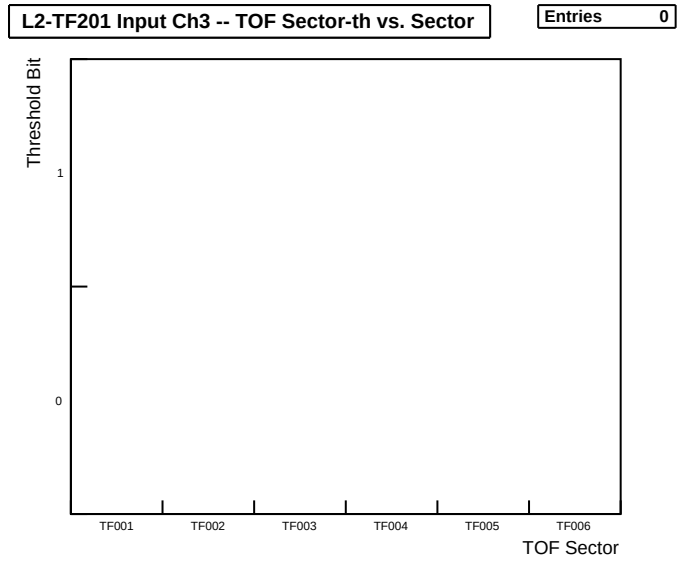

MIX-MT101 Input: VP003 Max TAC
Entries 0
10
1
10-1
10-2
0
500
1000
1500
2000
2500
3000
3500
4000

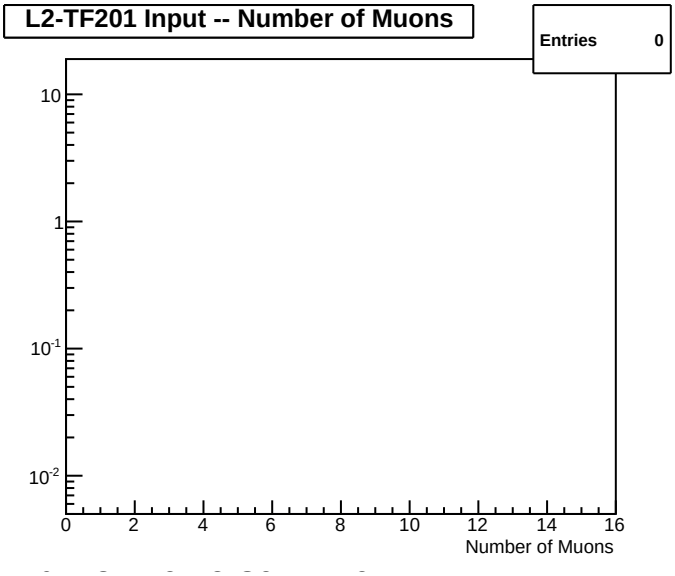

MIX-MT101 Input: VP004 Max TAC
Entries 0
10
1
10-1
10-2
0
500
1000
1500
2000
2500
3000
3500
4000

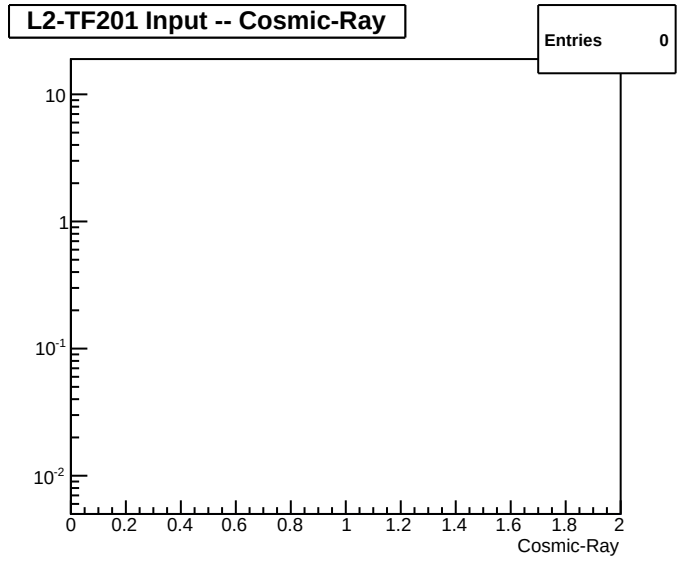

L2-TF201 Input Ch2 -- TOF Total Mult
Entries 0
10
1
10^{-1}
10^{-2}
0 100 200 300 400 500 600 700 800 900 1000
TOF total mult

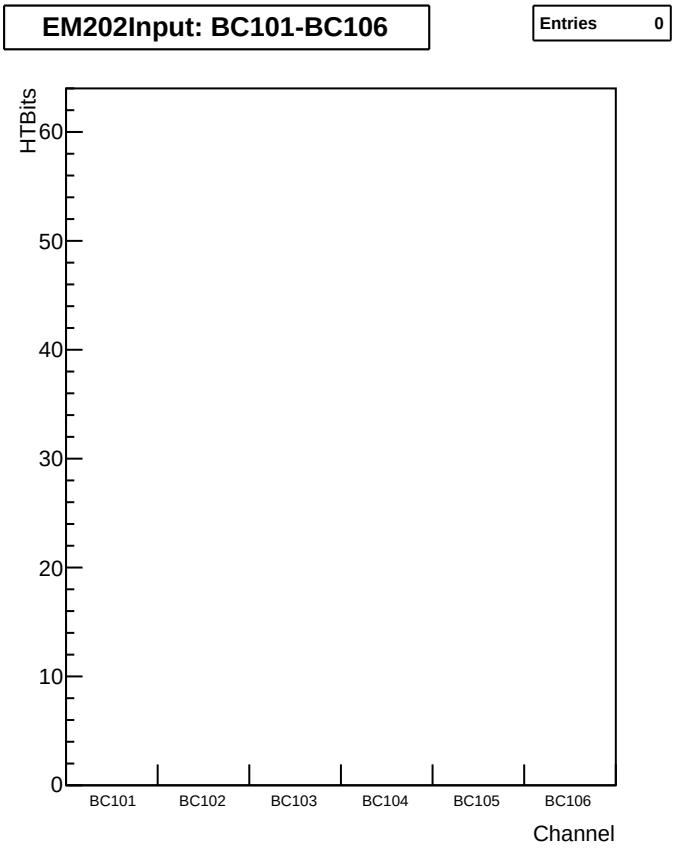

L2-TF201 Input Ch3 -- UPC Bit
Entries 0
10
1
10^{-1}
10^{-2}
0 0.2 0.4 0.6 0.8 1 1.2 1.4 1.6 1.8 2
UPC Bit

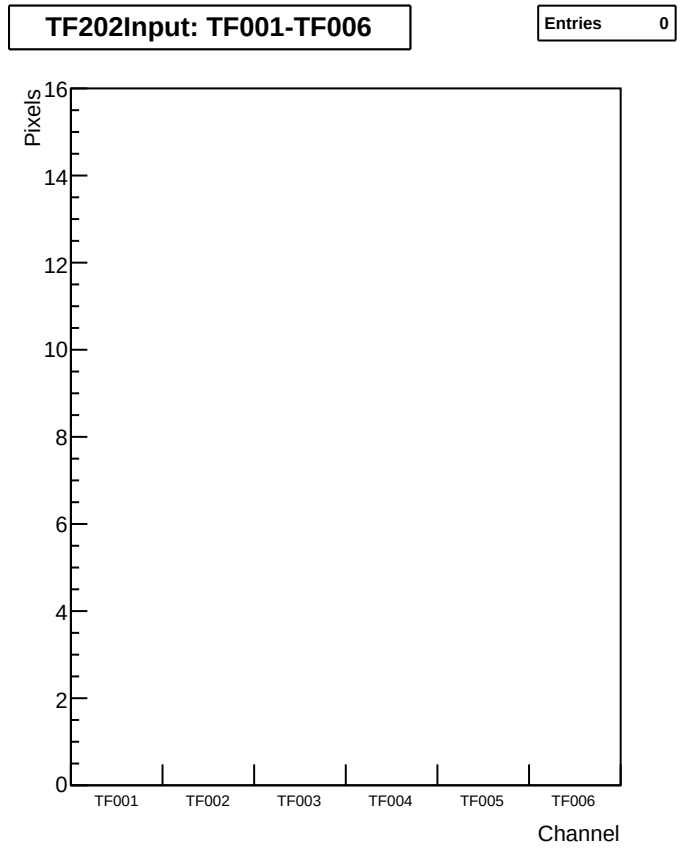

L2-TF201 Input Ch3 -- TOF Sector-th Bits
Entries 0
10
1
10^{-1}
10^{-2}
0 10 20 30 40 50 60
TOF Sector Threshold Bits

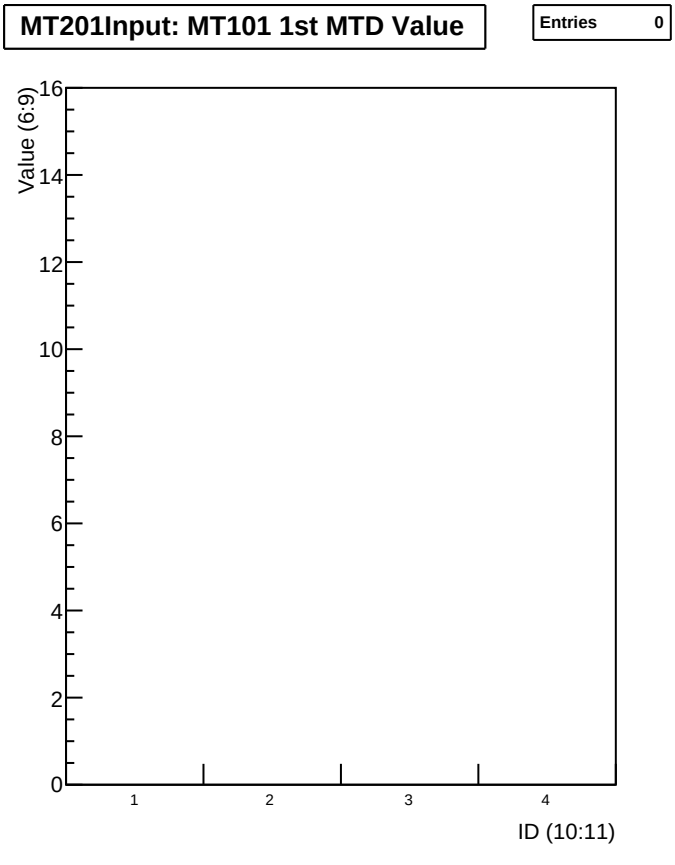

L2-TF201 Input Ch3 -- TOF Sector-th vs. Sector
Entries 0
Threshold Bit
1
0
TF001 TF002 TF003 TF004 TF005 TF006
TOF Sector

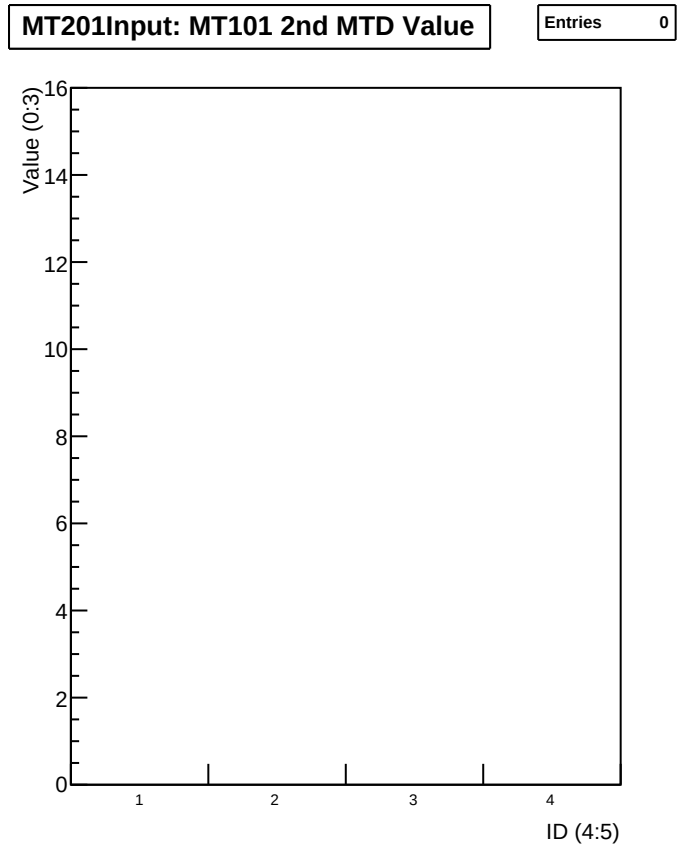

L2-TF201 Input -- Number of Muons
Entries 0
10
1
10^{-1}
10^{-2}
0 2 4 6 8 10 12 14 16
Number of Muons


L2-TF201 Input -- Cosmic-Ray
Entries 0
10
1
10^{-1}
10^{-2}
0 0.2 0.4 0.6 0.8 1 1.2 1.4 1.6 1.8 2
Cosmic-Ray


EM202Input: BC101-BC106
Entries 0
HTBits
0
10
20
30
40
50
60
BC101
BC102
BC103
BC104
BC105
BC106
Channel

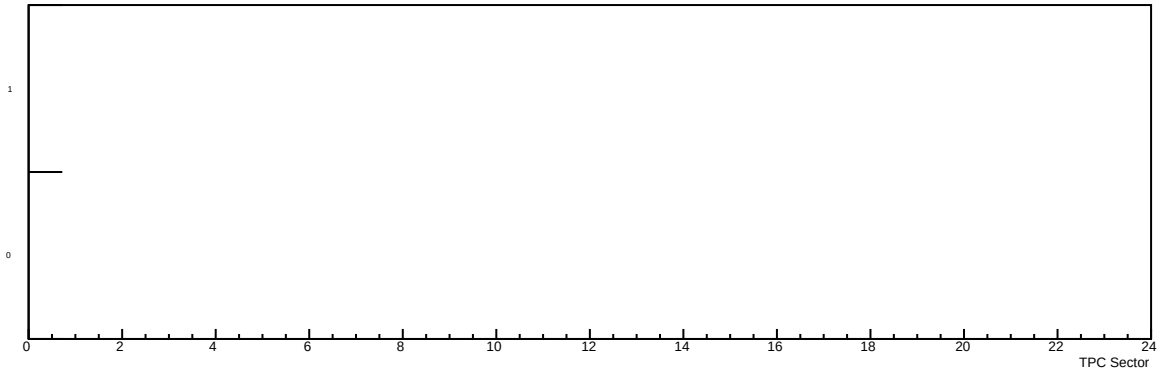

TF202Input: TF001-TF006
Entries 0
Pixels
0
2
4
6
8
10
12
14
16
TF001
TF002
TF003
TF004
TF005
TF006
Channel


MT201Input: MT101 1st MTD Value
Entries 0
Value (6:9)
0
2
4
6
8
10
12
14
16
1
2
3
4
ID (10:11)


MT201Input: MT101 2nd MTD Value
Entries 0
Value (0:3)
0
2
4
6
8
10
12
14
16
1
2
3
4
ID (4:5)

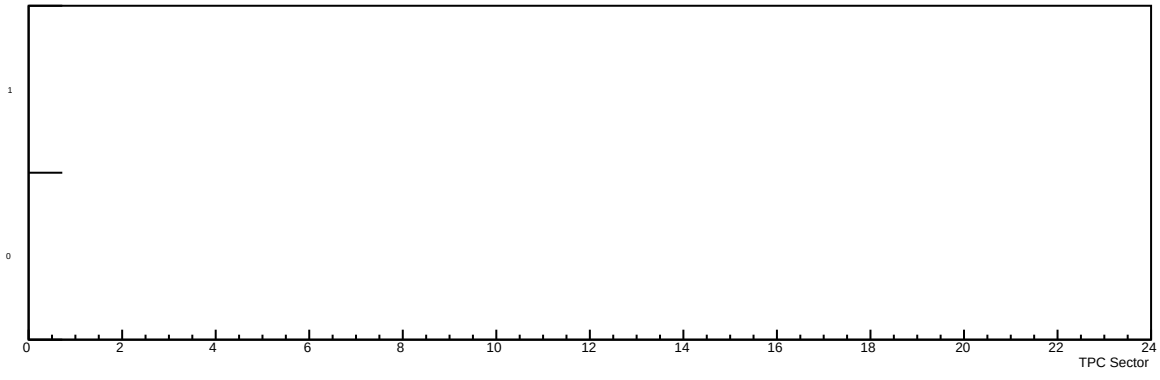
DQ301Input: EM202

Entries 0



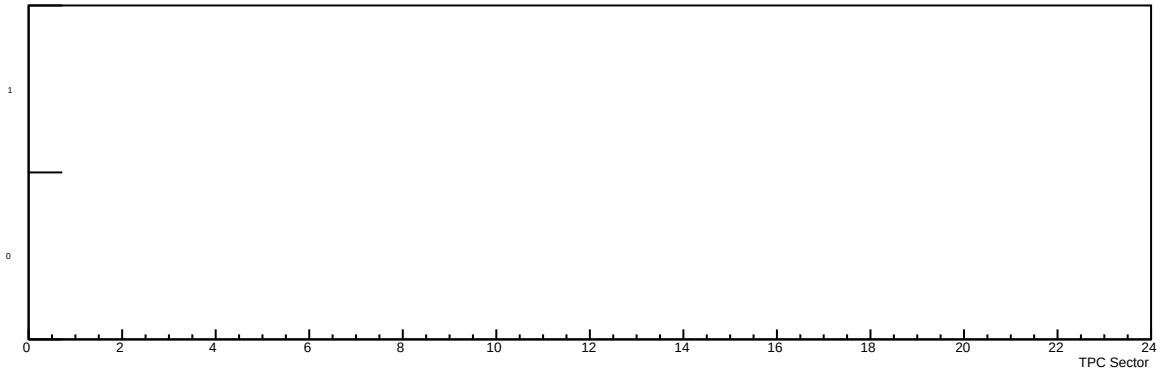
DQ301Input: TF202

Entries 0



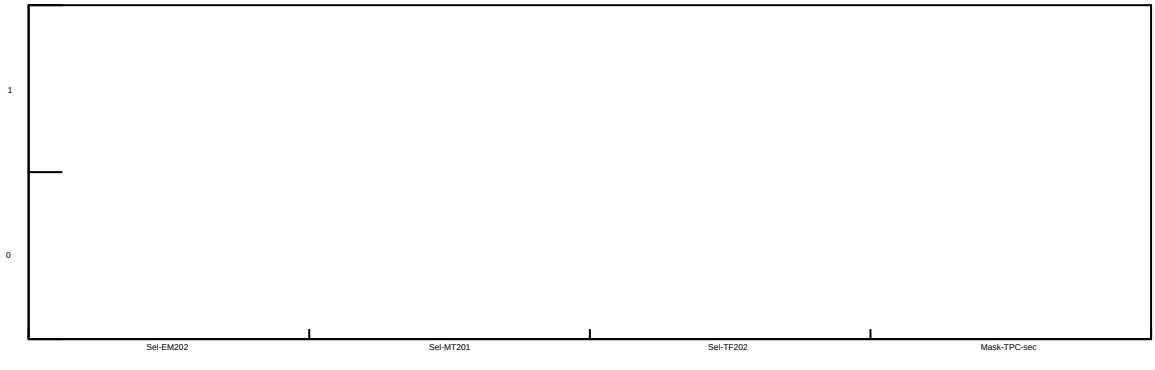
DQ301Input: MT201

Entries 0

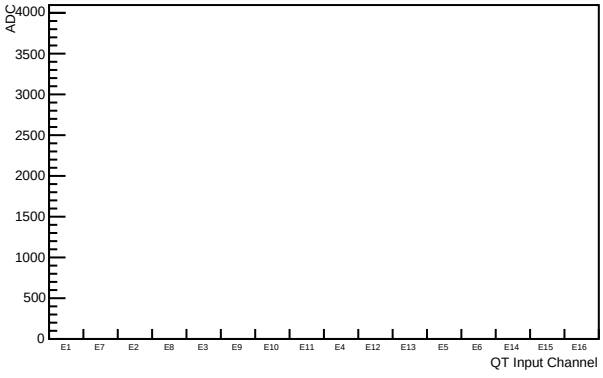


DQ301Input: TCU

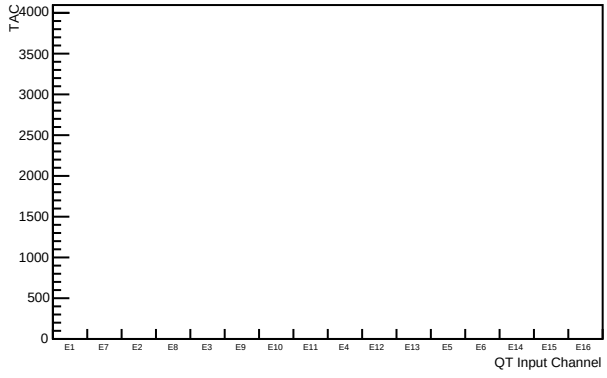
Entries 0



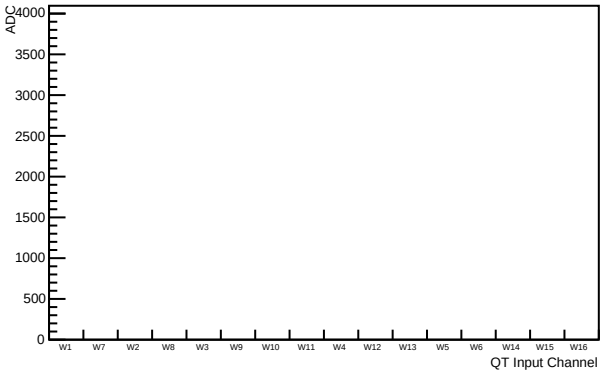
BBQ-BB001 (BBC east small tiles ADC) Entries 0



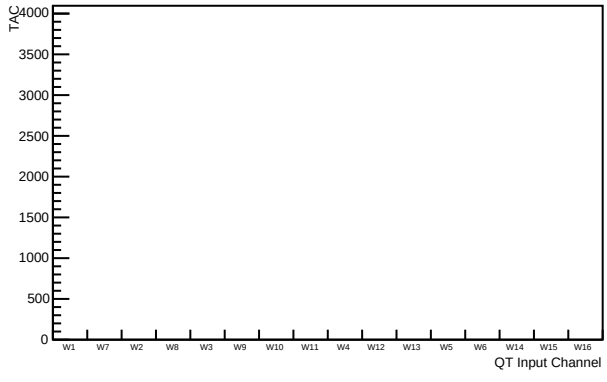
BBQ-BB001 (BBC east small tiles TAC) Entries 0



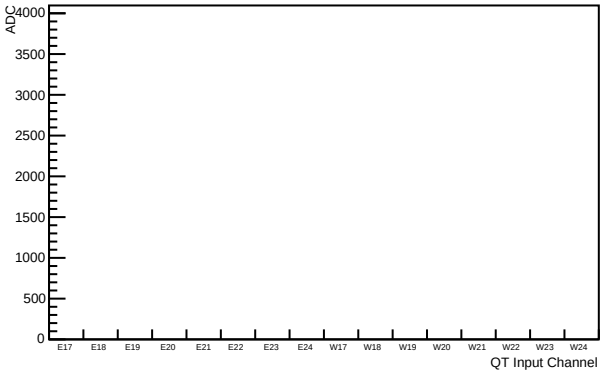
BBQ-BB002 (BBC west small tiles ADC) Entries 0



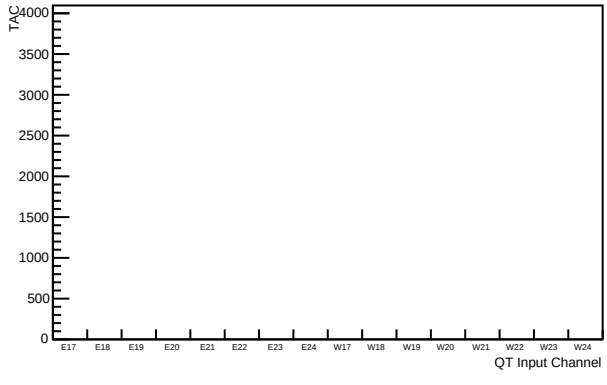
BBQ-BB002 (BBC west small tiles TAC) Entries 0



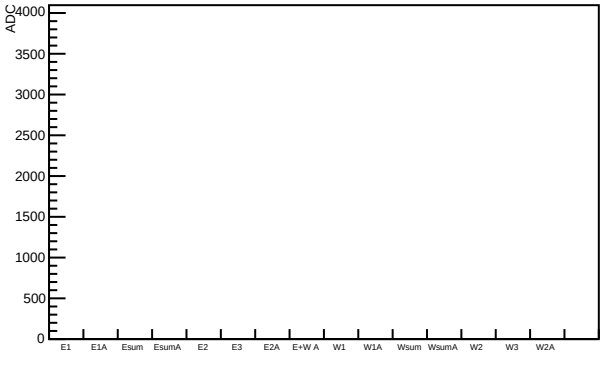
BBQ-BB003 (BBC E+W large tiles ADC) Entries 0



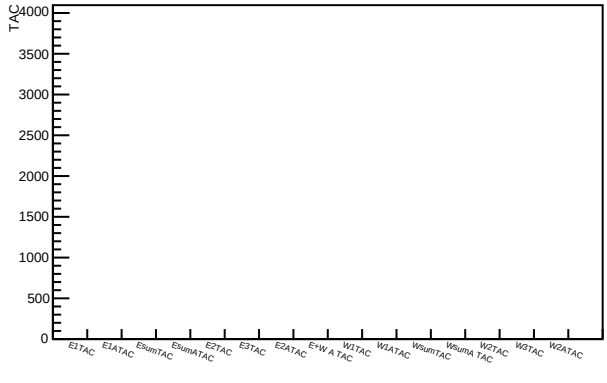
BBQ-BB003 (BBC E+W large tiles TAC) Entries 0

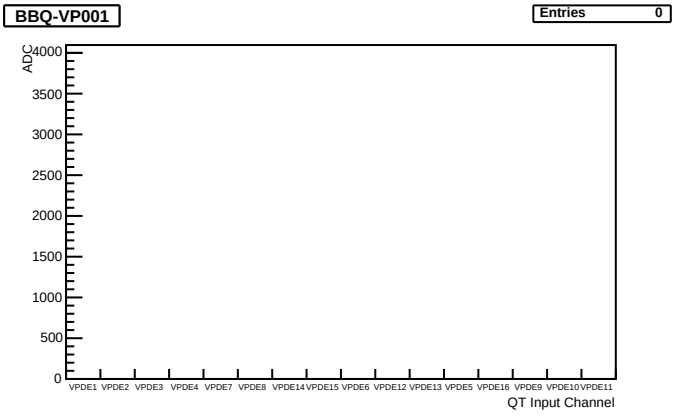


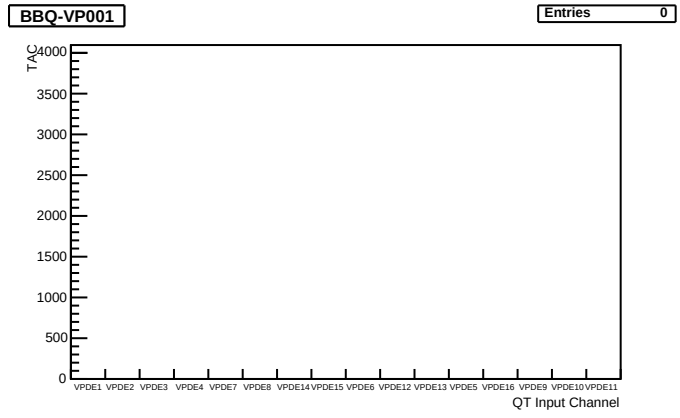
BBQ-ZD001 (ZDC TOWER) Entries 0

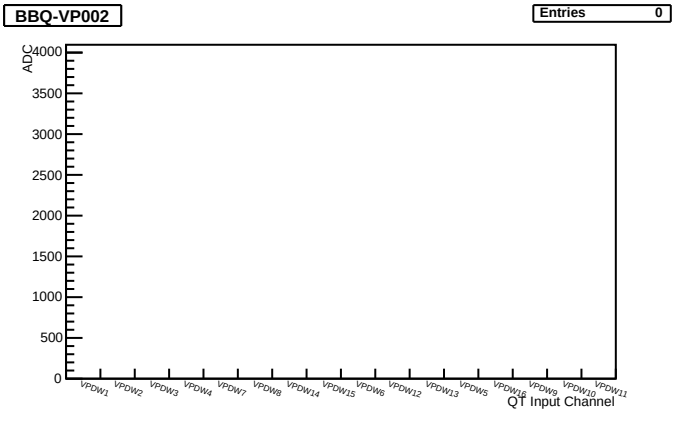


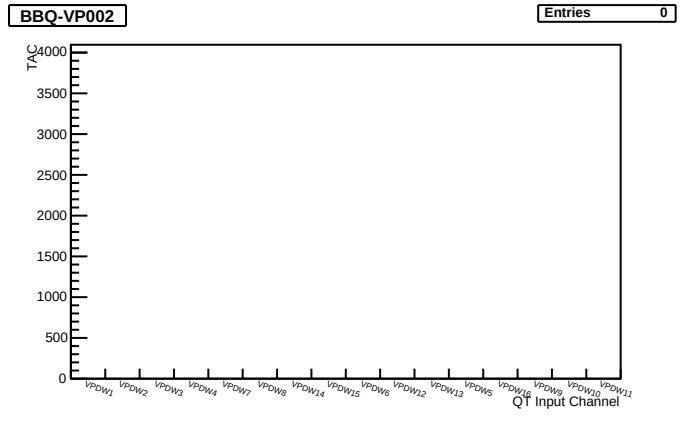
BBQ-ZD001 (ZDC TOWER) Entries 0

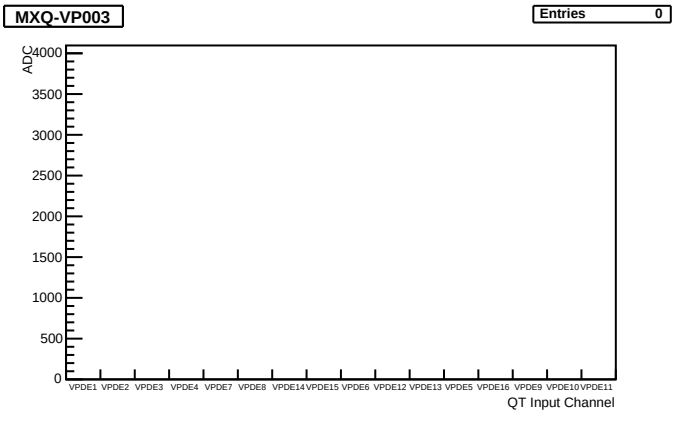


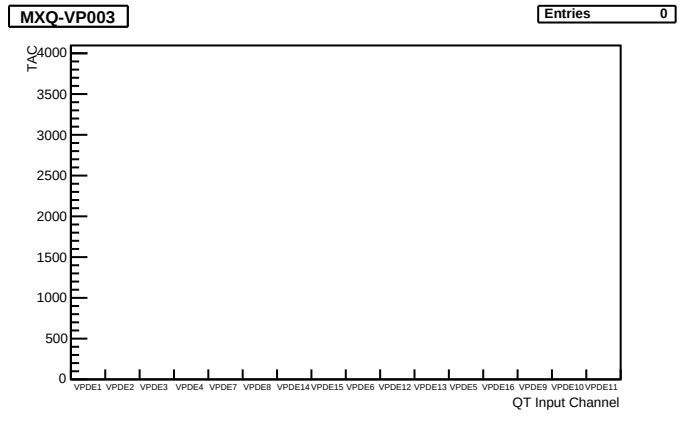

BBQ-VP001
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
VPDE1 VPDE2 VPDE3 VPDE4 VPDE7 VPDE8 VPDE14 VPDE15 VPDE6 VPDE12 VPDE13 VPDE5 VPDE16 VPDE9 VPDE10 VPDE11
QT Input Channel

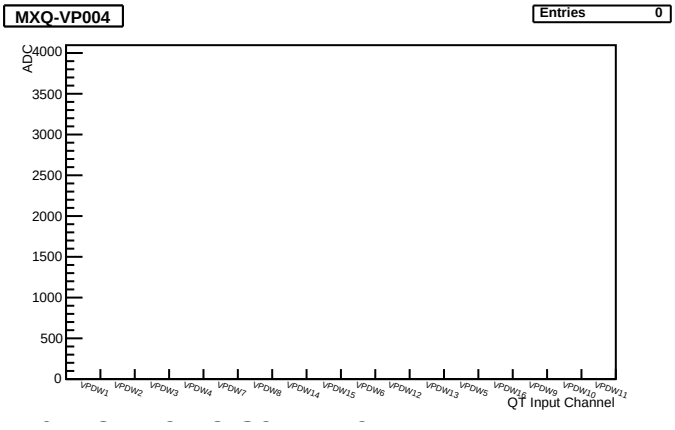

BBQ-VP001
Entries 0
TAC
4000
3500
3000
2500
2000
1500
1000
500
0
VPDE1 VPDE2 VPDE3 VPDE4 VPDE7 VPDE8 VPDE14 VPDE15 VPDE6 VPDE12 VPDE13 VPDE5 VPDE16 VPDE9 VPDE10 VPDE11
QT Input Channel

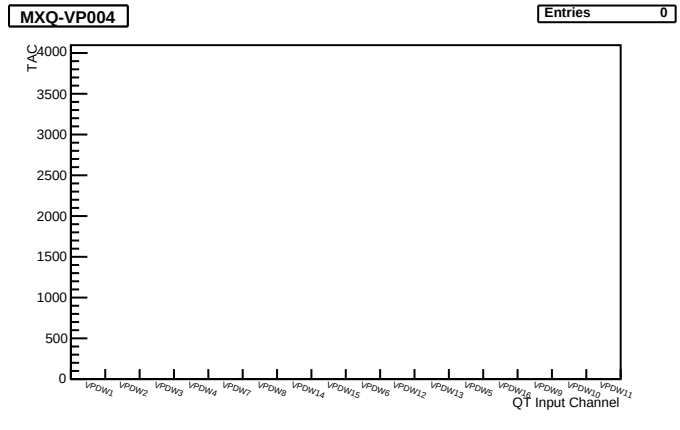

BBQ-VP002
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
VPDW1 VPDW2 VPDW3 VPDW4 VPDW7 VPDW8 VPDW14 VPDW15 VPDW6 VPDW12 VPDW13 VPDW5 VPDW16 VPDW9 VPDW10 VPDW11
QT Input Channel

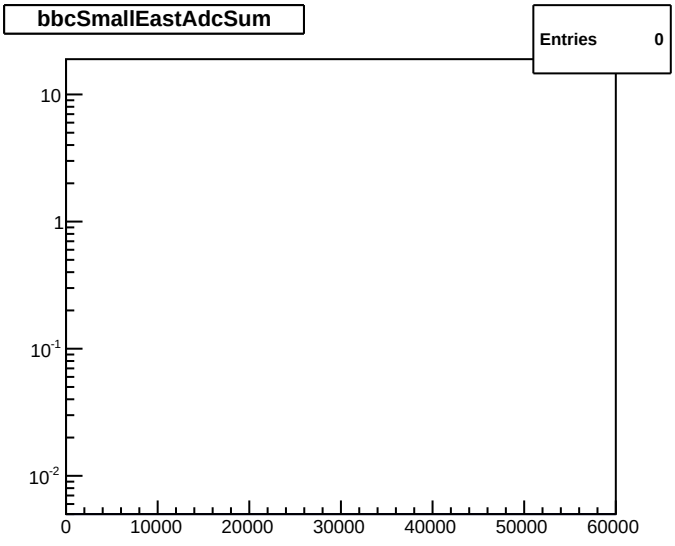

BBQ-VP002
Entries 0
TAC
4000
3500
3000
2500
2000
1500
1000
500
0
VPDW1 VPDW2 VPDW3 VPDW4 VPDW7 VPDW8 VPDW14 VPDW15 VPDW6 VPDW12 VPDW13 VPDW5 VPDW16 VPDW9 VPDW10 VPDW11
QT Input Channel

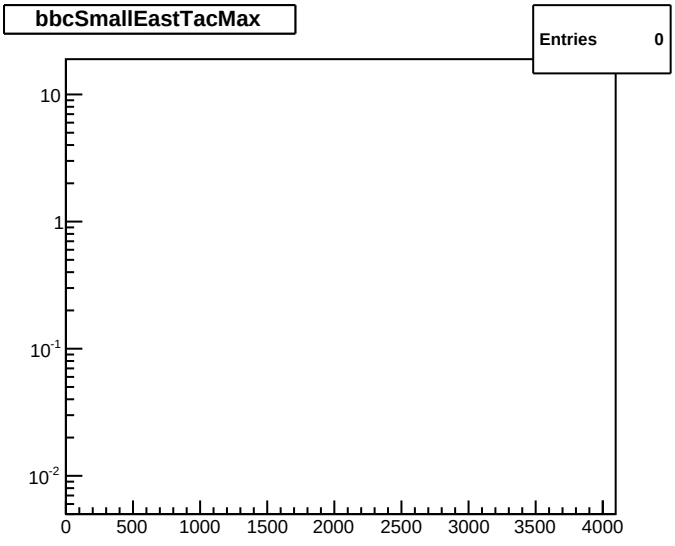

MXQ-VP003
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
VPDE1 VPDE2 VPDE3 VPDE4 VPDE7 VPDE8 VPDE14 VPDE15 VPDE6 VPDE12 VPDE13 VPDE5 VPDE16 VPDE9 VPDE10 VPDE11
QT Input Channel

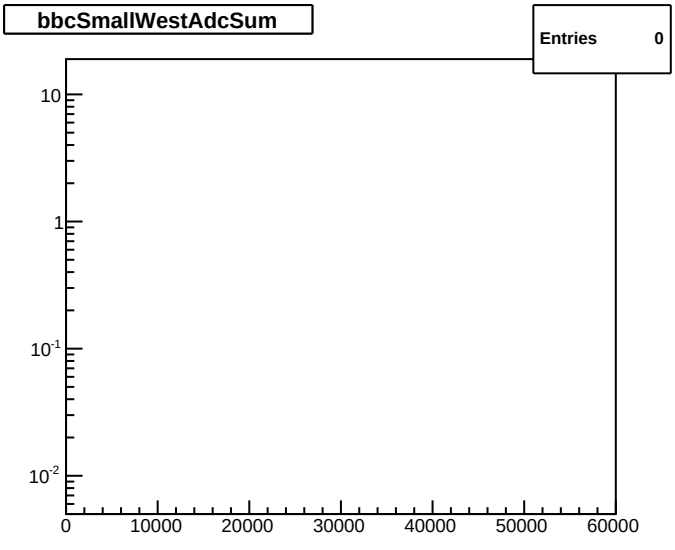

MXQ-VP003
Entries 0
TAC
4000
3500
3000
2500
2000
1500
1000
500
0
VPDE1 VPDE2 VPDE3 VPDE4 VPDE7 VPDE8 VPDE14 VPDE15 VPDE6 VPDE12 VPDE13 VPDE5 VPDE16 VPDE9 VPDE10 VPDE11
QT Input Channel

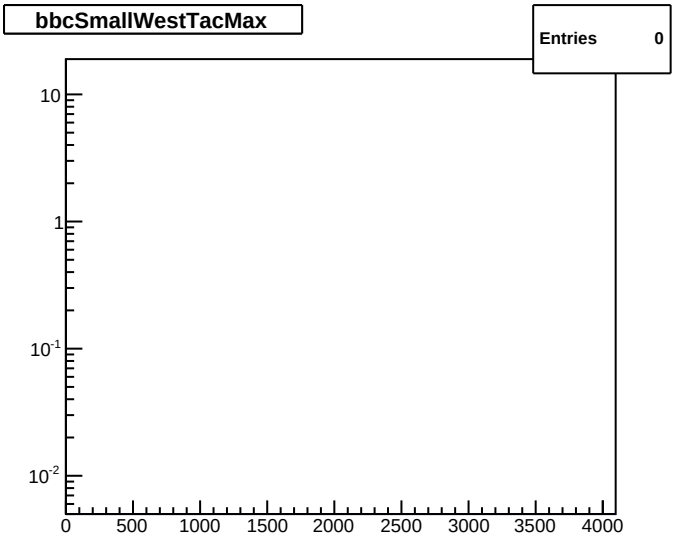

MXQ-VP004
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
VPDW1 VPDW2 VPDW3 VPDW4 VPDW7 VPDW8 VPDW14 VPDW15 VPDW6 VPDW12 VPDW13 VPDW5 VPDW16 VPDW9 VPDW10 VPDW11
QT Input Channel

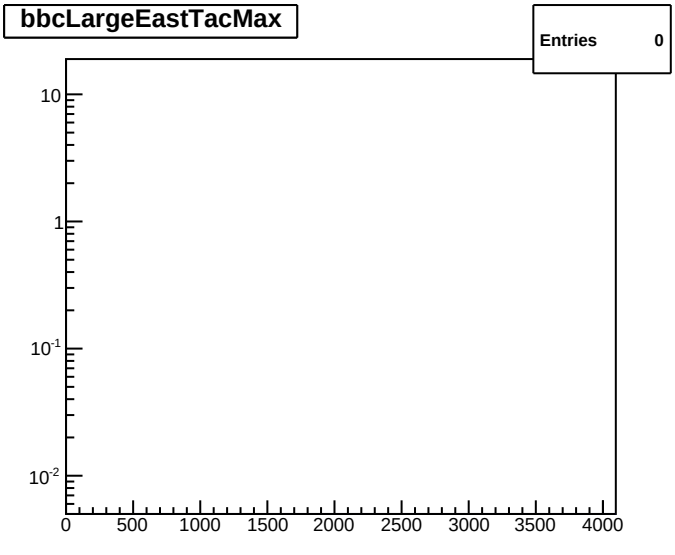

MXQ-VP004
Entries 0
TAC
4000
3500
3000
2500
2000
1500
1000
500
0
VPDW1 VPDW2 VPDW3 VPDW4 VPDW7 VPDW8 VPDW14 VPDW15 VPDW6 VPDW12 VPDW13 VPDW5 VPDW16 VPDW9 VPDW10 VPDW11
QT Input Channel

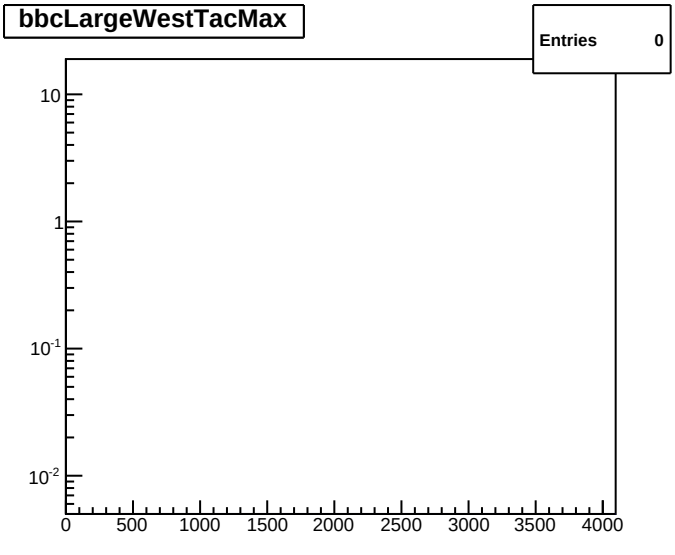

bbcSmallEastAdcSum
Entries 0
10
1
10-1
10-2
0
10000
20000
30000
40000
50000
60000

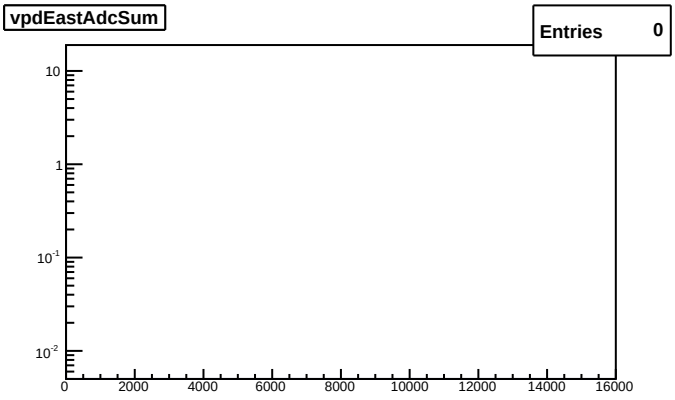

bbcSmallEastTacMax
Entries 0
10
1
10-1
10-2
0
500
1000
1500
2000
2500
3000
3500
4000

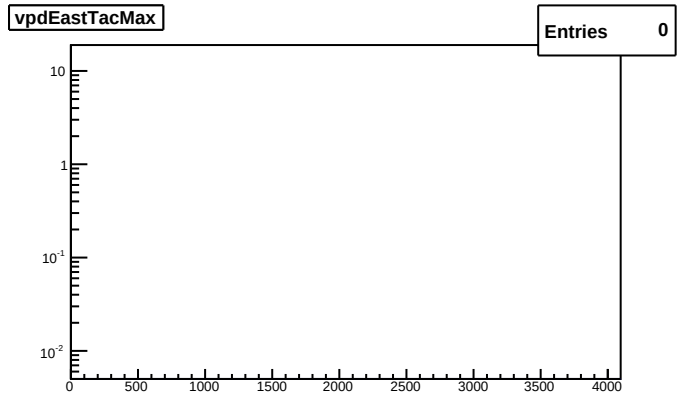

bbcSmallWestAdcSum
Entries 0
10
1
10-1
10-2
0
10000
20000
30000
40000
50000
60000

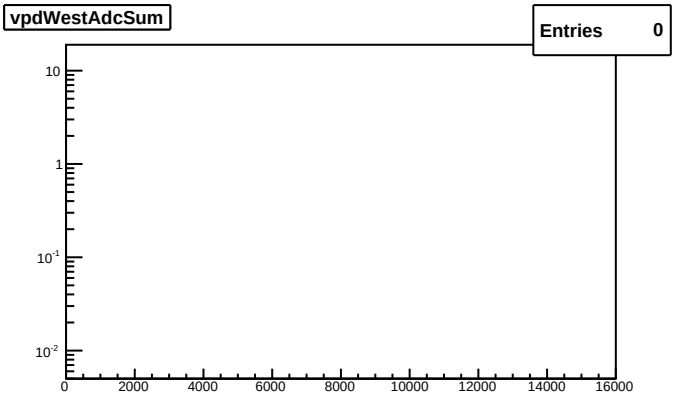

bbcSmallWestTacMax
Entries 0
10
1
10-1
10-2
0
500
1000
1500
2000
2500
3000
3500
4000

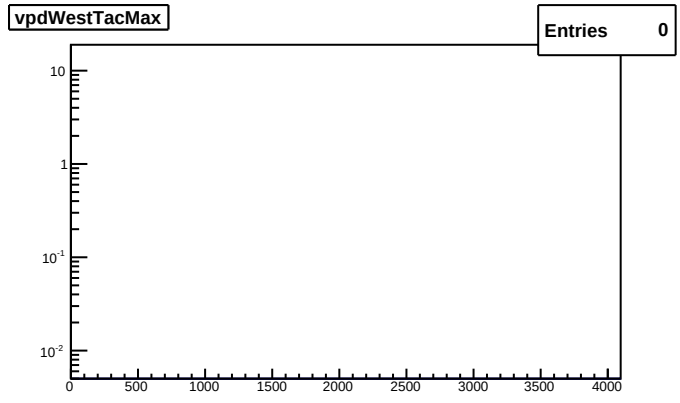

bbcLargeEastTacMax
Entries 0
10
1
10-1
10-2
0
500
1000
1500
2000
2500
3000
3500
4000

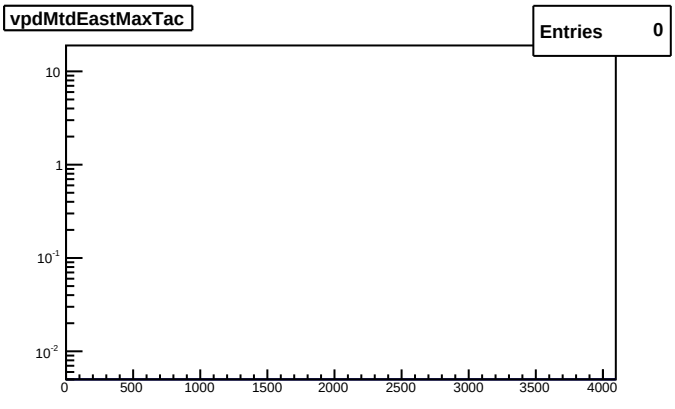

bbcLargeWestTacMax
Entries 0
10
1
10-1
10-2
0
500
1000
1500
2000
2500
3000
3500
4000

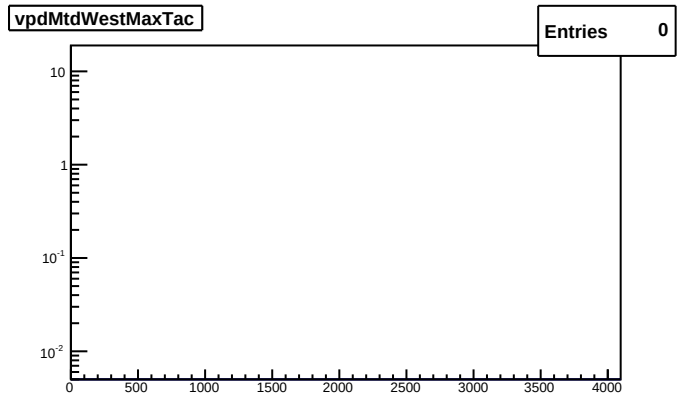

vpdEastAdcSum
Entries 0

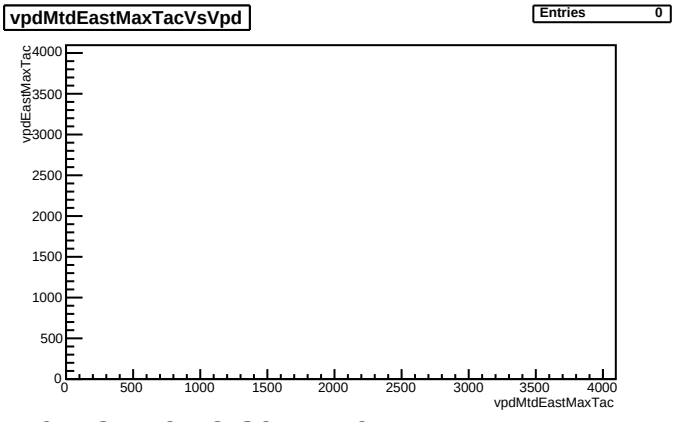

vpdEastTacMax
Entries 0

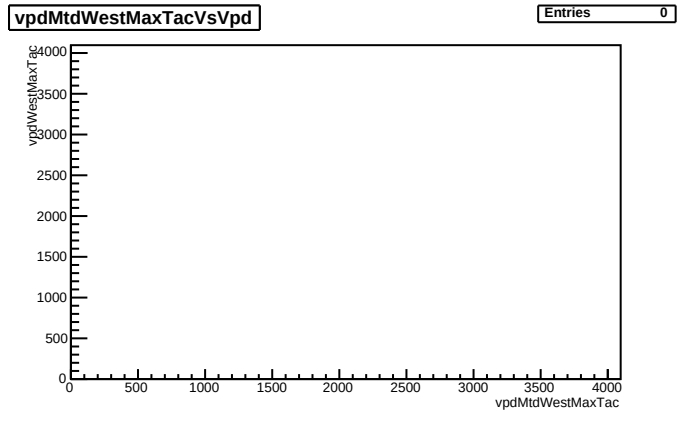

vpdWestAdcSum
Entries 0

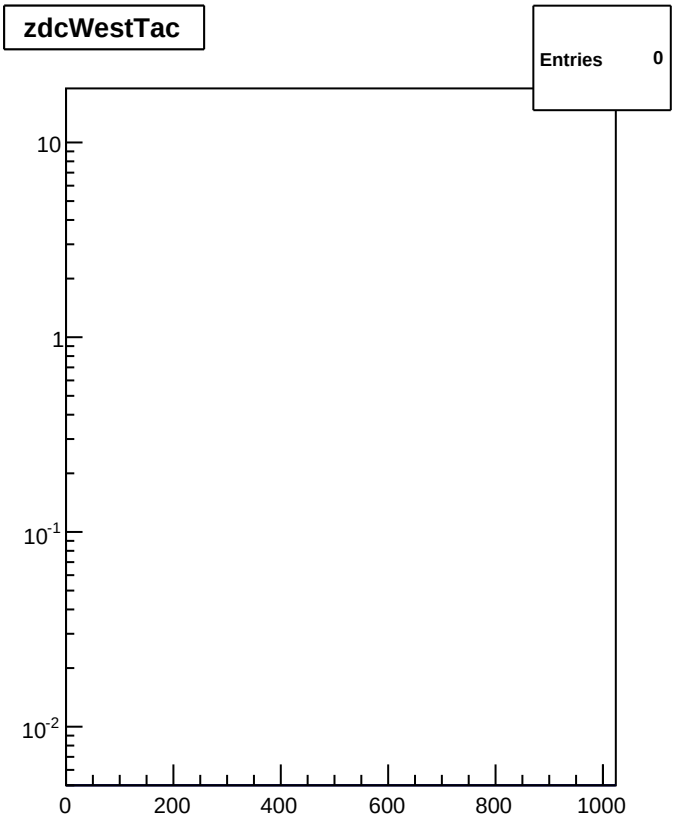

vpdWestTacMax
Entries 0

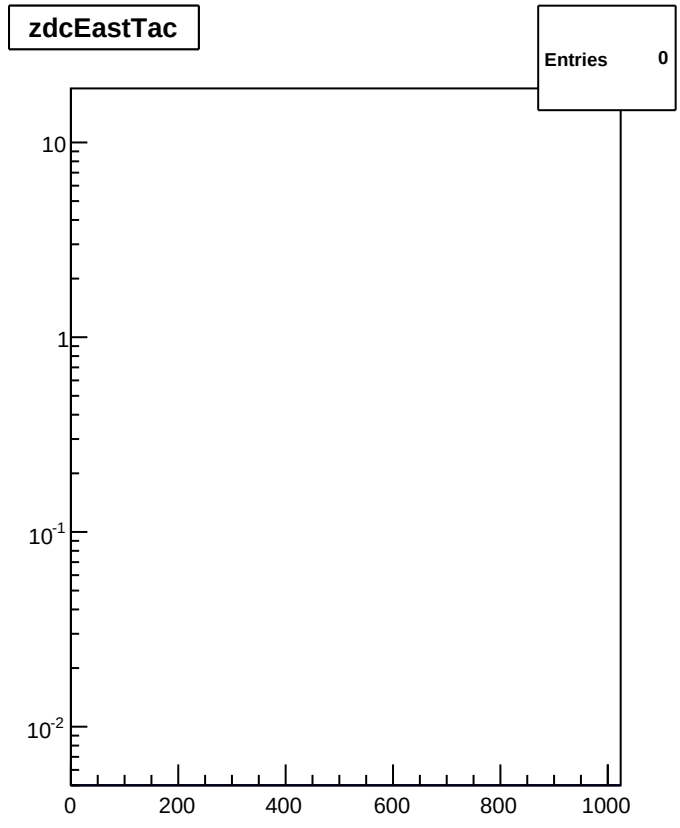

vpdMtdEastMaxTac
Entries 0

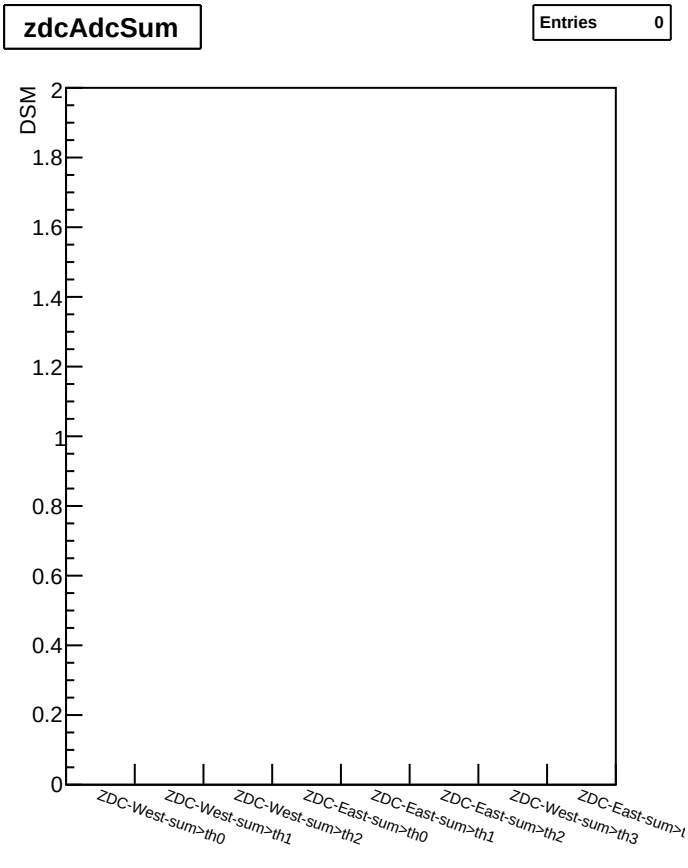

vpdMtdWestMaxTac
Entries 0

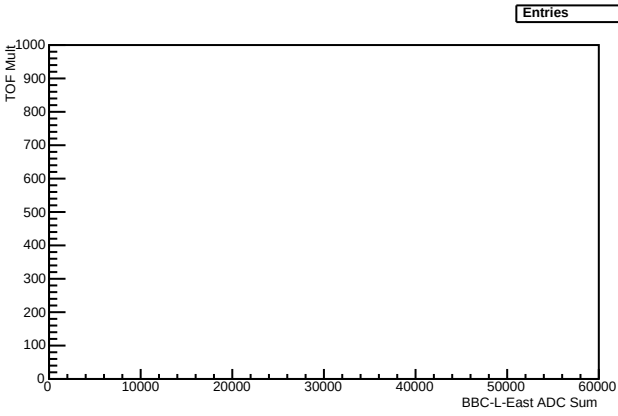

vpdMtdEastMaxTacVsVpd
Entries 0
vpdEastMaxTac
vpdMtdEastMaxTac

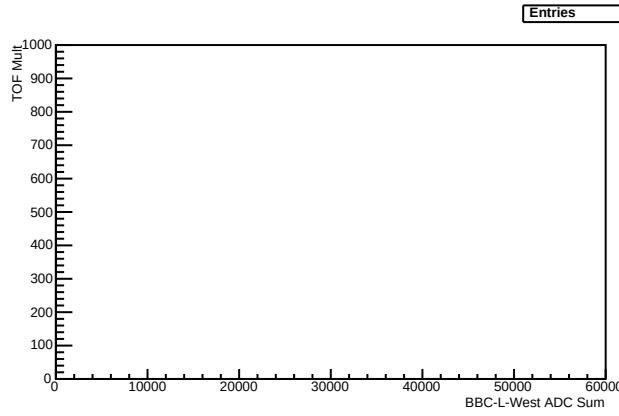

vpdMtdWestMaxTacVsVpd
Entries 0
vpdWestMaxTac
vpdMtdWestMaxTac

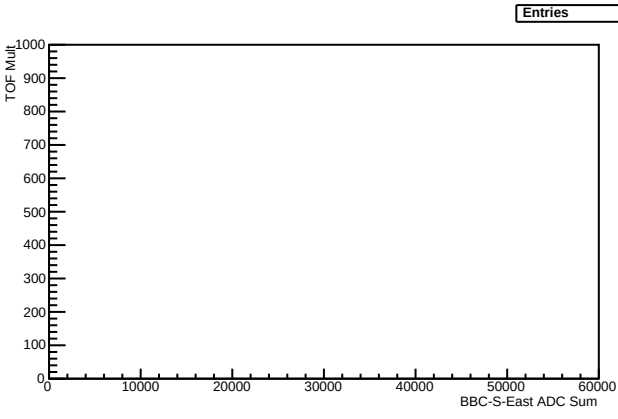

zdcWestTac
Entries 0
10
1
10^{-1}
10^{-2}
0
200
400
600
800
1000

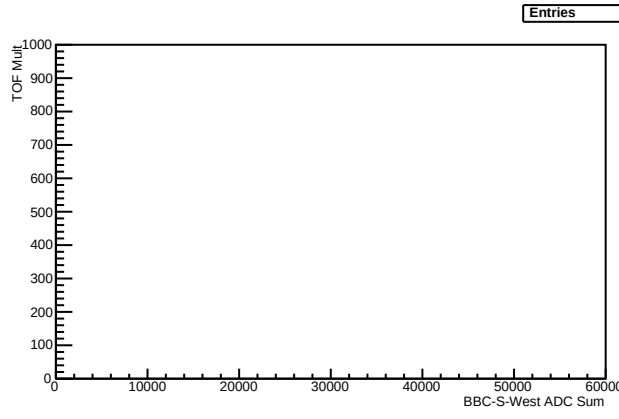

zdcEastTac
Entries 0
10
1
10^{-1}
10^{-2}
0
200
400
600
800
1000

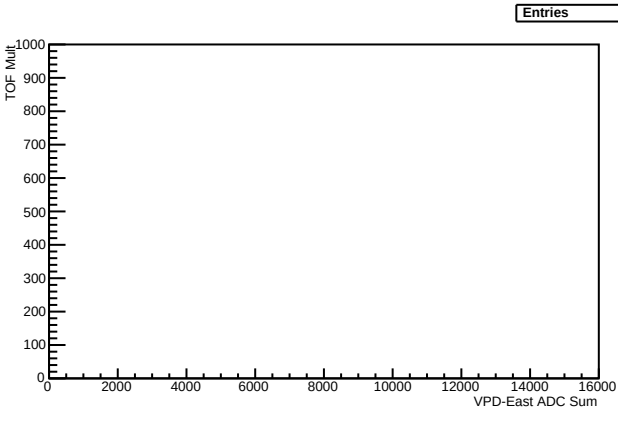

zdcAdcSum
Entries 0
DSM
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
ZDC-West-sum>th0
ZDC-West-sum>th1
ZDC-West-sum>th2
ZDC-East-sum>th0
ZDC-East-sum>th1
ZDC-East-sum>th2
ZDC-West-sum>th3

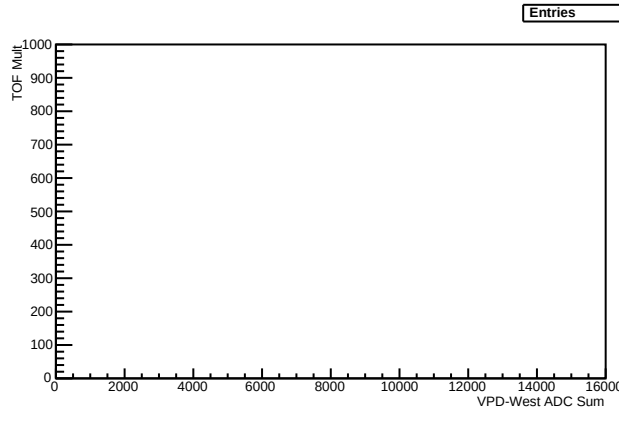

Entries 0
TOF Mult
BBC-L-East ADC Sum

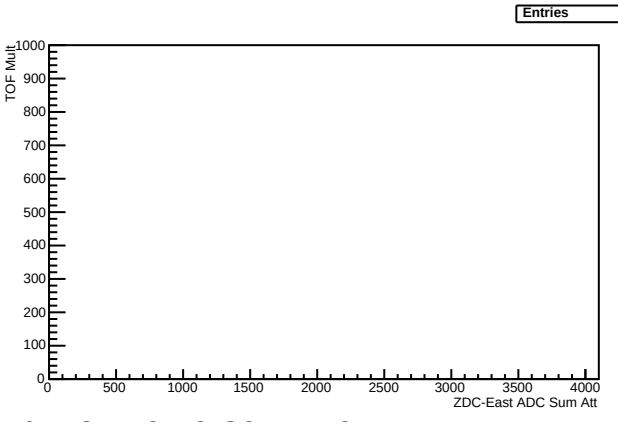

Entries 0
TOF Mult
BBC-L-West ADC Sum

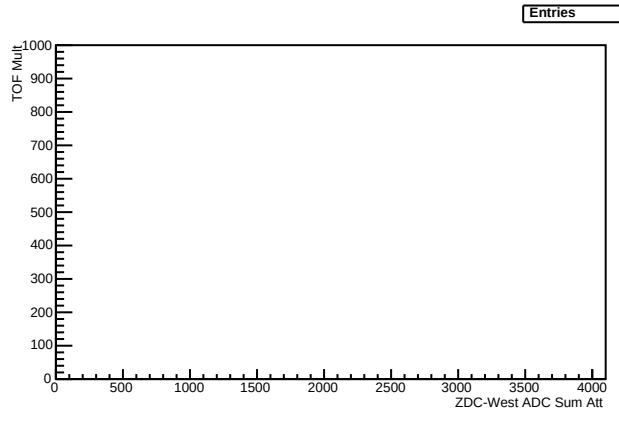

Entries 0
TOF Mult
BBC-S-East ADC Sum

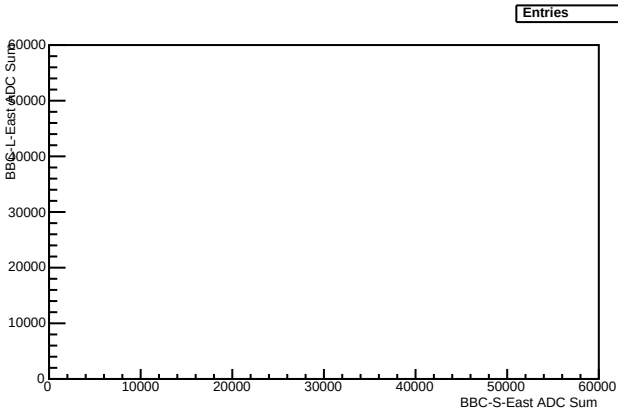

Entries 0
TOF Mult
BBC-S-West ADC Sum

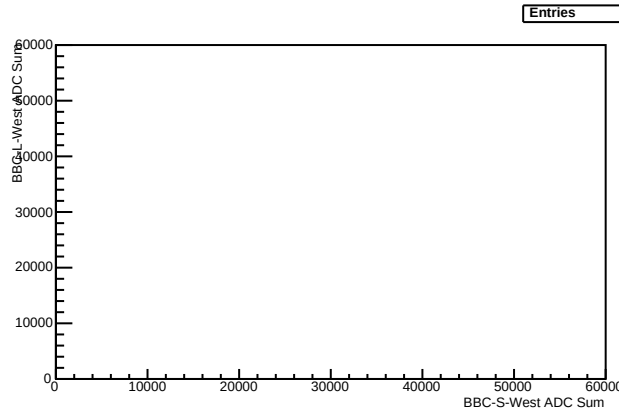

Entries 0
TOF Mult
VPD-East ADC Sum

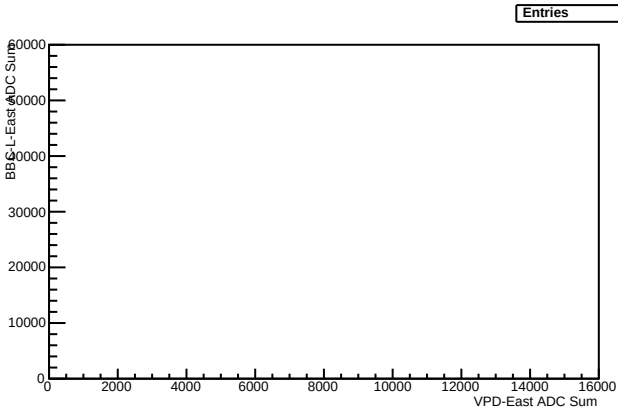

Entries 0
TOF Mult
VPD-West ADC Sum

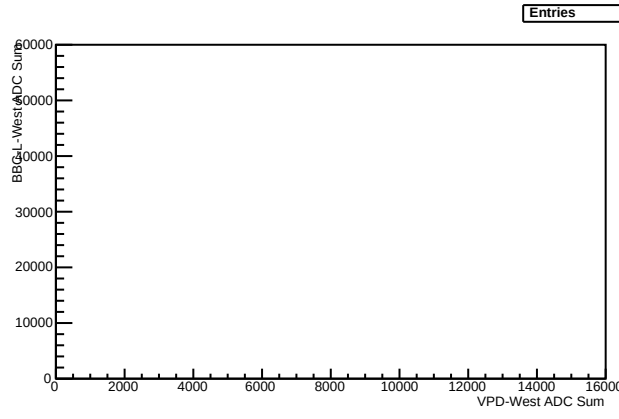

Entries 0
TOF Mult
ZDC-East ADC Sum Att

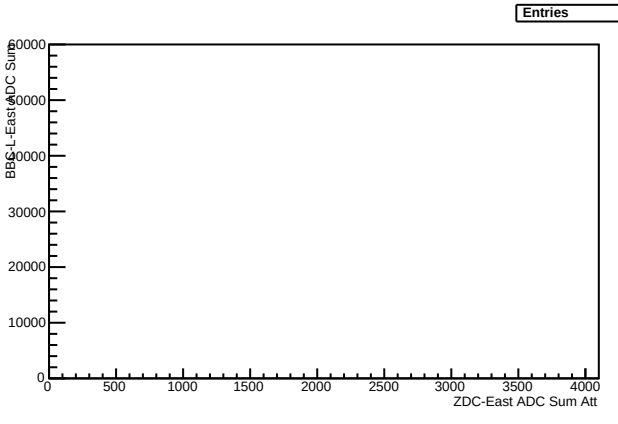

Entries 0
TOF Mult
ZDC-West ADC Sum Att

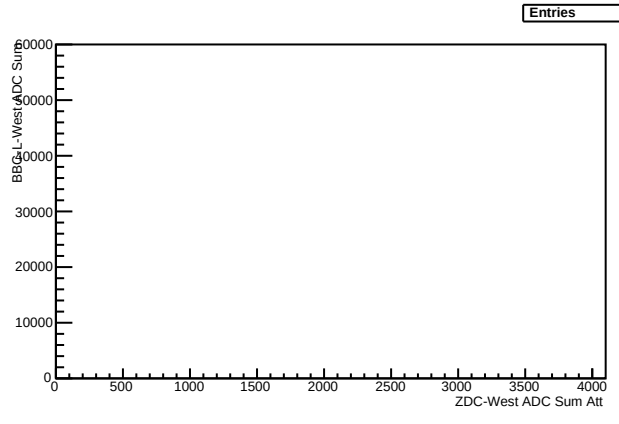

Entries 0
BBC-L-East ADC Sum
BBC-S-East ADC Sum

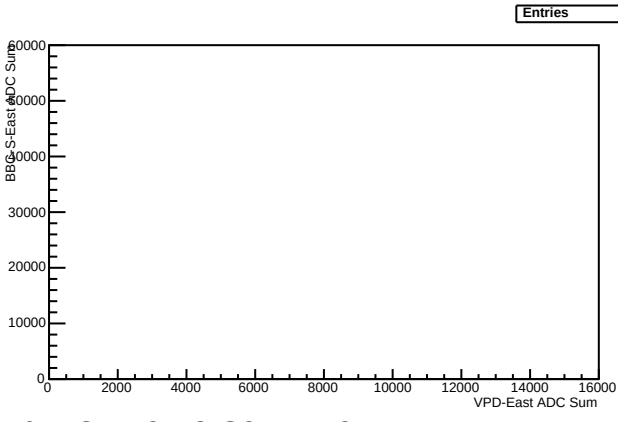

Entries 0
BBC-L-West ADC Sum
BBC-S-West ADC Sum

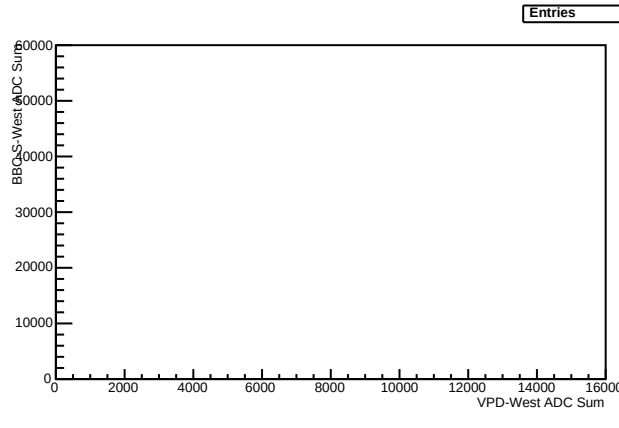

Entries 0
BBC-L-East ADC Sum
VPD-East ADC Sum

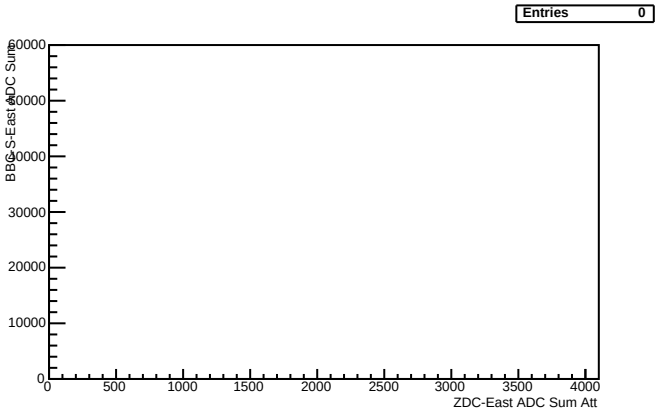

Entries 0
BBC-L-West ADC Sum
VPD-West ADC Sum

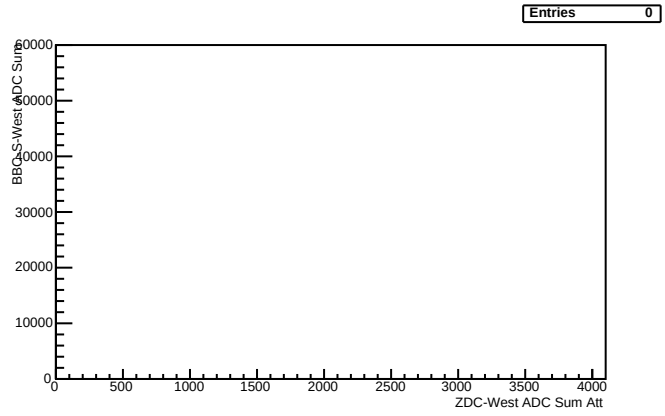

Entries 0
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

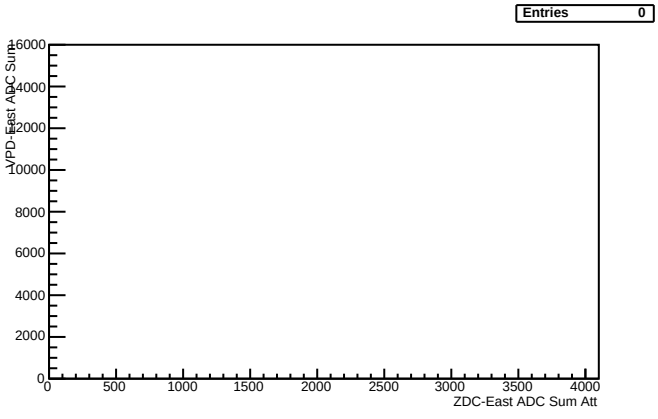

Entries 0
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

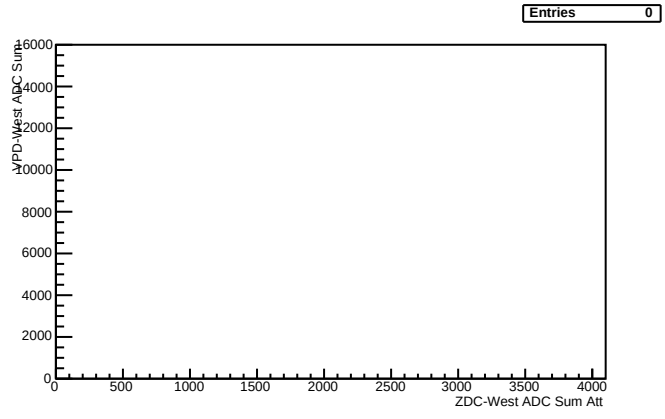

Entries 0
BBC-S-East ADC Sum
VPD-East ADC Sum

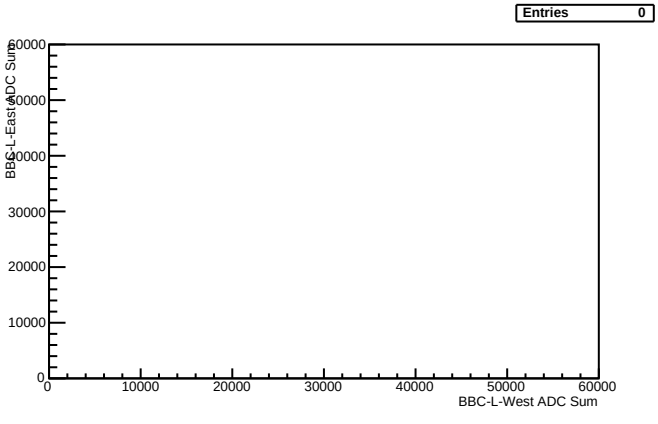

Entries 0
BBC-S-West ADC Sum
VPD-West ADC Sum

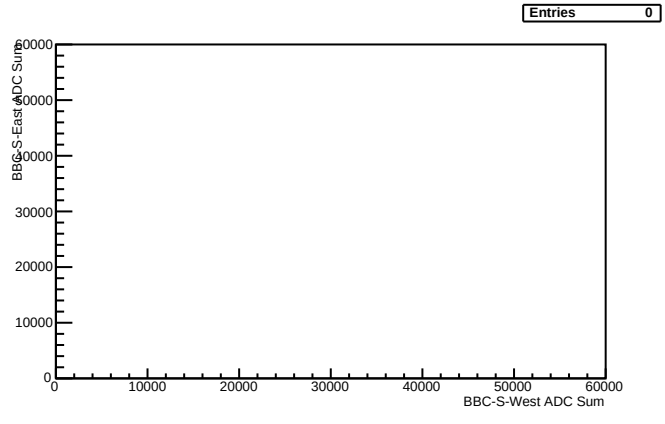

Entries 0
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

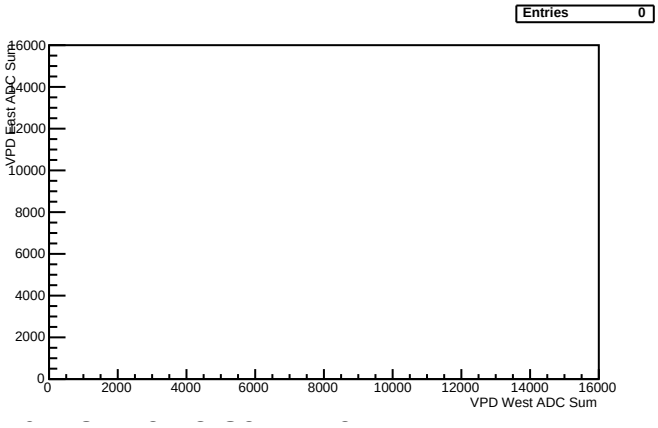

Entries 0
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

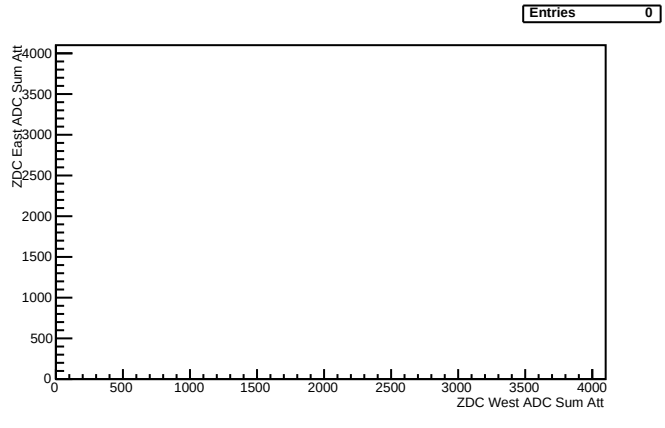

Entries 0
VPD-East ADC Sum
ZDC-East ADC Sum Att

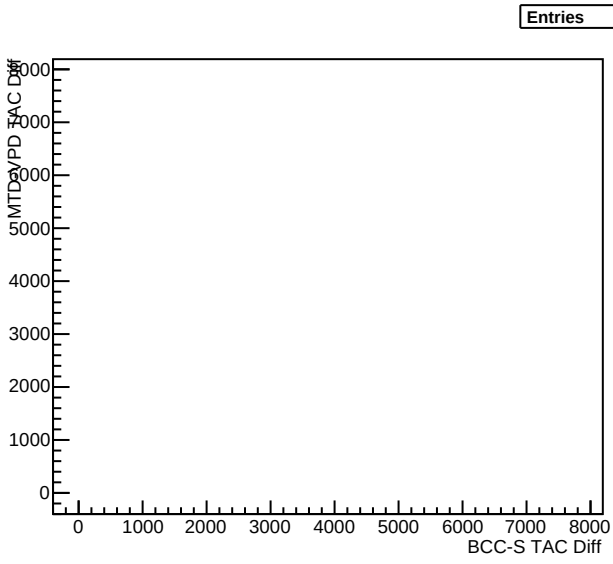

Entries 0
VPD-West ADC Sum
ZDC-West ADC Sum Att

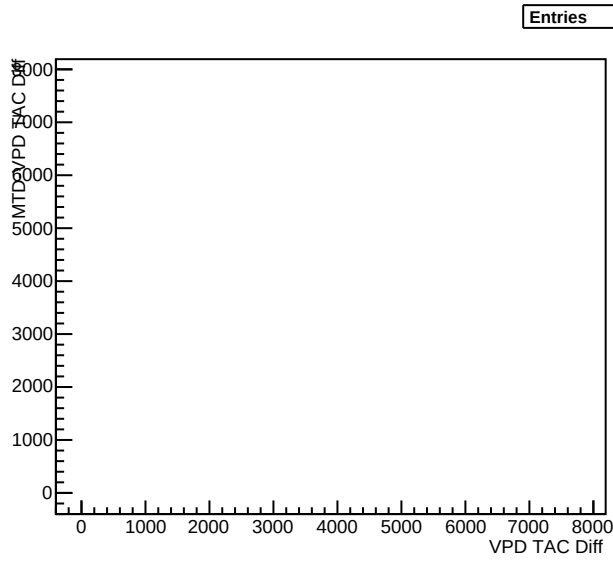

Entries 0
BBC-L-East ADC Sum
BBC-L-West ADC Sum

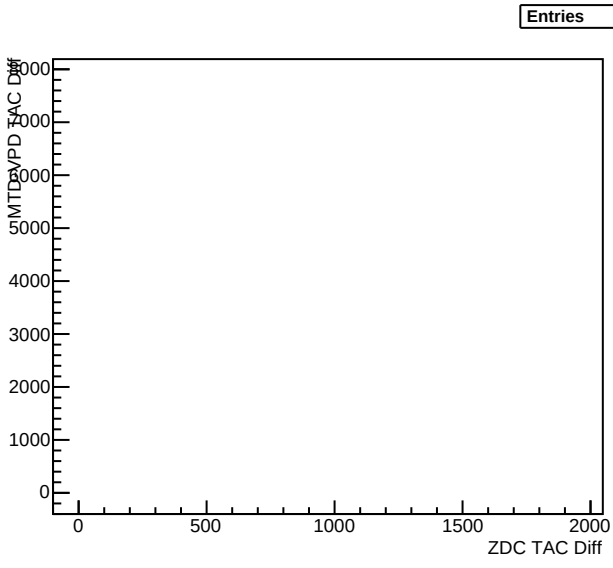

Entries 0
BBC-S-East ADC Sum
BBC-S-West ADC Sum

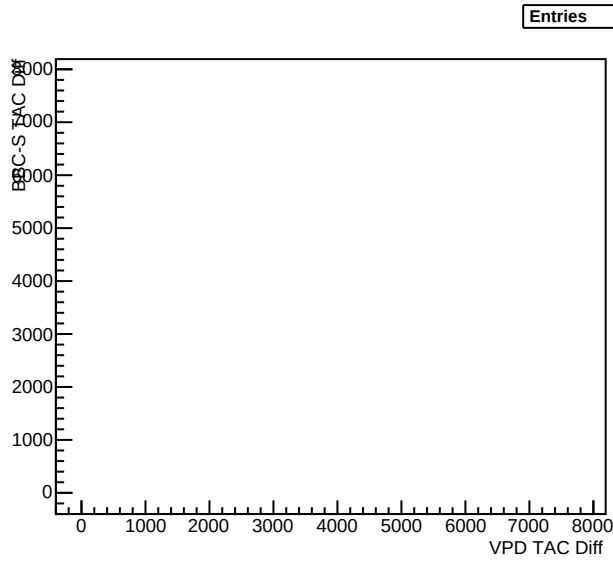

Entries 0
VPD East ADC Sum
VPD West ADC Sum

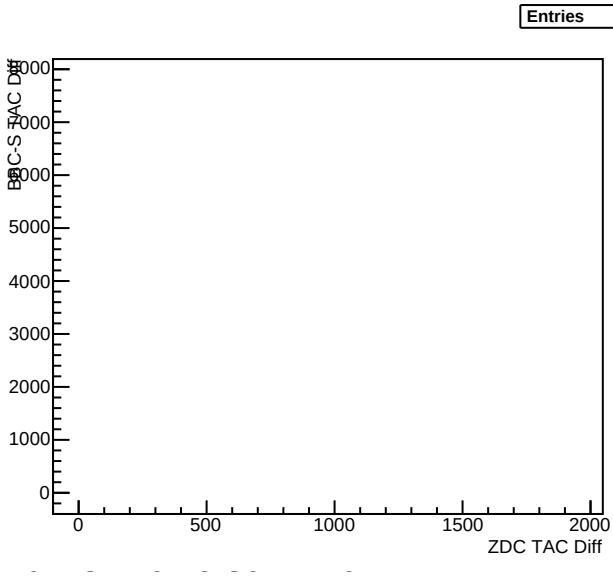

Entries 0
ZDC East ADC Sum Att
ZDC West ADC Sum Att

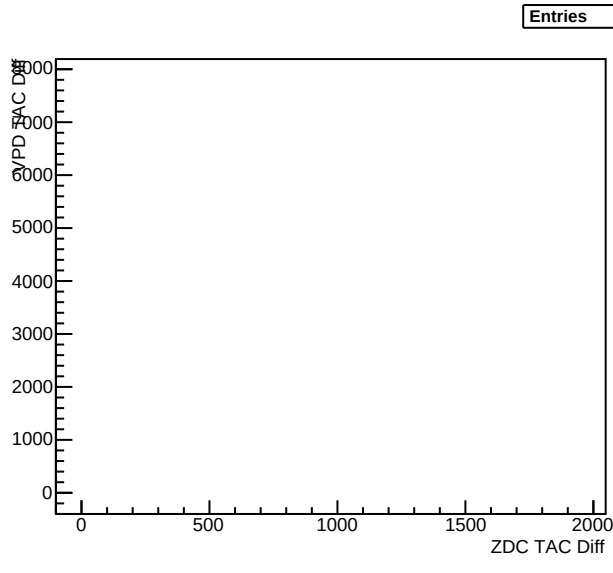

Entries 0
MTD-VPD TAC Diff
BCC-S TAC Diff

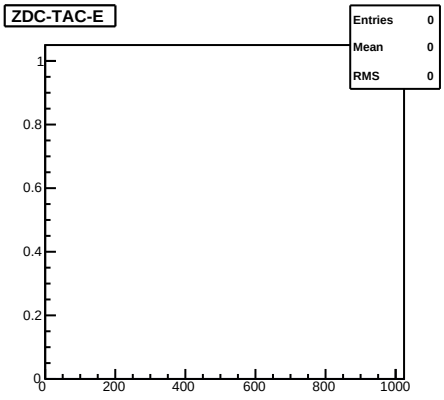

Entries 0
MTD-VPD TAC Diff
VPD TAC Diff

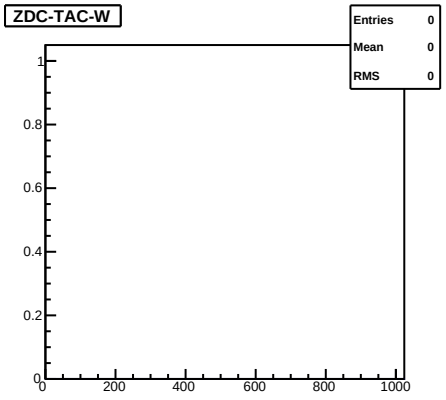

Entries 0
MTD-VPD TAC Diff
ZDC TAC Diff

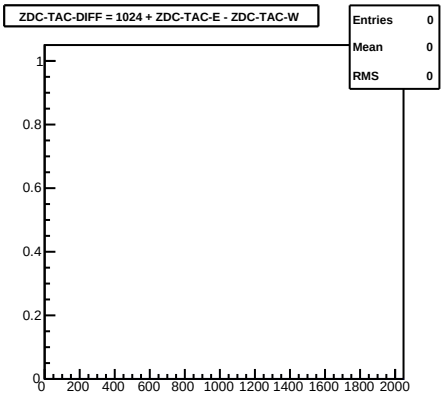

Entries 0
BCC-S TAC Diff
VPD TAC Diff

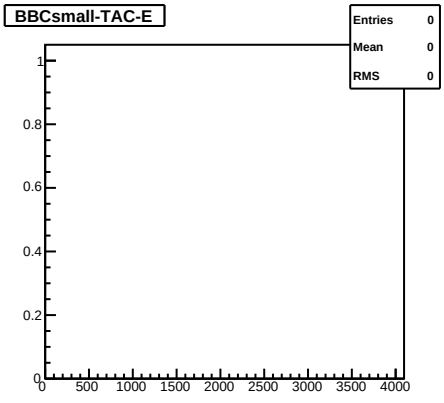

Entries 0
BCC-S TAC Diff
ZDC TAC Diff

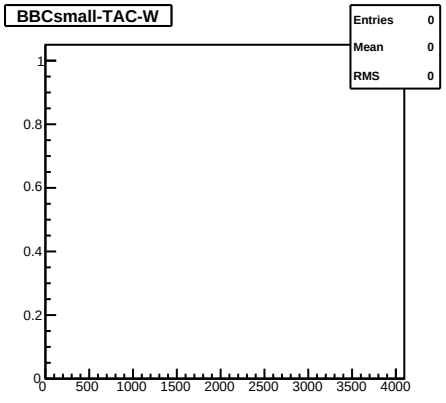

Entries 0
VPD TAC Diff
ZDC TAC Diff

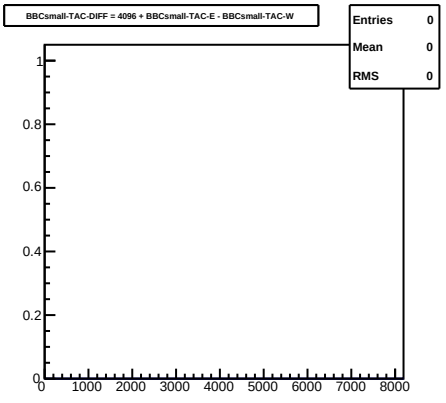

ZDC-TAC-E
Entries 0
Mean 0
RMS 0

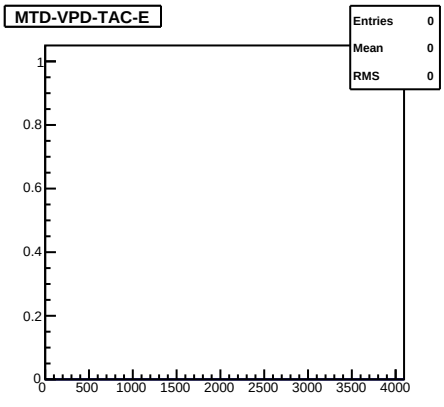

ZDC-TAC-W
Entries 0
Mean 0
RMS 0

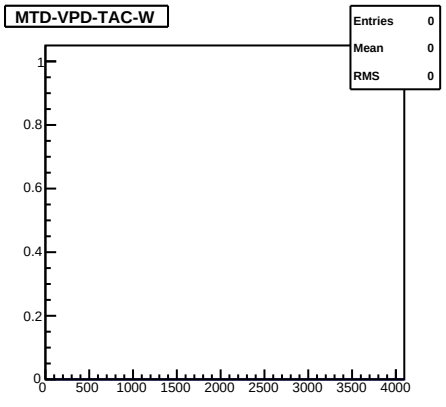

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 0
Mean 0
RMS 0

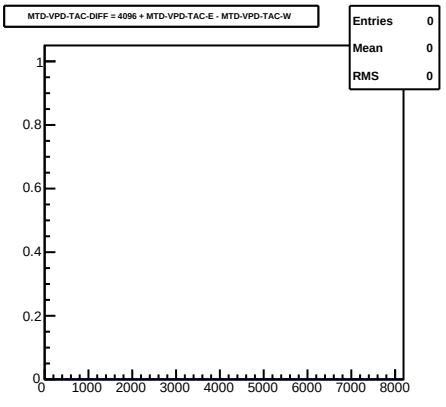

BBCsmall-TAC-E
Entries 0
Mean 0
RMS 0

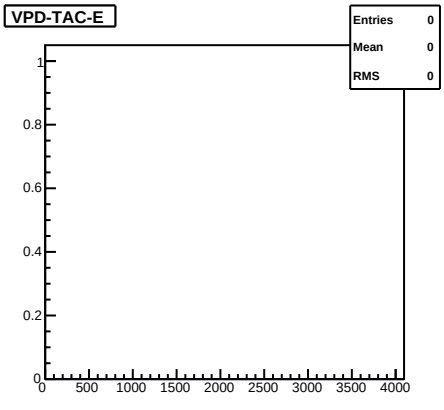

BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

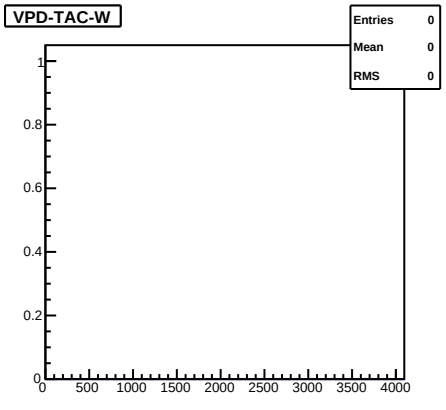

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

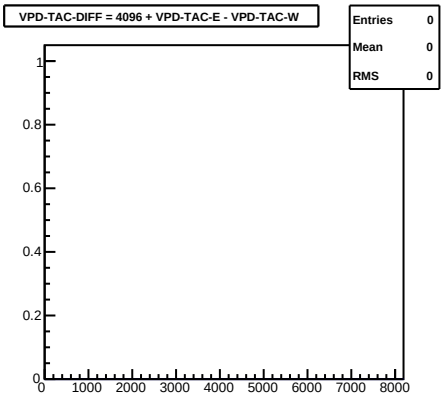

MTD-VPD-TAC-E
Entries 0
Mean 0
RMS 0

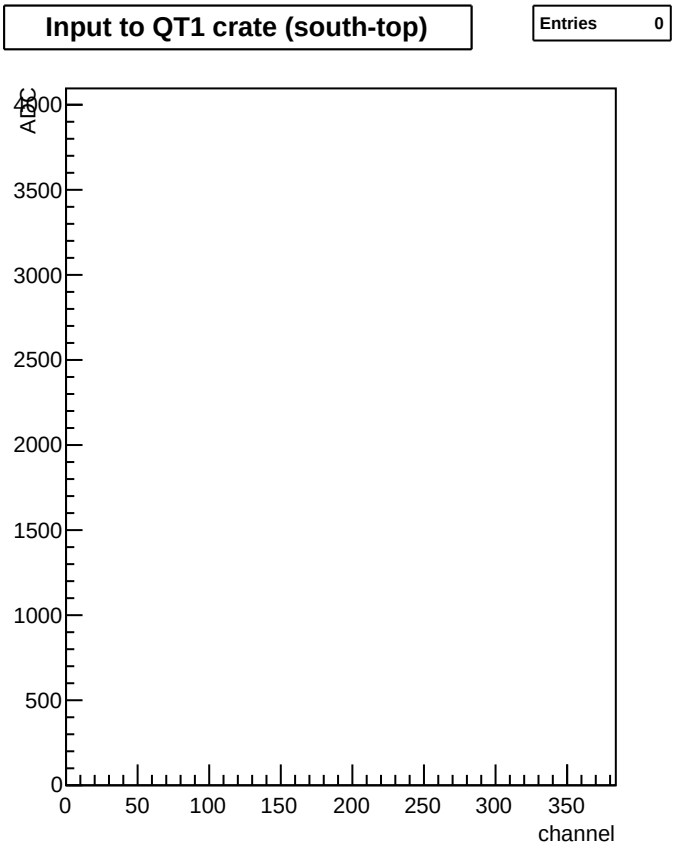

MTD-VPD-TAC-W
Entries 0
Mean 0
RMS 0

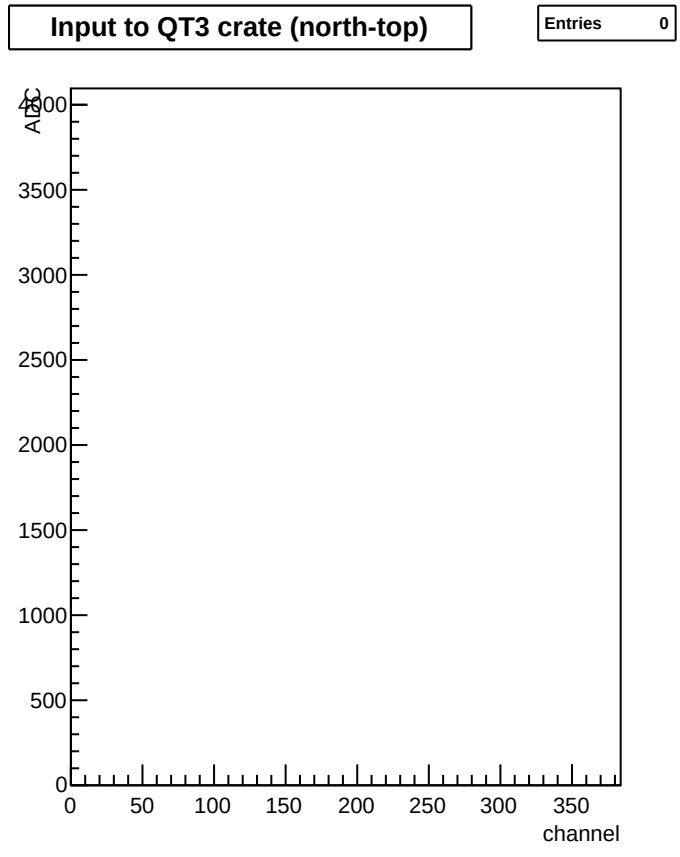

MTD-VPD-TAC-DIFF = 4096 + MTD-VPD-TAC-E - MTD-VPD-TAC-W
Entries 0
Mean 0
RMS 0

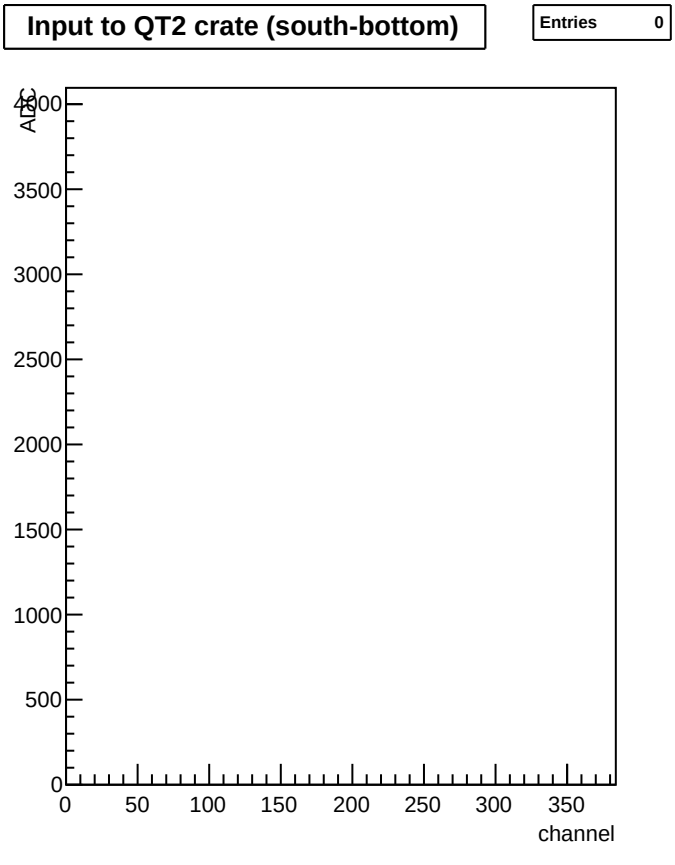

VPD-TAC-E
Entries 0
Mean 0
RMS 0

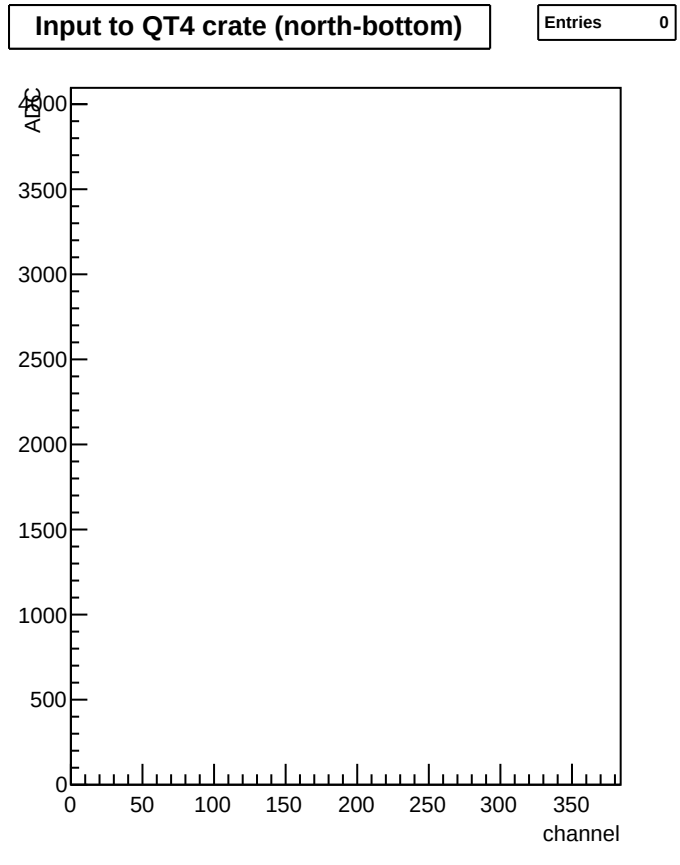

VPD-TAC-W
Entries 0
Mean 0
RMS 0

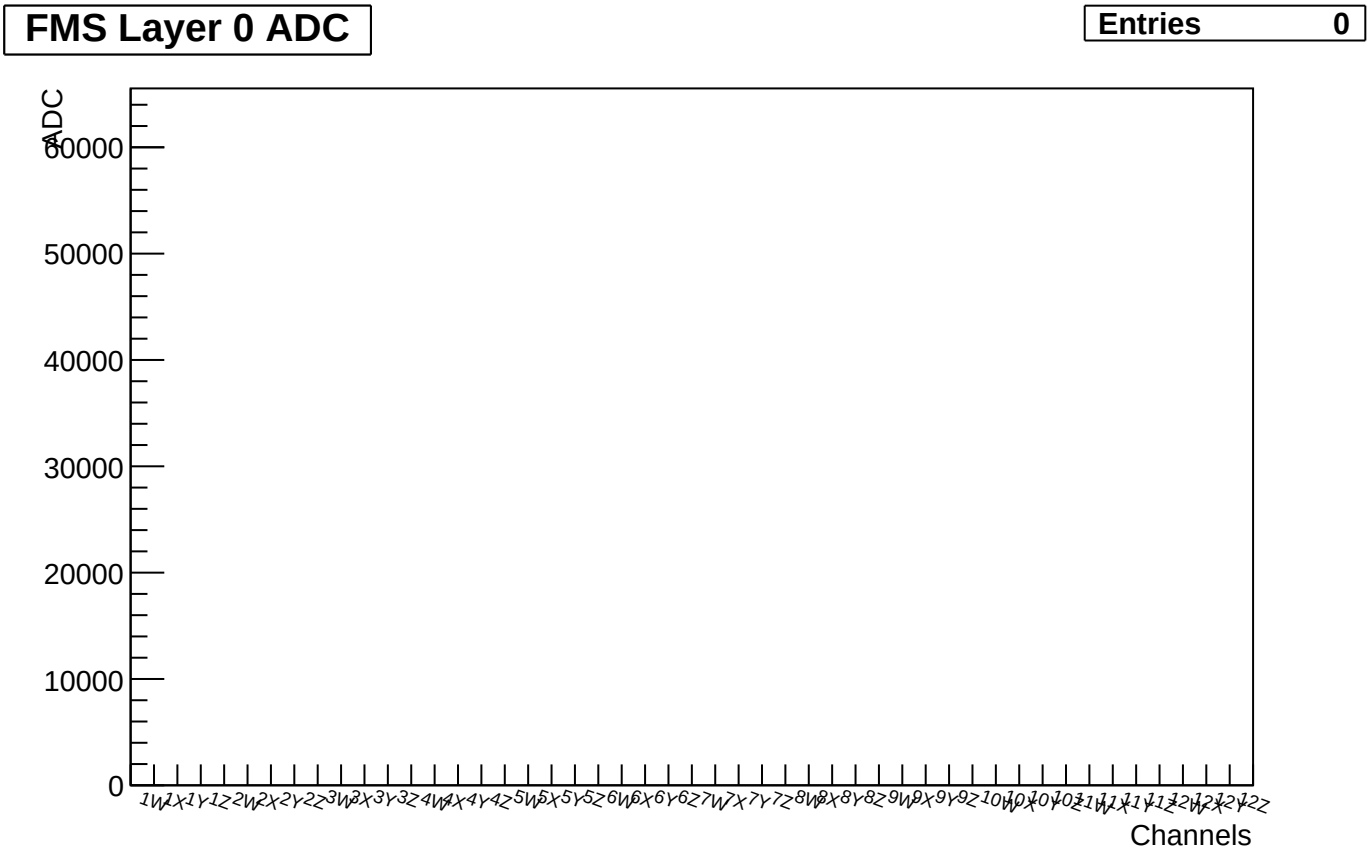

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

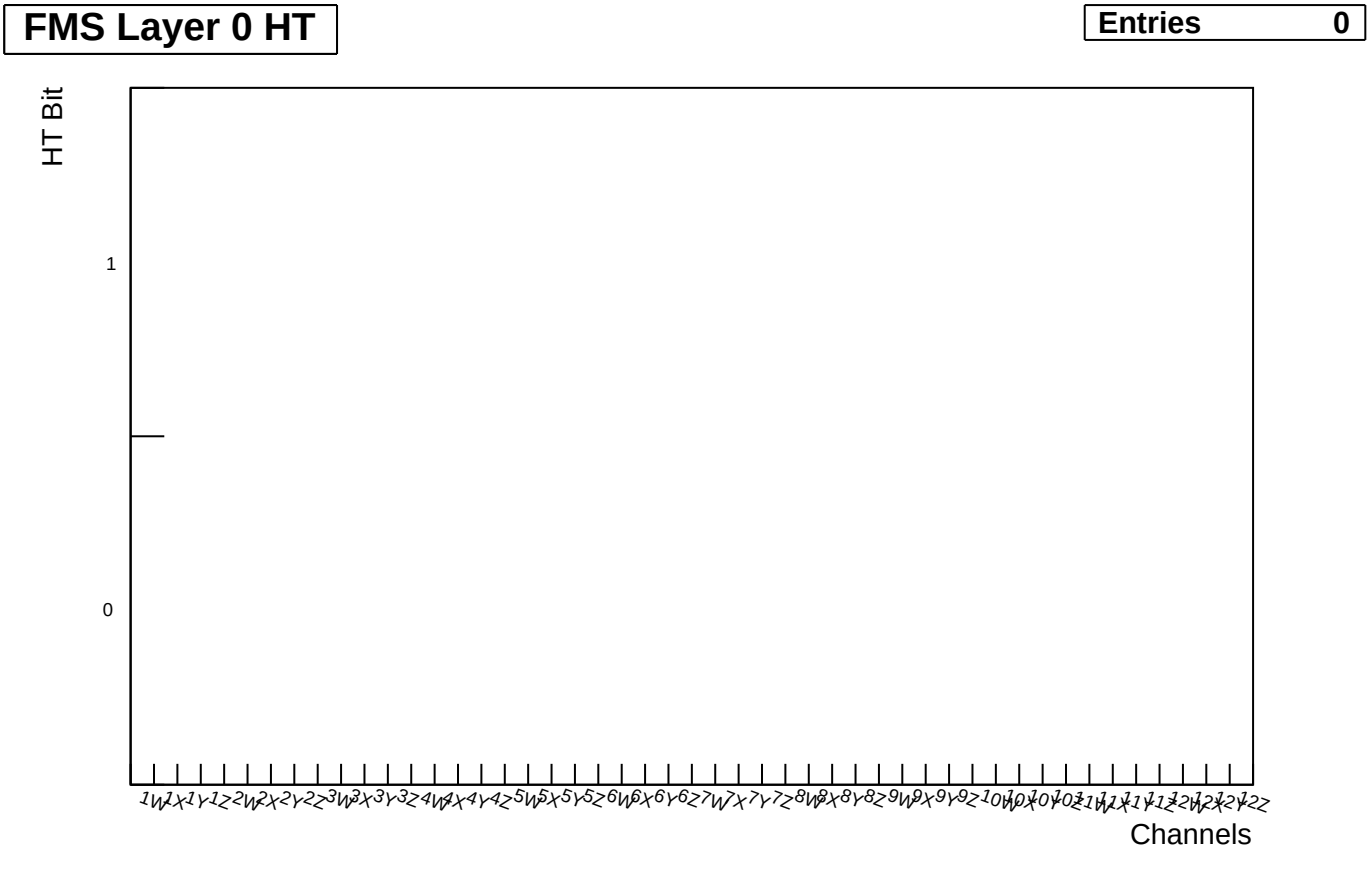

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

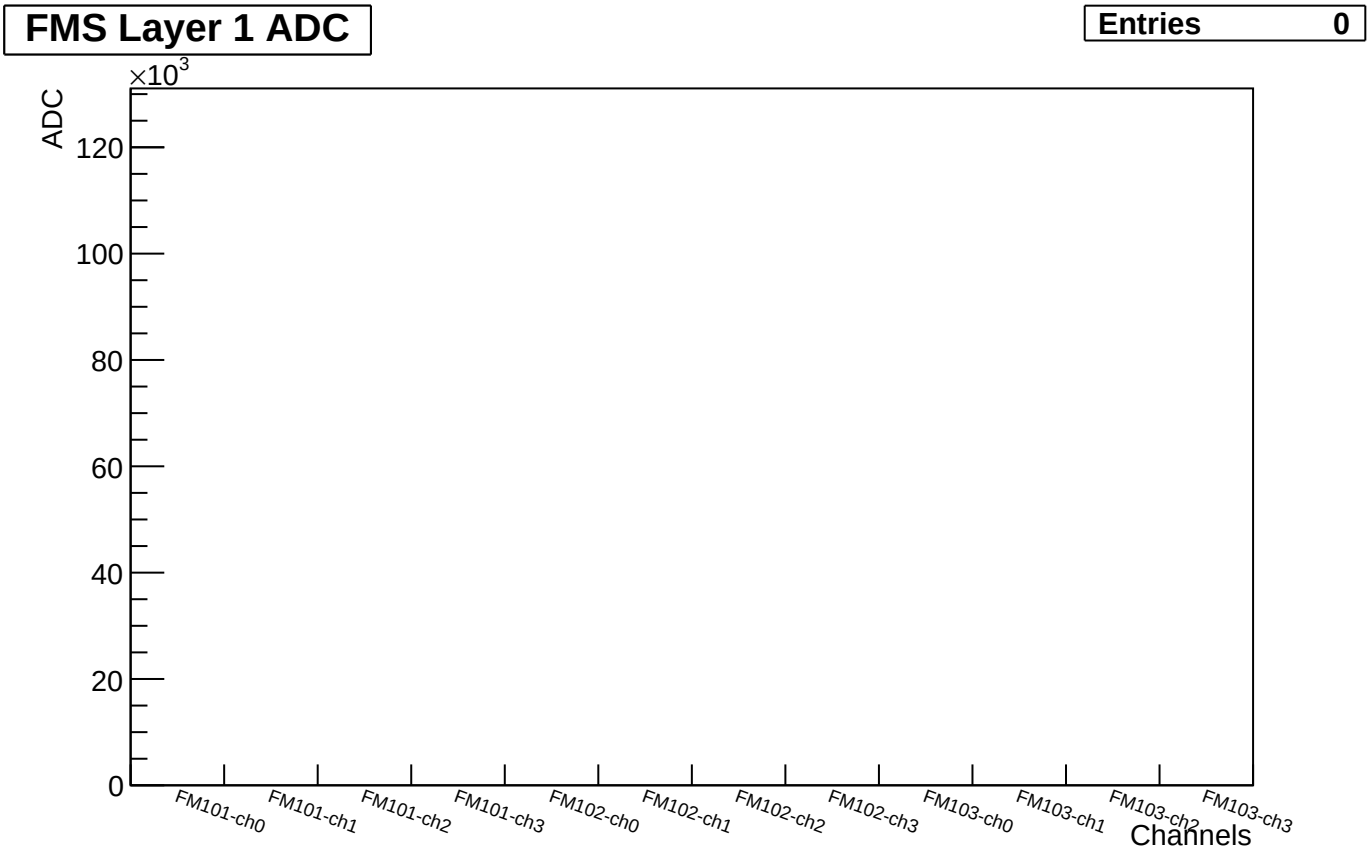

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

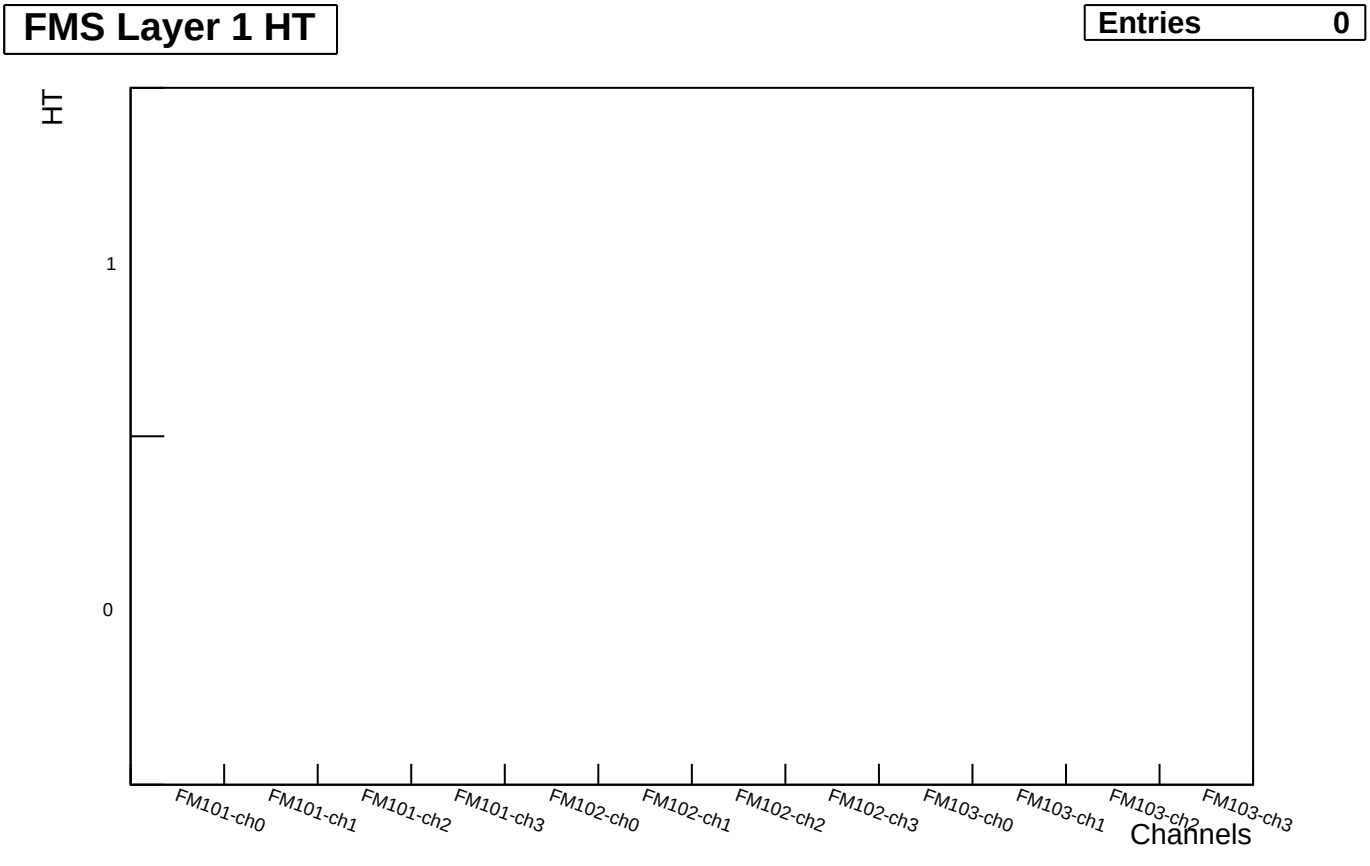

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

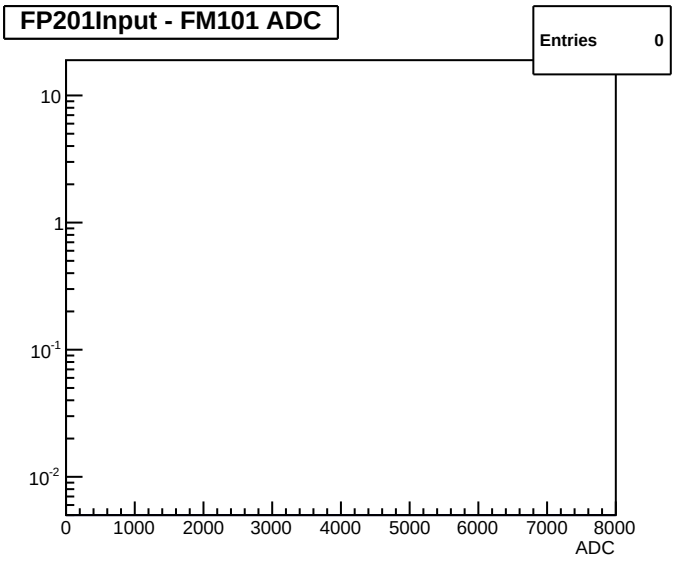

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

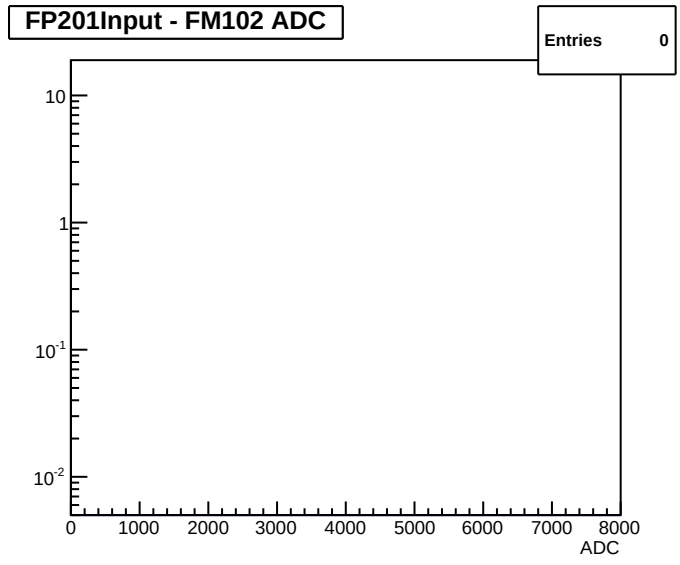

FMS Layer 0 ADC
Entries 0
ADC
60000
50000
40000
30000
20000
10000
0
Channels

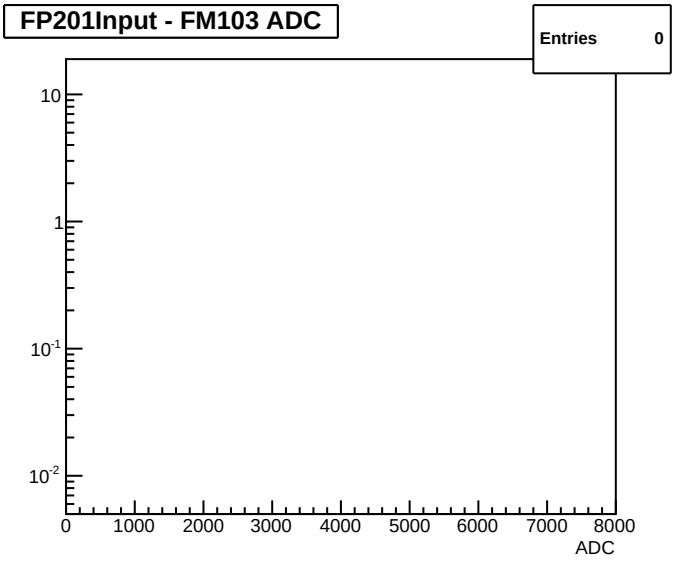

FMS Layer 0 HT
Entries 0
HT Bit
1
0
Channels

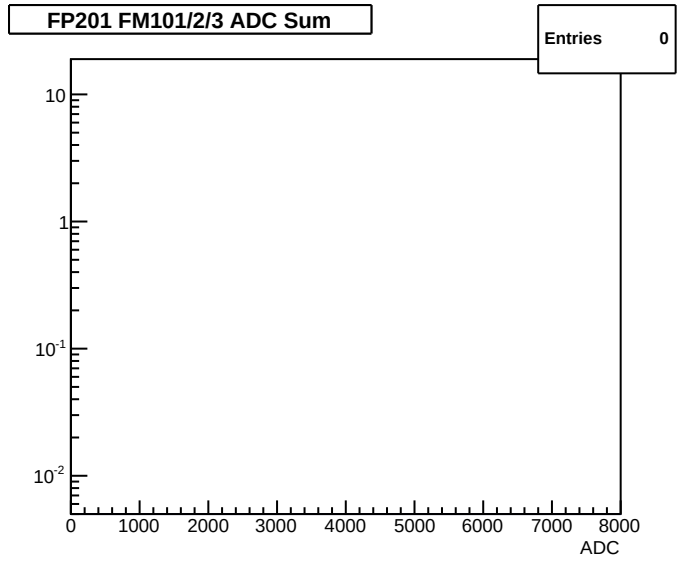

FMS Layer 1 ADC
Entries 0
ADC
×10^3
120
100
80
60
40
20
0
FM101-ch0
FM101-ch1
FM101-ch2
FM101-ch3
FM102-ch0
FM102-ch1
FM102-ch2
FM102-ch3
FM103-ch0
FM103-ch1
FM103-ch2
FM103-ch3
Channels

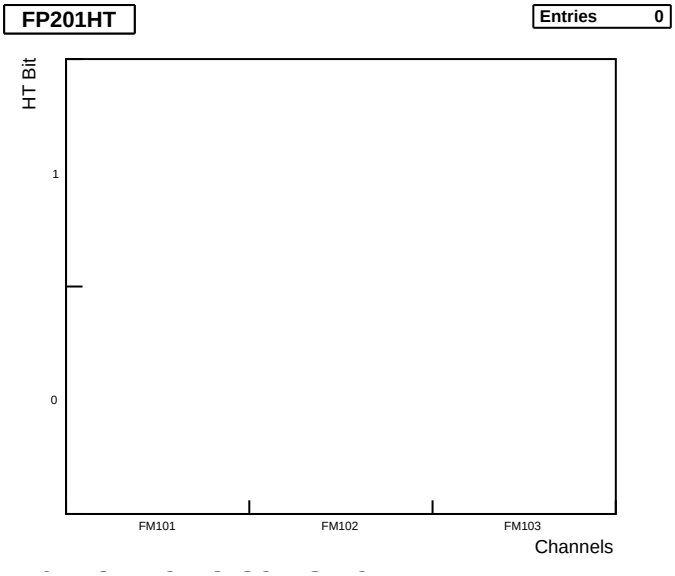

FMS Layer 1 HT
Entries 0
HT
1
0
FM101-ch0
FM101-ch1
FM101-ch2
FM101-ch3
FM102-ch0
FM102-ch1
FM102-ch2
FM102-ch3
FM103-ch0
FM103-ch1
FM103-ch2
FM103-ch3
Channels

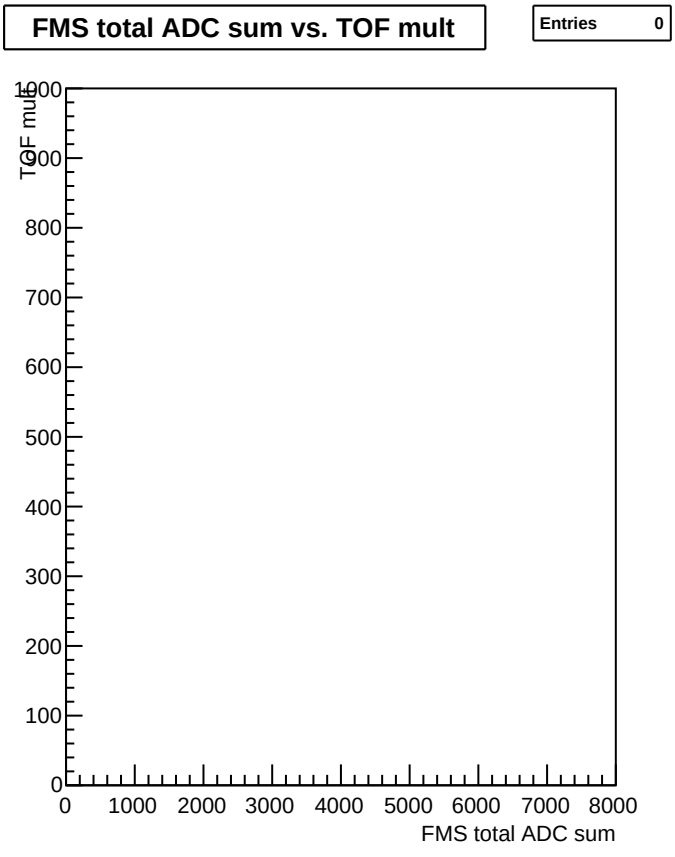

FP201Input - FM101 ADC
Entries 0
10
1
10-1
10-2
0 1000 2000 3000 4000 5000 6000 7000 8000
ADC

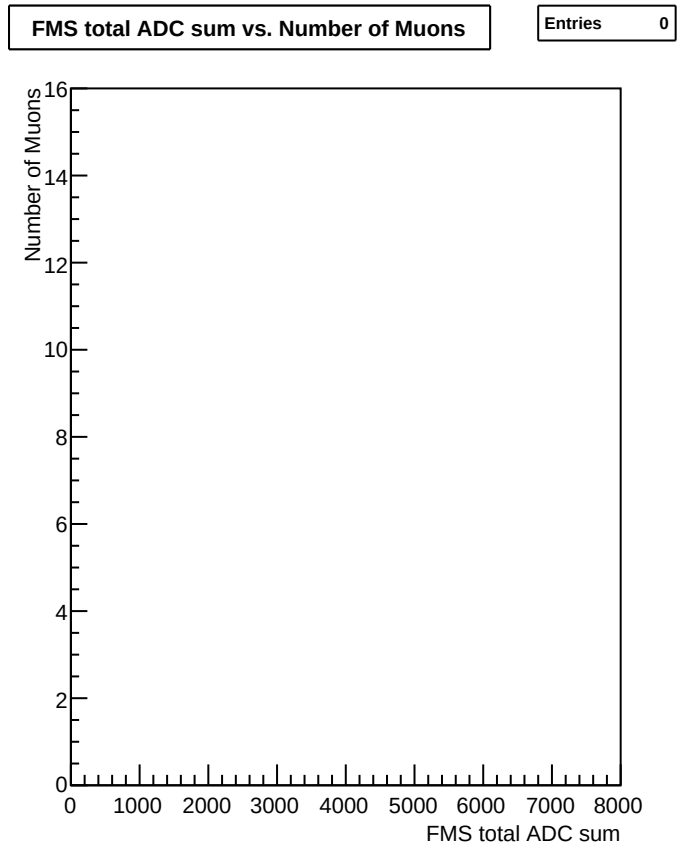

FP201Input - FM102 ADC
Entries 0
10
1
10-1
10-2
0 1000 2000 3000 4000 5000 6000 7000 8000
ADC

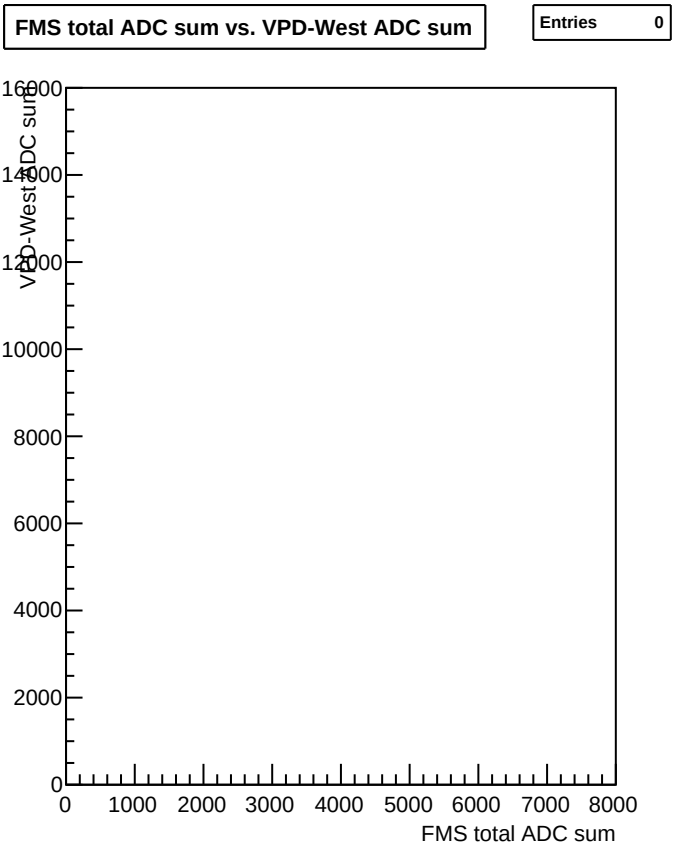

FP201Input - FM103 ADC
Entries 0
10
1
10-1
10-2
0 1000 2000 3000 4000 5000 6000 7000 8000
ADC

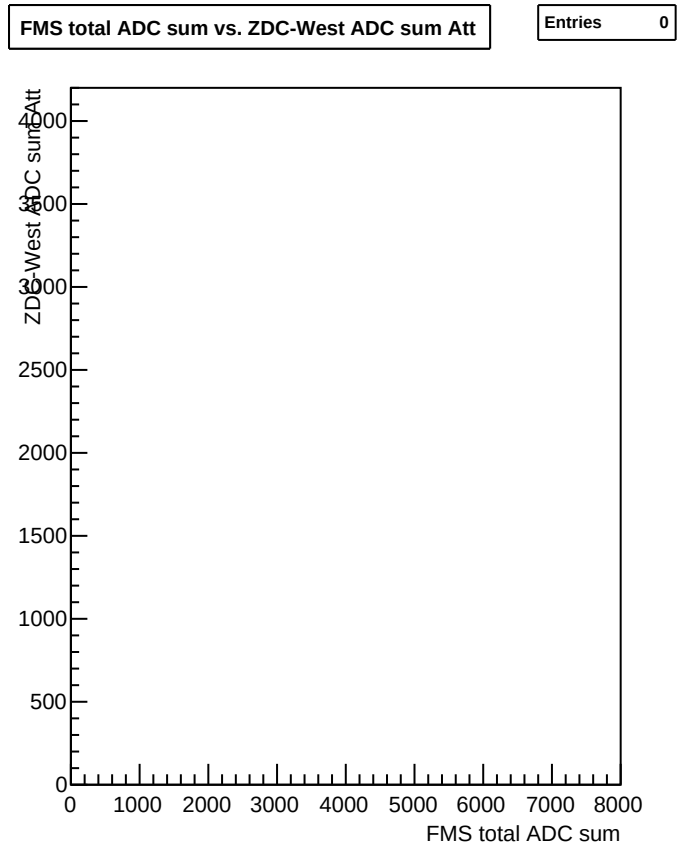

FP201 FM101/2/3 ADC Sum
Entries 0
10
1
10-1
10-2
0 1000 2000 3000 4000 5000 6000 7000 8000
ADC


FP201HT
Entries 0
HT Bit
1
0
FM101
FM102
FM103
Channels


FMS total ADC sum vs. TOF mult
Entries 0
TOF mult
FMS total ADC sum

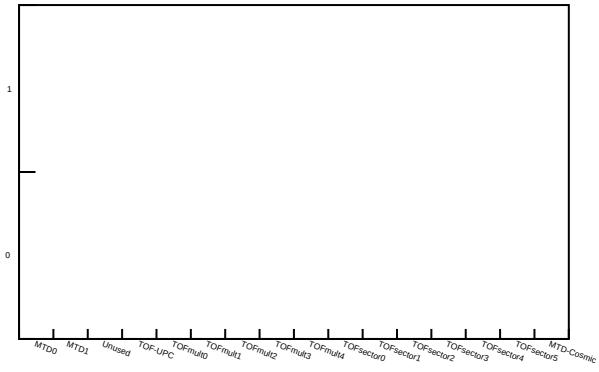

FMS total ADC sum vs. Number of Muons
Entries 0
Number of Muons
FMS total ADC sum


FMS total ADC sum vs. VPD-West ADC sum
Entries 0
VPD-West ADC sum
FMS total ADC sum


FMS total ADC sum vs. ZDC-West ADC sum Att
Entries 0
ZDC-West ADC sum Att
FMS total ADC sum

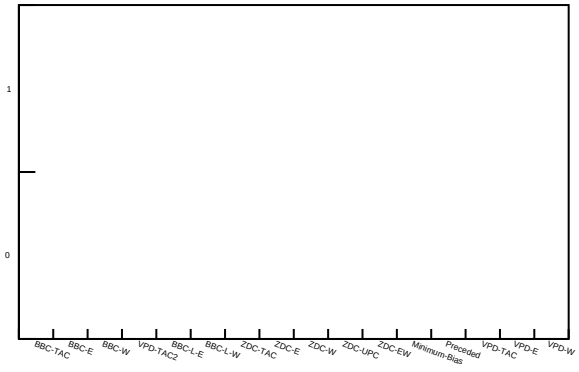
TF201 0-15 (ch0)

Entries 0



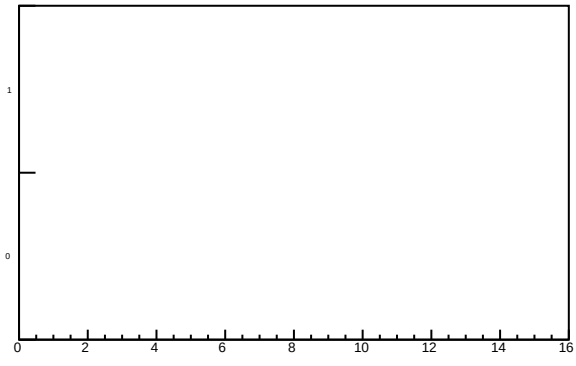
VT201 0-15 (ch1)

Entries 0



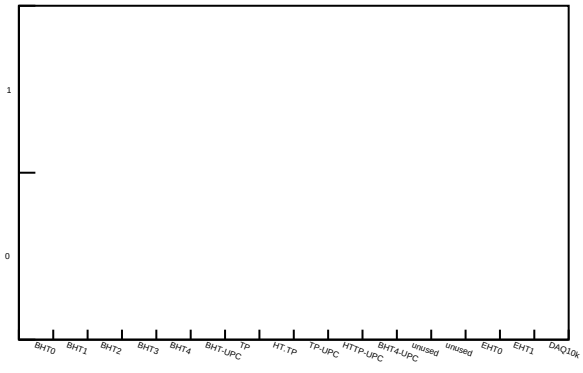
Unused (ch2)

Entries 0



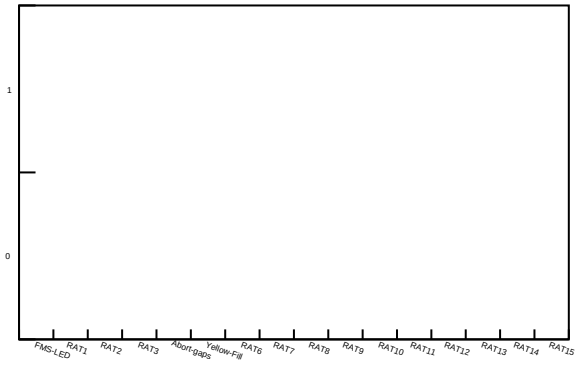
EM201 0-15 (ch3)

Entries 0



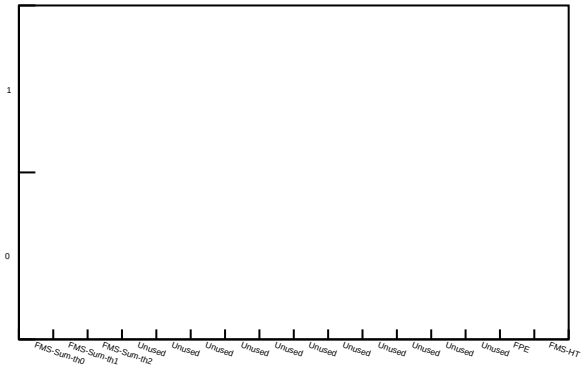
RAT board (ch4)

Entries 0



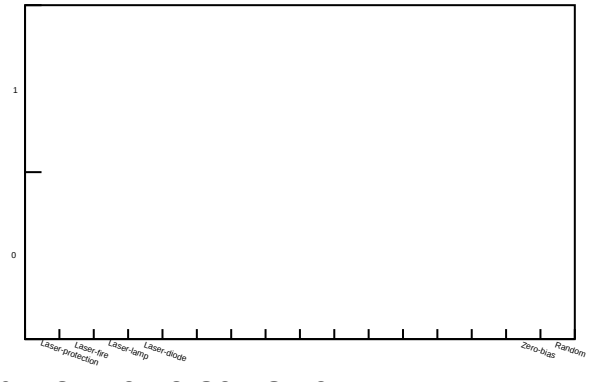
FP201 0-15 (ch5)

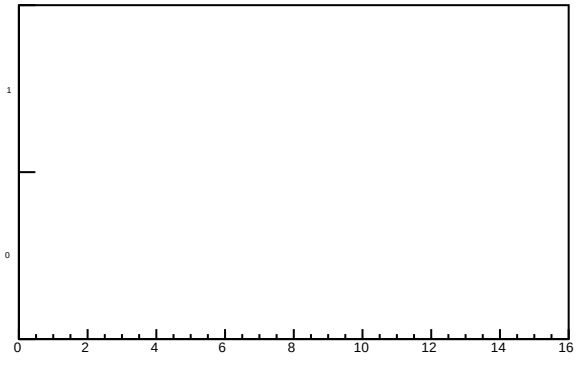
Entries 0



ST201 0-15 (ch6)

Entries 0



Unused (ch7)

Entries 0