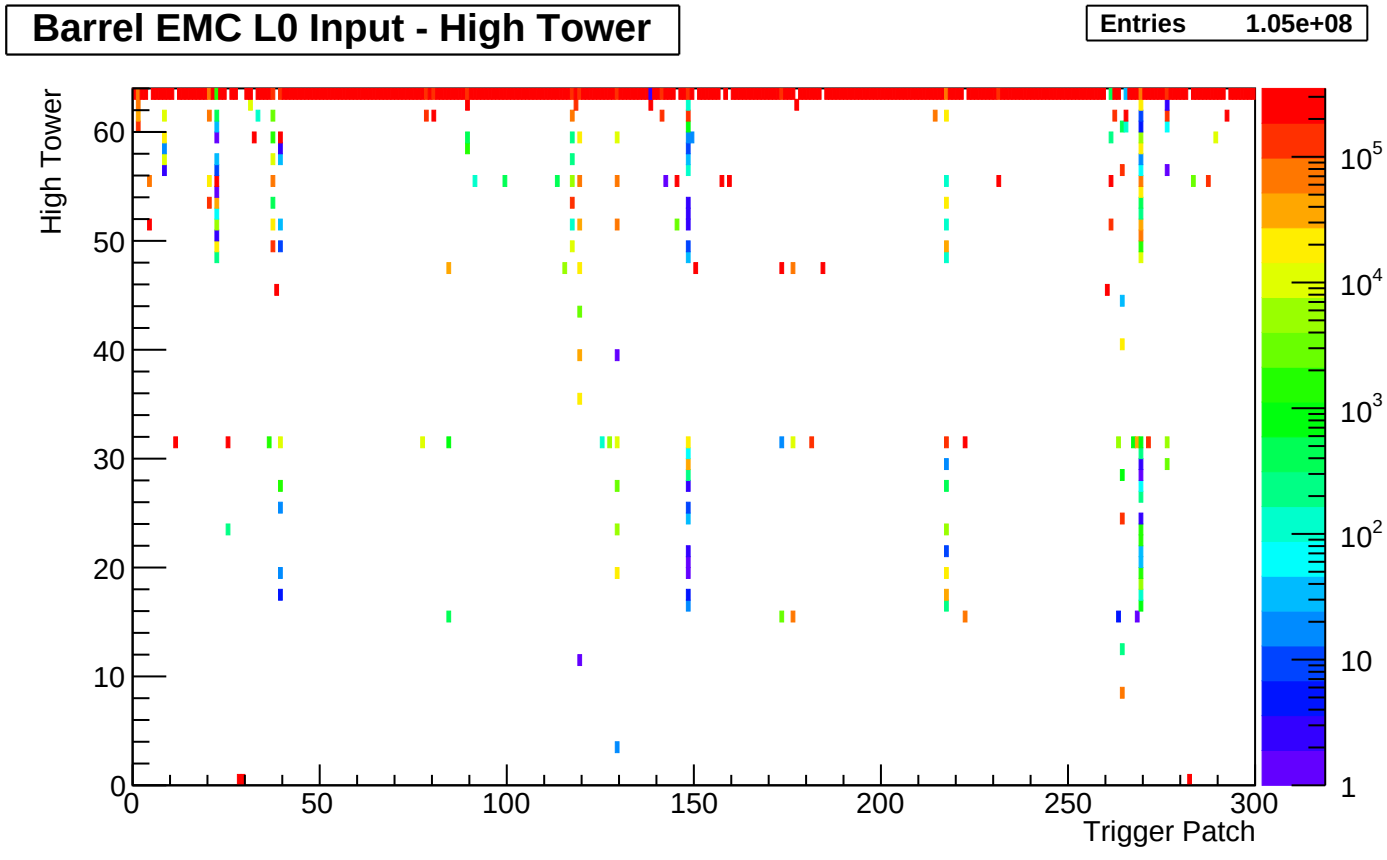
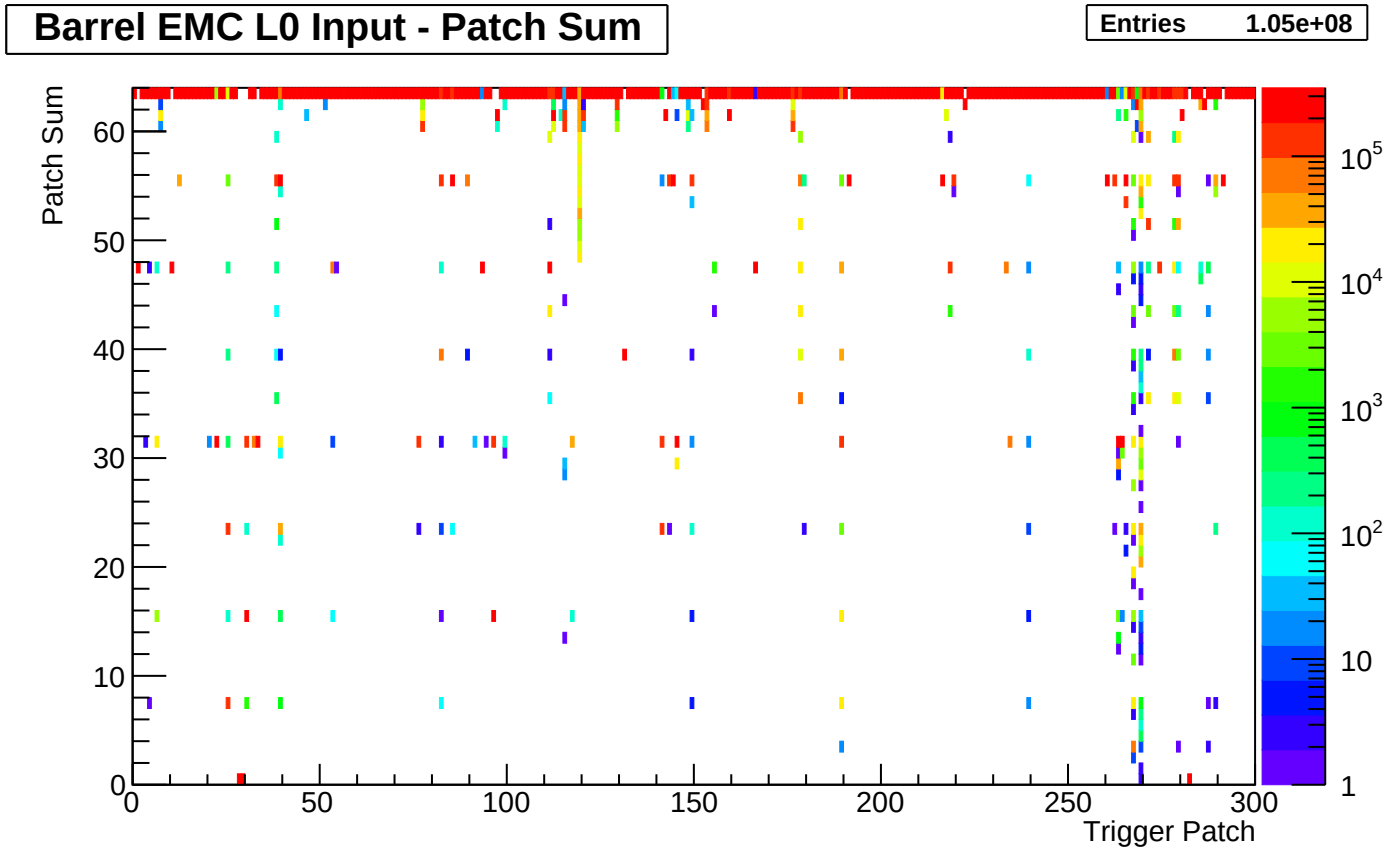
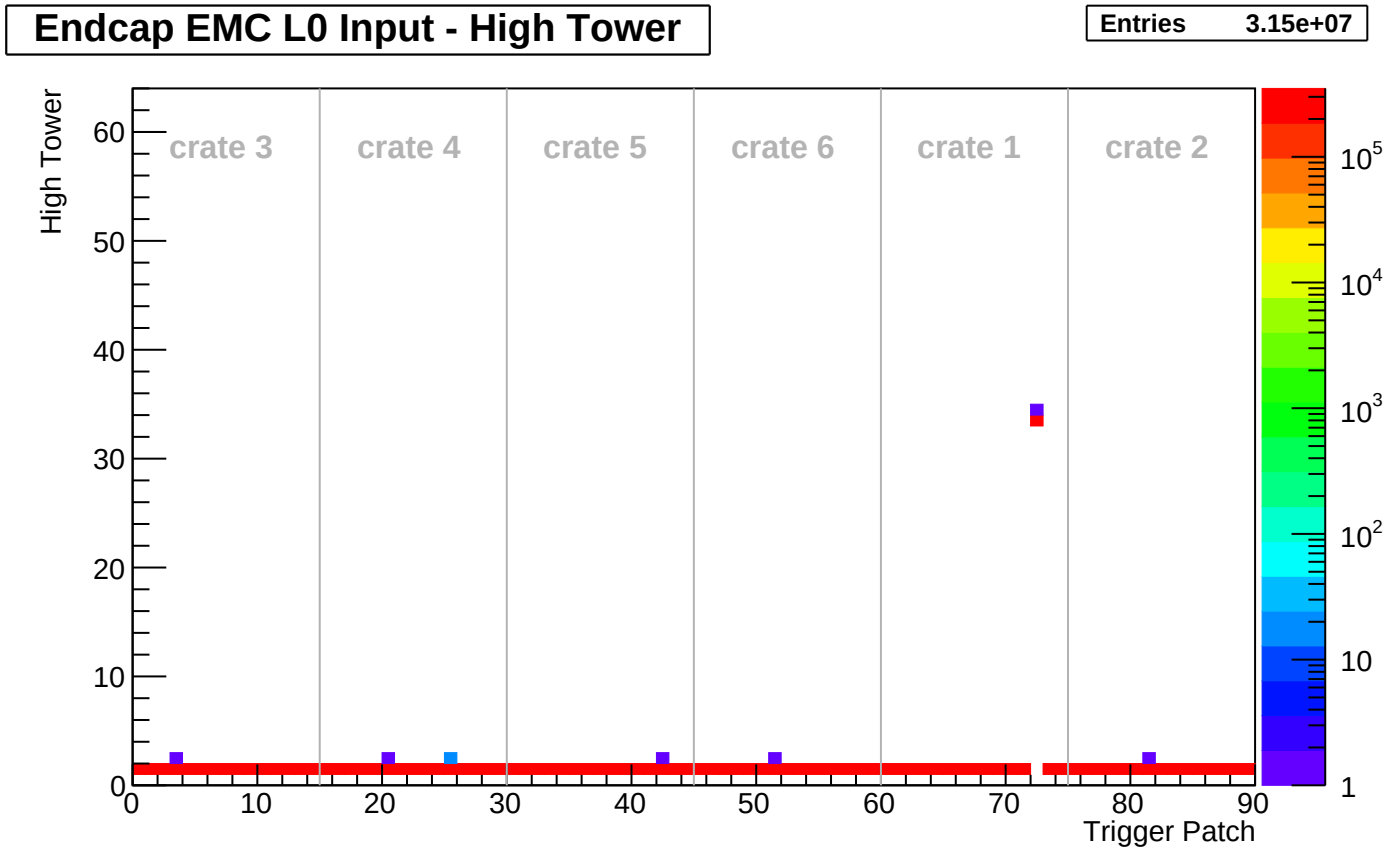
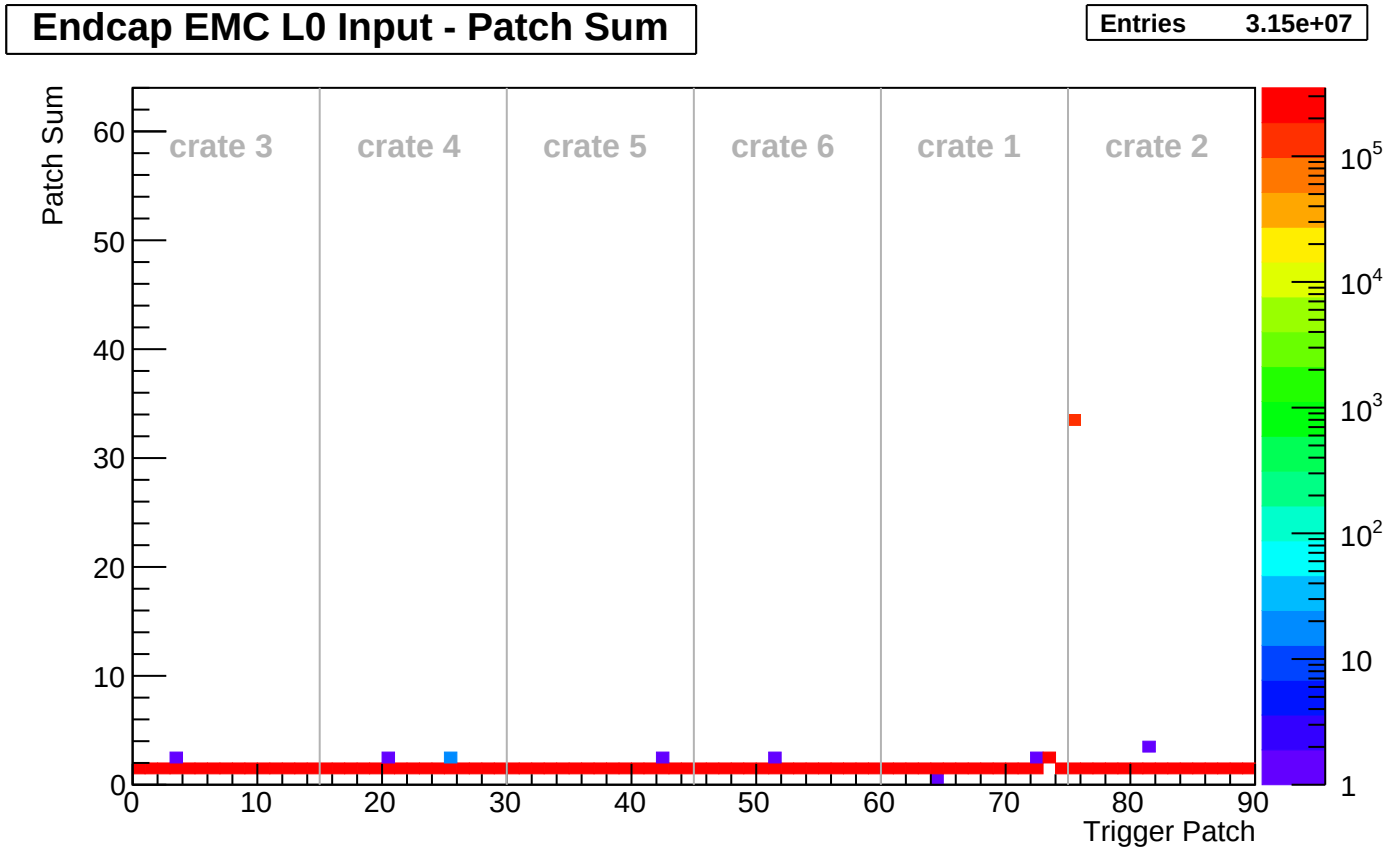
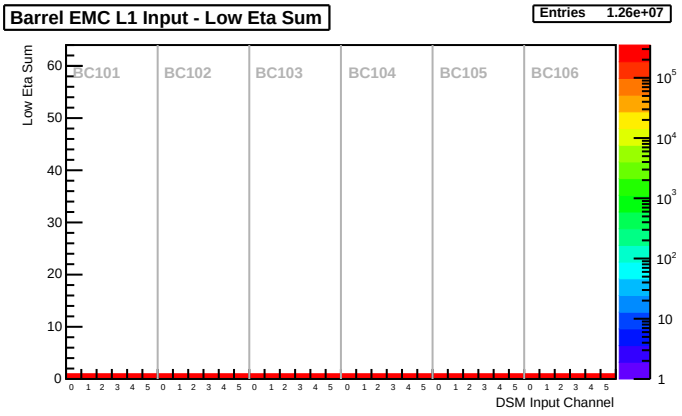
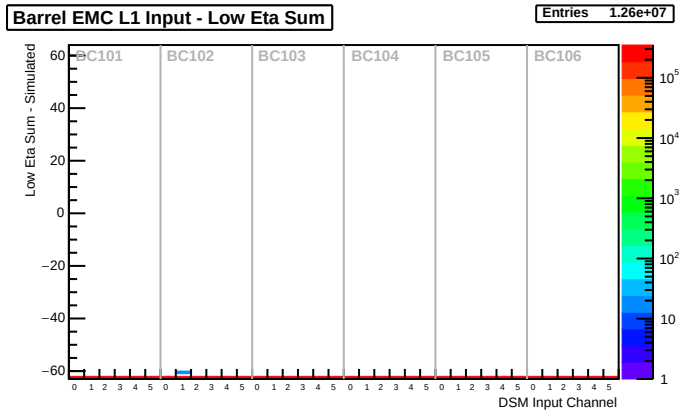

Barrel EMC L0 Input - High Tower
Entries 1.05e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

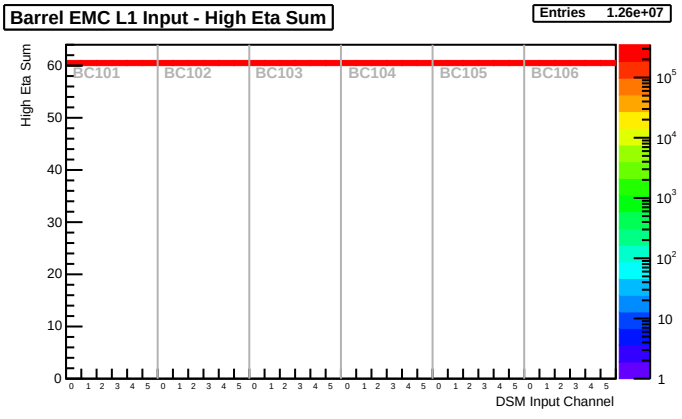

Barrel EMC L0 Input - Patch Sum
Entries 1.05e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

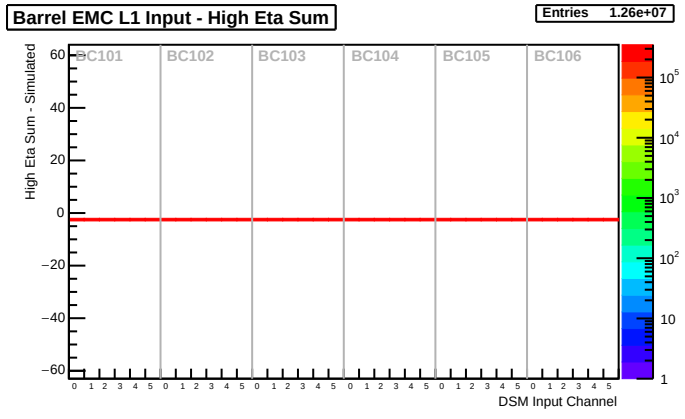

Endcap EMC L0 Input - High Tower
Entries 3.15e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

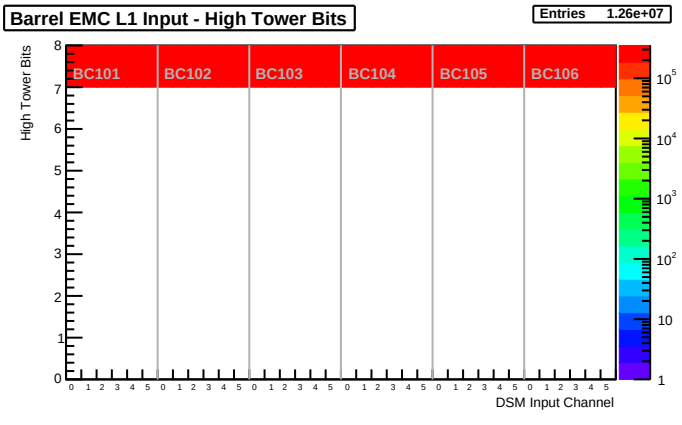

Endcap EMC L0 Input - Patch Sum
Entries 3.15e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

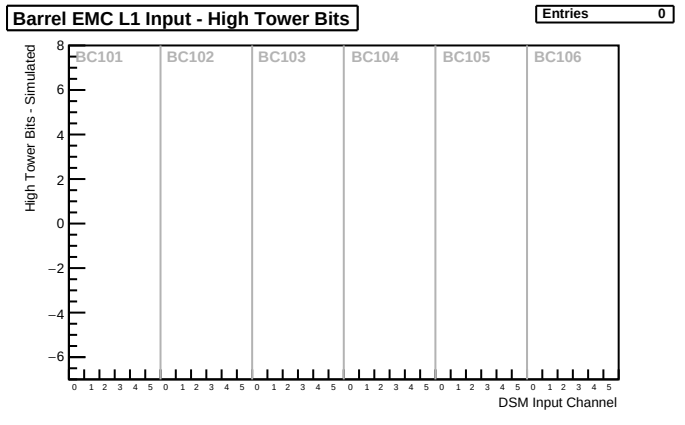

Barrel EMC L1 Input - Low Eta Sum
Entries 1.26e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

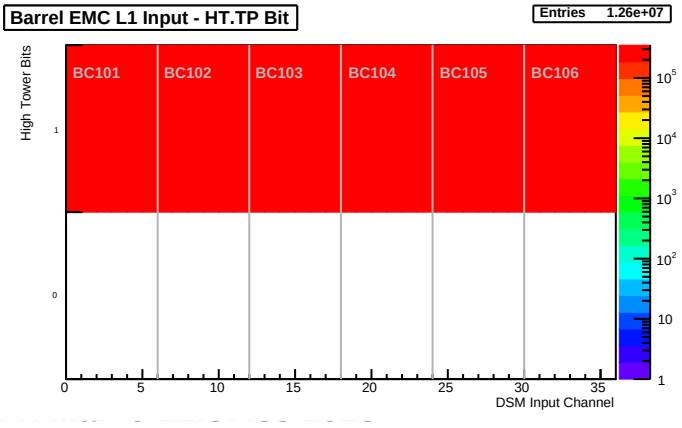

Barrel EMC L1 Input - Low Eta Sum
Entries 1.26e+07
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

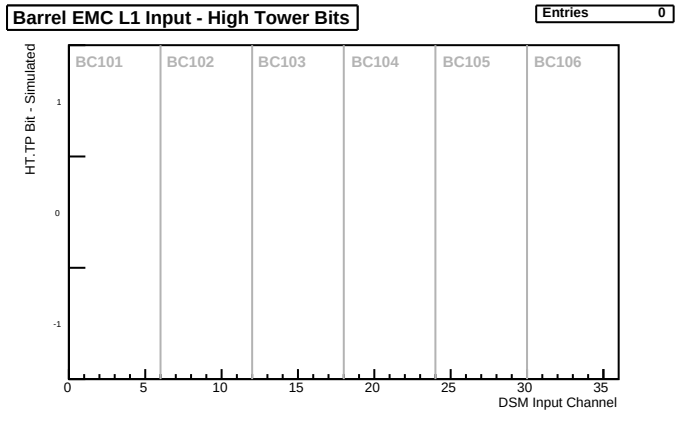

Barrel EMC L1 Input - High Eta Sum
Entries 1.26e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

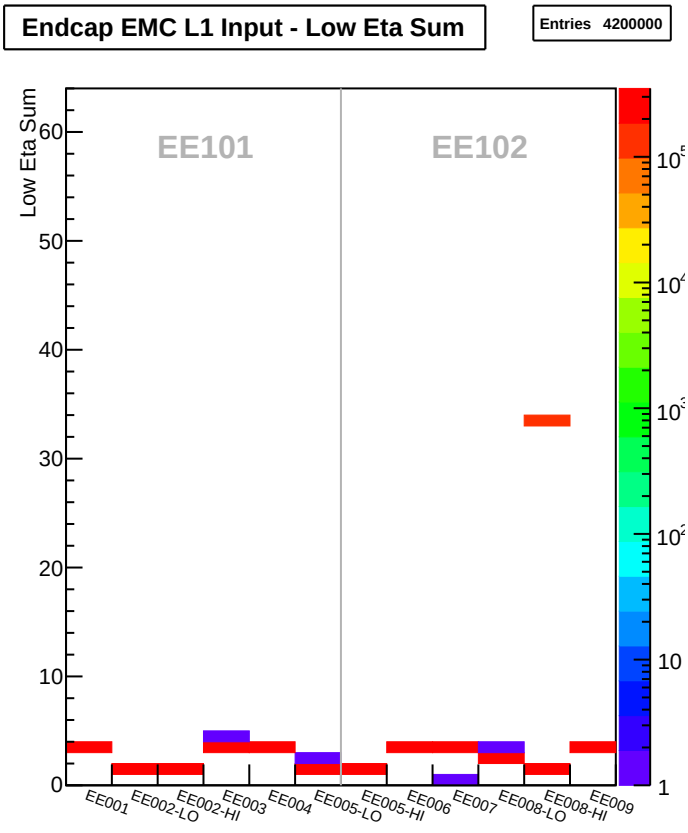

Barrel EMC L1 Input - High Eta Sum
Entries 1.26e+07
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

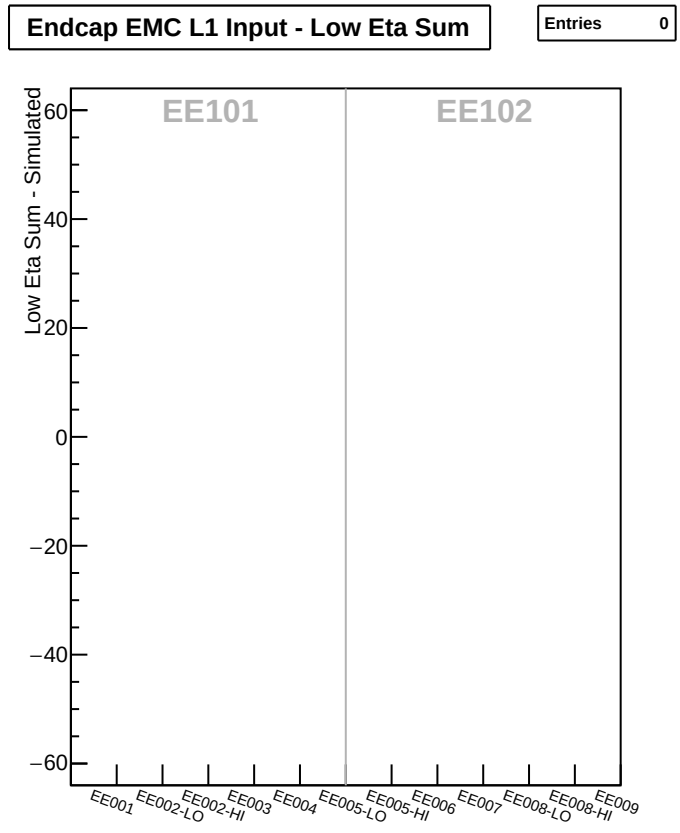

Barrel EMC L1 Input - High Tower Bits
Entries 1.26e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

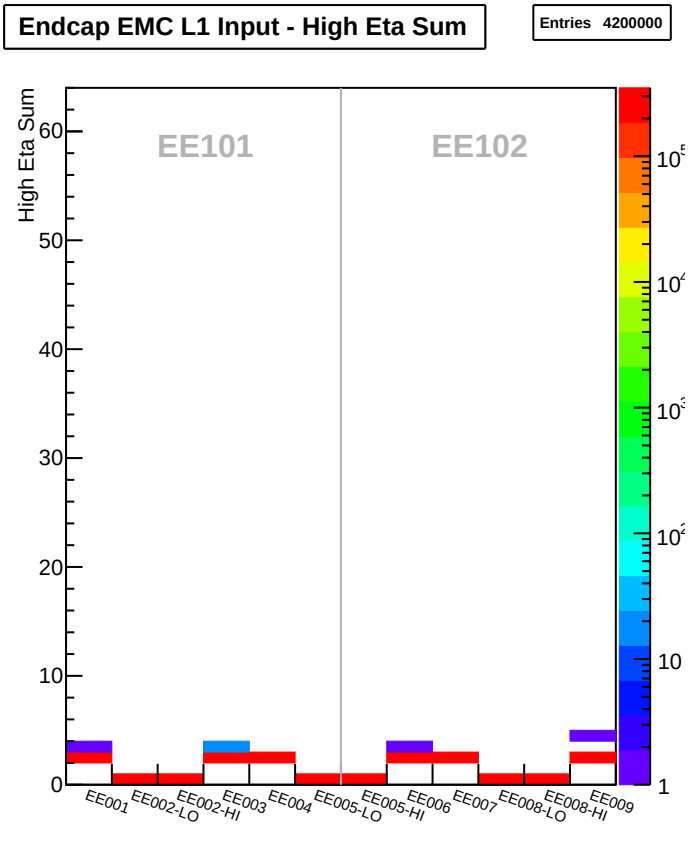

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

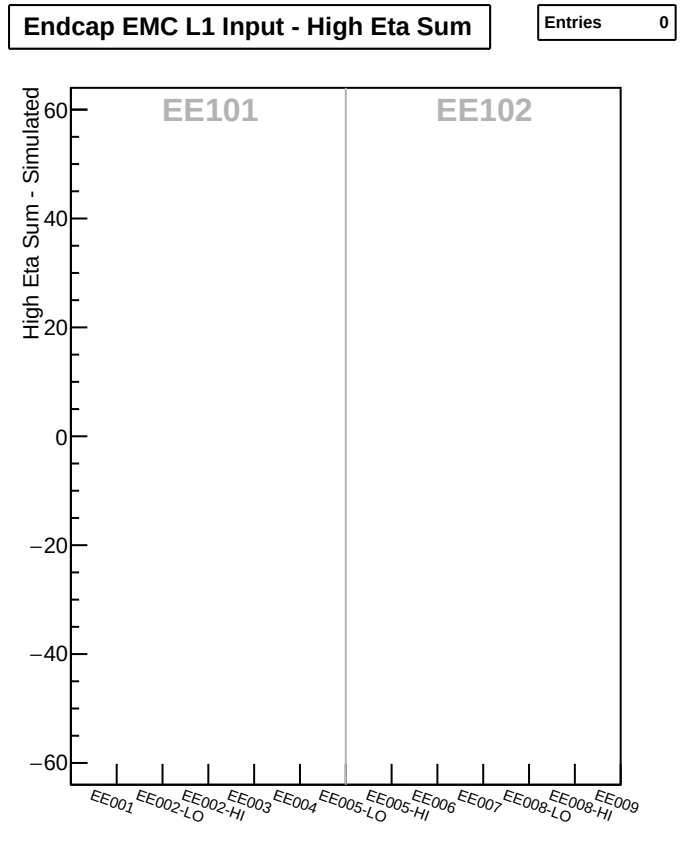

Barrel EMC L1 Input - HT.TP Bit
Entries 1.26e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

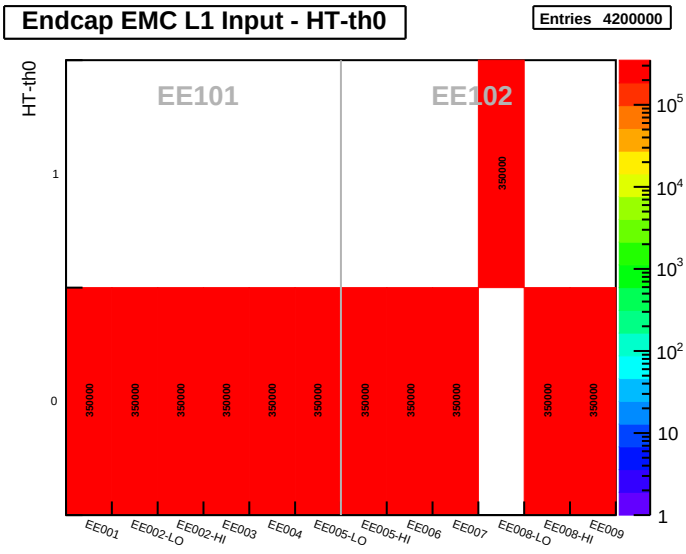

Barrel EMC L1 Input - High Tower Bits
Entries 0
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

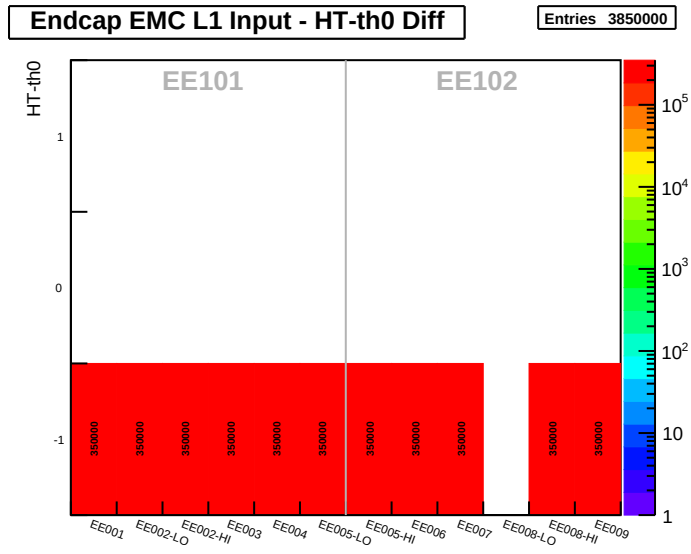

Endcap EMC L1 Input - Low Eta Sum
Entries 4200000
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

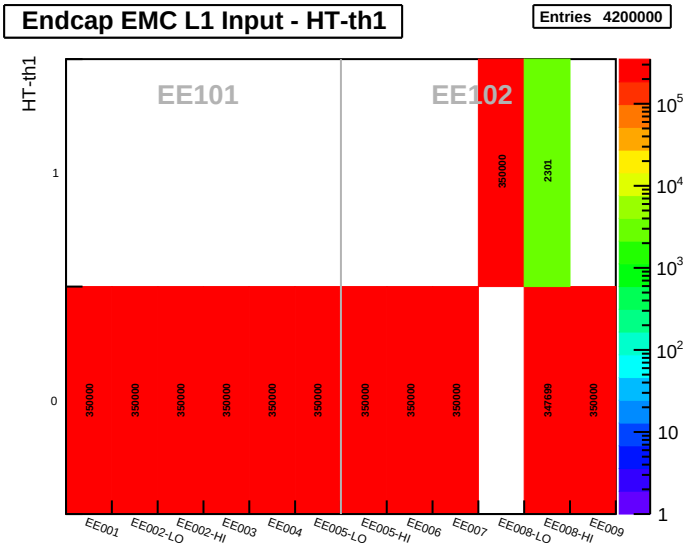

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

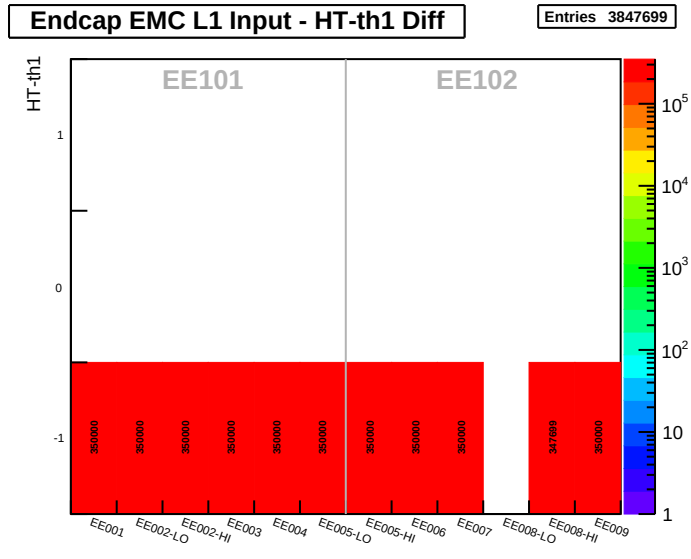

Endcap EMC L1 Input - High Eta Sum
Entries 4200000
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

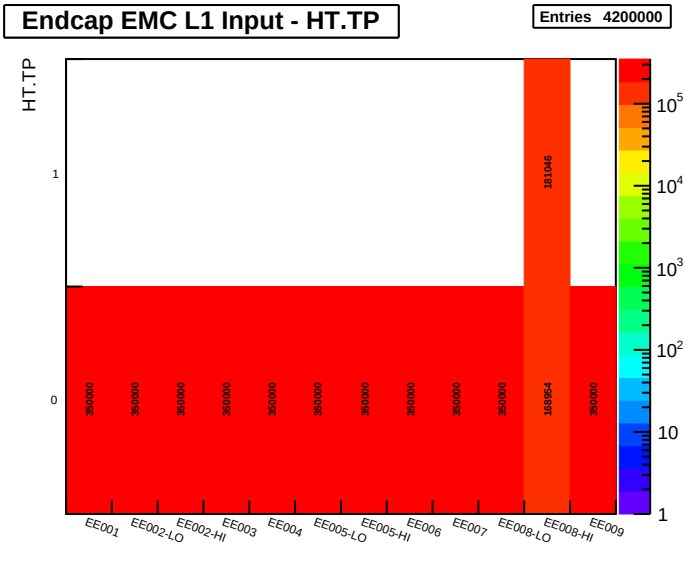

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

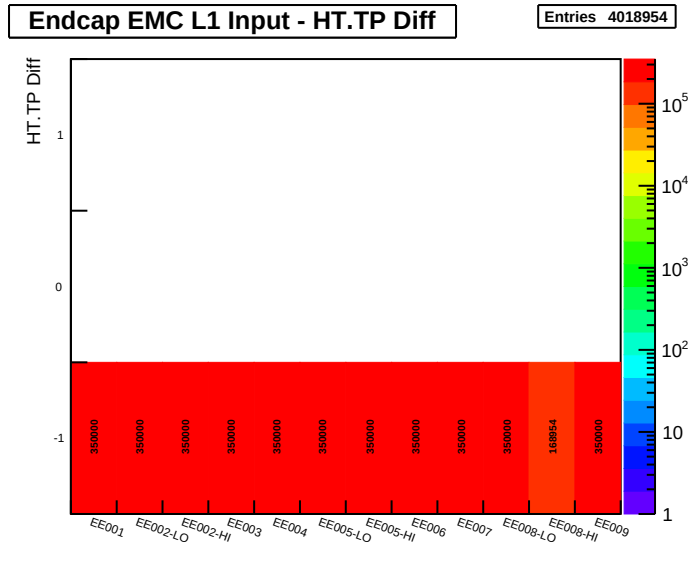

Endcap EMC L1 Input - HT-th0
Entries 4200000
HT-th0
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

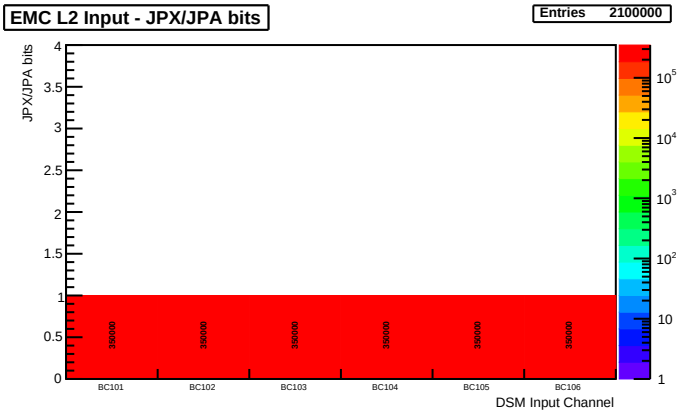

Endcap EMC L1 Input - HT-th0 Diff
Entries 3850000
HT-th0
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

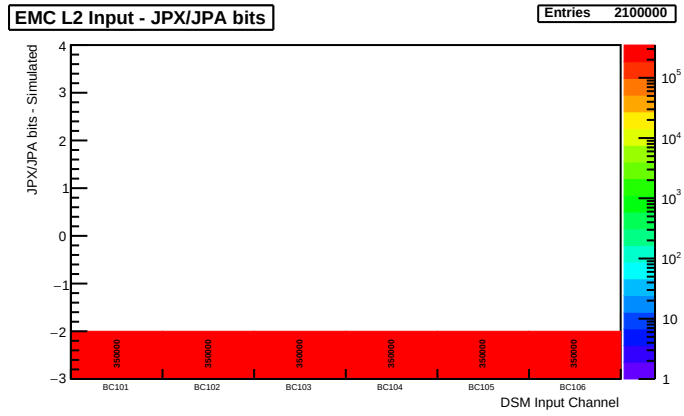

Endcap EMC L1 Input - HT-th1
Entries 4200000
HT-th1
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

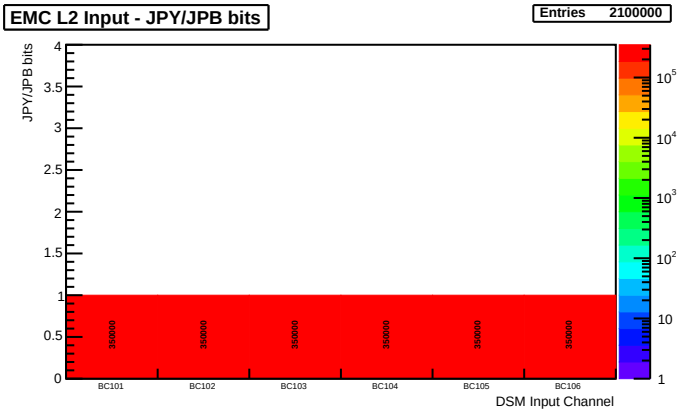

Endcap EMC L1 Input - HT-th1 Diff
Entries 3847699
HT-th1
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

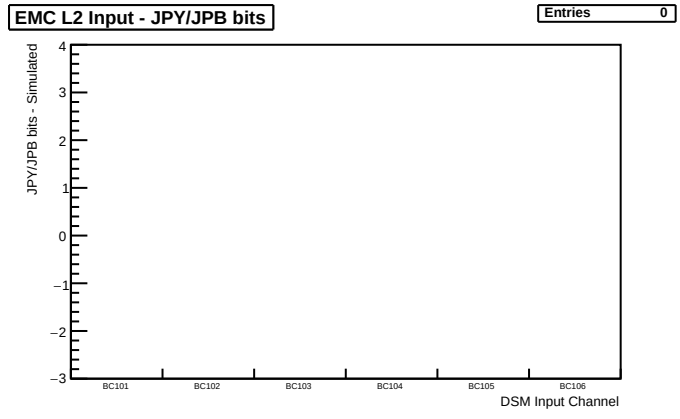

Endcap EMC L1 Input - HT.TP
Entries 4200000
HT.TP
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

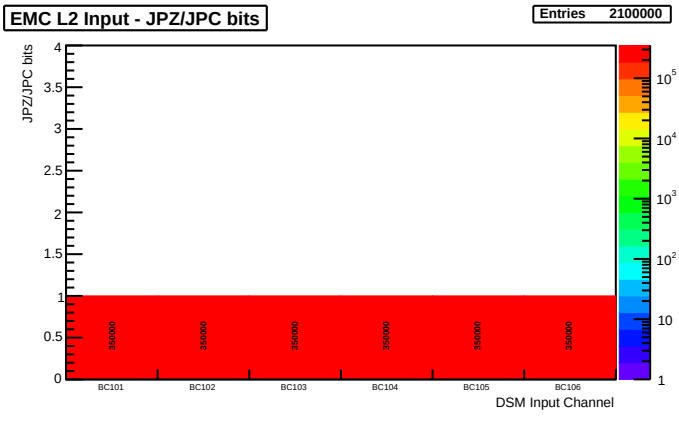

Endcap EMC L1 Input - HT.TP Diff
Entries 4018954
HT.TP Diff
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

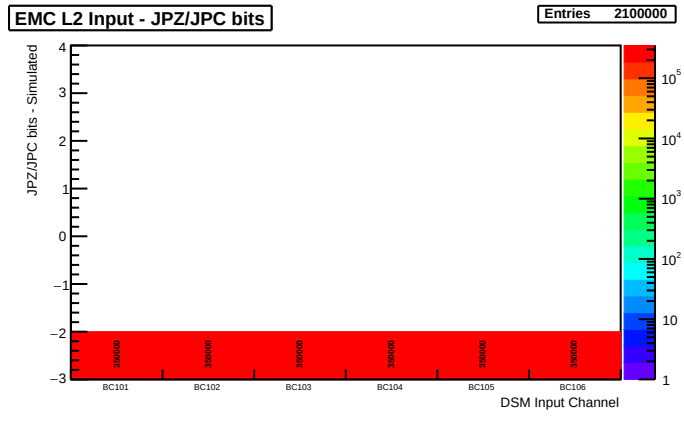

EMC L2 Input - JPX/JPA bits
Entries 2100000
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
350000

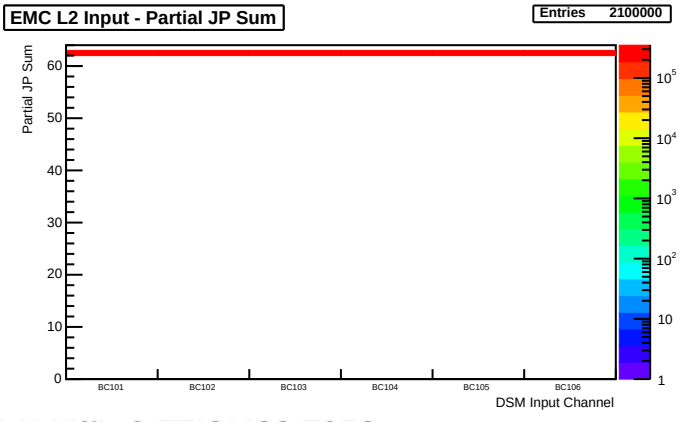

EMC L2 Input - JPX/JPA bits
Entries 2100000
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
350000

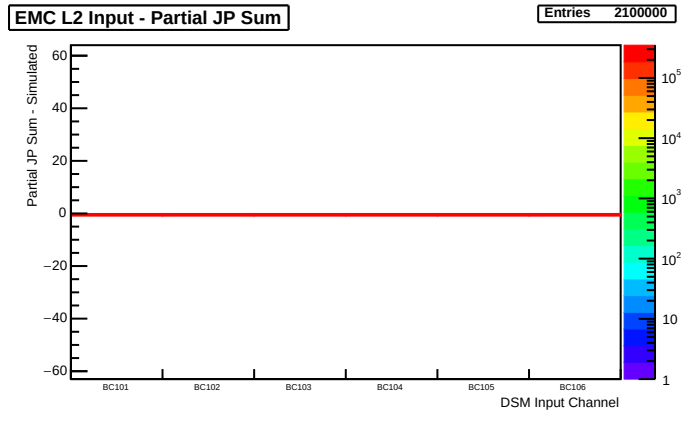

EMC L2 Input - JPY/JPB bits
Entries 2100000
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
350000

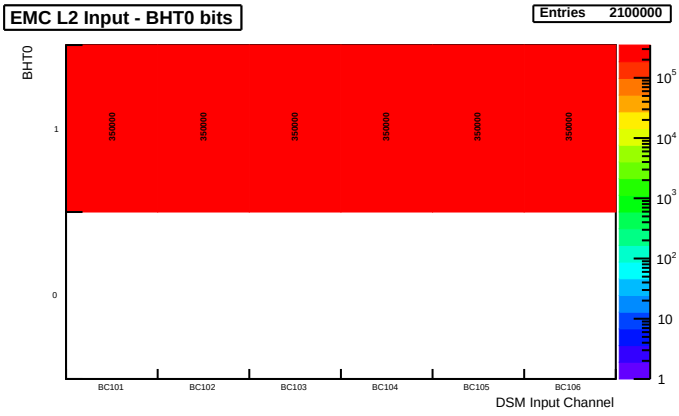

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

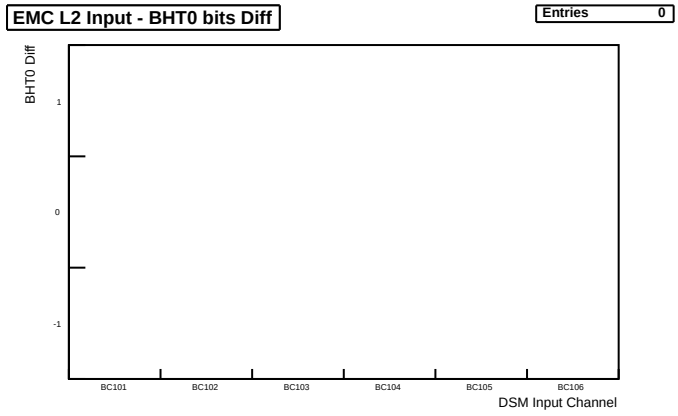

EMC L2 Input - JPZ/JPC bits
Entries 2100000
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
350000

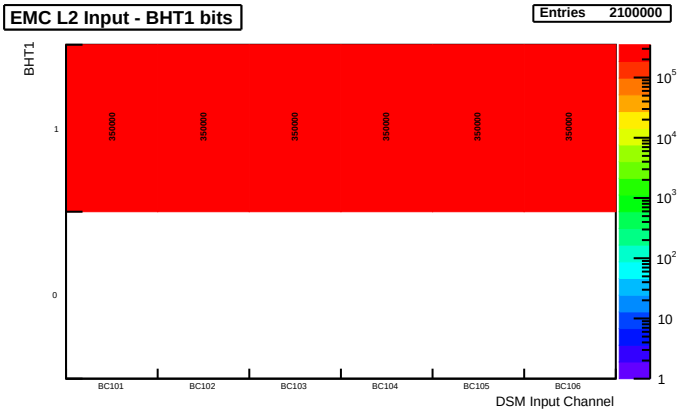

EMC L2 Input - JPZ/JPC bits
Entries 2100000
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
350000

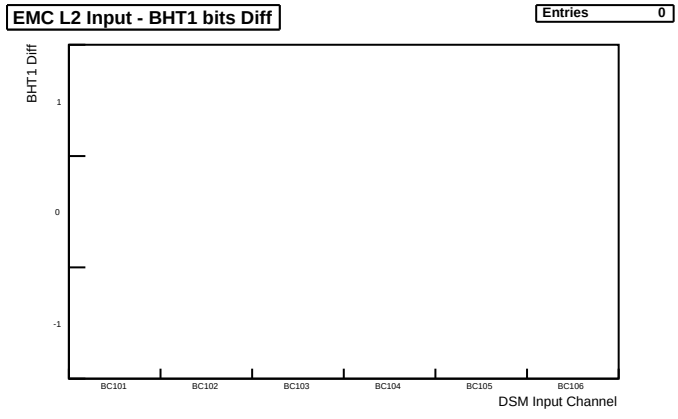

EMC L2 Input - Partial JP Sum
Entries 2100000
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

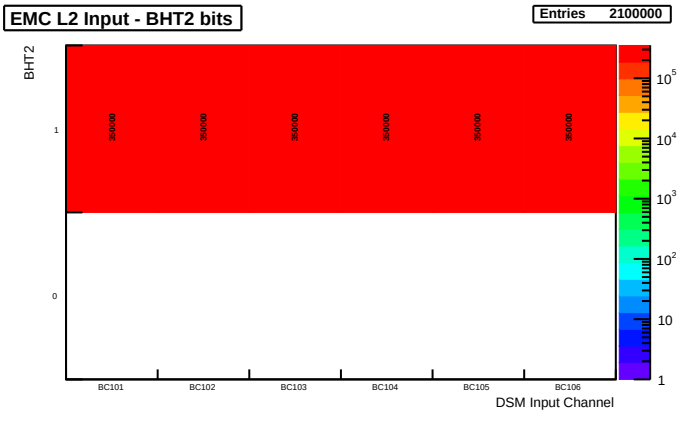

EMC L2 Input - Partial JP Sum
Entries 2100000
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

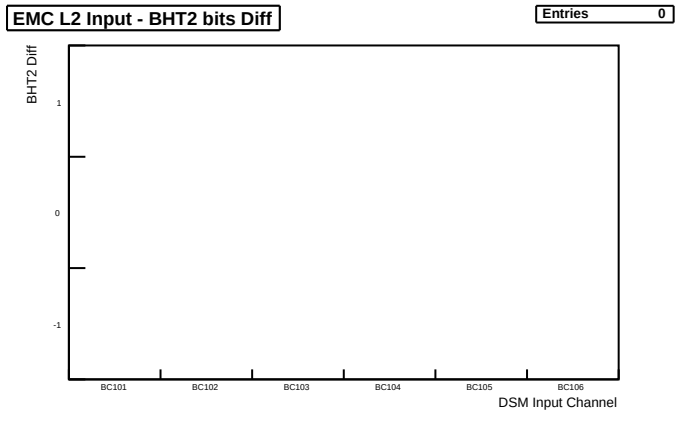

EMC L2 Input - BHT0 bits
Entries 2100000
BHT0
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
350000

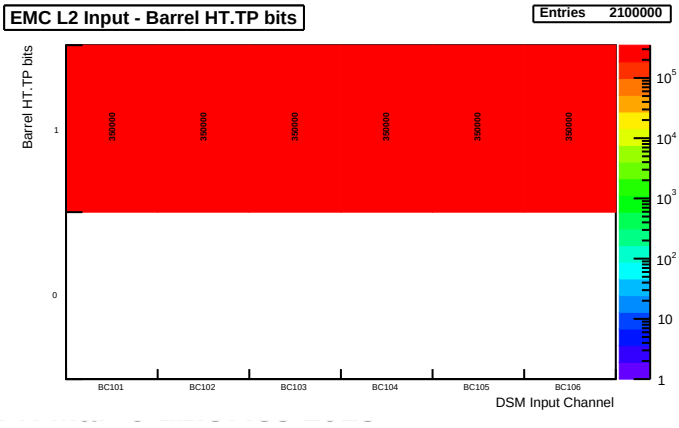

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

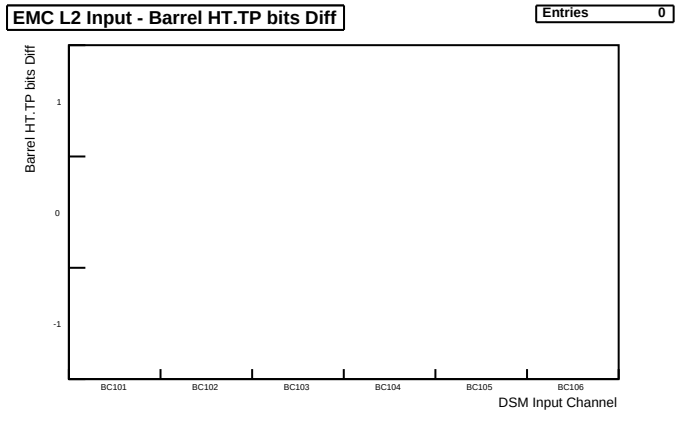

EMC L2 Input - BHT1 bits
Entries 2100000
BHT1
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
350000

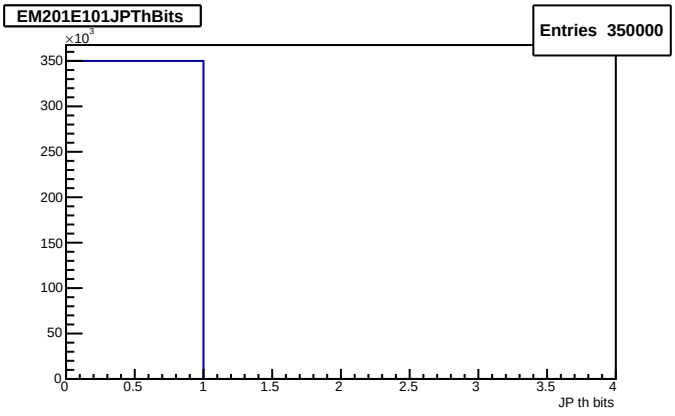

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

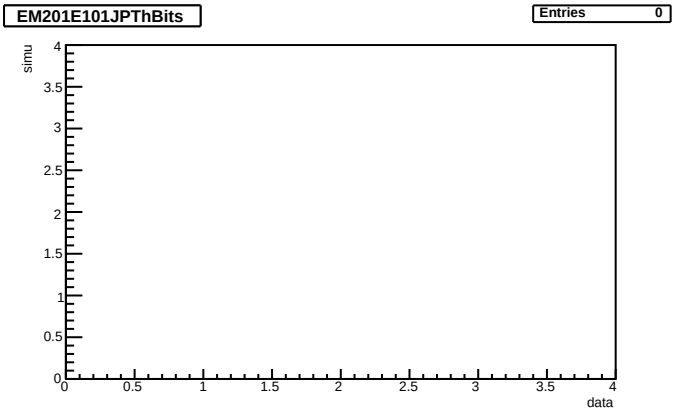

EMC L2 Input - BHT2 bits
Entries 2100000
BHT2
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
350000

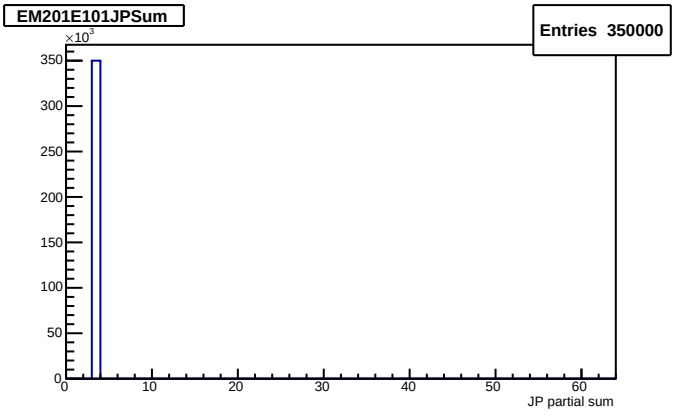

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

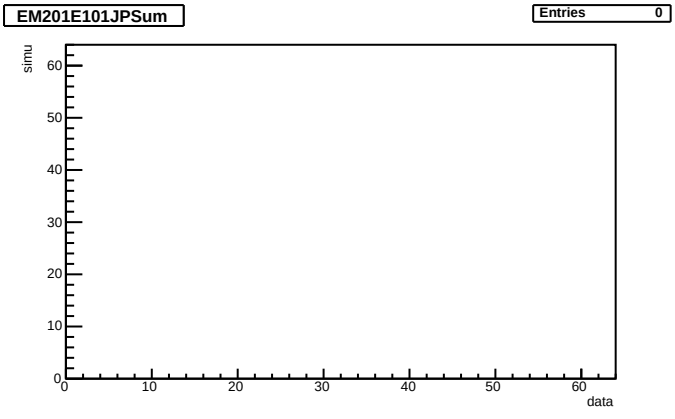

EMC L2 Input - Barrel HT.TP bits
Entries 2100000
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
350000

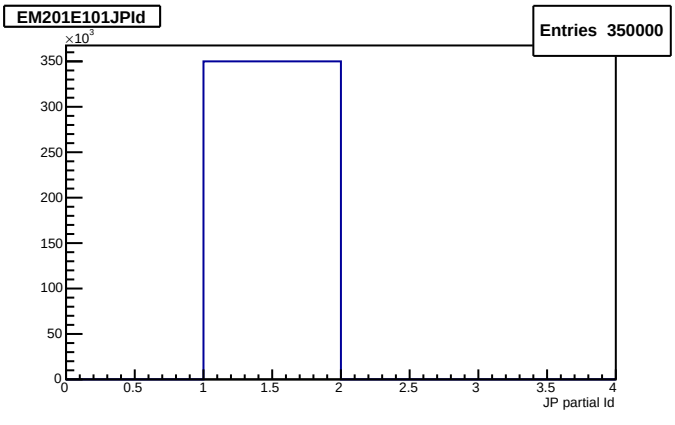

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

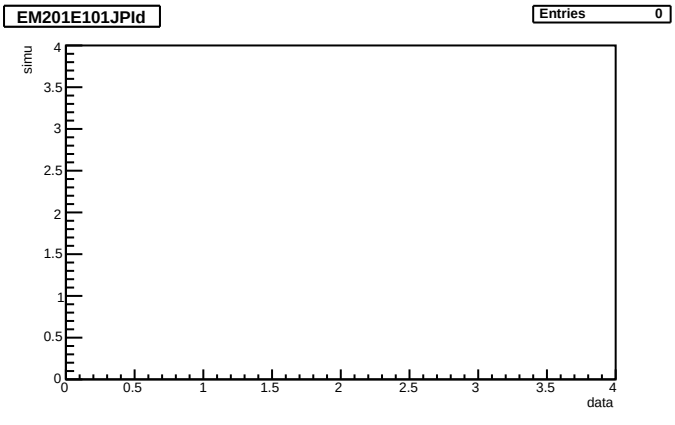

EM201E101JPThBits
Entries 350000
JP th bits

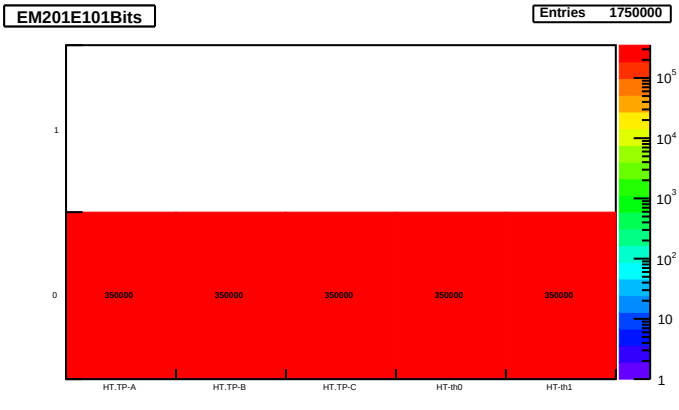

EM201E101JPThBits
Entries 0
simu
data

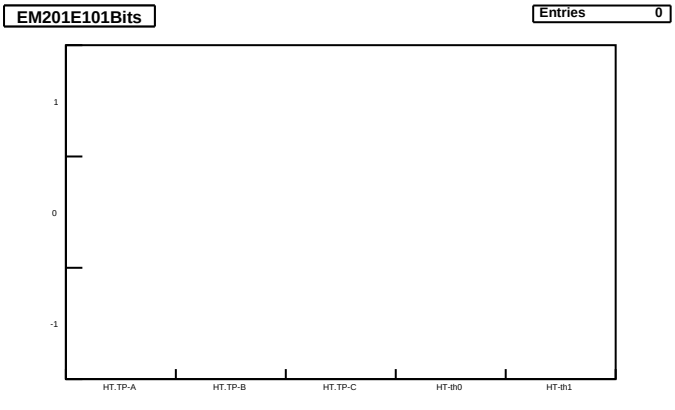

EM201E101JPSum
Entries 350000
JP partial sum

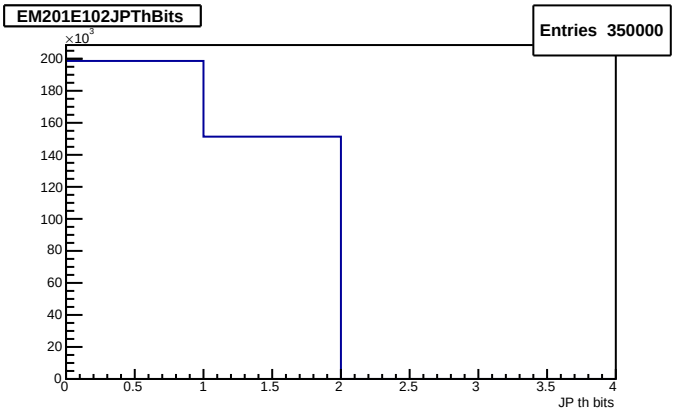

EM201E101JPSum
Entries 0
simu
data

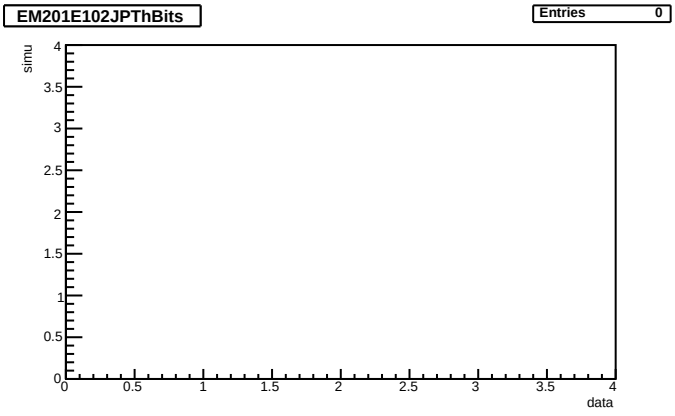

EM201E101JPId
Entries 350000
JP partial Id

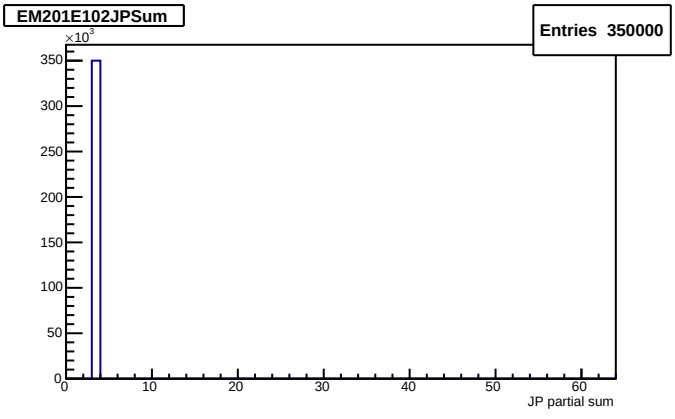

EM201E101JPId
Entries 0
simu
data

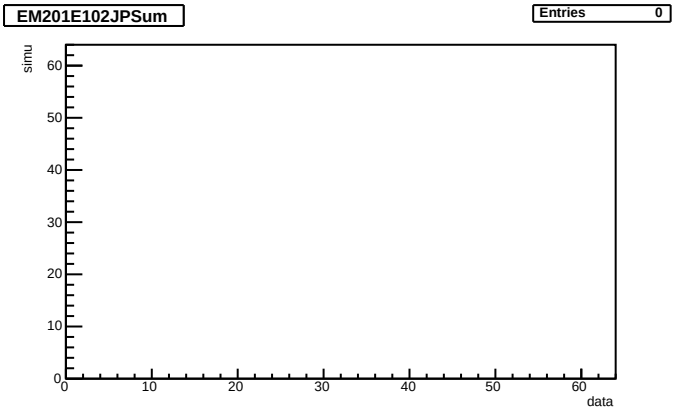

EM201E101Bits
Entries 1750000
350000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

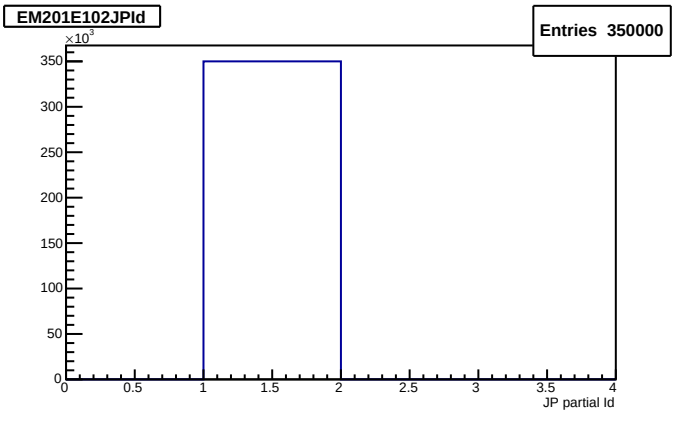

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

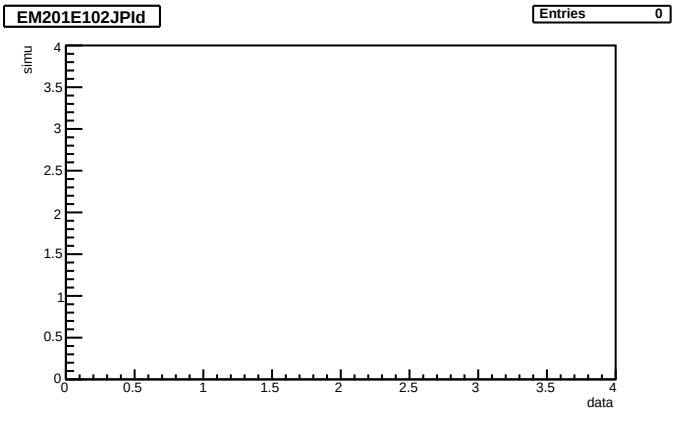

EM201E102JPThBits
Entries 350000
JP th bits

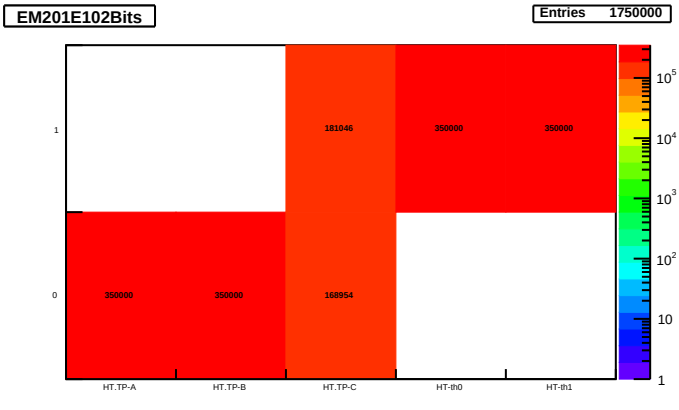

EM201E102JPThBits
Entries 0
simu
data

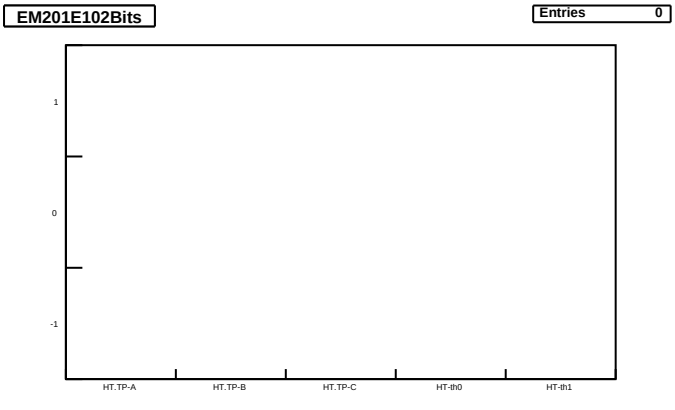

EM201E102JPSum
Entries 350000
JP partial sum

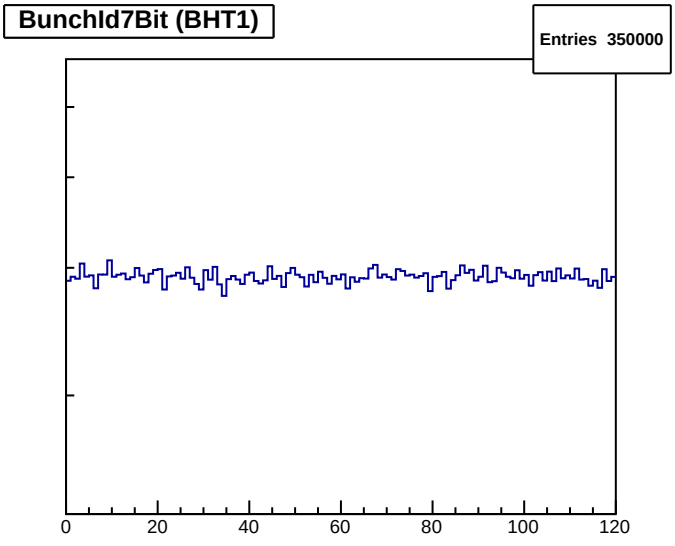

EM201E102JPSum
Entries 0
simu
data

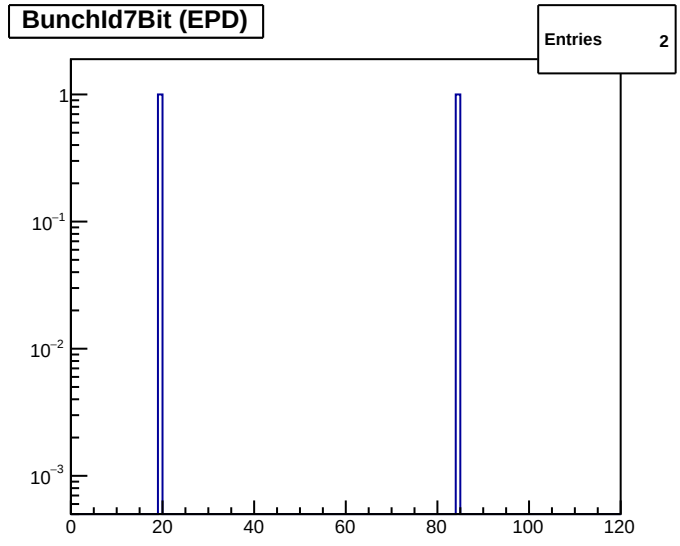

EM201E102JPId
Entries 350000
JP partial Id

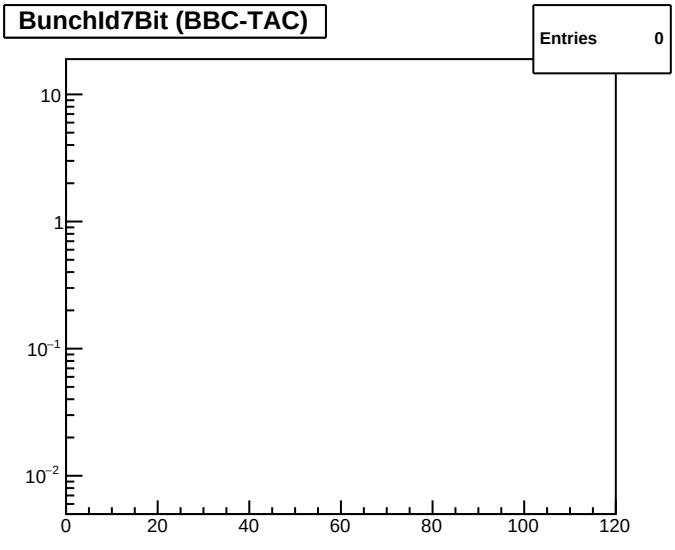

EM201E102JPId
Entries 0
simu
data

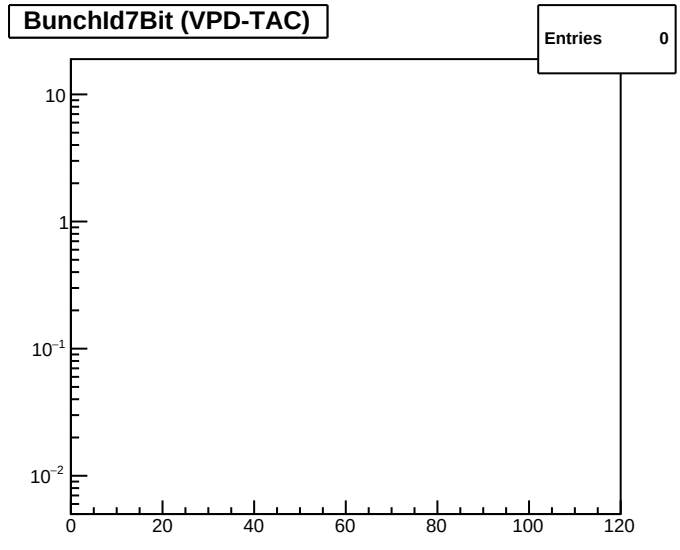

EM201E102Bits
Entries 1750000
181046
350000
350000
350000
350000
168954
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

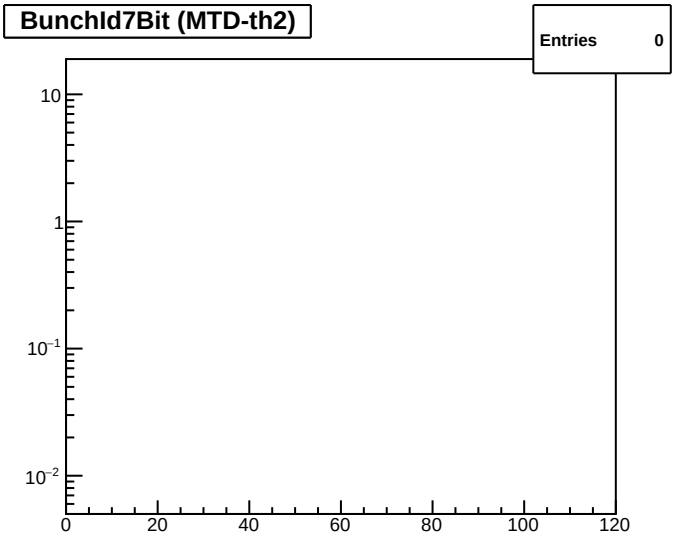

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

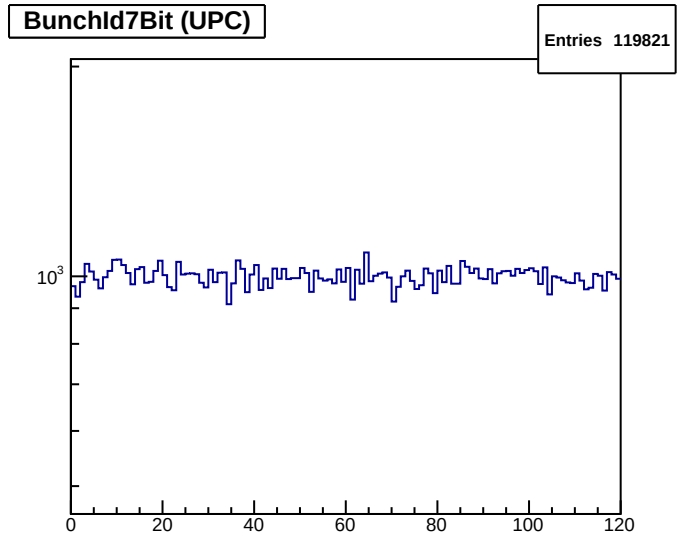

BunchId7Bit (BHT1)
Entries 350000
0
20
40
60
80
100
120

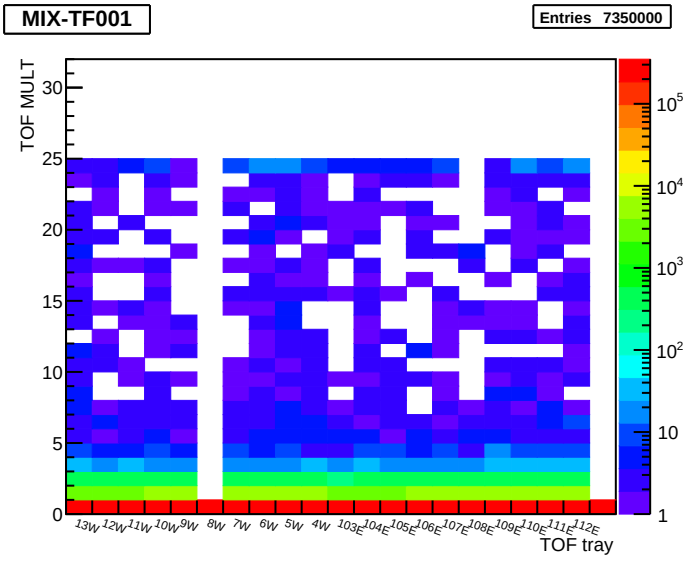

BunchId7Bit (EPD)
Entries 2
1
$10^{-1}$
$10^{-2}$
$10^{-3}$
0
20
40
60
80
100
120

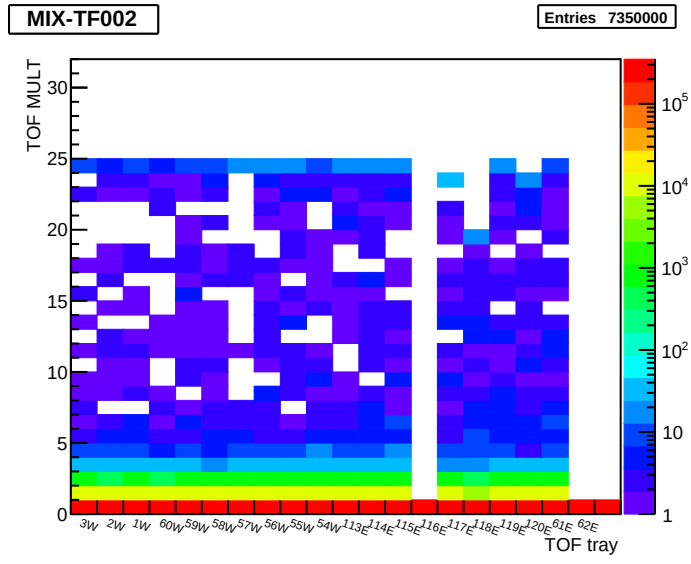

BunchId7Bit (BBC-TAC)
Entries 0
10
1
$10^{-1}$
$10^{-2}$
0
20
40
60
80
100
120

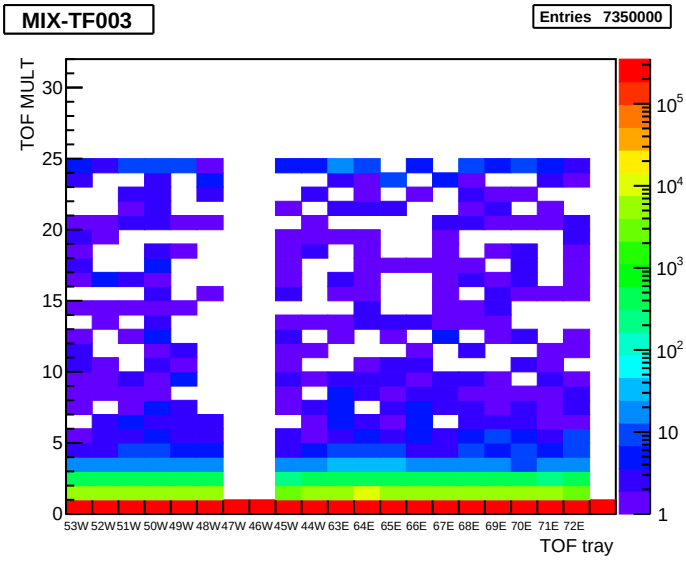

BunchId7Bit (VPD-TAC)
Entries 0
10
1
$10^{-1}$
$10^{-2}$
0
20
40
60
80
100
120

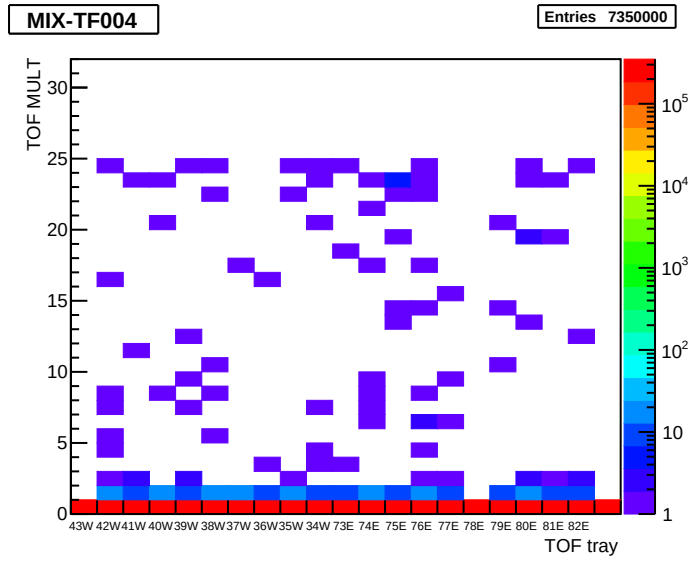

BunchId7Bit (MTD-th2)
Entries 0
10
1
$10^{-1}$
$10^{-2}$
0
20
40
60
80
100
120

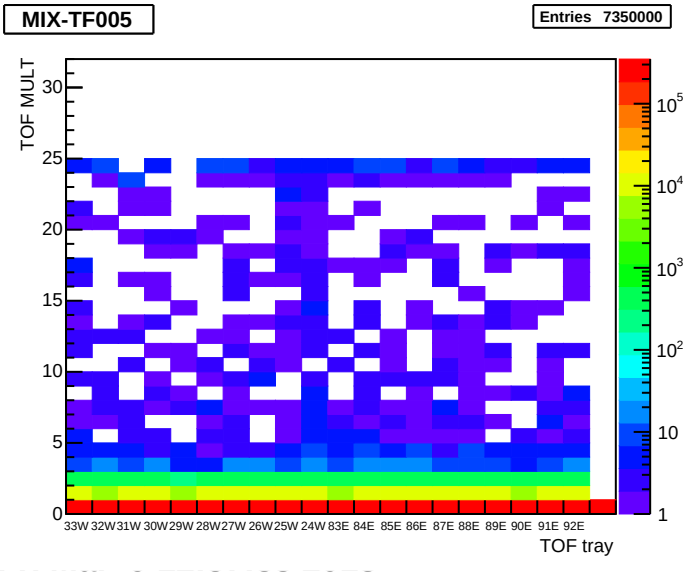

BunchId7Bit (UPC)
Entries 119821
$10^{3}$
0
20
40
60
80
100
120

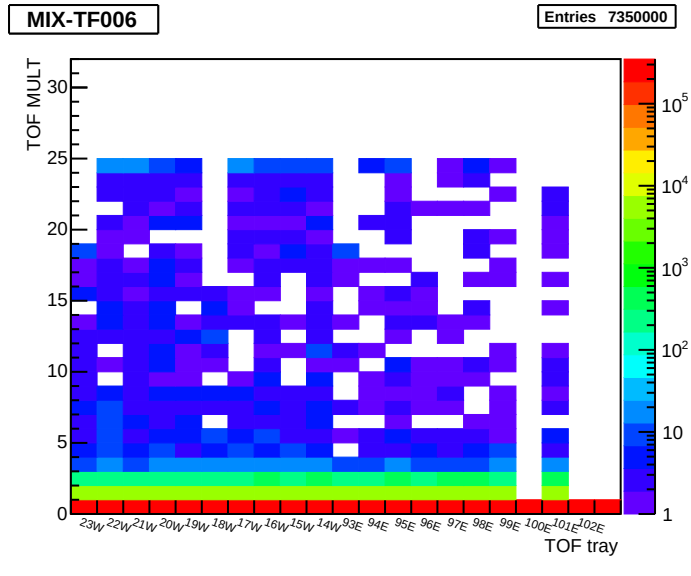

MIX-TF001
Entries 7350000
TOF MULT
TOF tray

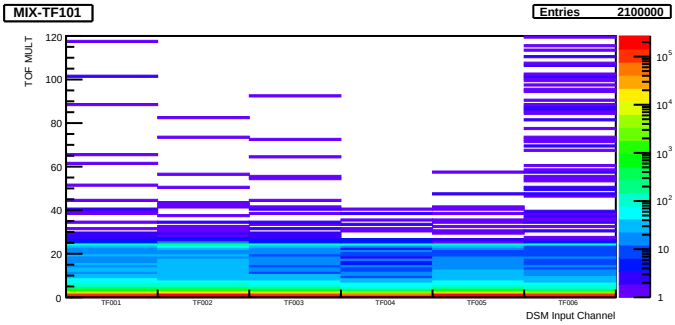

MIX-TF002
Entries 7350000
TOF MULT
TOF tray

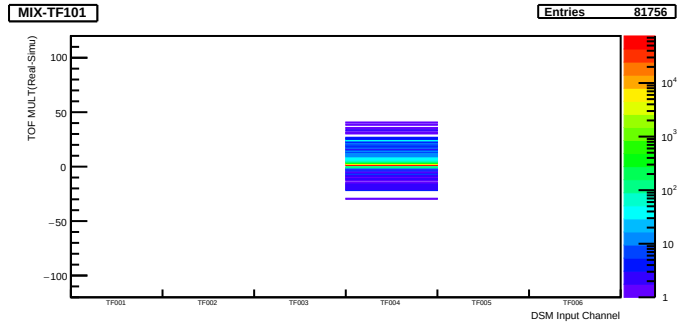

MIX-TF003
Entries 7350000
TOF MULT
TOF tray

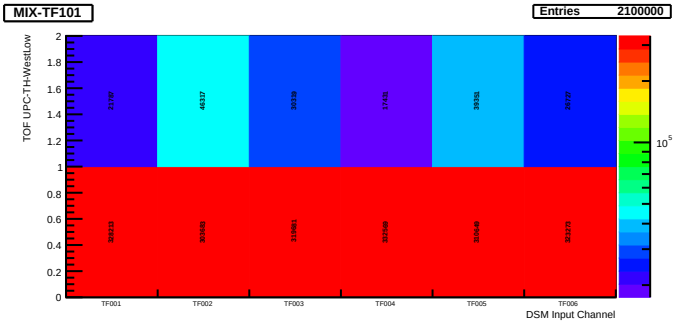

MIX-TF004
Entries 7350000
TOF MULT
TOF tray

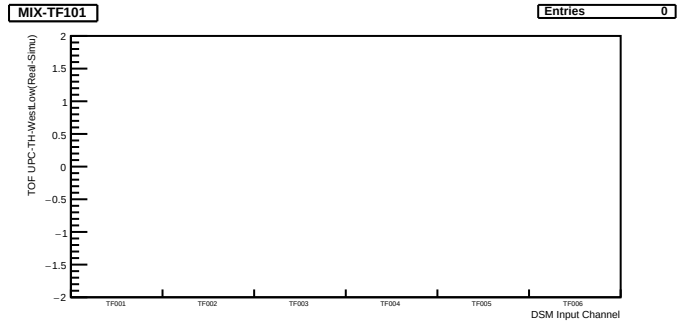

MIX-TF005
Entries 7350000
TOF MULT
TOF tray

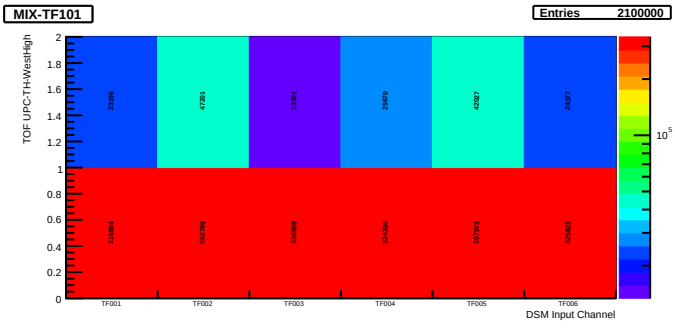

MIX-TF006
Entries 7350000
TOF MULT
TOF tray

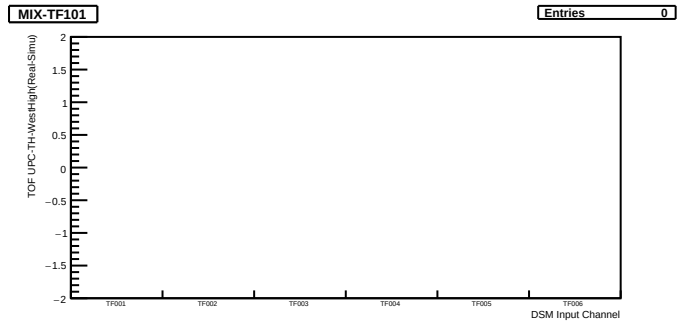

MIX-TF101
Entries 2100000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

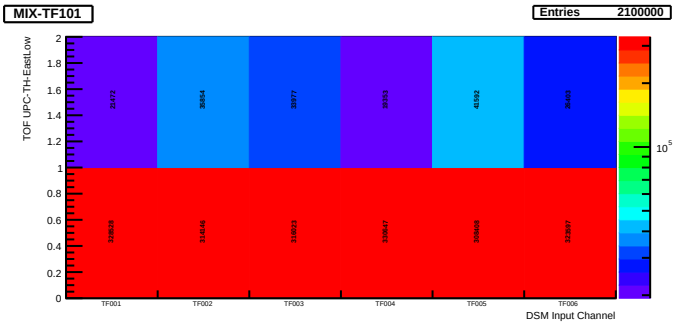

MIX-TF101
Entries 81756
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

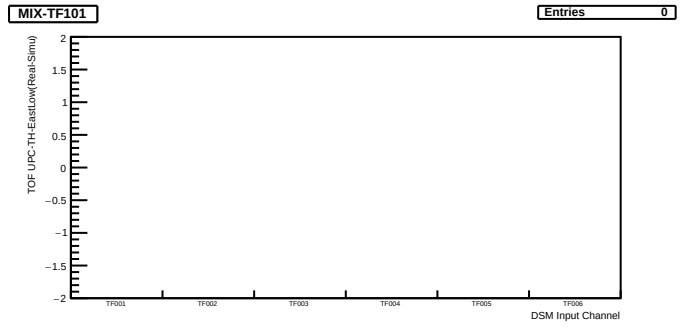

MIX-TF101
Entries 2100000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

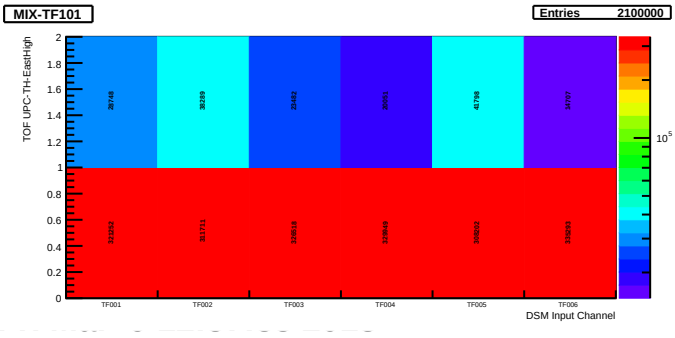

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

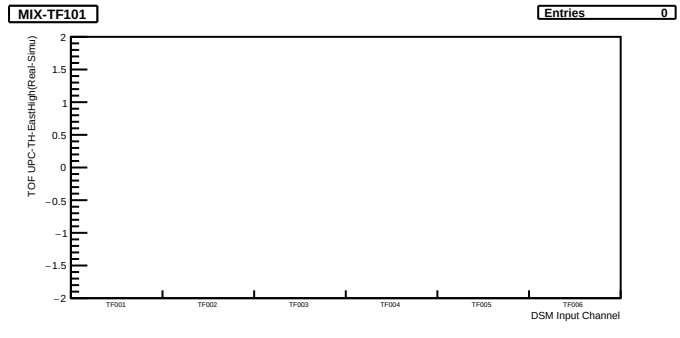

MIX-TF101
Entries 2100000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

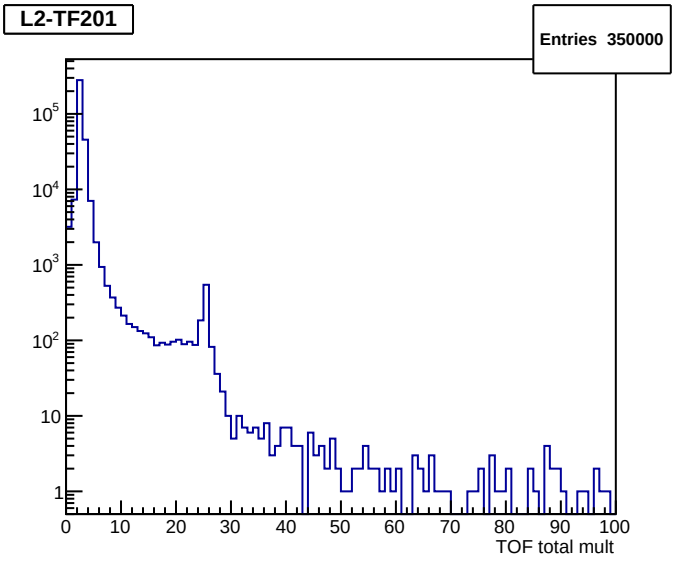

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

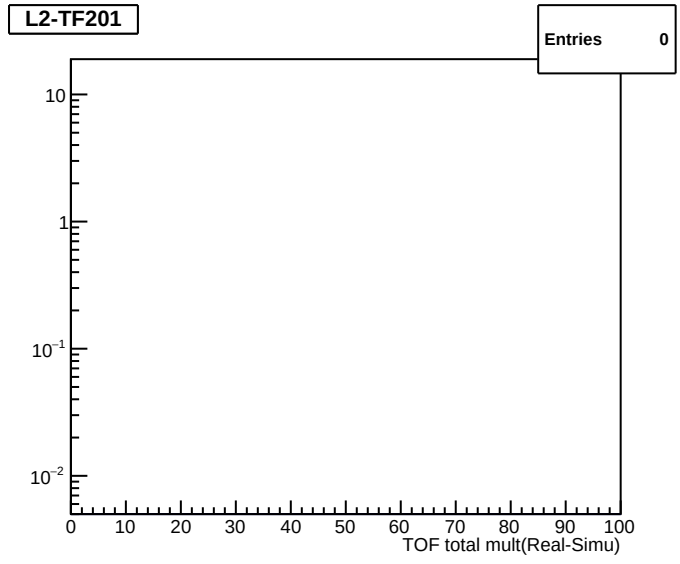

MIX-TF101
Entries 2100000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

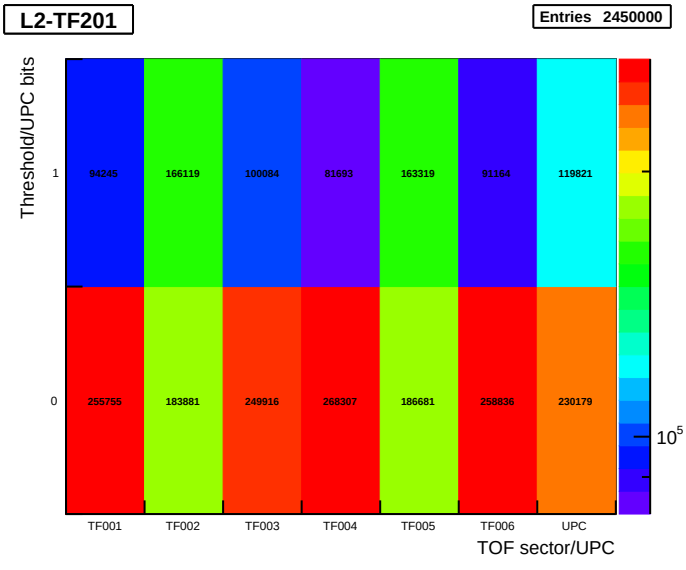

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

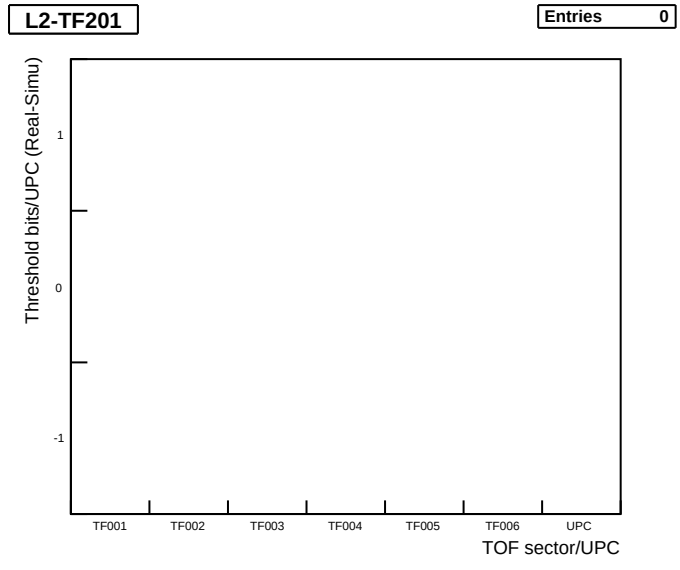

MIX-TF101
Entries 2100000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

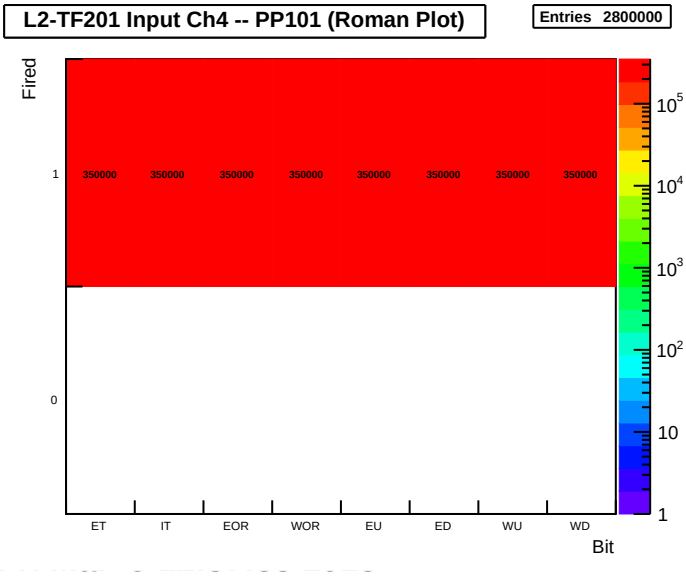

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

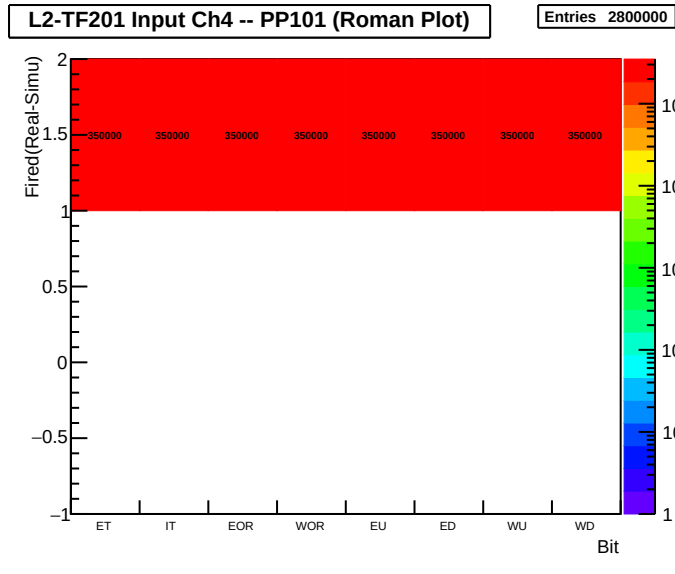

L2-TF201
Entries 350000
TOF total mult

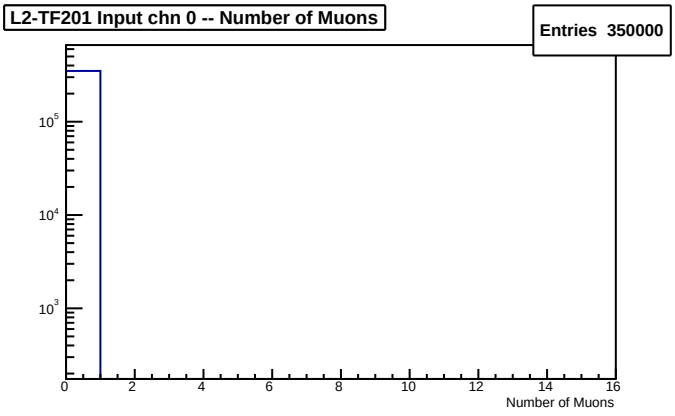

L2-TF201
Entries 0
TOF total mult(Real-Simu)

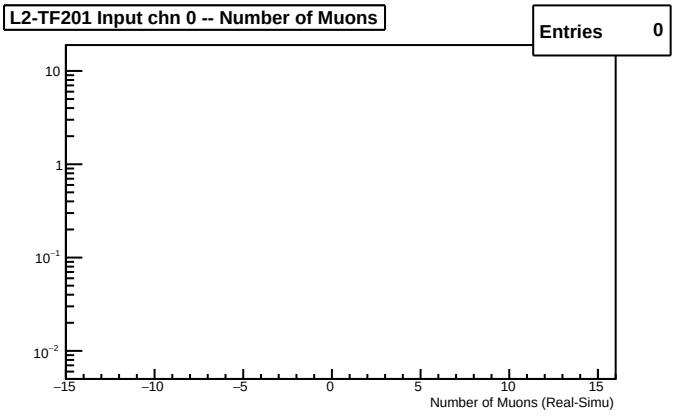

L2-TF201
Entries 2450000
Threshold/UPC bits
94245
166119
100084
81693
163319
91164
119821
255755
183881
249916
268307
186681
258836
230179
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

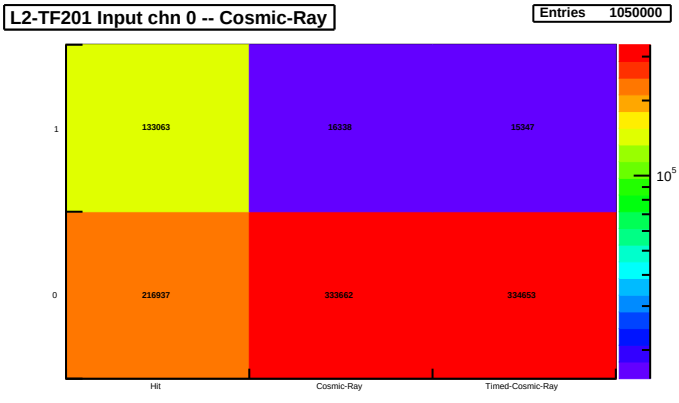

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

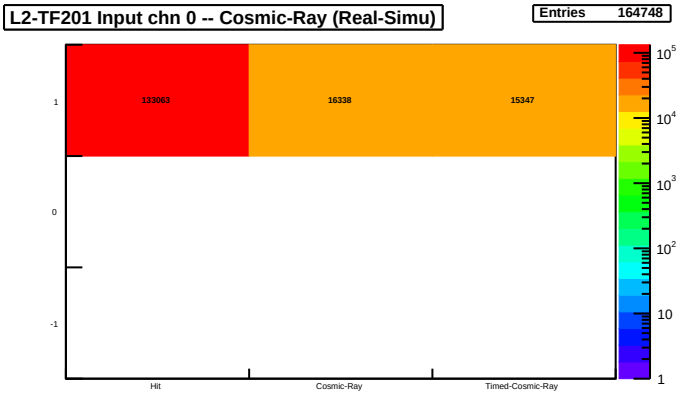

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 2800000
Fired
350000
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

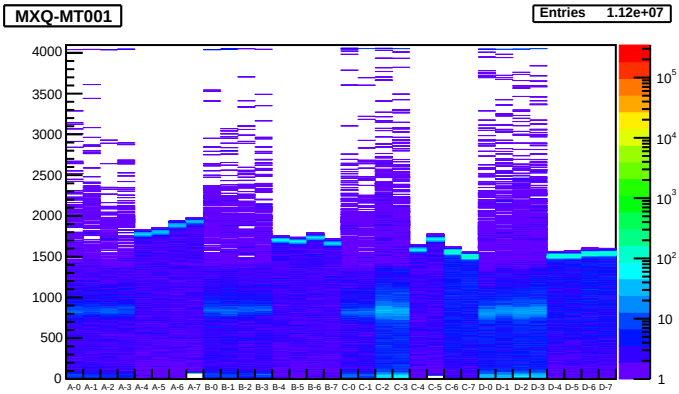

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 2800000
Fired(Real-Simu)
350000
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

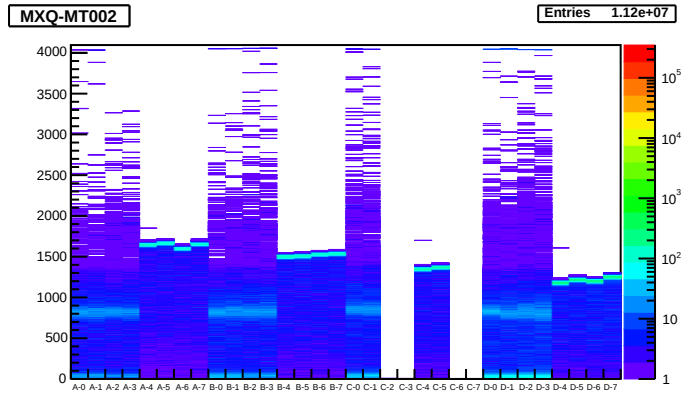

L2-TF201 Input chn 0 -- Number of Muons
Entries 350000
Number of Muons

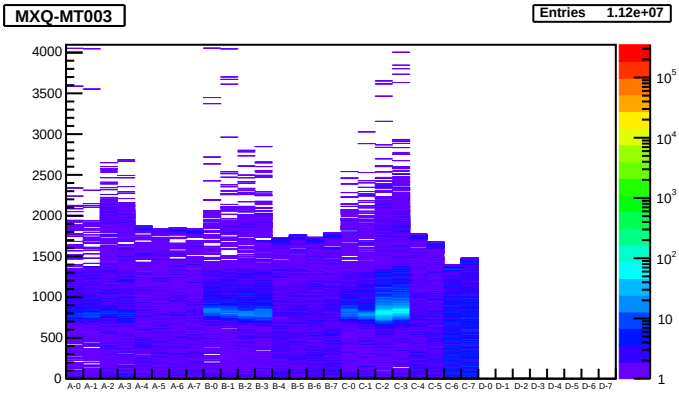

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

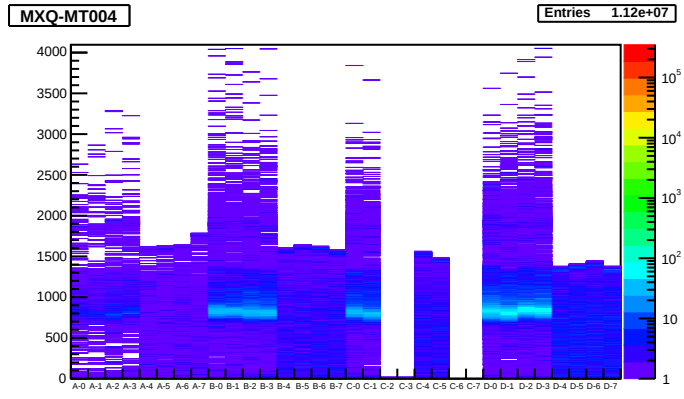

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 1050000
133063
16338
15347
216937
333662
334653
Hit
Cosmic-Ray
Timed-Cosmic-Ray

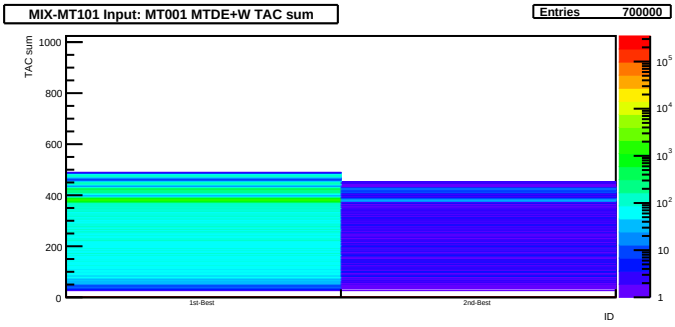

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 164748
133063
16338
15347
Hit
Cosmic-Ray
Timed-Cosmic-Ray

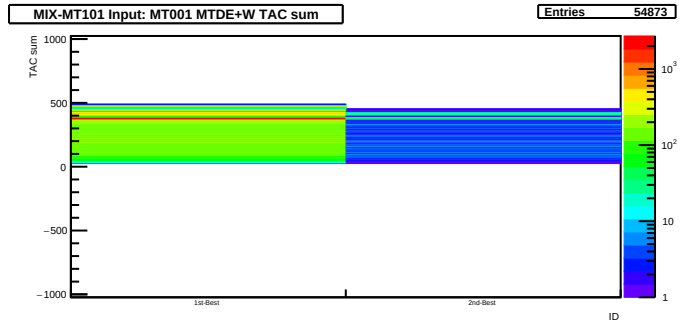

MXQ-MT001
Entries 1.12e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
$10^5$
$10^4$
$10^3$
$10^2$
10
1

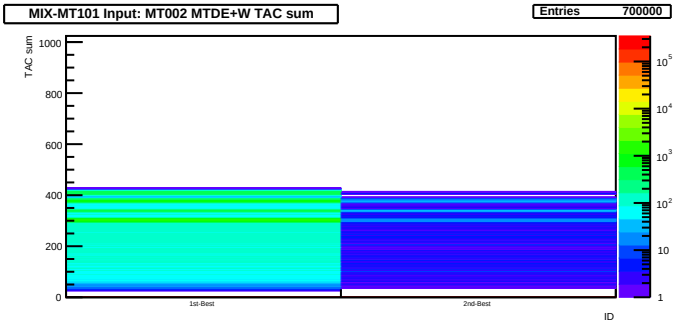

MXQ-MT002
Entries 1.12e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
$10^5$
$10^4$
$10^3$
$10^2$
10
1

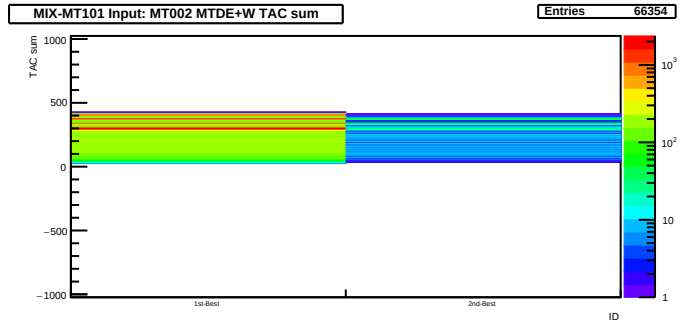

MXQ-MT003
Entries 1.12e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
$10^5$
$10^4$
$10^3$
$10^2$
10
1

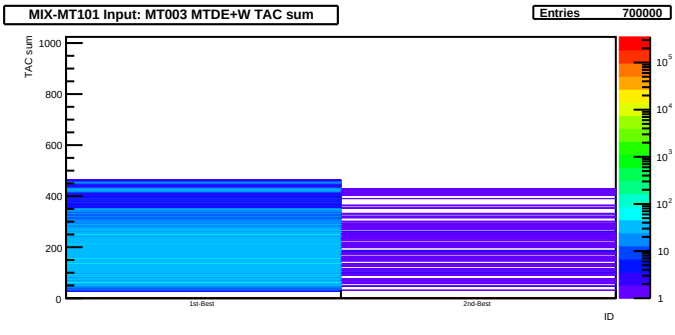

MXQ-MT004
Entries 1.12e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
$10^5$
$10^4$
$10^3$
$10^2$
10
1

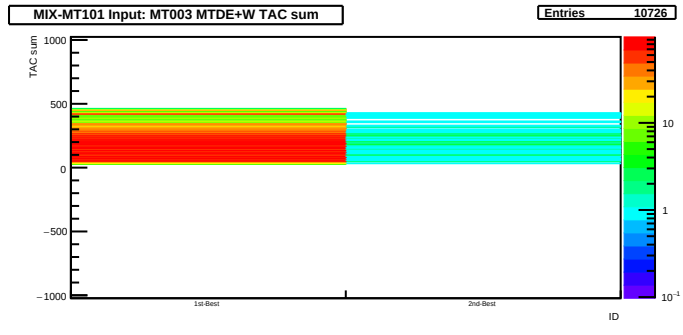

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 700000
TAC sum
1st-Best
2nd-Best
ID

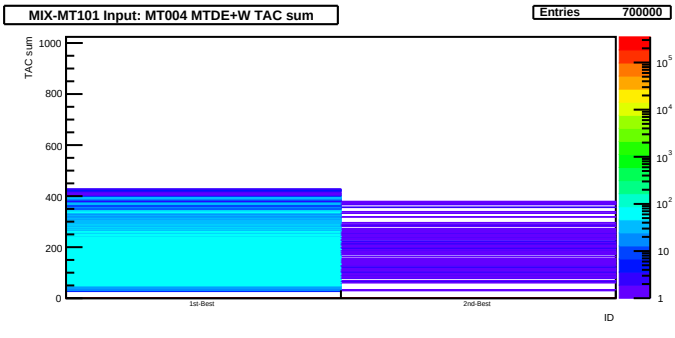

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 54873
TAC sum
1st-Best
2nd-Best
ID

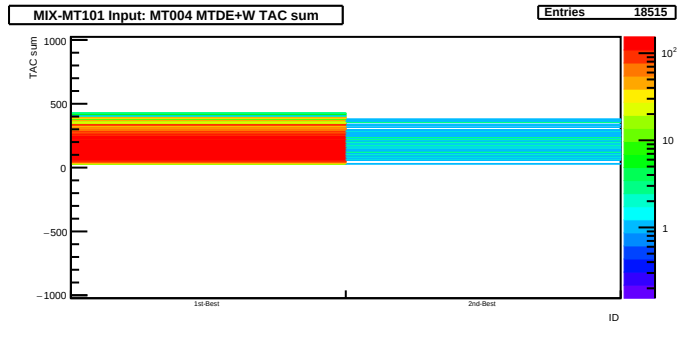

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 700000
TAC sum
1st-Best
2nd-Best
ID

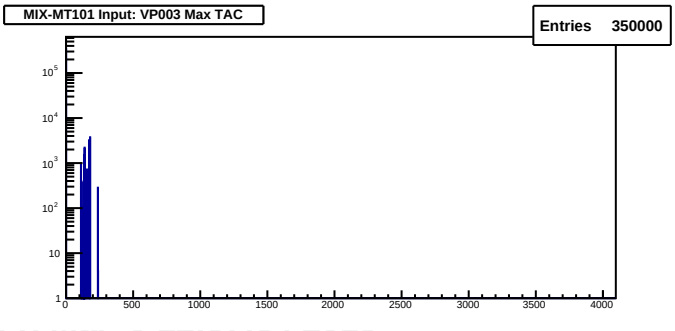

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 66354
TAC sum
1st-Best
2nd-Best
ID

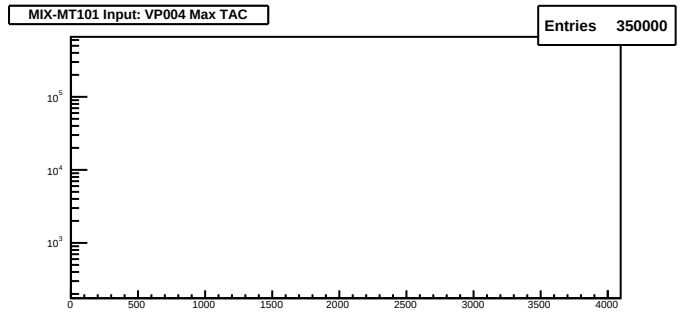

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 700000
TAC sum
1st-Best
2nd-Best
ID

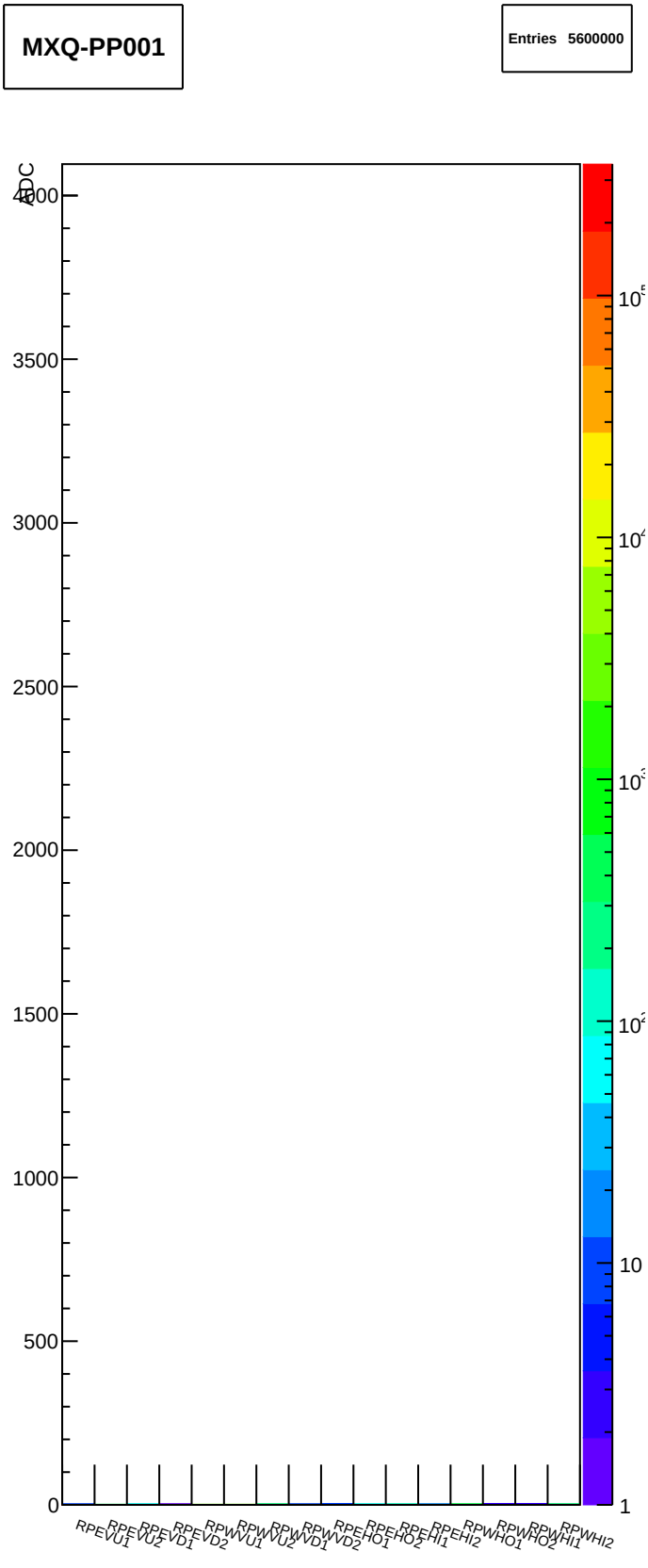

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 10726
TAC sum
1st-Best
2nd-Best
ID

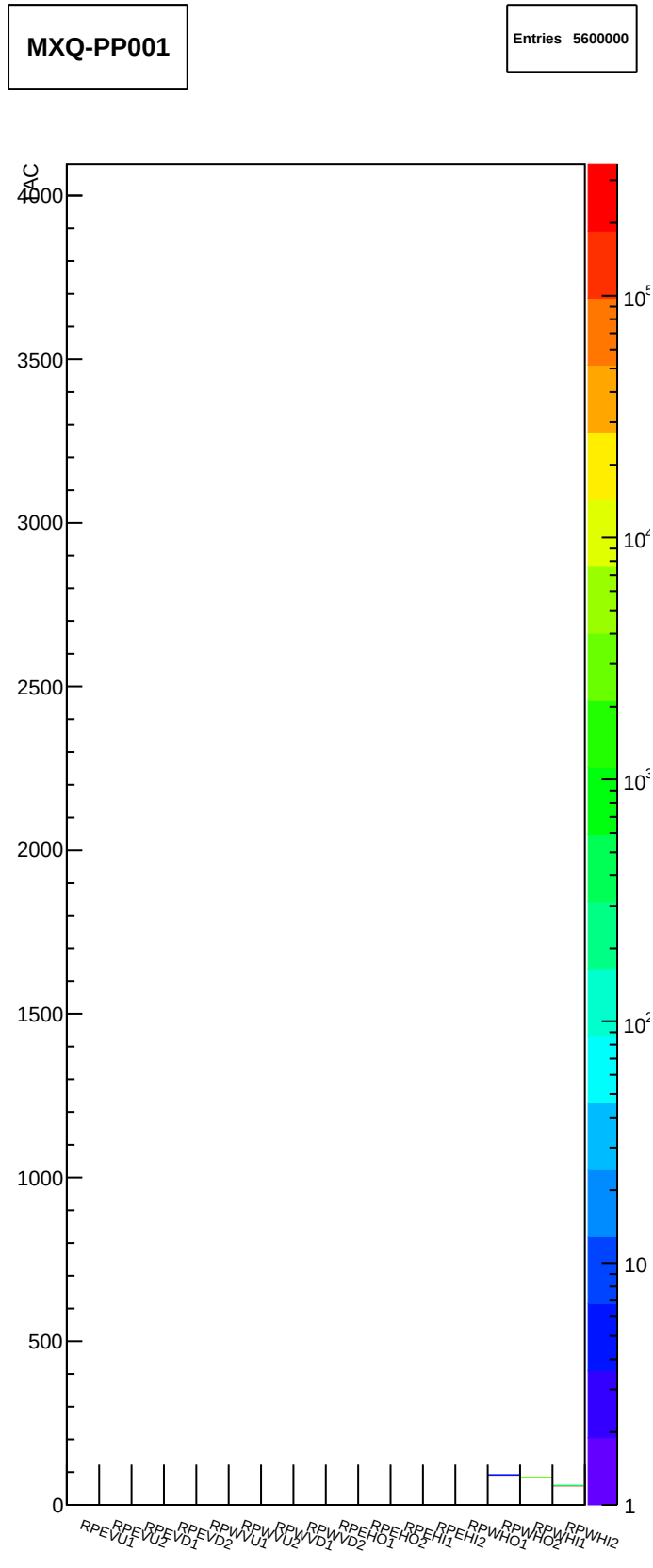

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 700000
TAC sum
1st-Best
2nd-Best
ID

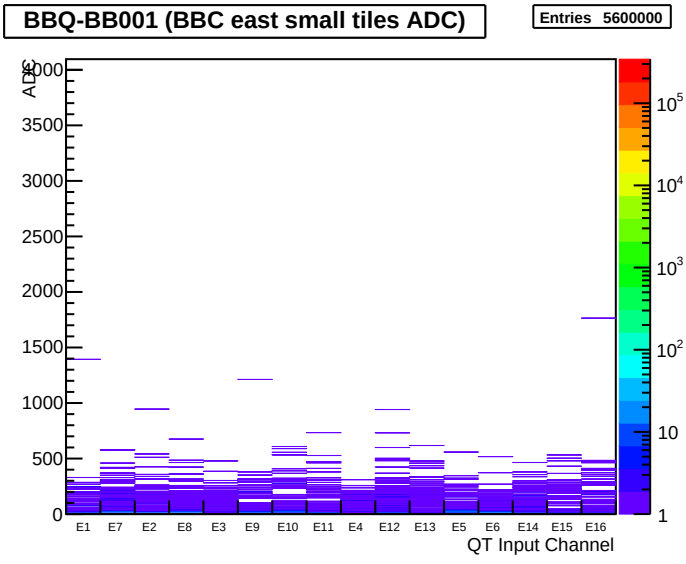

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 18515
TAC sum
1st-Best
2nd-Best
ID

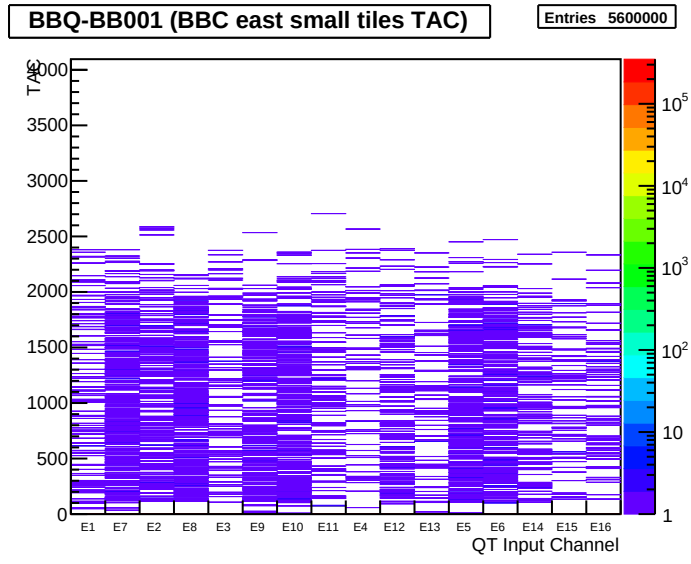

MIX-MT101 Input: VP003 Max TAC
Entries 350000

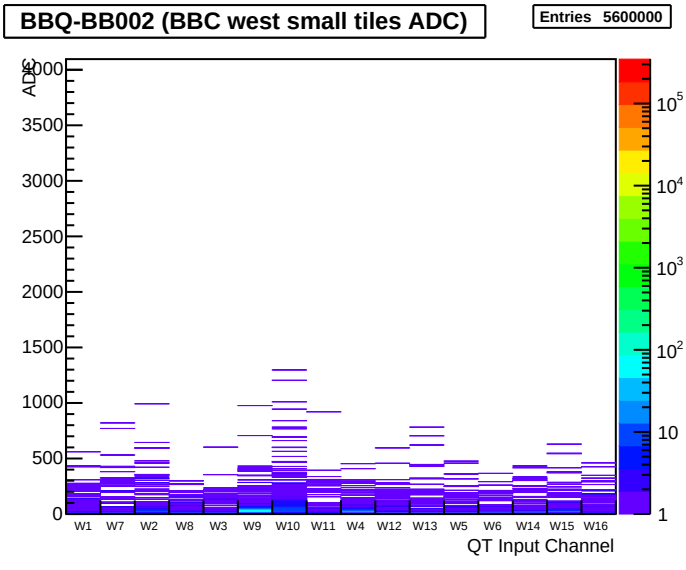

MIX-MT101 Input: VP004 Max TAC
Entries 350000

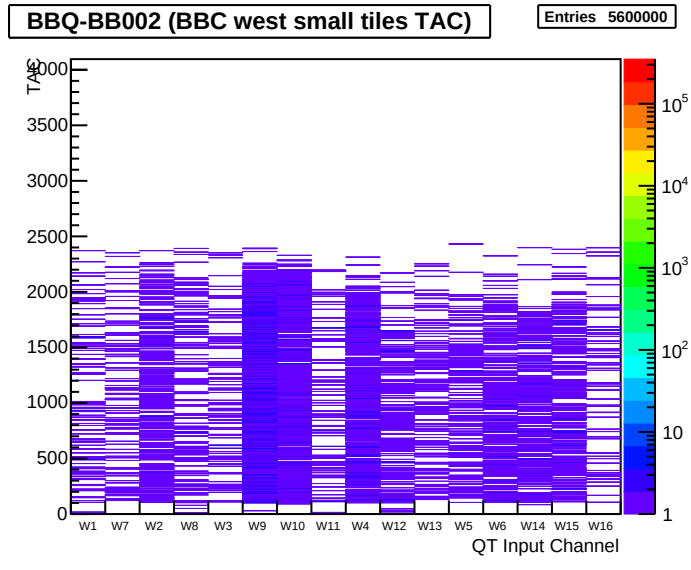

MXQ-PP001
Entries 5600000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

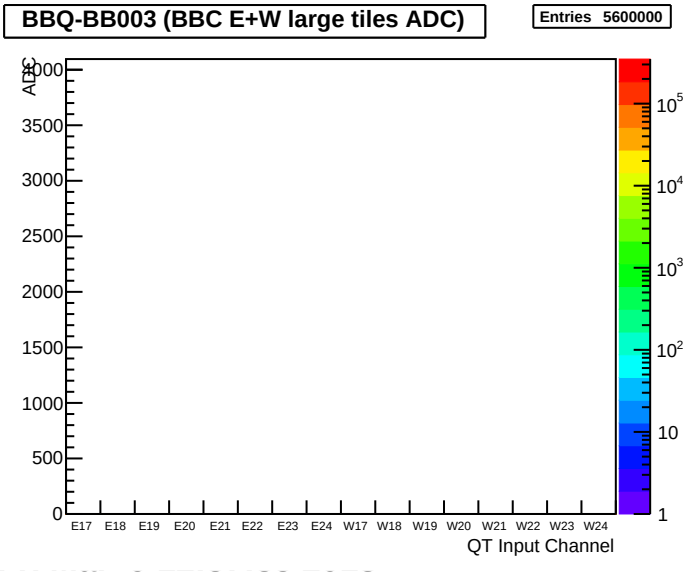

MXQ-PP001
Entries 5600000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

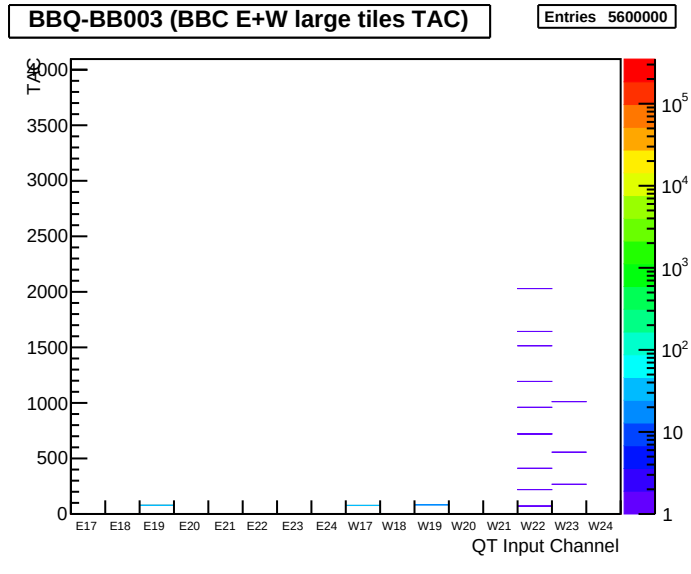

BBQ-BB001 (BBC east small tiles ADC)
Entries 5600000
ADC
QT Input Channel

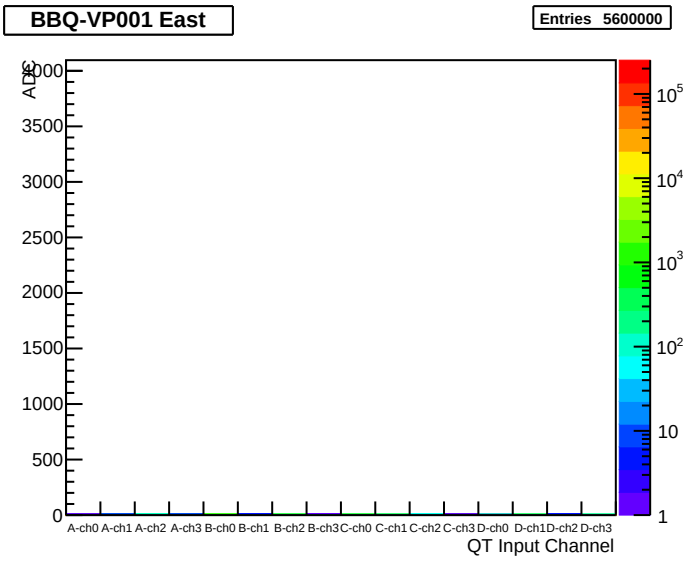

BBQ-BB001 (BBC east small tiles TAC)
Entries 5600000
TAC
QT Input Channel

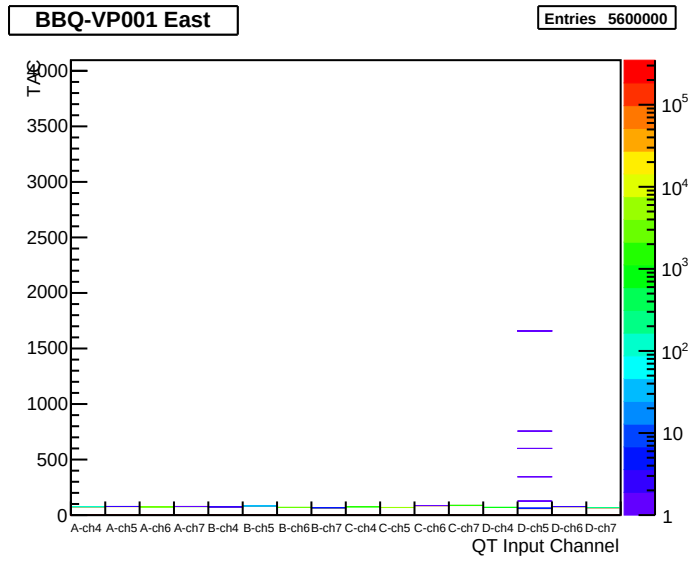

BBQ-BB002 (BBC west small tiles ADC)
Entries 5600000
ADC
QT Input Channel

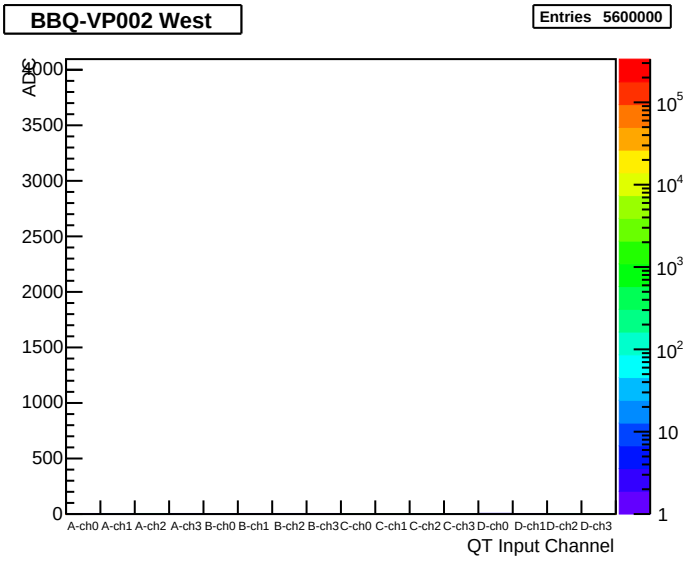

BBQ-BB002 (BBC west small tiles TAC)
Entries 5600000
TAC
QT Input Channel

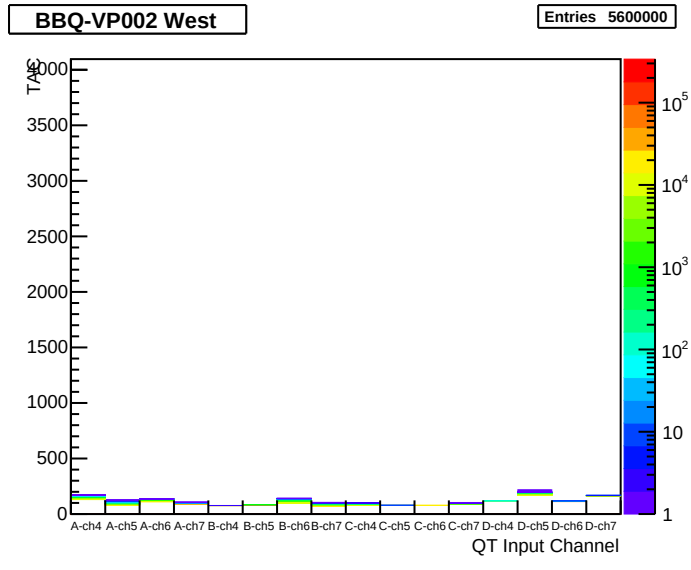

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5600000
ADC
QT Input Channel

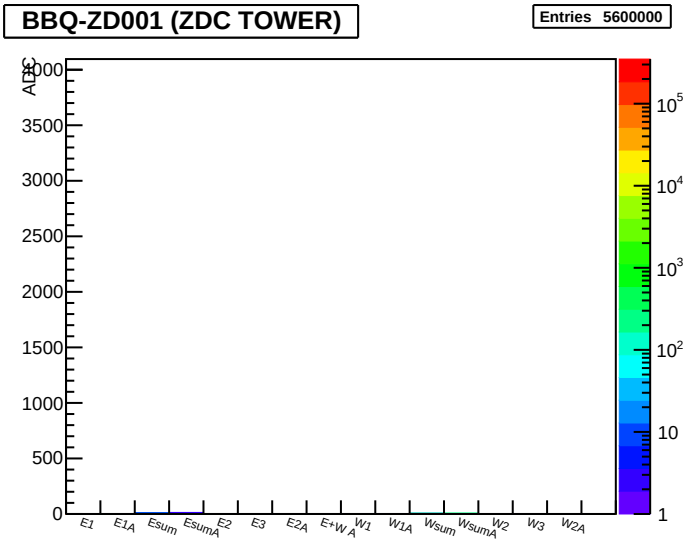

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5600000
TAC
QT Input Channel

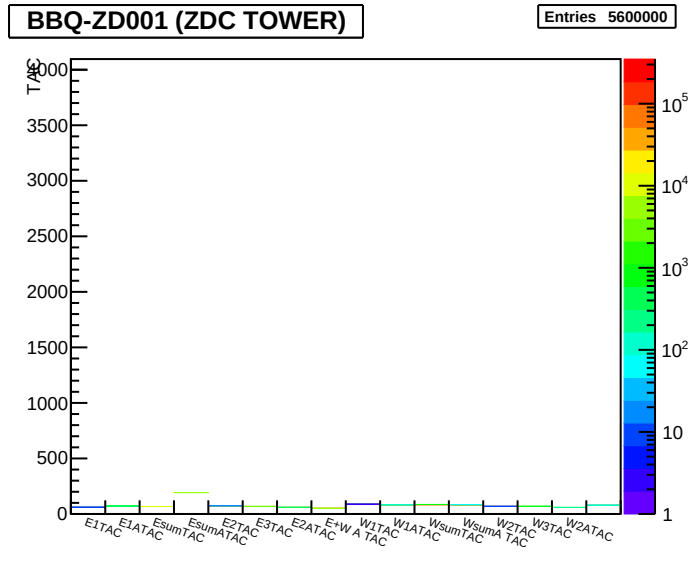

BBQ-VP001 East
Entries 5600000
ADC
QT Input Channel

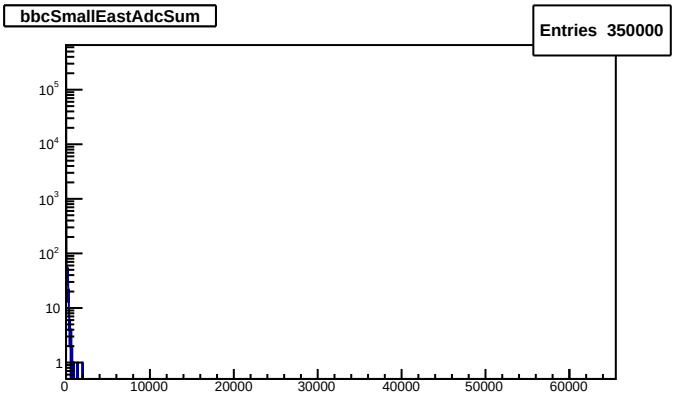

BBQ-VP001 East
Entries 5600000
TAC
QT Input Channel

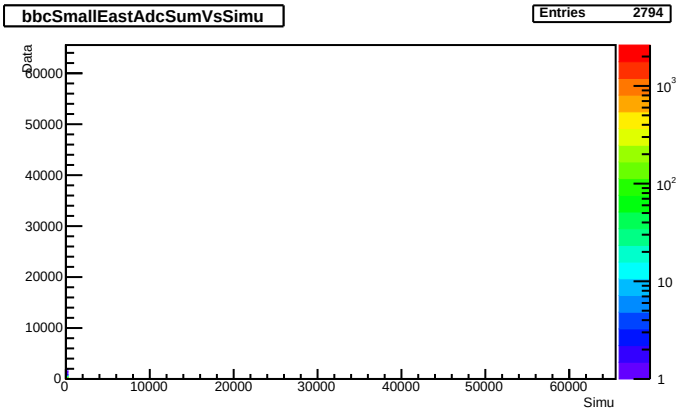

BBQ-VP002 West
Entries 5600000
ADC
QT Input Channel

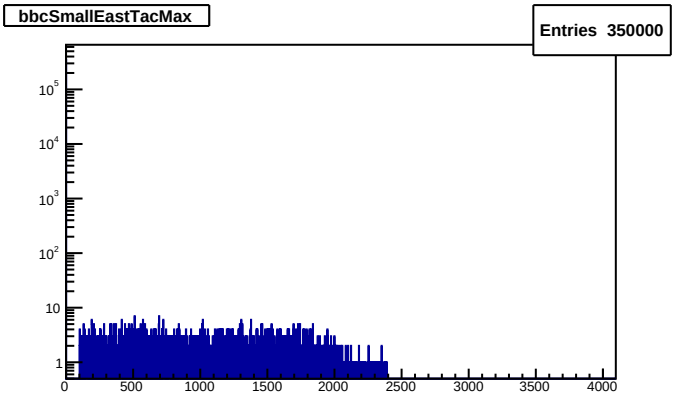

BBQ-VP002 West
Entries 5600000
TAC
QT Input Channel

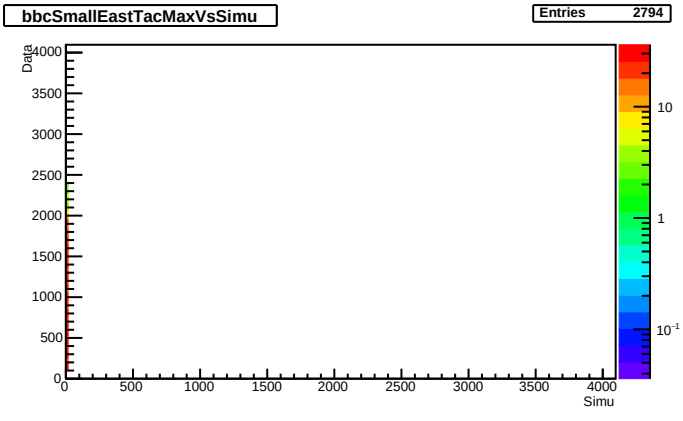

BBQ-ZD001 (ZDC TOWER)
Entries 5600000
ADC

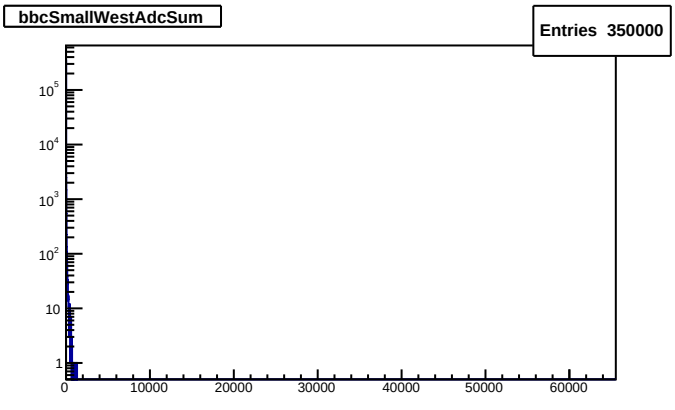

BBQ-ZD001 (ZDC TOWER)
Entries 5600000
TAC

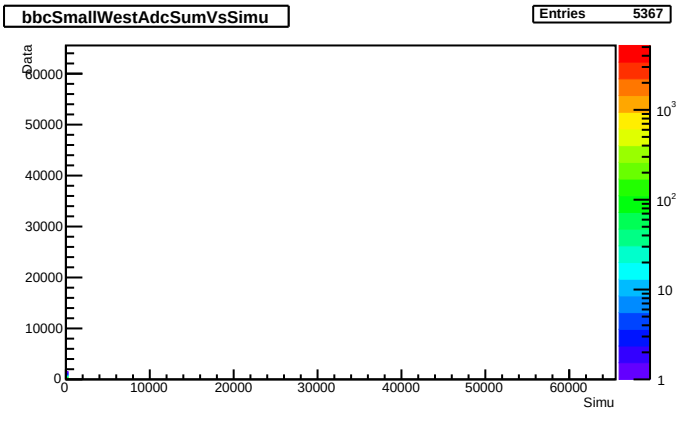

bbcSmallEastAdcSum
Entries 350000

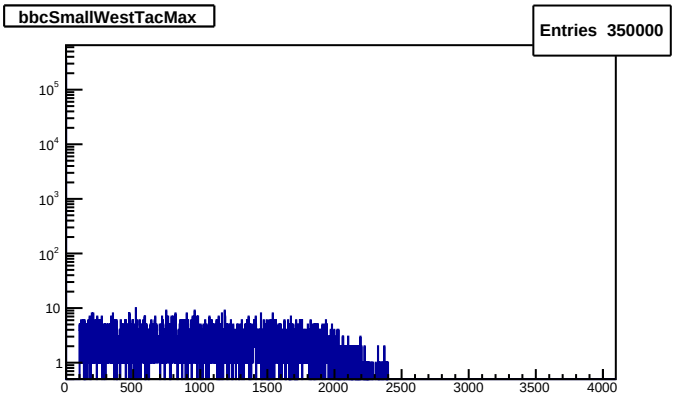

bbcSmallEastAdcSumVsSimu
Entries 2794
Data
Simu

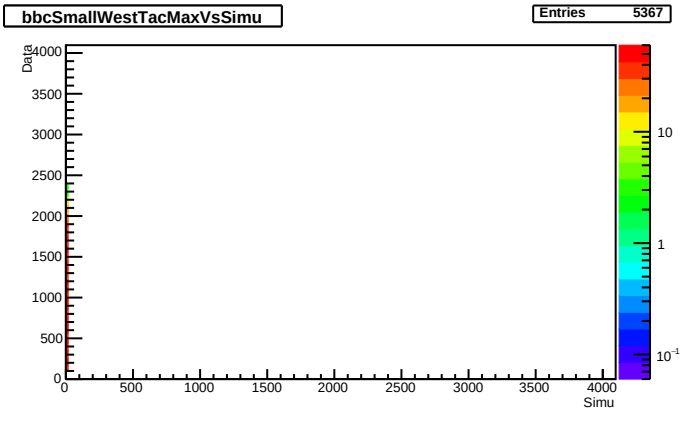

bbcSmallEastTacMax
Entries 350000

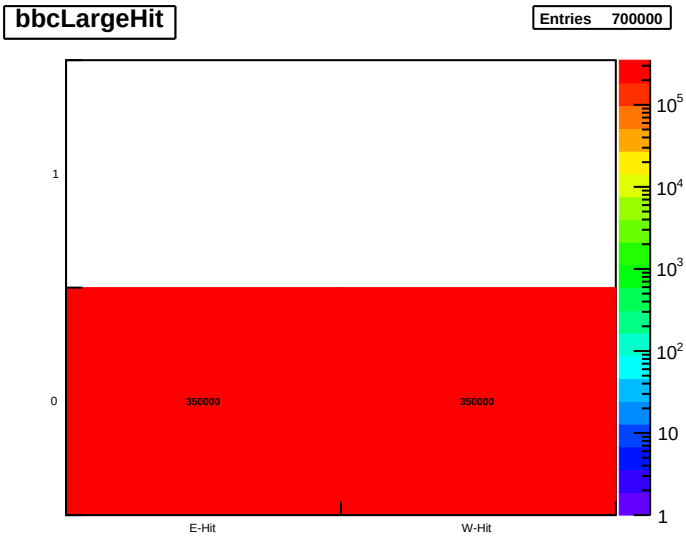

bbcSmallEastTacMaxVsSimu
Entries 2794
Data
Simu

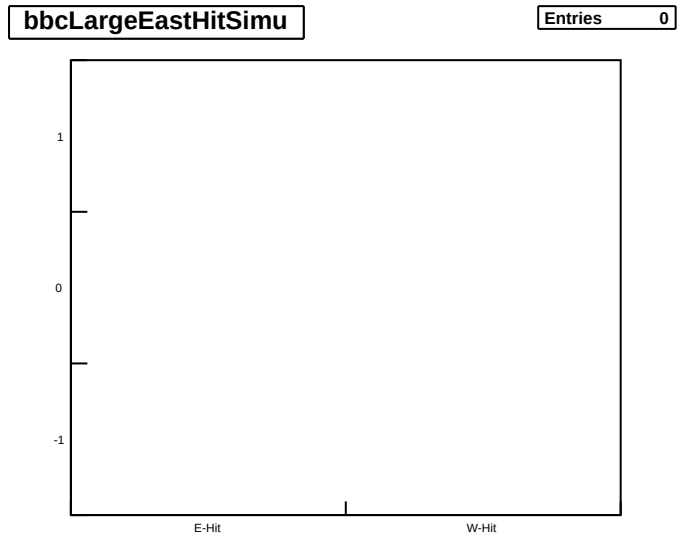

bbcSmallWestAdcSum
Entries 350000

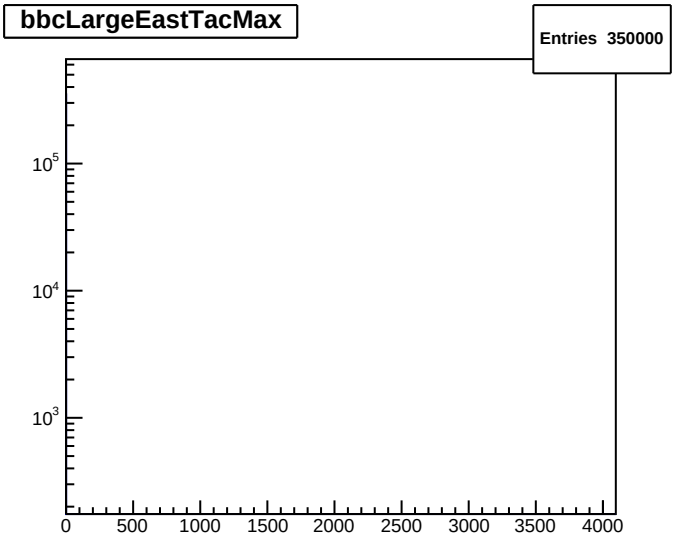

bbcSmallWestAdcSumVsSimu
Entries 5367
Data
Simu

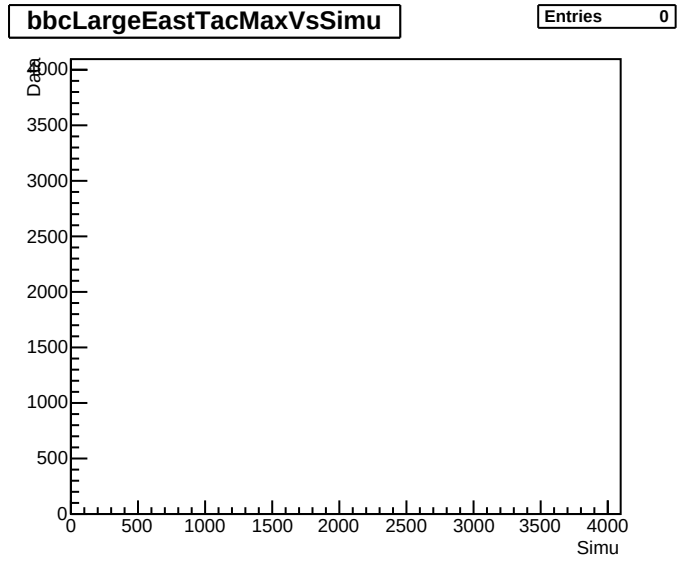

bbcSmallWestTacMax
Entries 350000

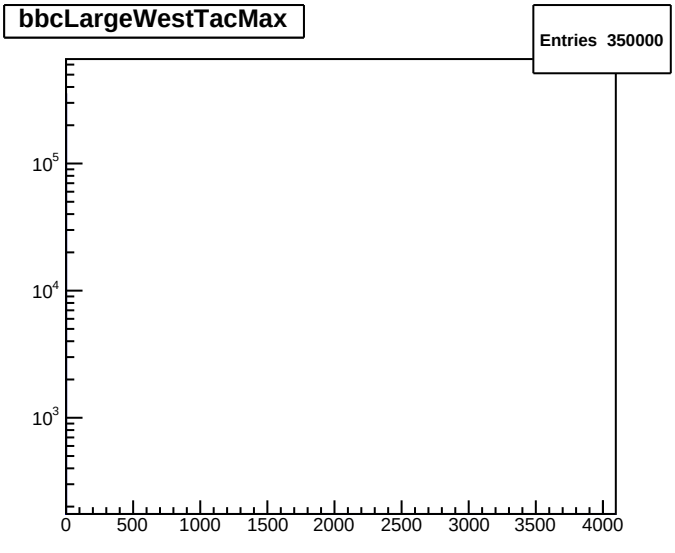

bbcSmallWestTacMaxVsSimu
Entries 5367
Data
Simu

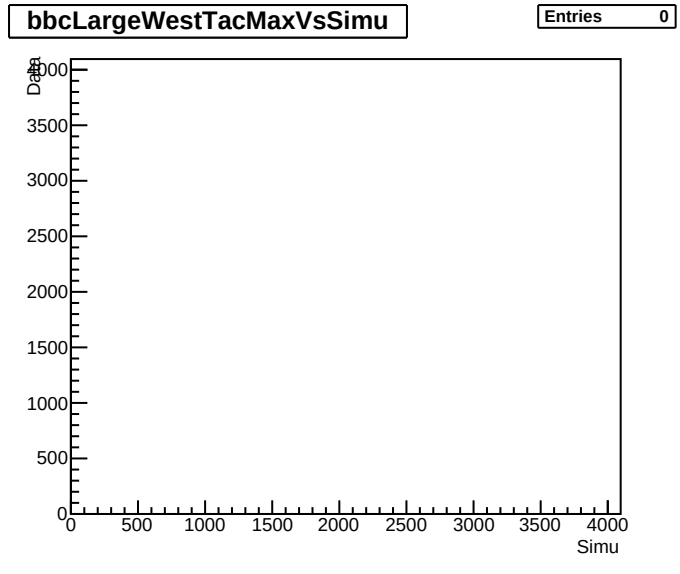

bbcLargeHit
Entries 700000
1
0
350000
350000
E-Hit
W-Hit
$10^5$
$10^4$
$10^3$
$10^2$
10
1

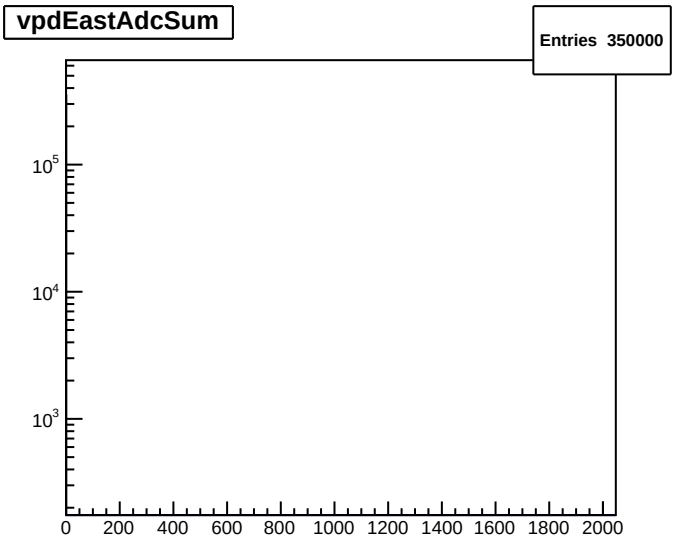

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

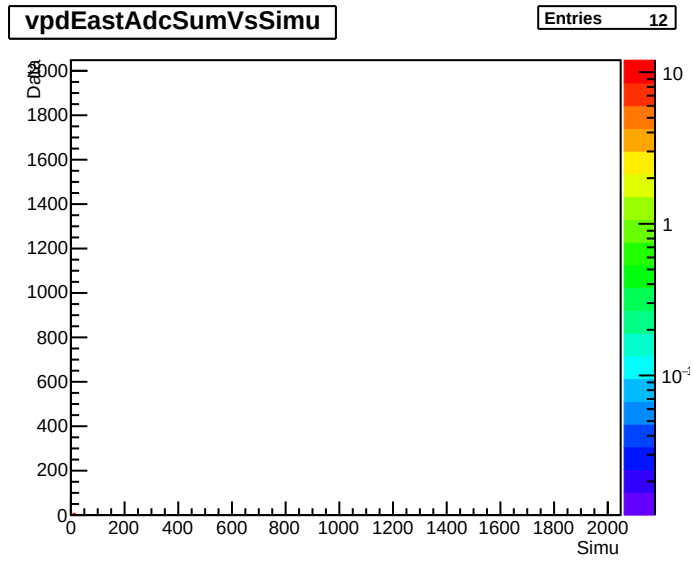

bbcLargeEastTacMax
Entries 350000
$10^5$
$10^4$
$10^3$
0
500
1000
1500
2000
2500
3000
3500
4000

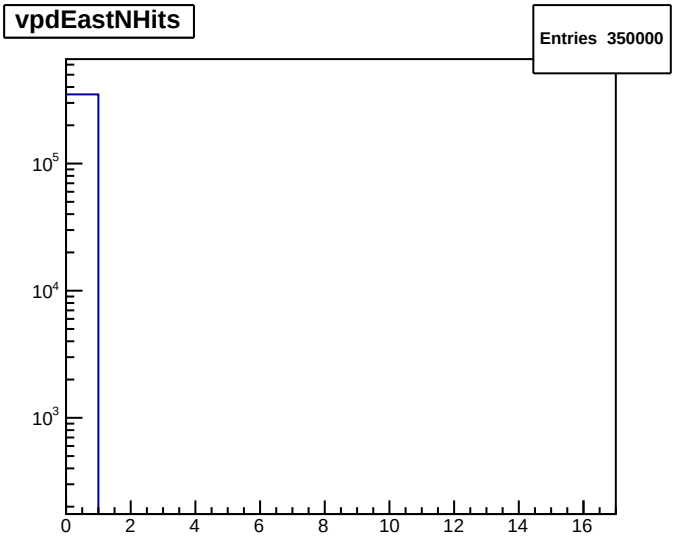

bbcLargeEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

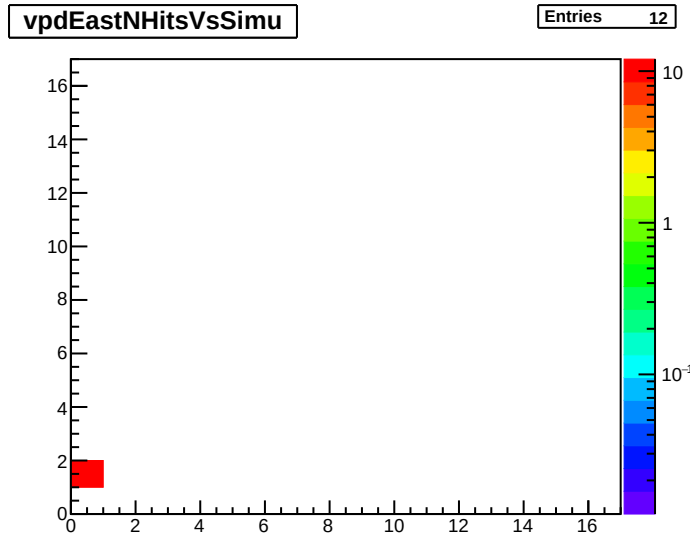

bbcLargeWestTacMax
Entries 350000
$10^5$
$10^4$
$10^3$
0
500
1000
1500
2000
2500
3000
3500
4000

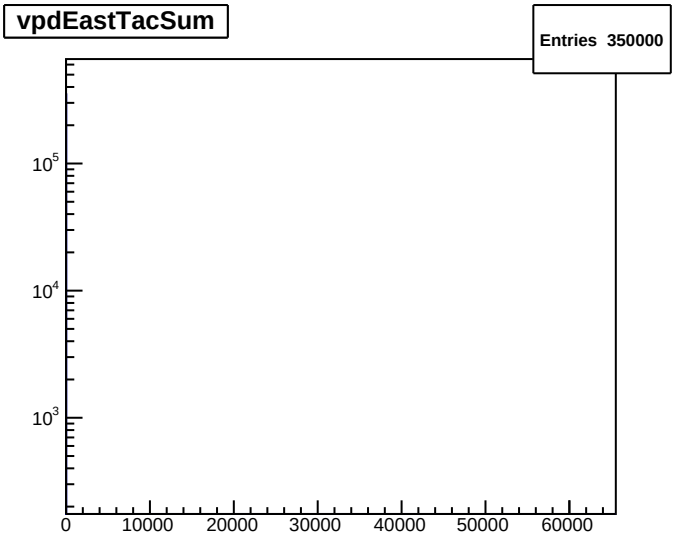

bbcLargeWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

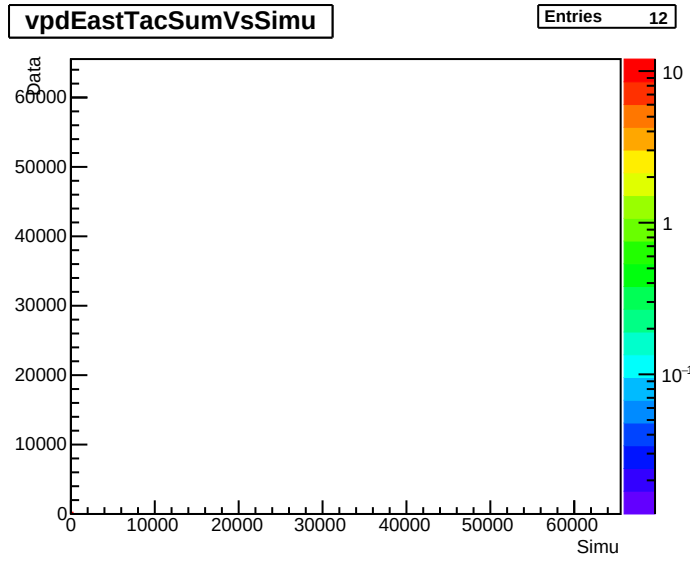

vpdEastAdcSum
Entries 350000

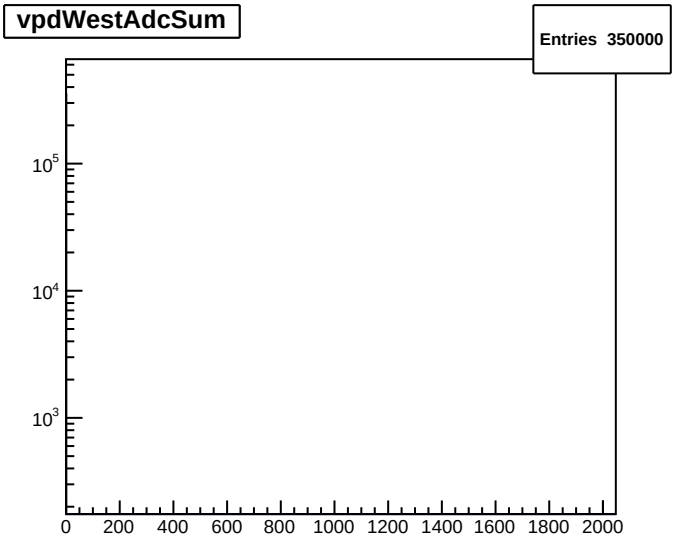

vpdEastAdcSumVsSimu
Entries 12
Data
Simu

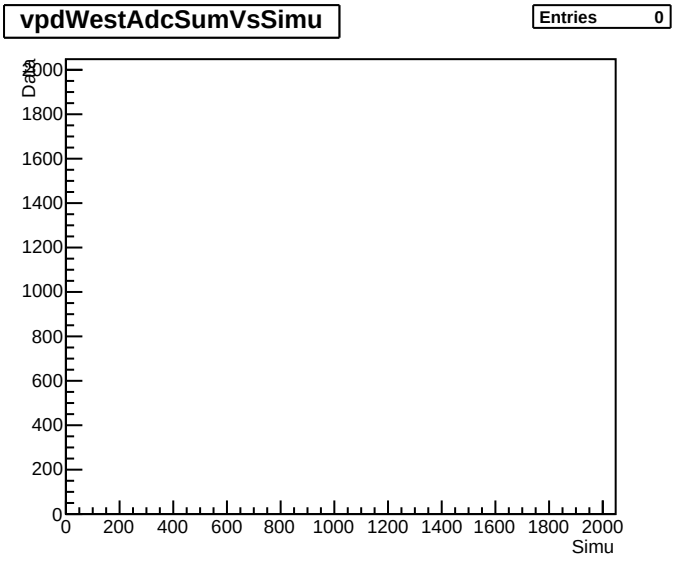

vpdEastNHits
Entries 350000

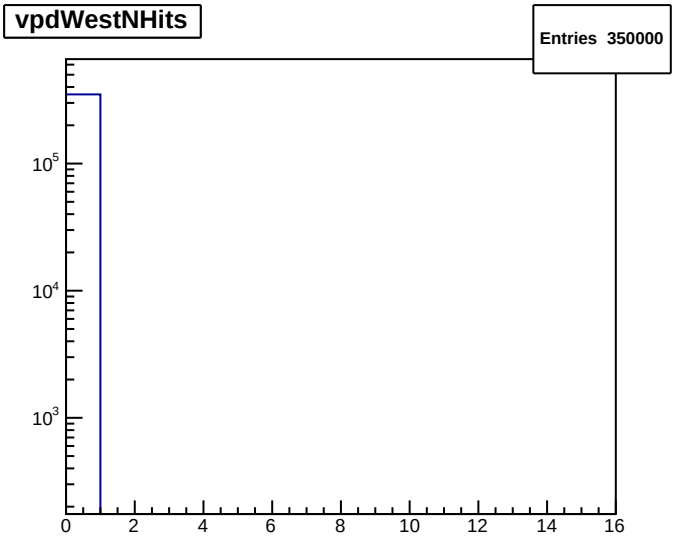

vpdEastNHitsVsSimu
Entries 12

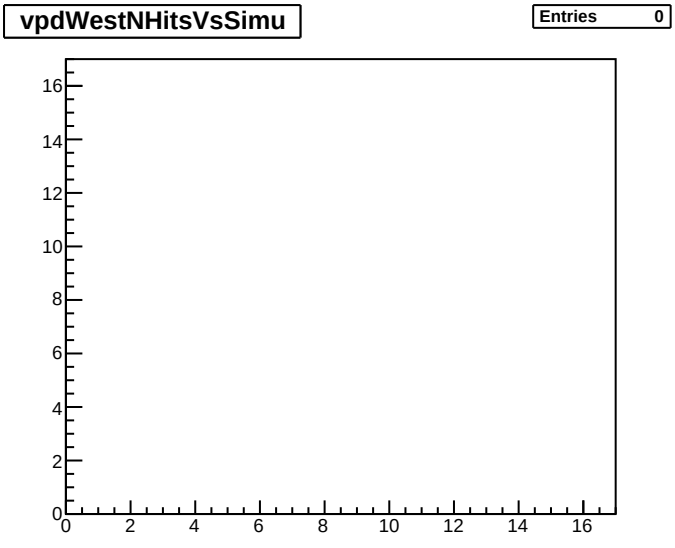

vpdEastTacSum
Entries 350000

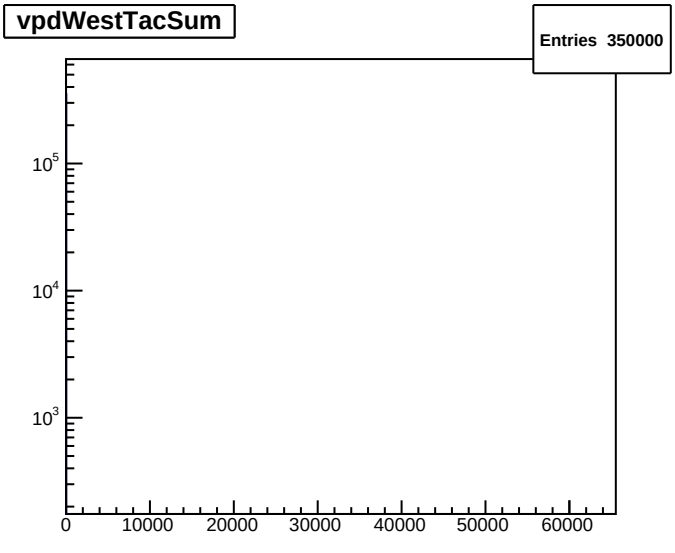

vpdEastTacSumVsSimu
Entries 12
Data
Simu

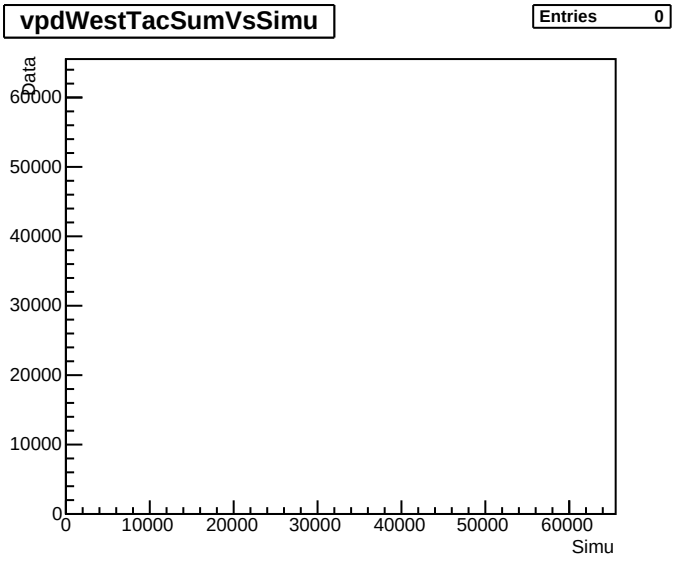

vpdWestAdcSum
Entries 350000
$10^5$
$10^4$
$10^3$
0 200 400 600 800 1000 1200 1400 1600 1800 2000

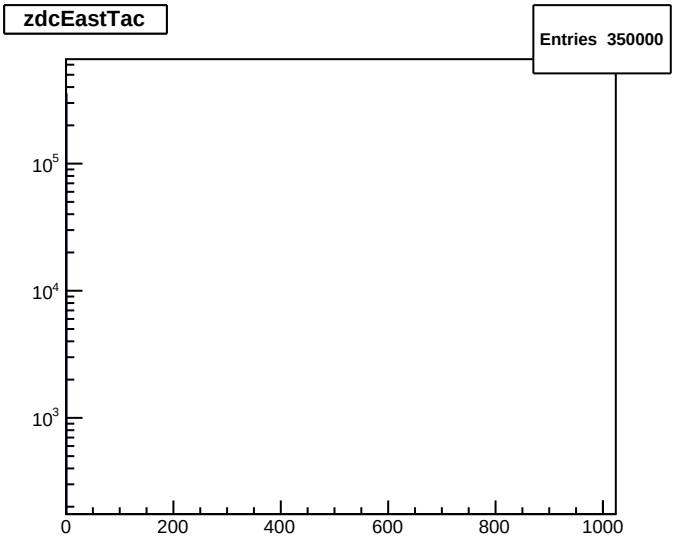

vpdWestAdcSumVsSimu
Entries 0
Data
2000 1800 1600 1400 1200 1000 800 600 400 200 0
0 200 400 600 800 1000 1200 1400 1600 1800 2000
Simu

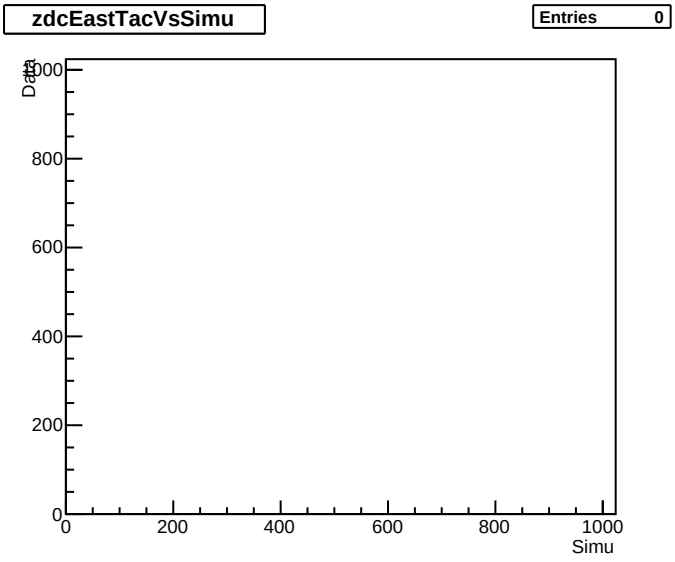

vpdWestNHits
Entries 350000
$10^5$
$10^4$
$10^3$
0 2 4 6 8 10 12 14 16

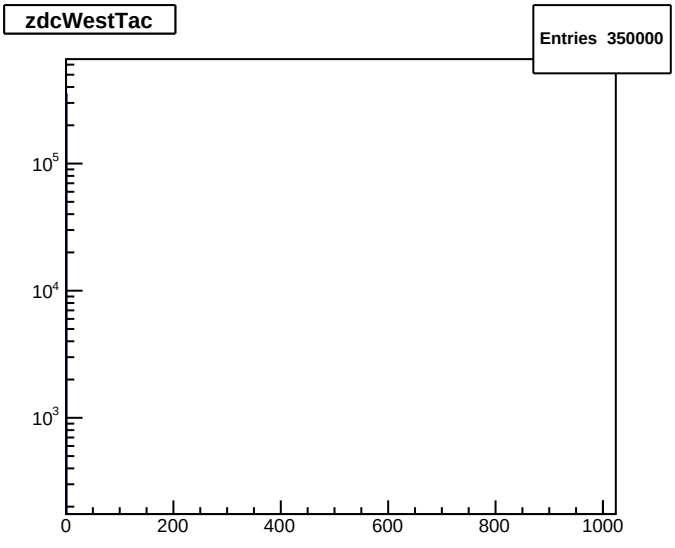

vpdWestNHitsVsSimu
Entries 0
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16

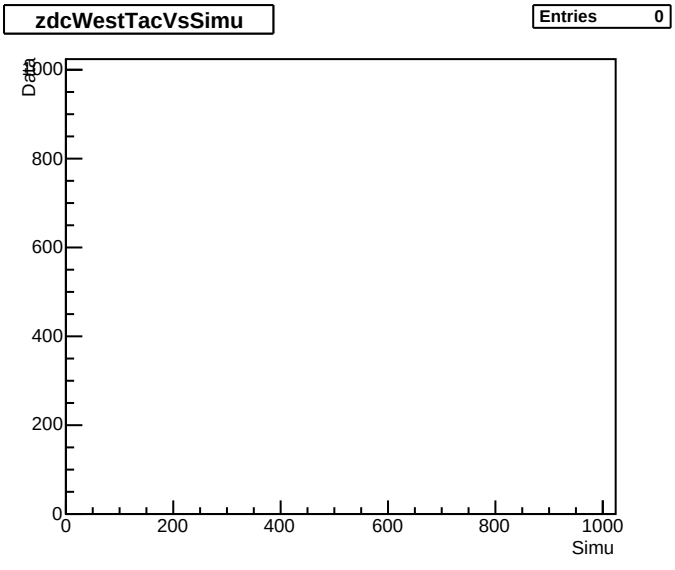

vpdWestTacSum
Entries 350000
$10^5$
$10^4$
$10^3$
0 10000 20000 30000 40000 50000 60000

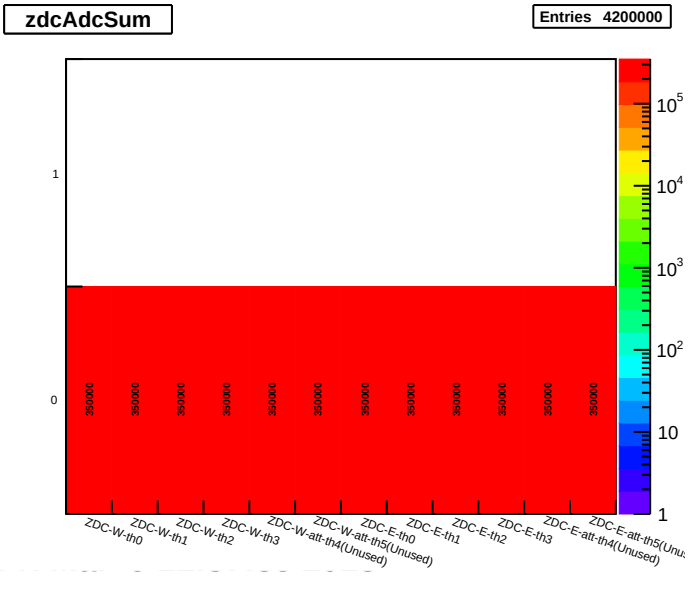

vpdWestTacSumVsSimu
Entries 0
Data
60000 50000 40000 30000 20000 10000 0
0 10000 20000 30000 40000 50000 60000
Simu

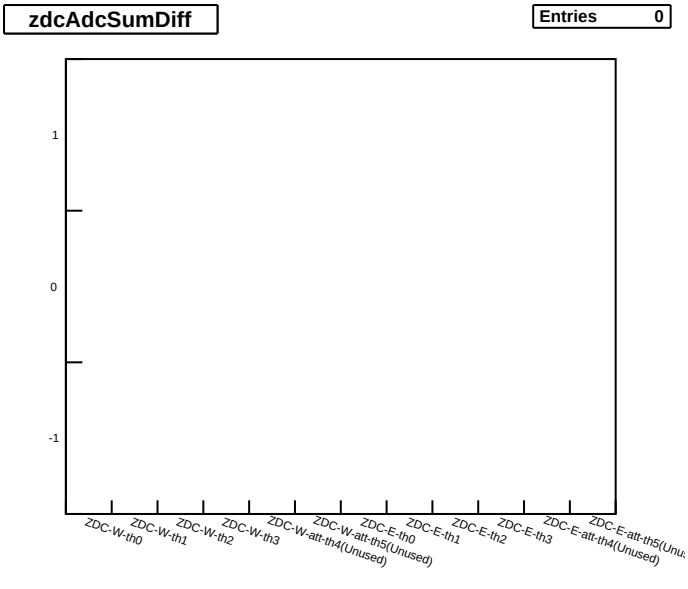

zdcEastTac
Entries 350000
$10^5$
$10^4$
$10^3$
0
200
400
600
800
1000

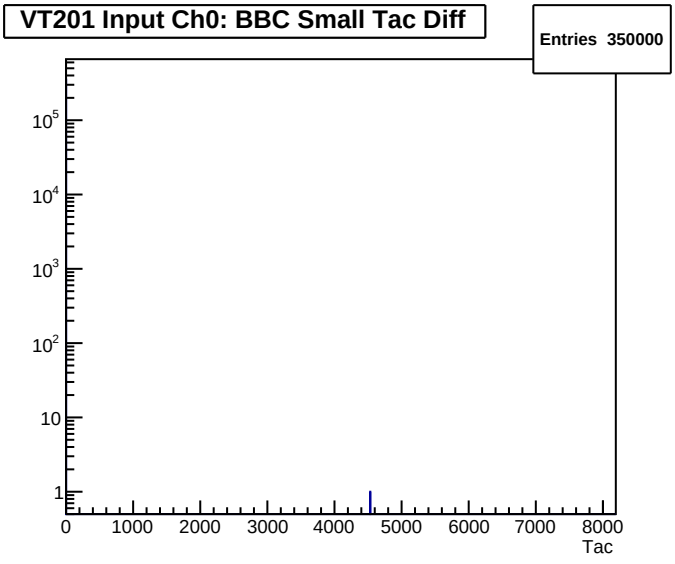

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

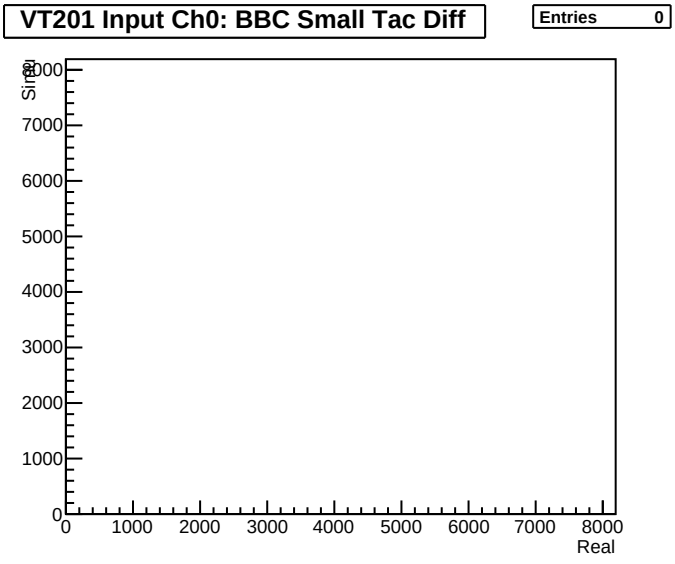

zdcWestTac
Entries 350000
$10^5$
$10^4$
$10^3$
0
200
400
600
800
1000

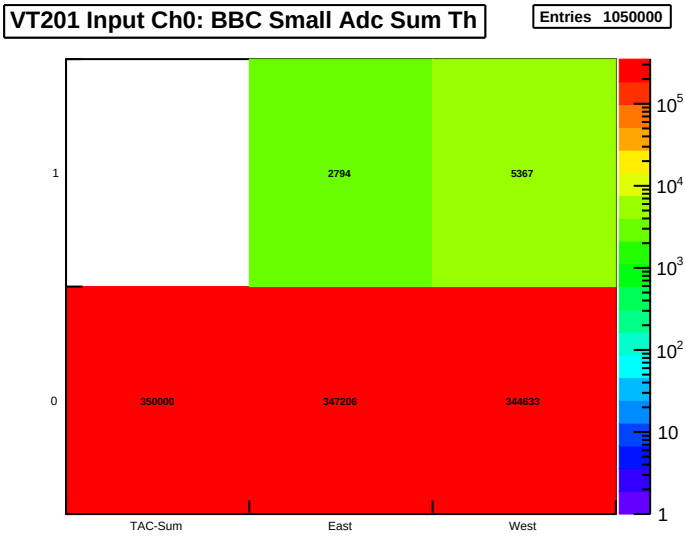

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

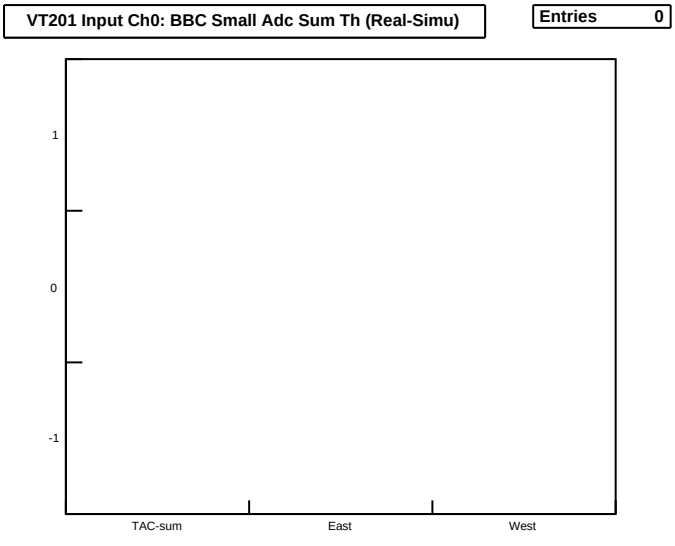

zdcAdcSum
Entries 4200000
1
0
350000
$10^5$
$10^4$
$10^3$
$10^2$
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

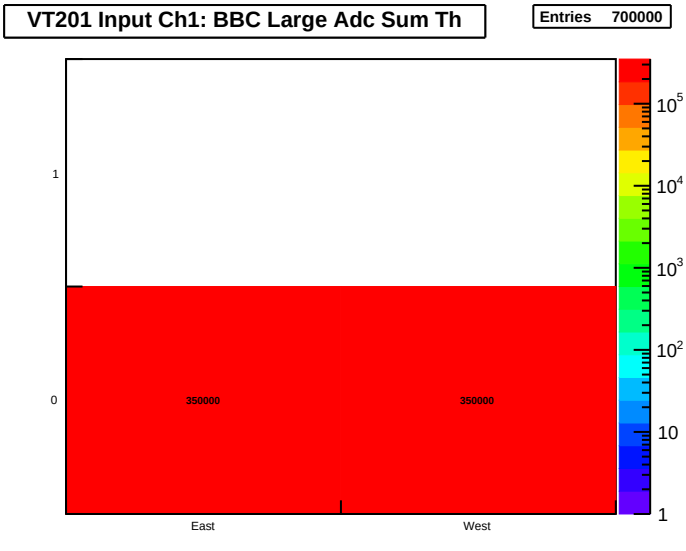

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

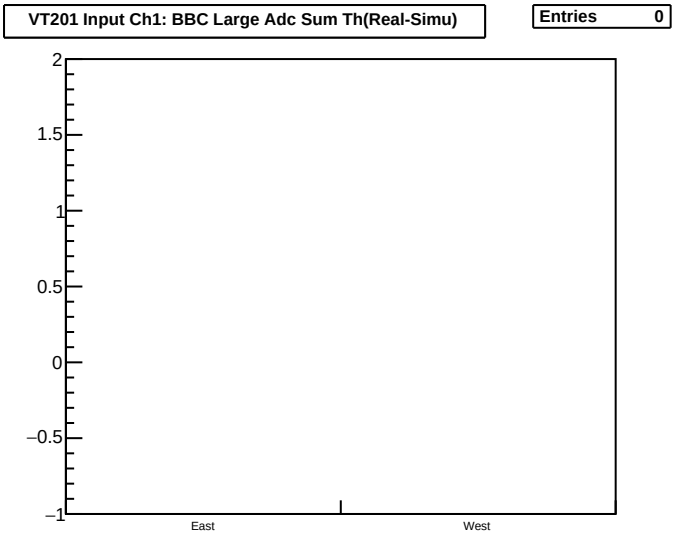

VT201 Input Ch0: BBC Small Tac Diff
Entries 350000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

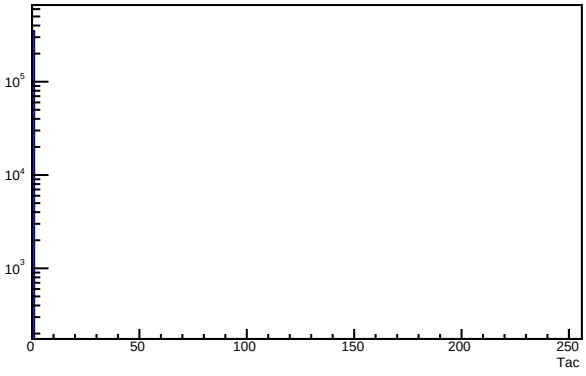

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 1050000
2794
5367
350000
347206
344633
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

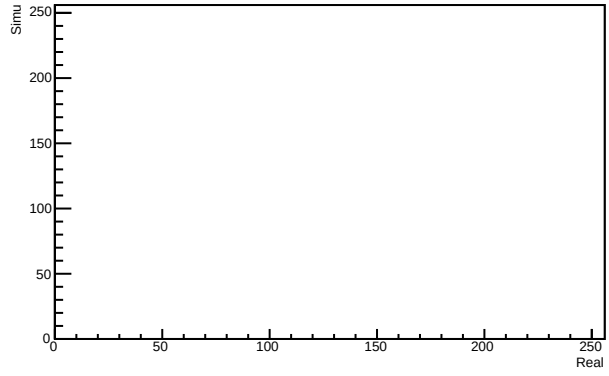

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 700000
350000
350000
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

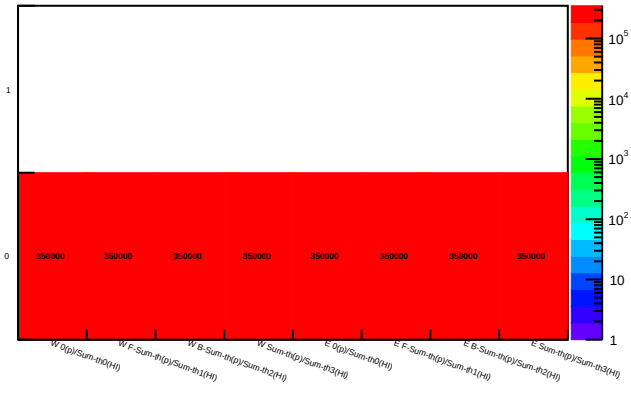
**VT201 Input Ch2: ZDC Tac Diff**



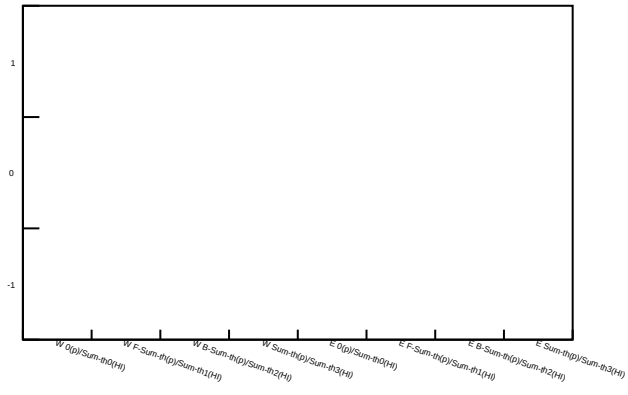

**VT201 Input Ch2: ZDC Tac Diff**



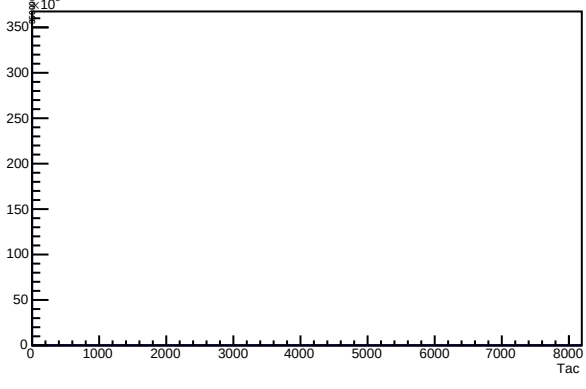

**VT201 Input Ch2: ZDC Adc Sum Th**



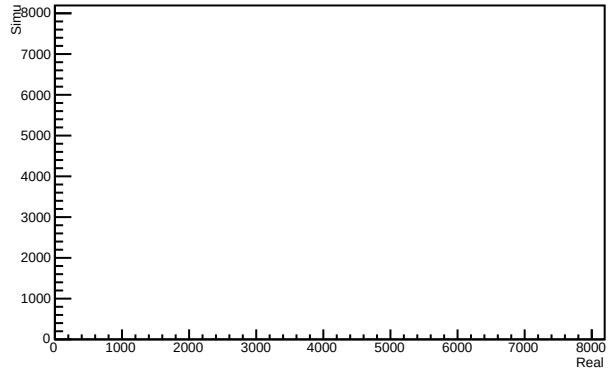

**VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)**



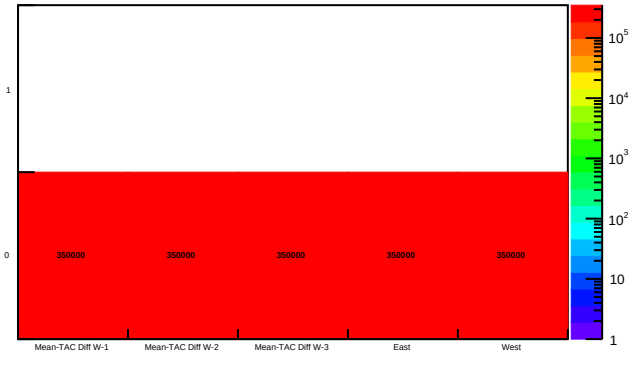

**VT201 Input Ch4: VPD Tac Diff**



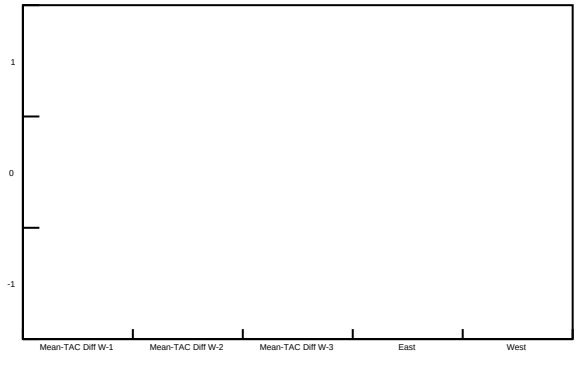

**VT201 Input Ch4: VPD Tac Diff**

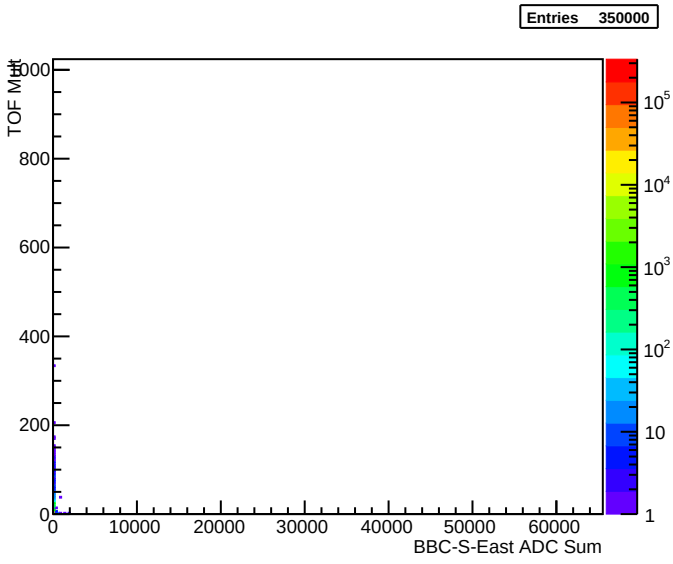


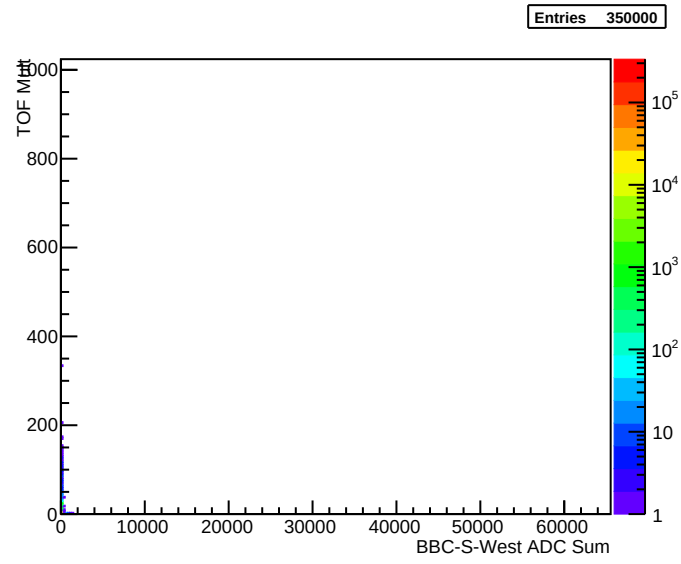

**VT201 Input Ch4: VPD Adc Sum Th**

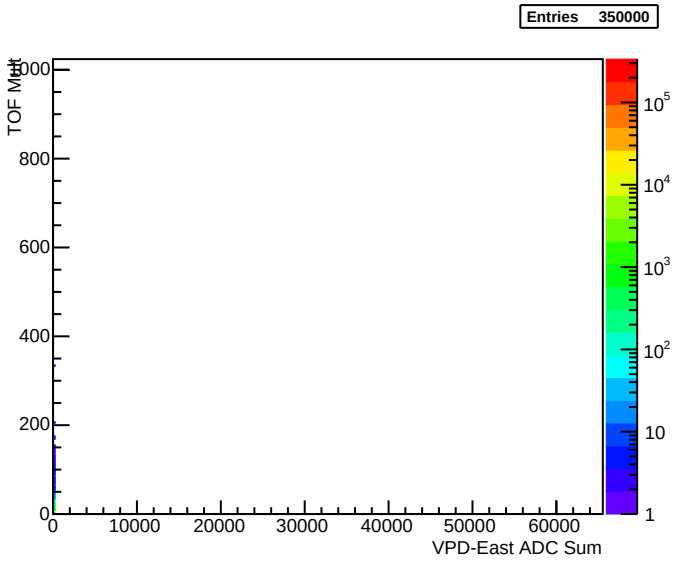


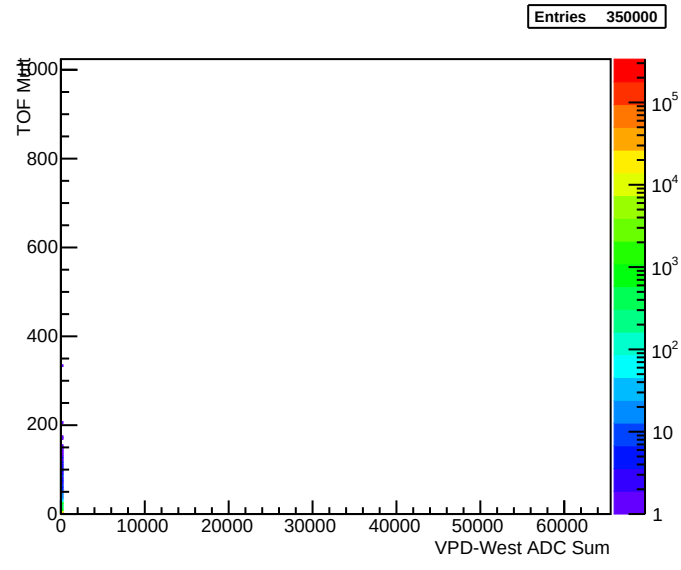

**VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)**

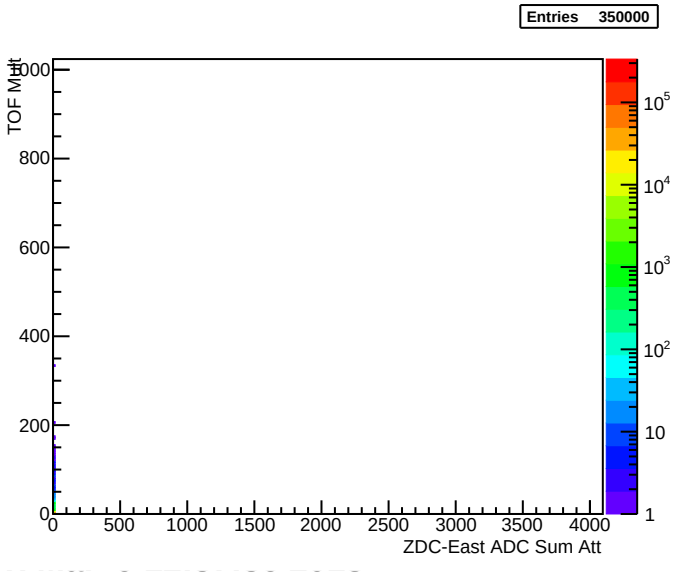


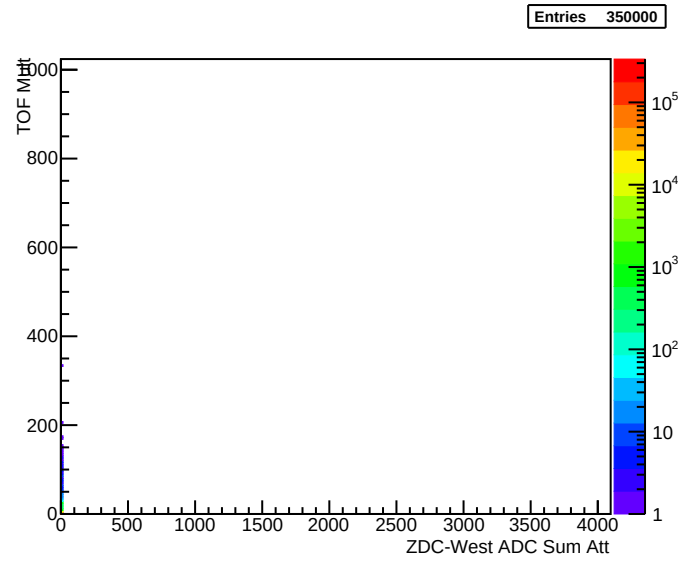

Entries 350000
TOF Mult
BBC-S-East ADC Sum

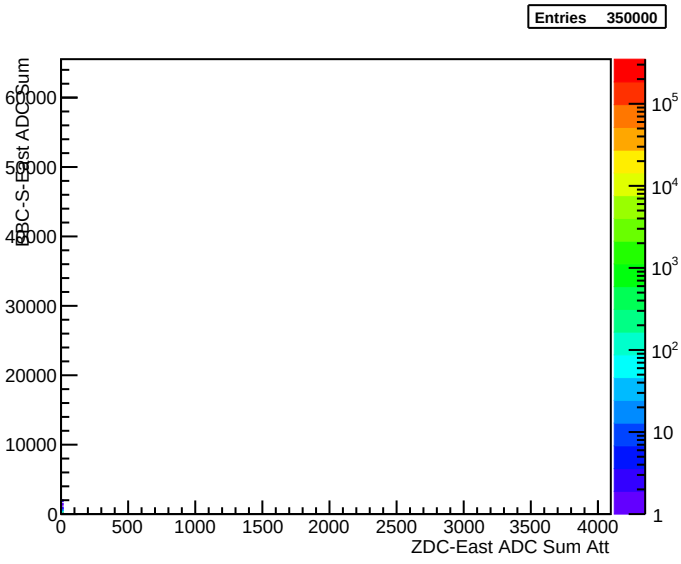

Entries 350000
TOF Mult
BBC-S-West ADC Sum

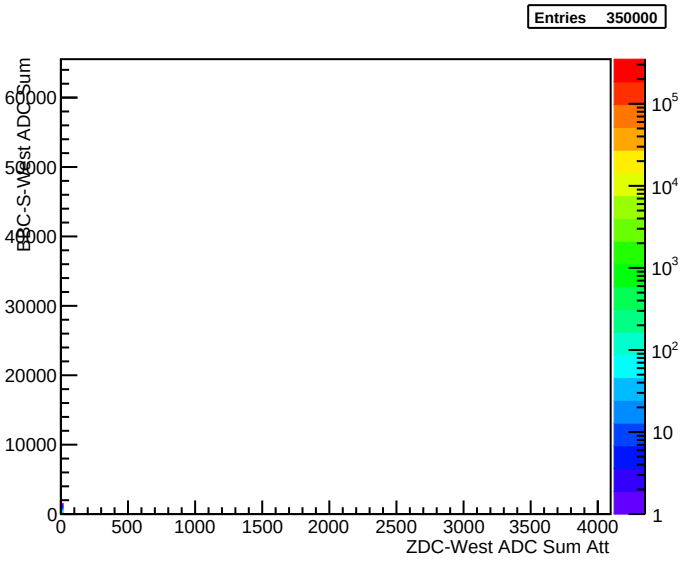

Entries 350000
TOF Mult
VPD-East ADC Sum

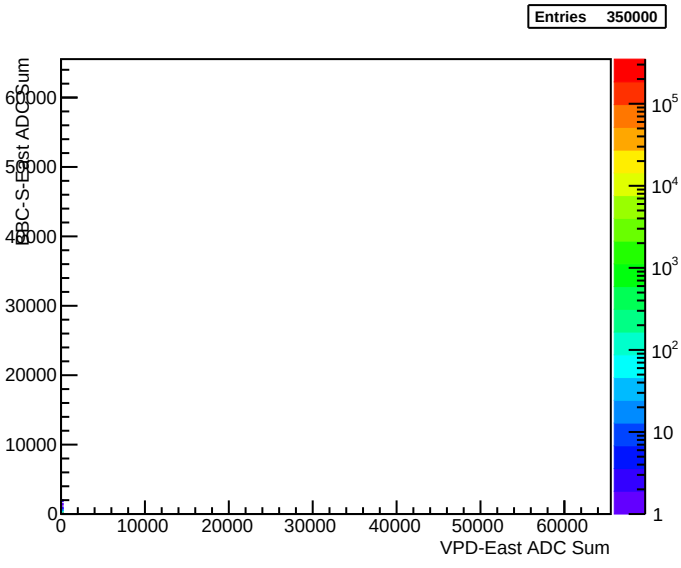

Entries 350000
TOF Mult
VPD-West ADC Sum

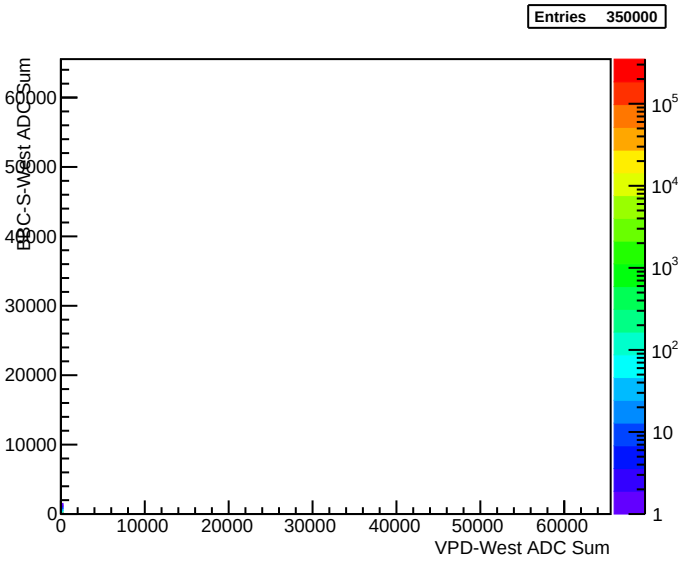

Entries 350000
TOF Mult
ZDC-East ADC Sum Att

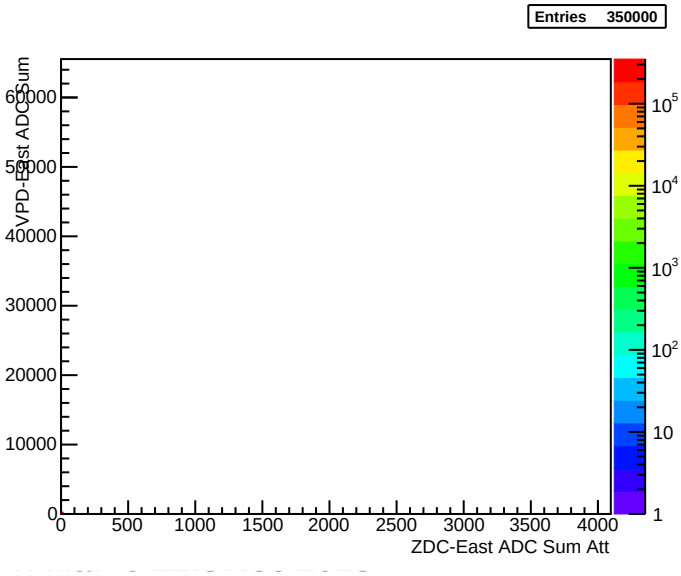

Entries 350000
TOF Mult
ZDC-West ADC Sum Att

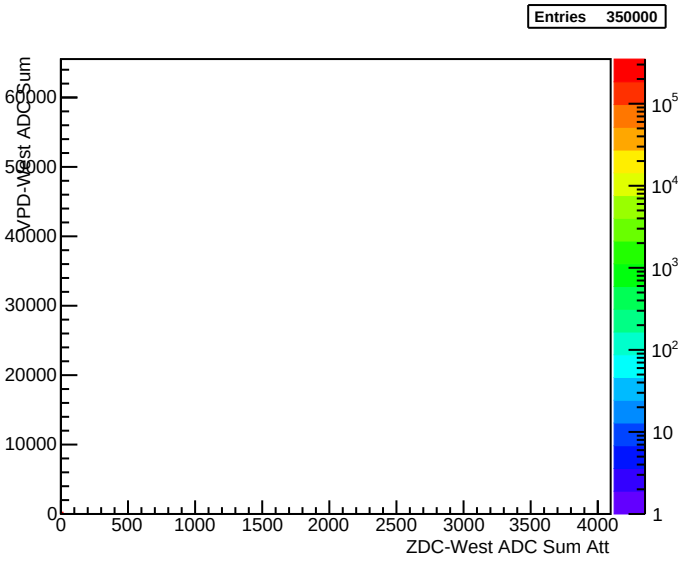

Entries 350000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

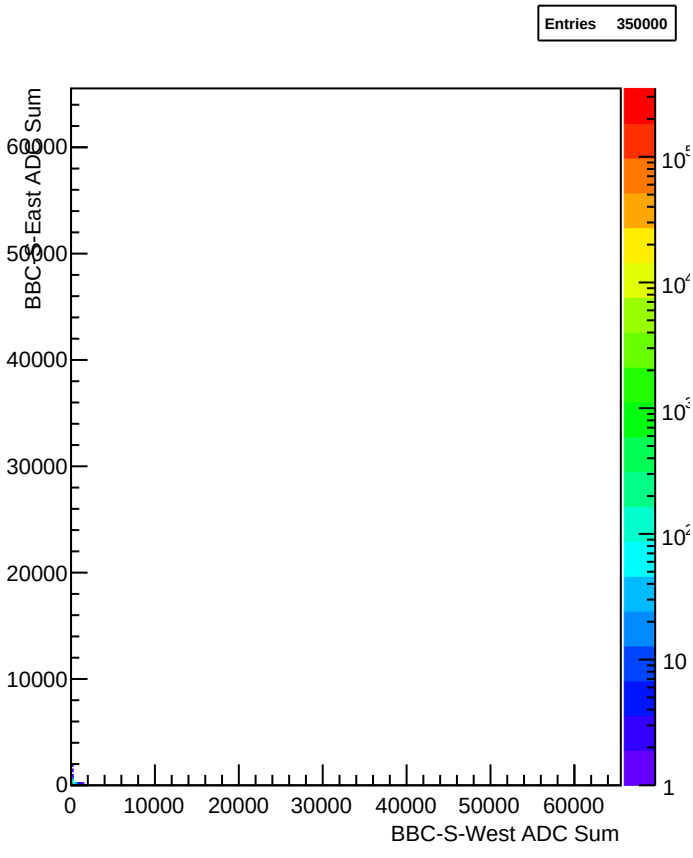

Entries 350000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

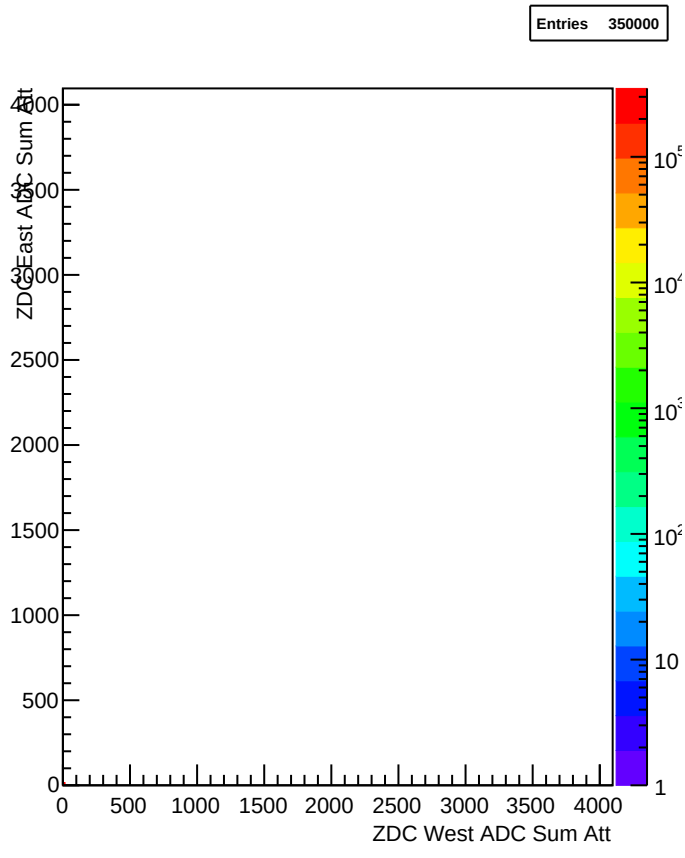

Entries 350000
BBC-S-East ADC Sum
VPD-East ADC Sum

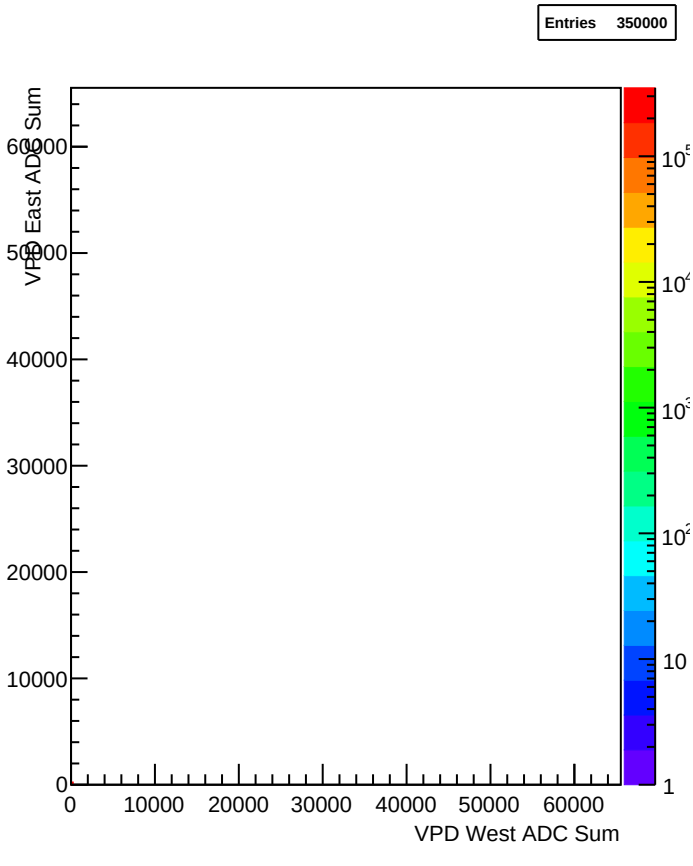

Entries 350000
BBC-S-West ADC Sum
VPD-West ADC Sum

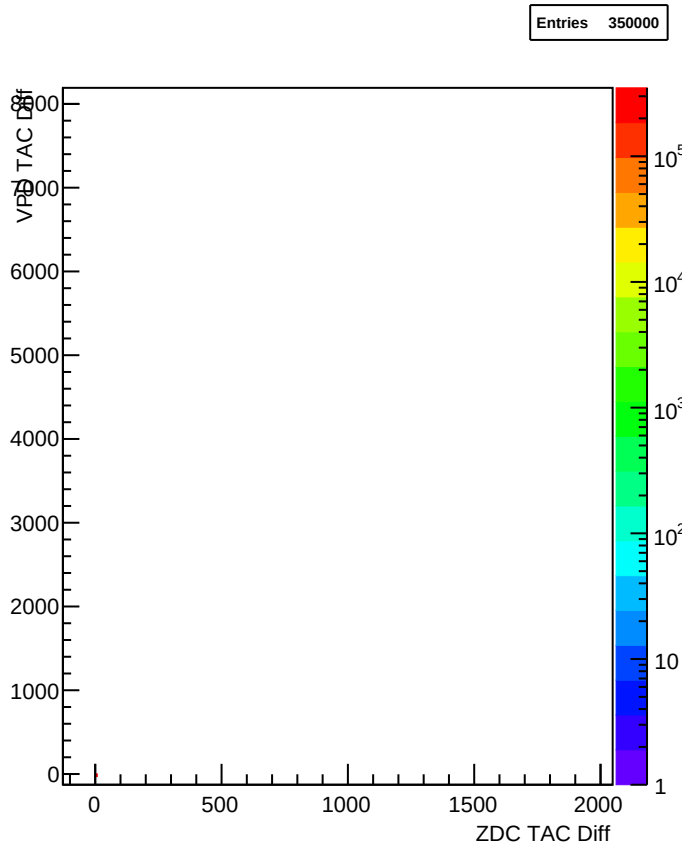

Entries 350000
VPD-East ADC Sum
ZDC-East ADC Sum Att

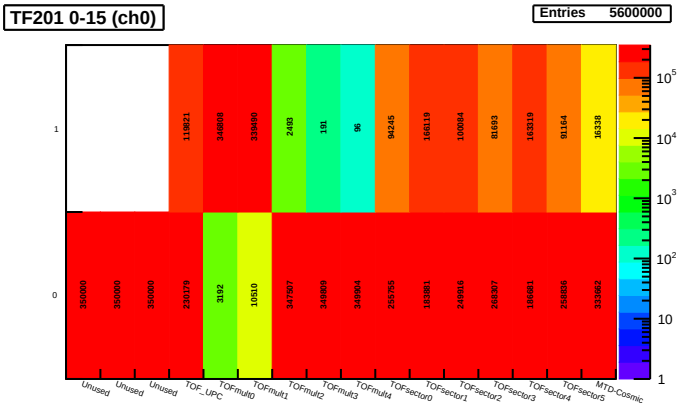

Entries 350000
VPD-West ADC Sum
ZDC-West ADC Sum Att

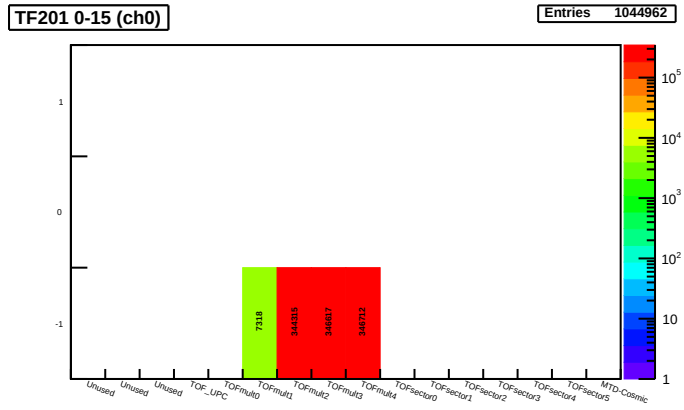

Entries 350000
BBC-S-East ADC Sum
BBC-S-West ADC Sum
0
10000
20000
30000
40000
50000
60000
1
10
$10^2$
$10^3$
$10^4$
$10^5$

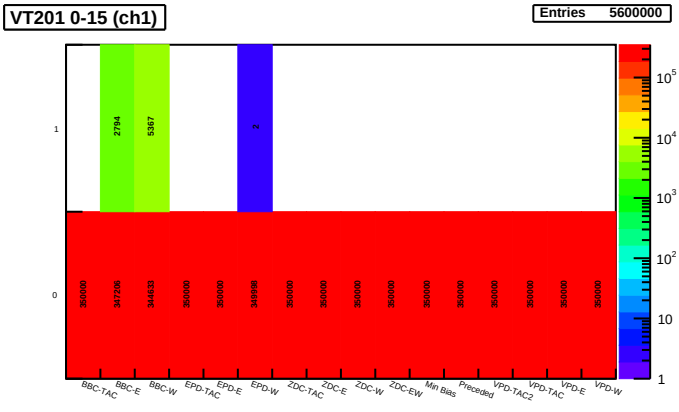

Entries 350000
ZDC East ADC Sum Att
ZDC West ADC Sum Att
0
500
1000
1500
2000
2500
3000
3500
4000
1
10
$10^2$
$10^3$
$10^4$
$10^5$

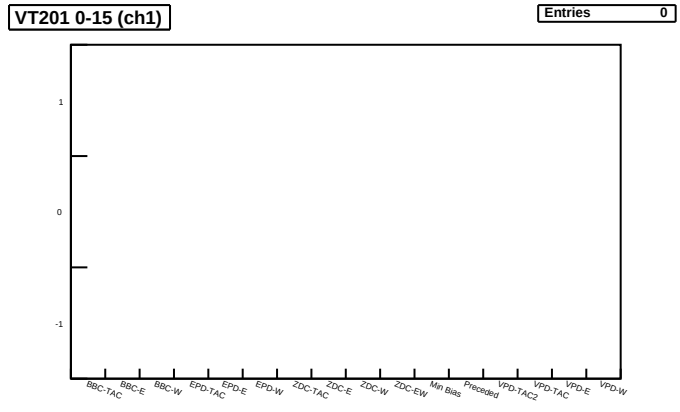

Entries 350000
VPD East ADC Sum
VPD West ADC Sum
0
10000
20000
30000
40000
50000
60000
1
10
$10^2$
$10^3$
$10^4$
$10^5$

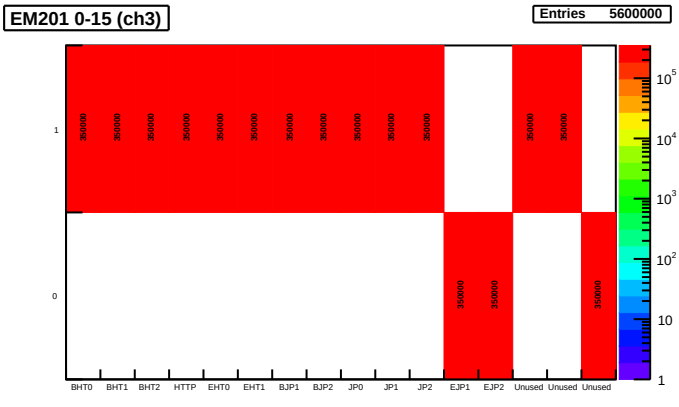

Entries 350000
VPD TAC Diff
ZDC TAC Diff
0
500
1000
1500
2000
1000
2000
3000
4000
5000
6000
7000
8000
1
10
$10^2$
$10^3$
$10^4$
$10^5$

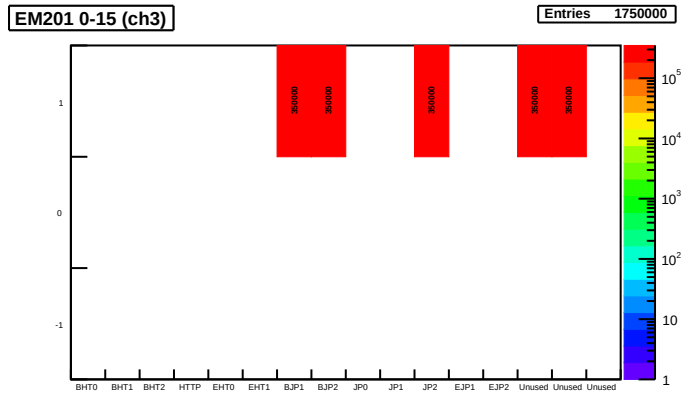

TF201 0-15 (ch0)
Entries 5600000
119821 348808 339490 2493 191 96 94245 166119 100864 81693 163319 81164 16338
350000 350000 350000 230179 3192 10510 347937 349809 349904 255755 183381 249816 268307 186081 258836 333662
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 1044962
7318 344315 346617 346712
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 5600000
2794 5367 2
350000 347226 344633 350000 350000 349998 350000 350000 350000 350000 350000 350000 350000 350000 350000 350000
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 5600000
350000 350000 350000 350000 350000 350000 350000 350000 350000 350000 350000 350000 350000 350000 350000
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused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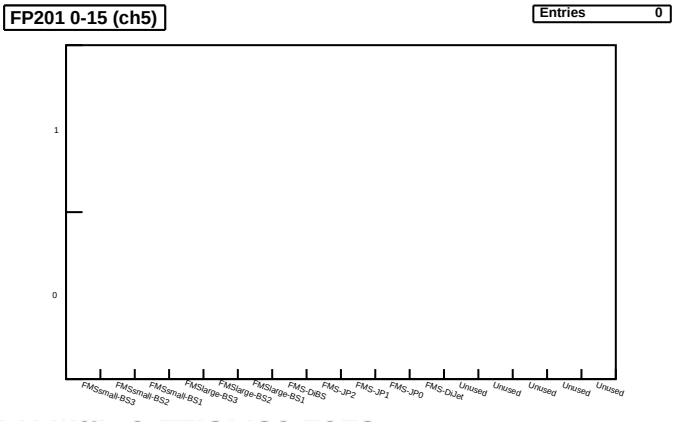
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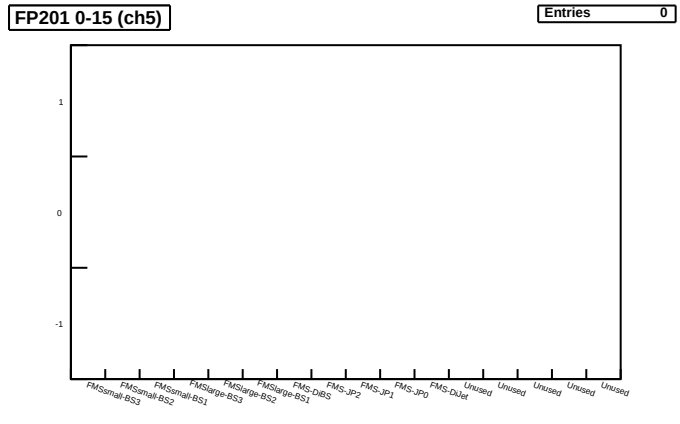
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