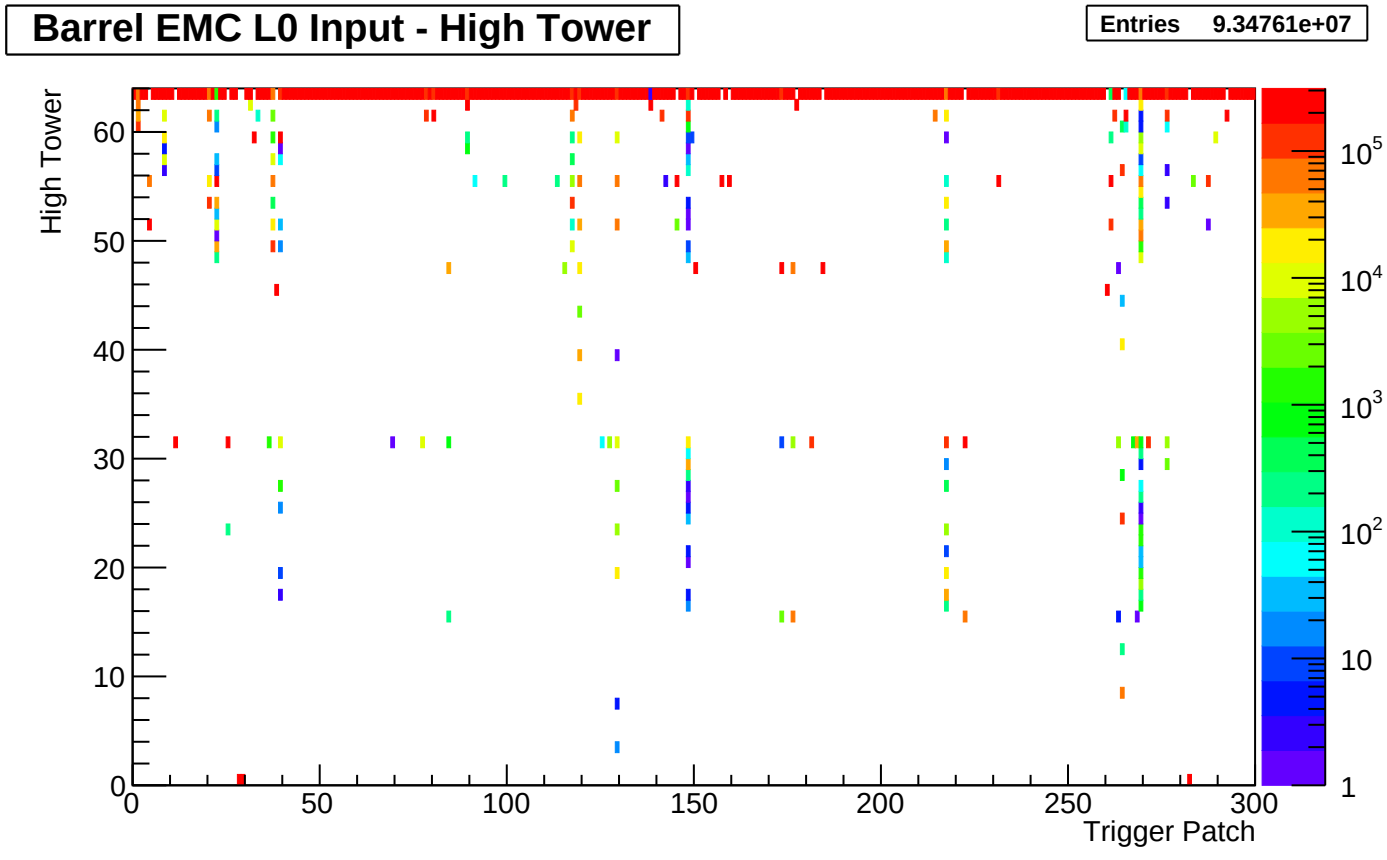
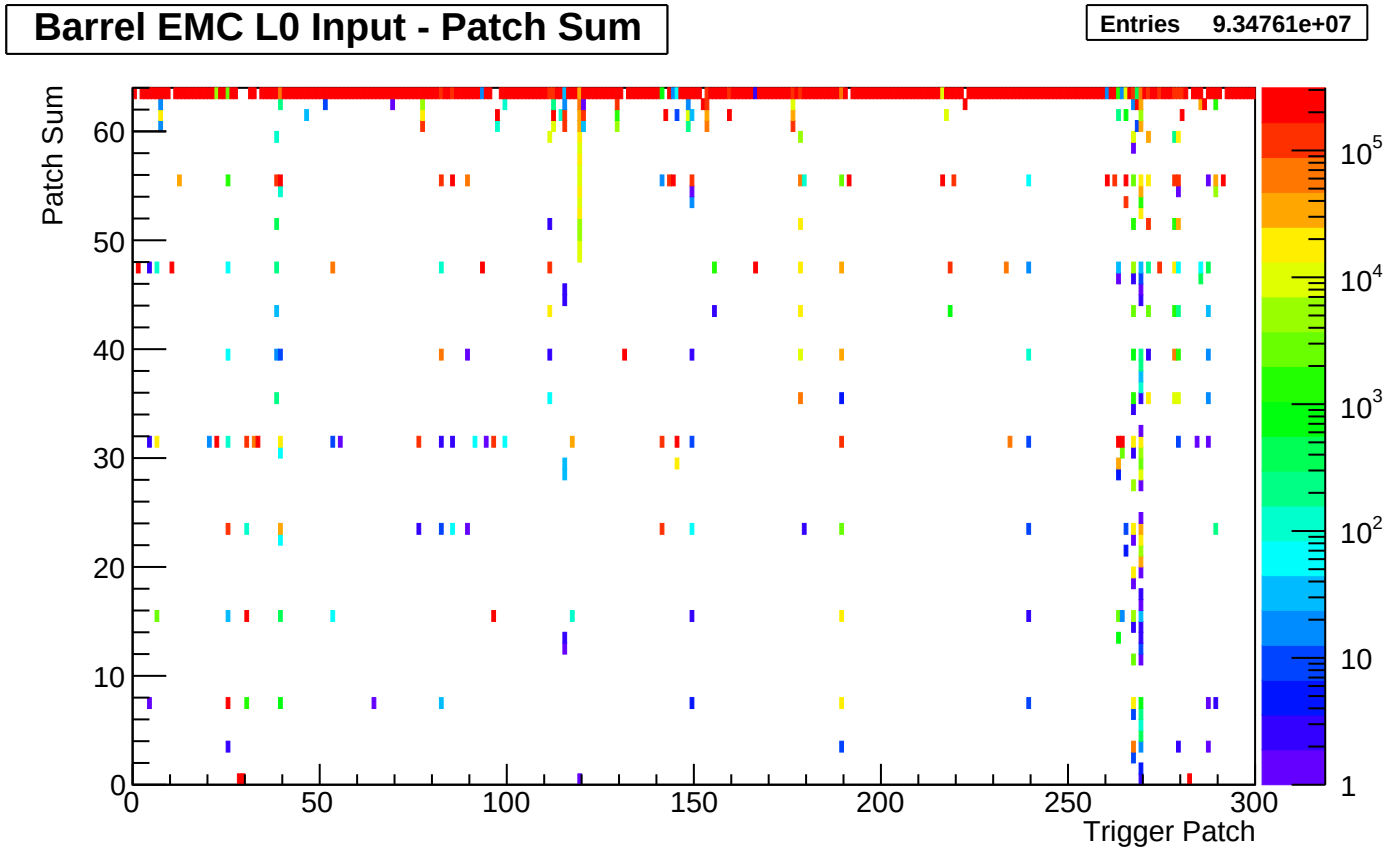
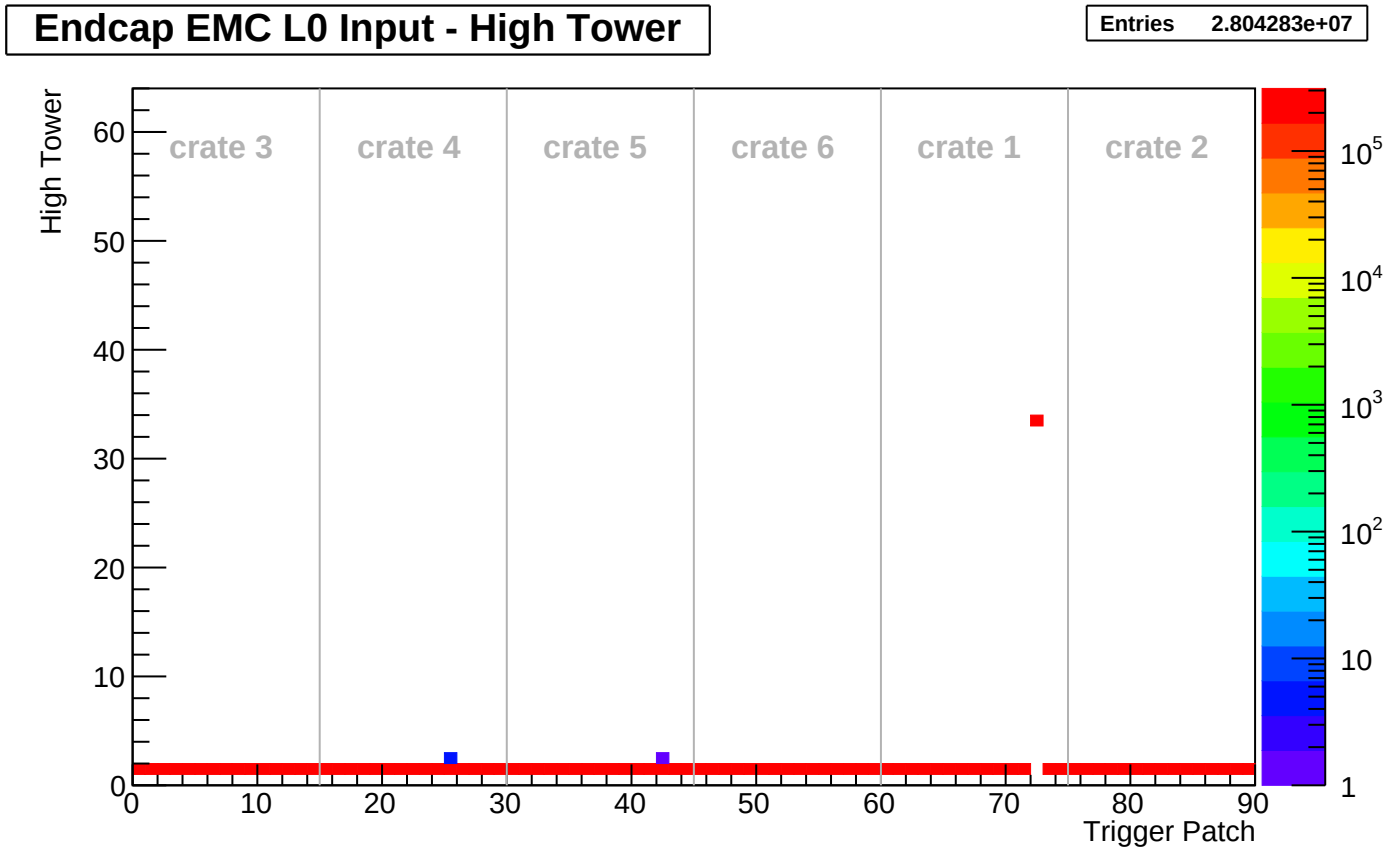
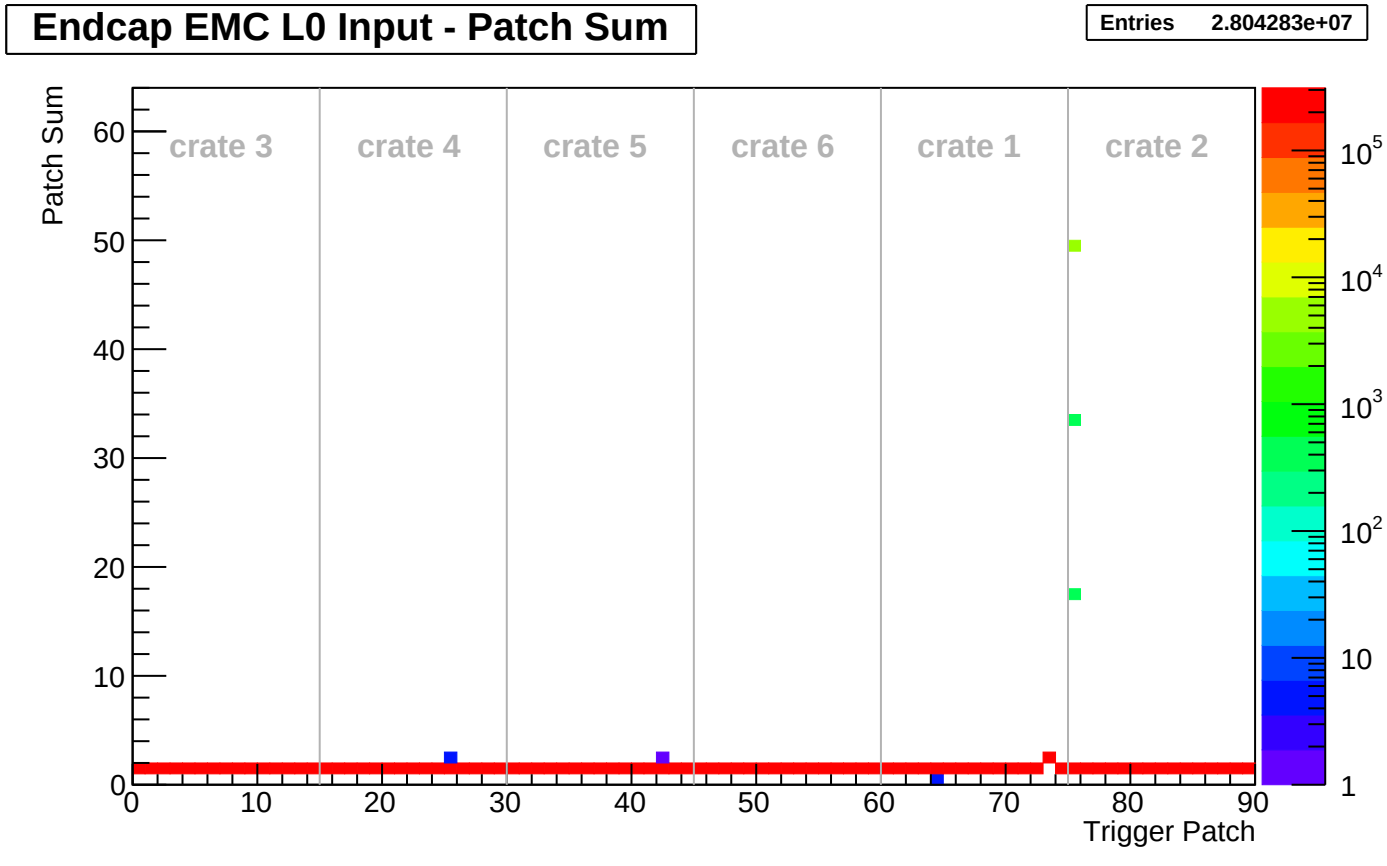
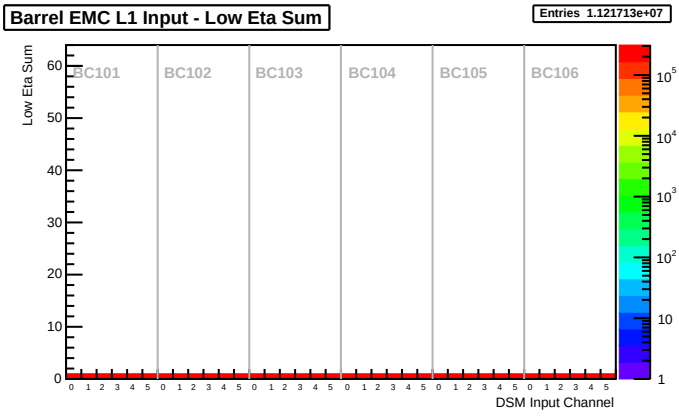
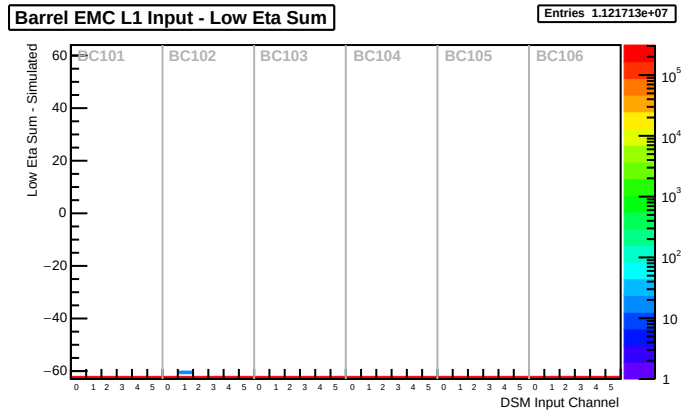

Barrel EMC L0 Input - High Tower
Entries 9.34761e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

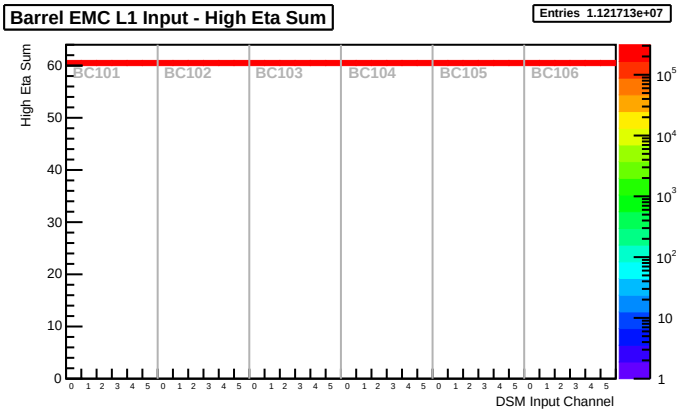

Barrel EMC L0 Input - Patch Sum
Entries 9.34761e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

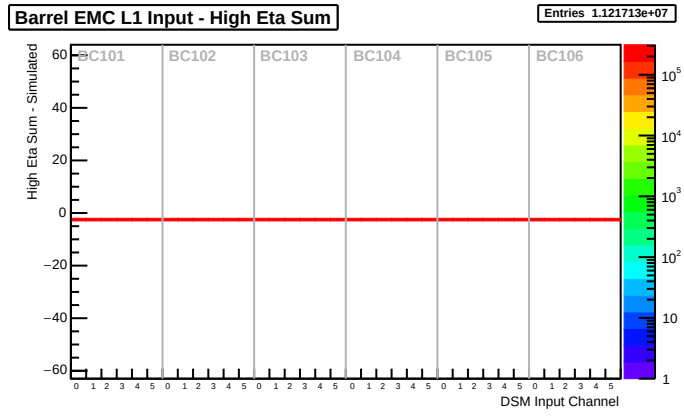

Endcap EMC L0 Input - High Tower
Entries 2.804283e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

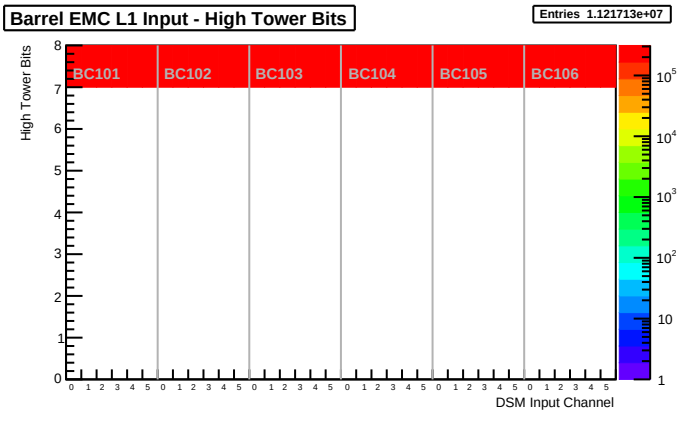

Endcap EMC L0 Input - Patch Sum
Entries 2.804283e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

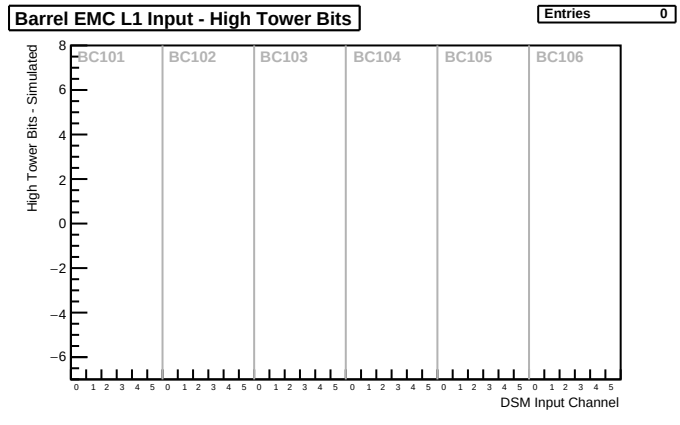

Barrel EMC L1 Input - Low Eta Sum
Entries 1.121713e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

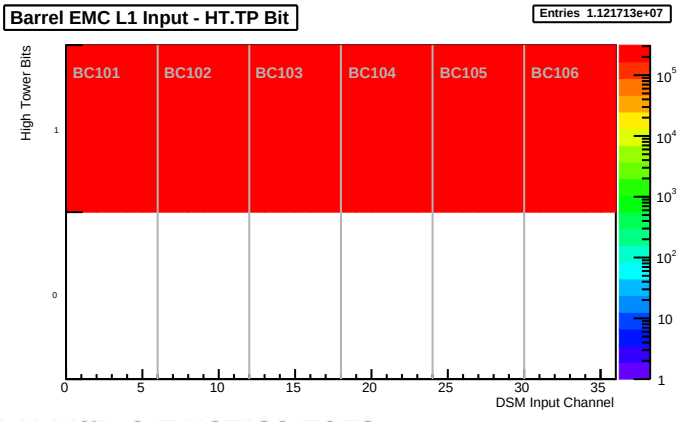

Barrel EMC L1 Input - Low Eta Sum
Entries 1.121713e+07
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

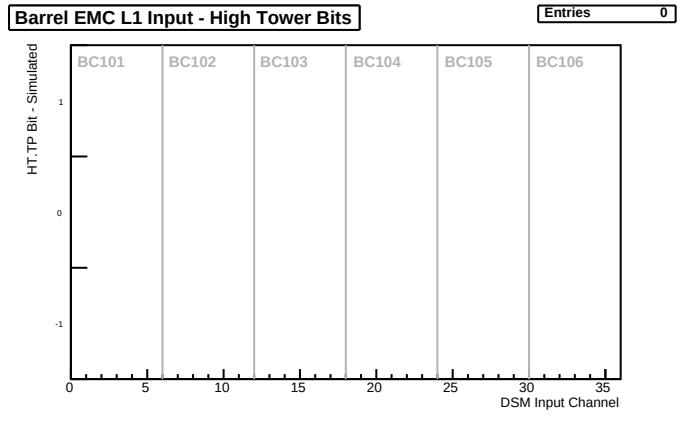

Barrel EMC L1 Input - High Eta Sum
Entries 1.121713e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

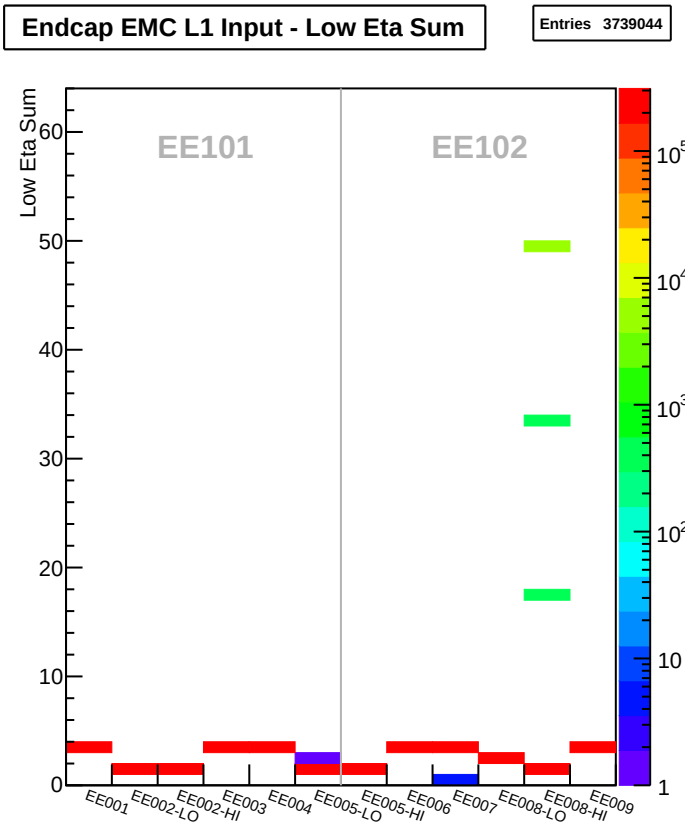

Barrel EMC L1 Input - High Eta Sum
Entries 1.121713e+07
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

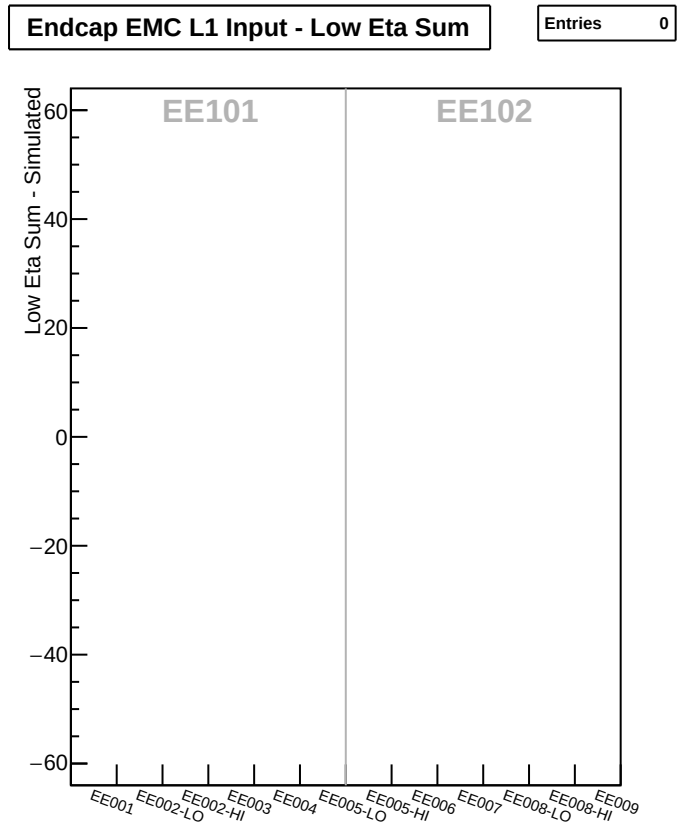

Barrel EMC L1 Input - High Tower Bits
Entries 1.121713e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

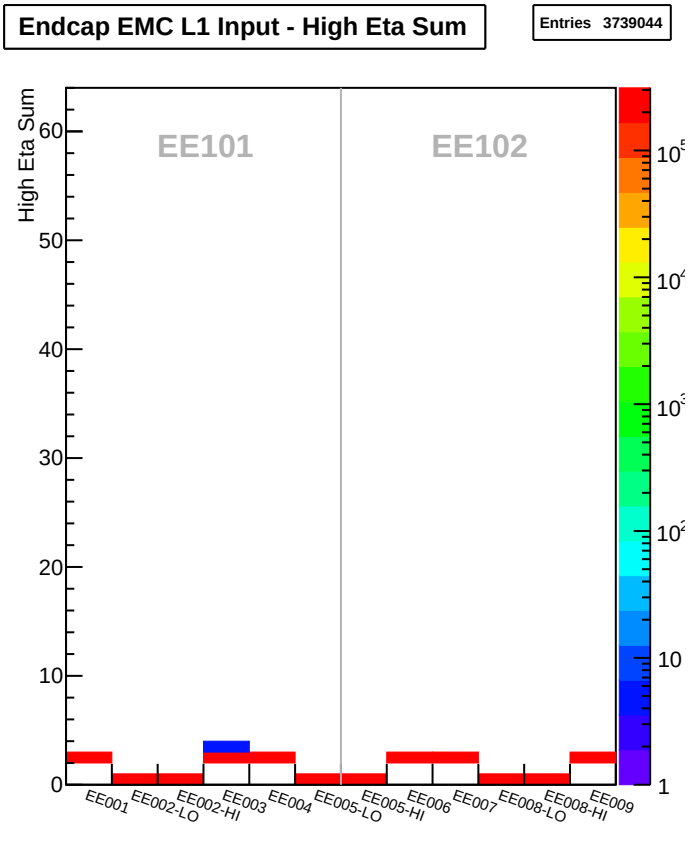

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

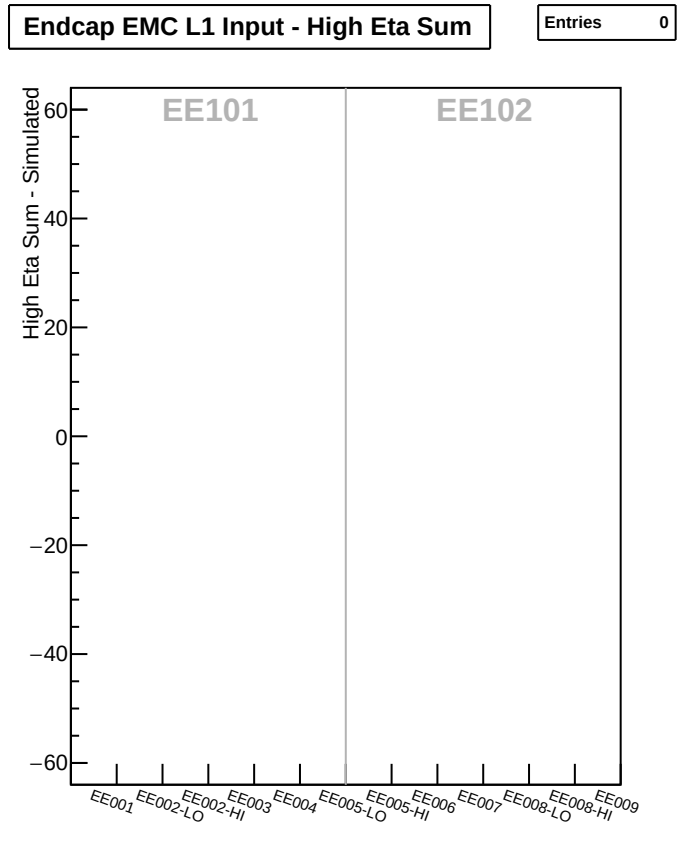

Barrel EMC L1 Input - HT.TP Bit
Entries 1.121713e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

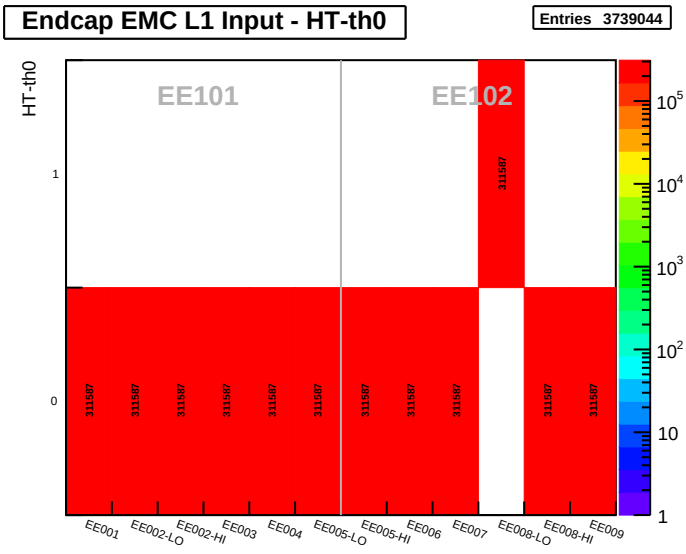

Barrel EMC L1 Input - High Tower Bits
Entries 0
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

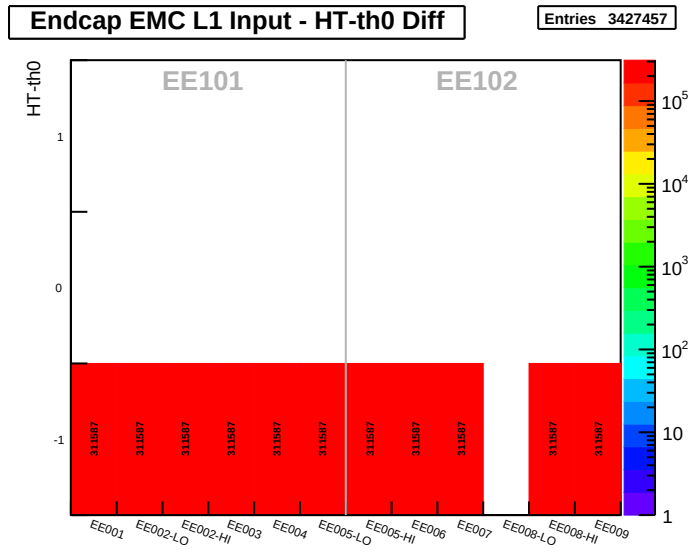

Endcap EMC L1 Input - Low Eta Sum
Entries 3739044
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

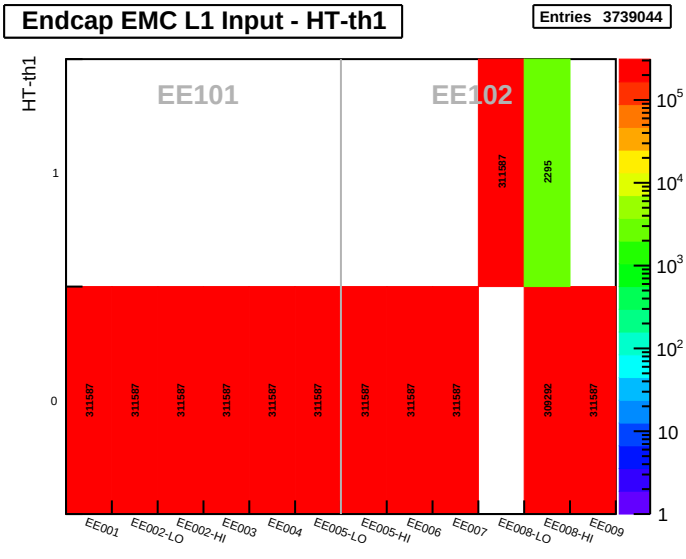

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

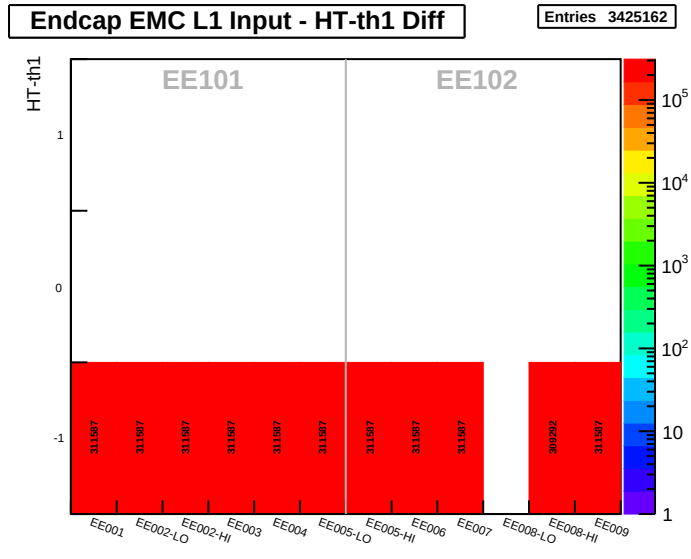

Endcap EMC L1 Input - High Eta Sum
Entries 3739044
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

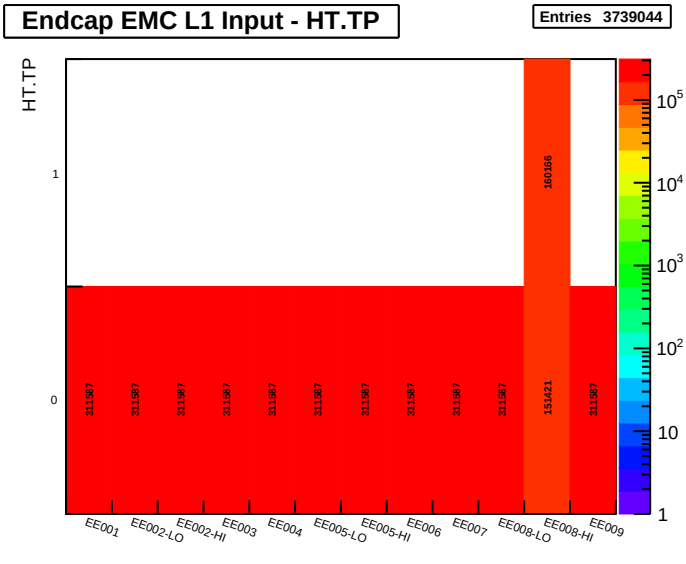

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

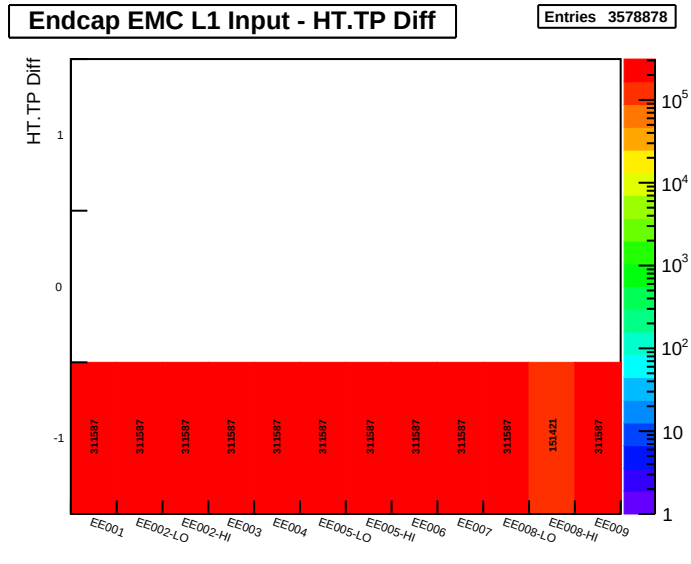

Endcap EMC L1 Input - HT-th0
Entries 3739044
HT-th0
EE101
EE102
311587
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

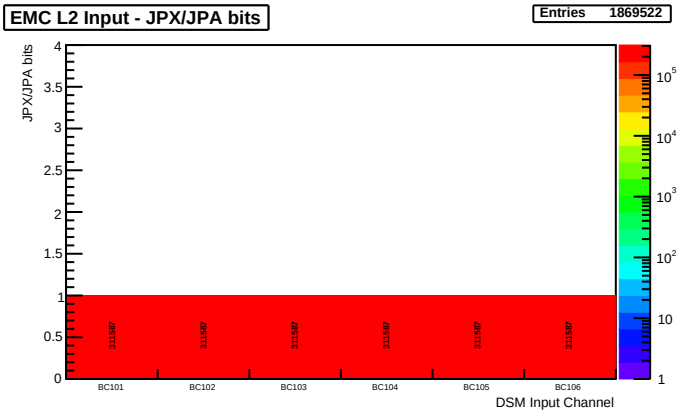

Endcap EMC L1 Input - HT-th0 Diff
Entries 3427457
HT-th0
EE101
EE102
311587
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

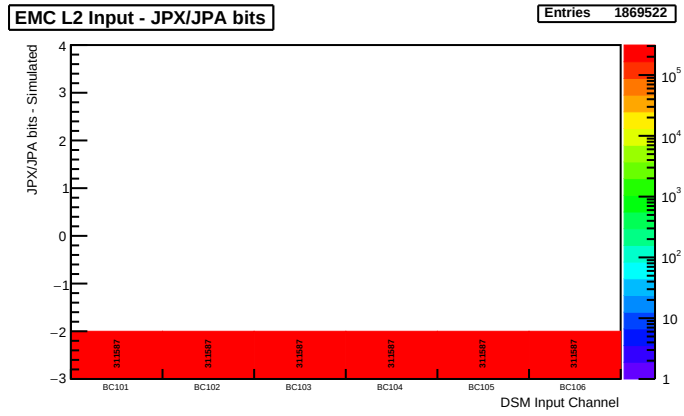

Endcap EMC L1 Input - HT-th1
Entries 3739044
HT-th1
EE101
EE102
311587
2295
309292
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

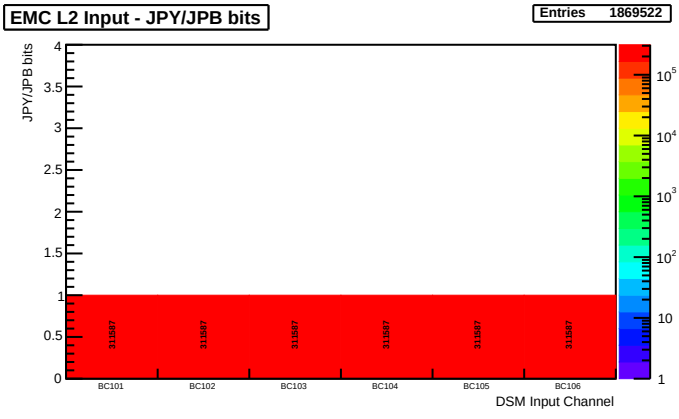

Endcap EMC L1 Input - HT-th1 Diff
Entries 3425162
HT-th1
EE101
EE102
311587
309292
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

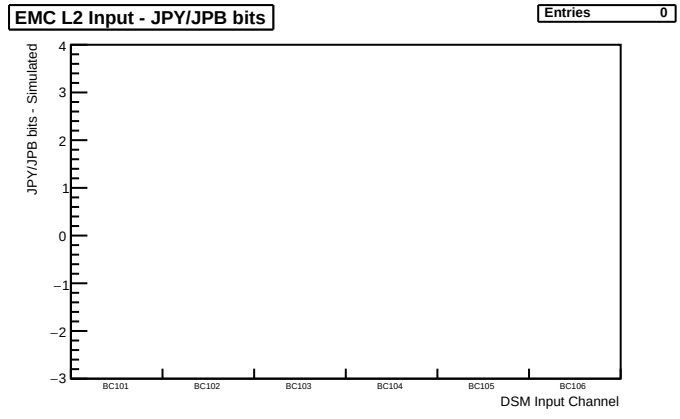

Endcap EMC L1 Input - HT.TP
Entries 3739044
HT.TP
160166
311587
151421
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

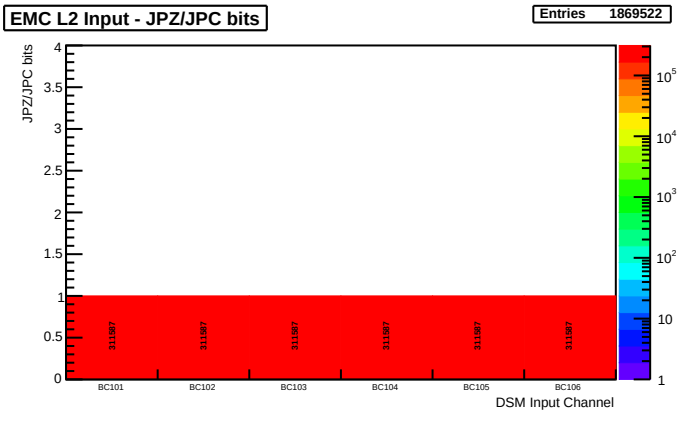

Endcap EMC L1 Input - HT.TP Diff
Entries 3578878
HT.TP Diff
311587
151421
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

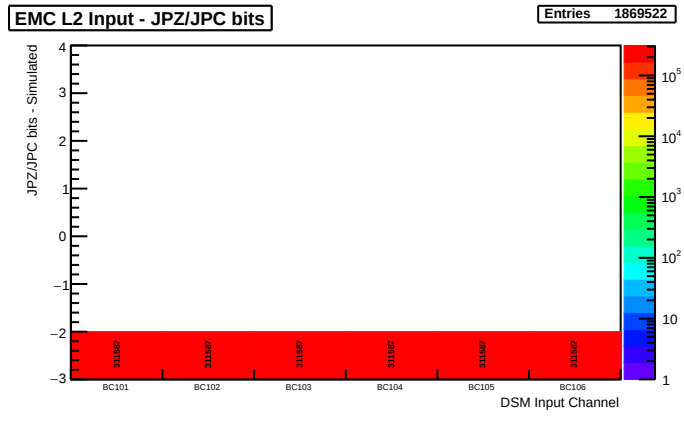

EMC L2 Input - JPX/JPA bits
Entries 1869522
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
311587

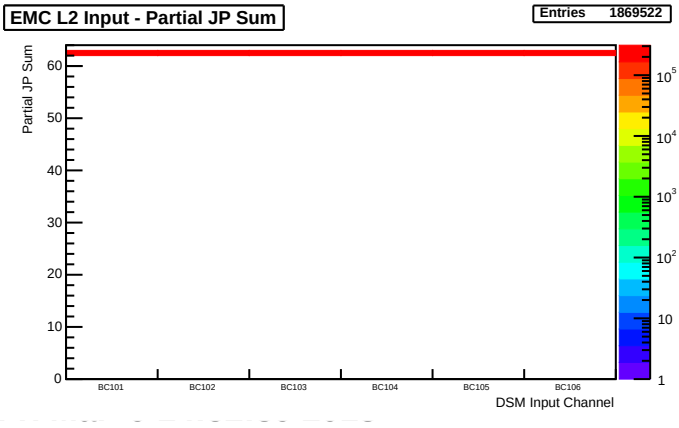

EMC L2 Input - JPX/JPA bits
Entries 1869522
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
311587

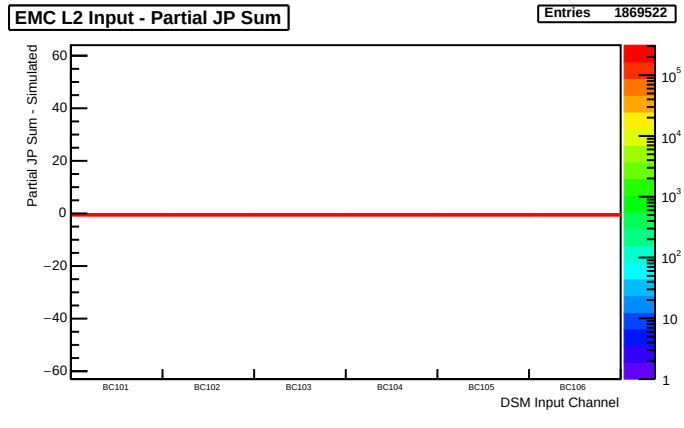

EMC L2 Input - JPY/JPB bits
Entries 1869522
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
311587

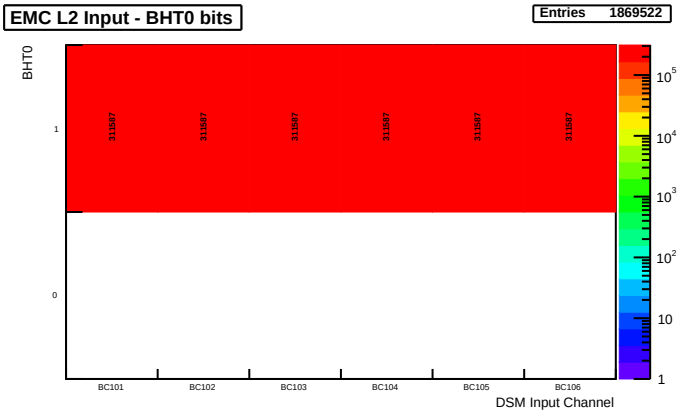

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

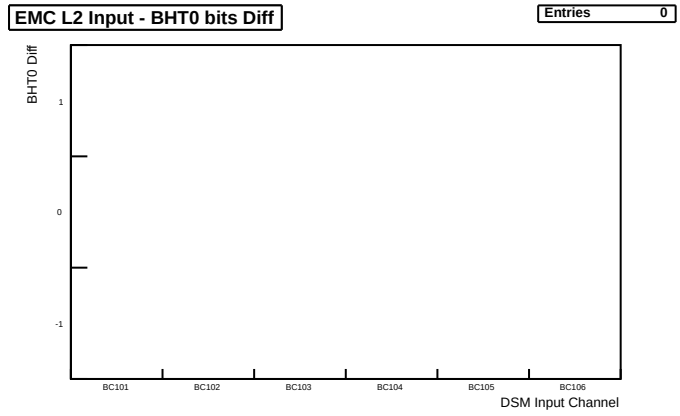

EMC L2 Input - JPZ/JPC bits
Entries 1869522
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
311587

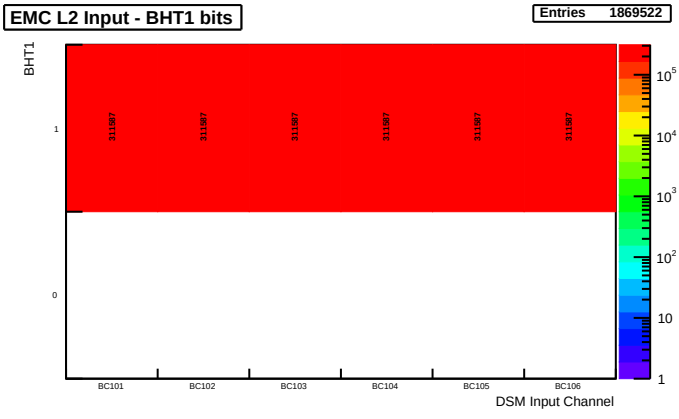

EMC L2 Input - JPZ/JPC bits
Entries 1869522
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
311587

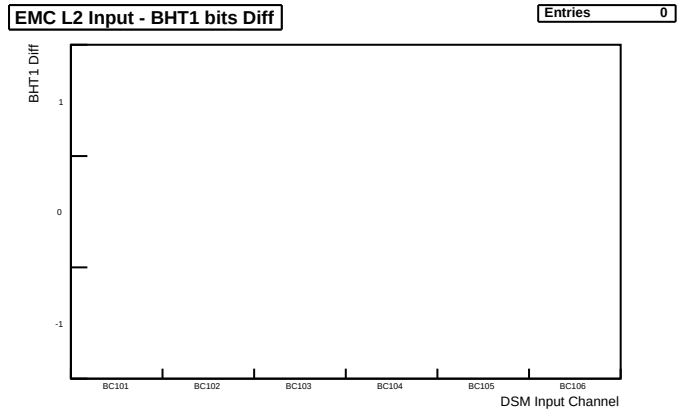

EMC L2 Input - Partial JP Sum
Entries 1869522
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

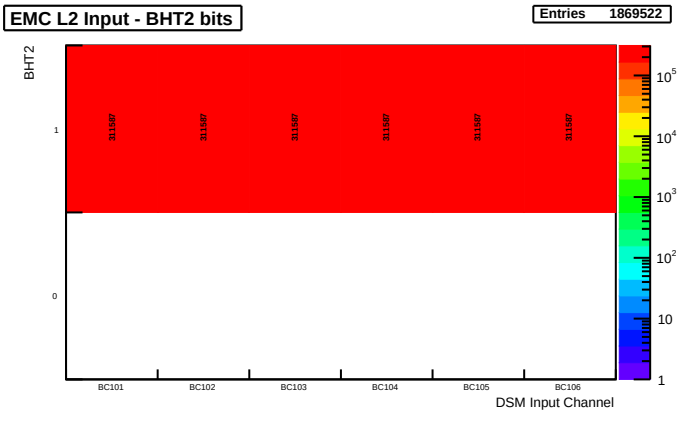

EMC L2 Input - Partial JP Sum
Entries 1869522
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

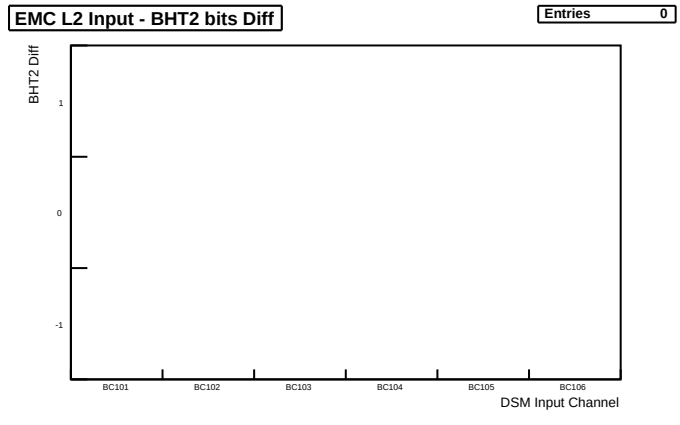

EMC L2 Input - BHT0 bits
Entries 1869522
BHT0
311587
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

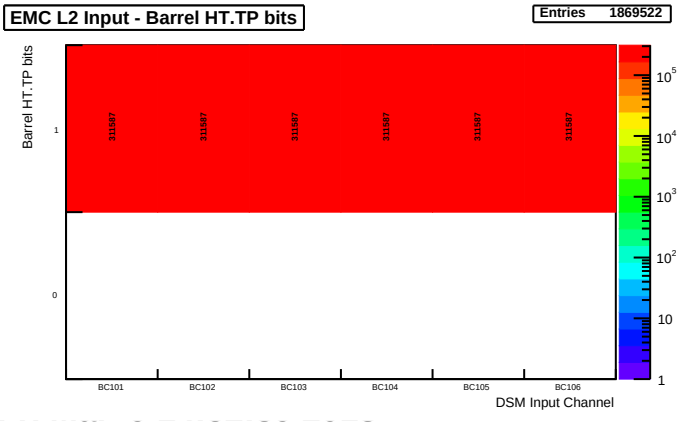

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

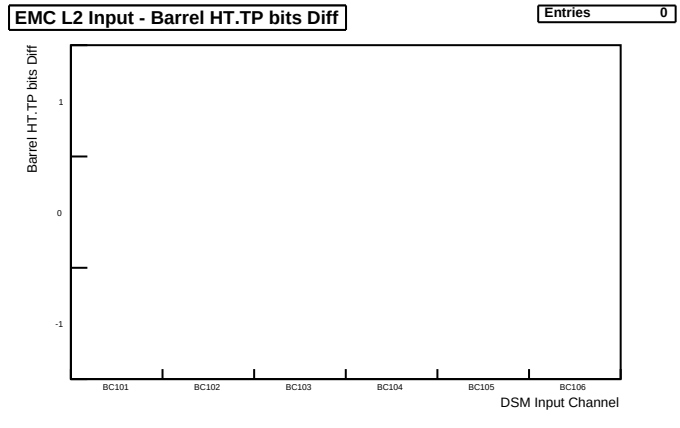

EMC L2 Input - BHT1 bits
Entries 1869522
BHT1
311587
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

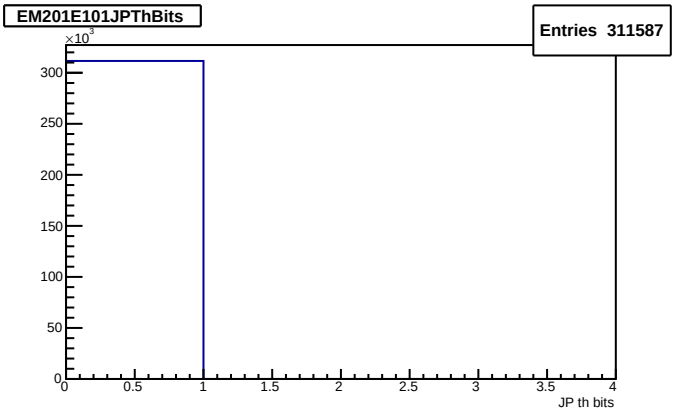

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

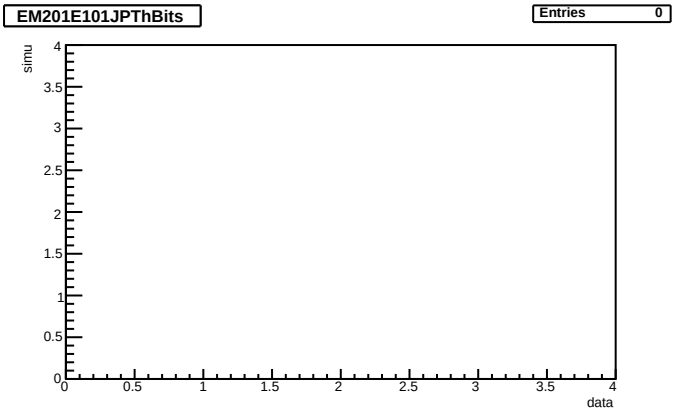

EMC L2 Input - BHT2 bits
Entries 1869522
BHT2
311587
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

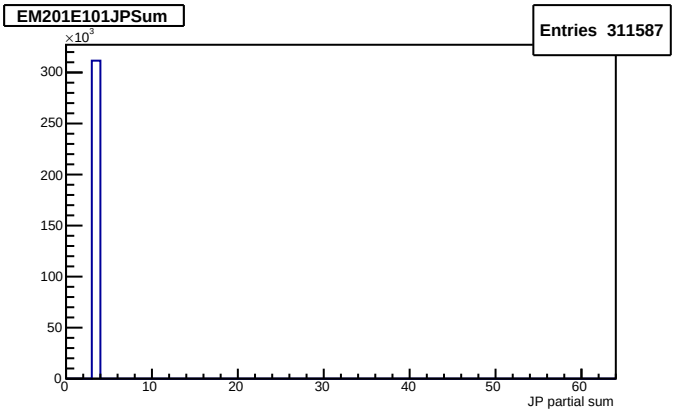

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

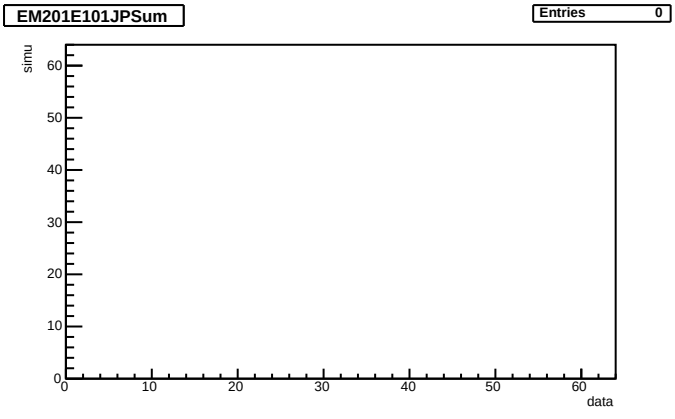

EMC L2 Input - Barrel HT.TP bits
Entries 1869522
Barrel HT.TP bits
311587
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

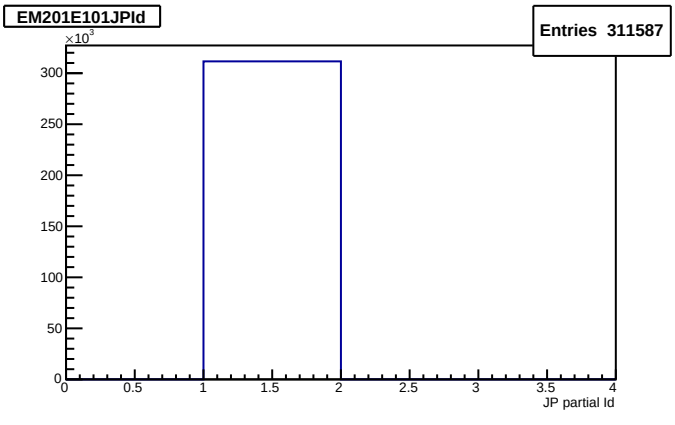

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

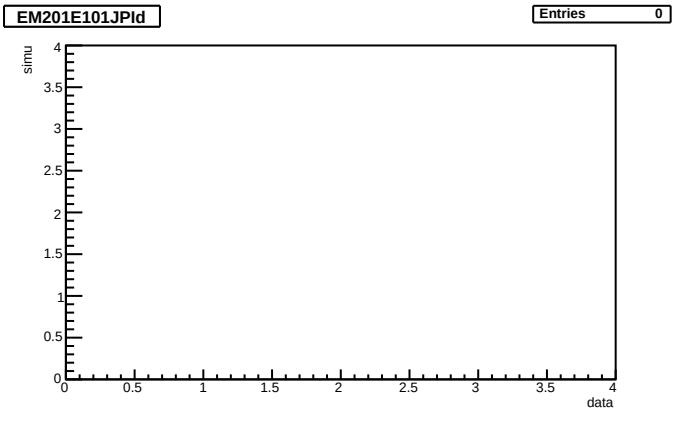

EM201E101JPThBits
Entries 311587
JP th bits

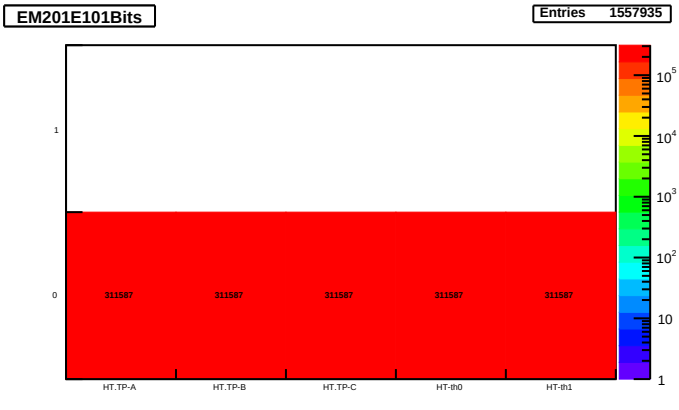

EM201E101JPThBits
Entries 0
simu
data

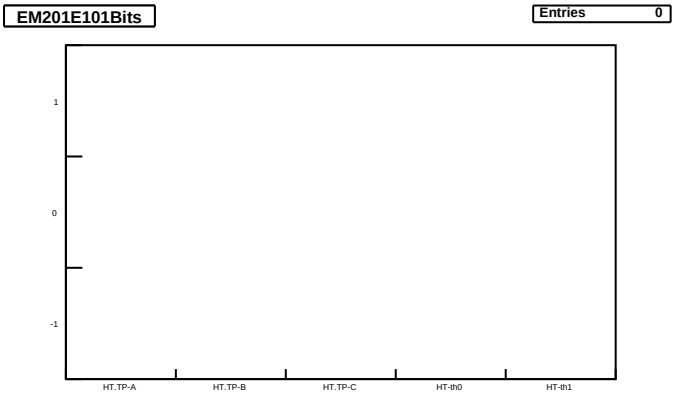

EM201E101JPSum
Entries 311587
JP partial sum

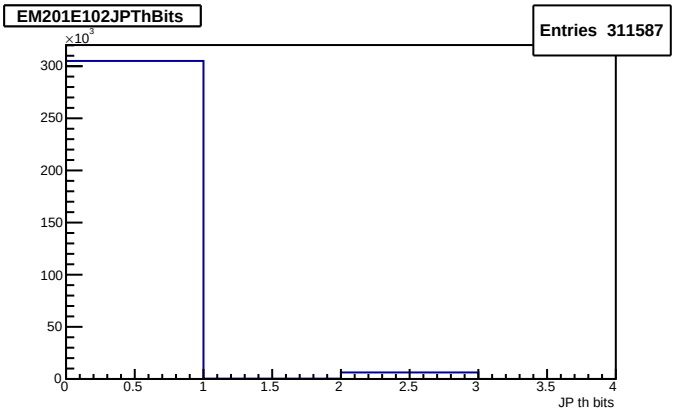

EM201E101JPSum
Entries 0
simu
data

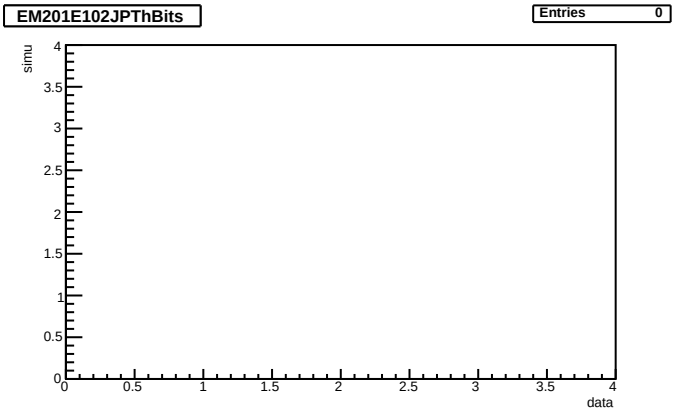

EM201E101JPId
Entries 311587
JP partial Id

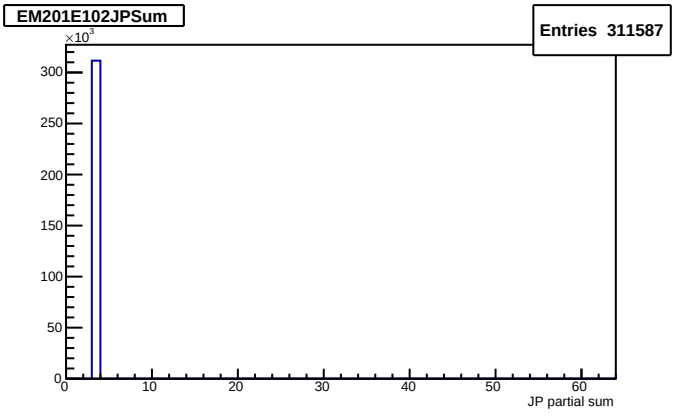

EM201E101JPId
Entries 0
simu
data

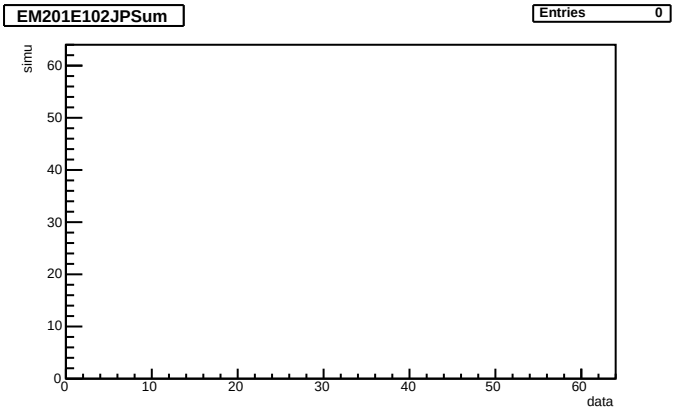

EM201E101Bits
Entries 1557935
311587
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

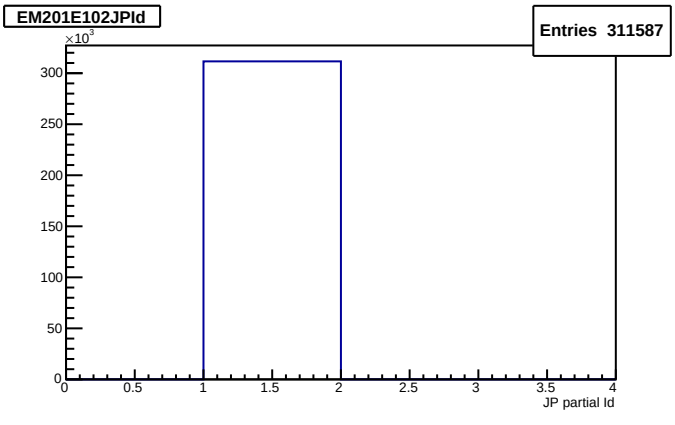

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

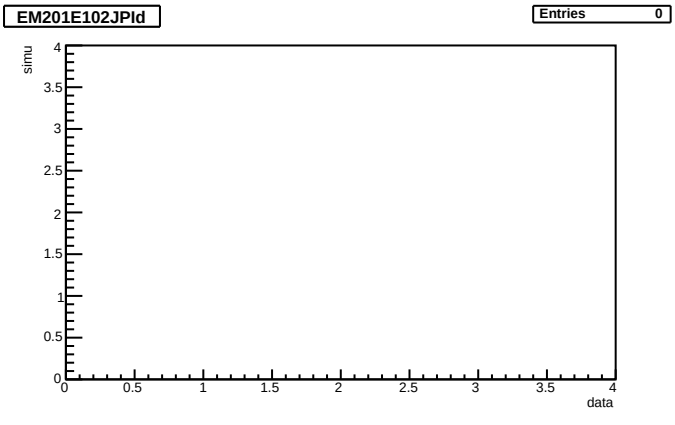

EM201E102JPThBits
Entries 311587
×10^3
JP th bits

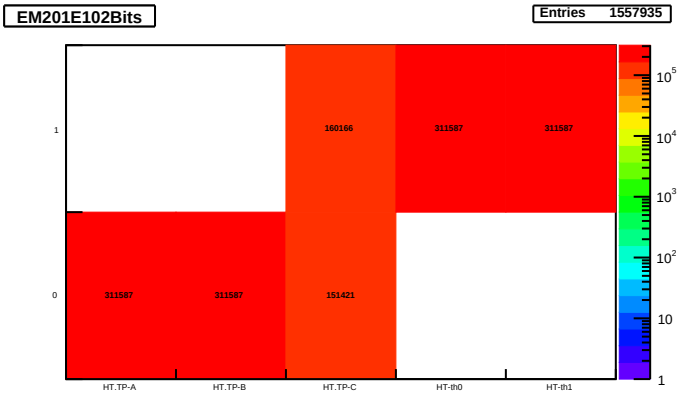

EM201E102JPThBits
Entries 0
simu
data

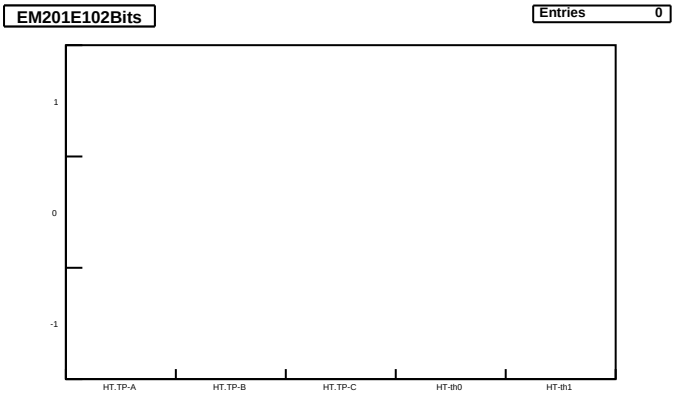

EM201E102JPSum
Entries 311587
×10^3
JP partial sum

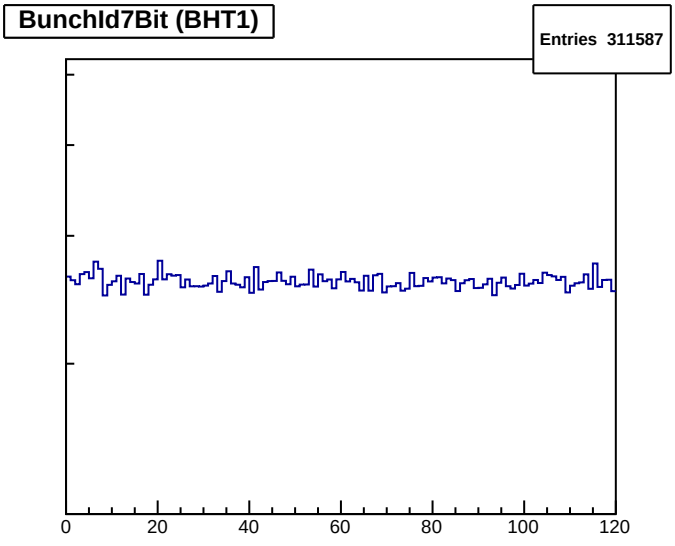

EM201E102JPSum
Entries 0
simu
data

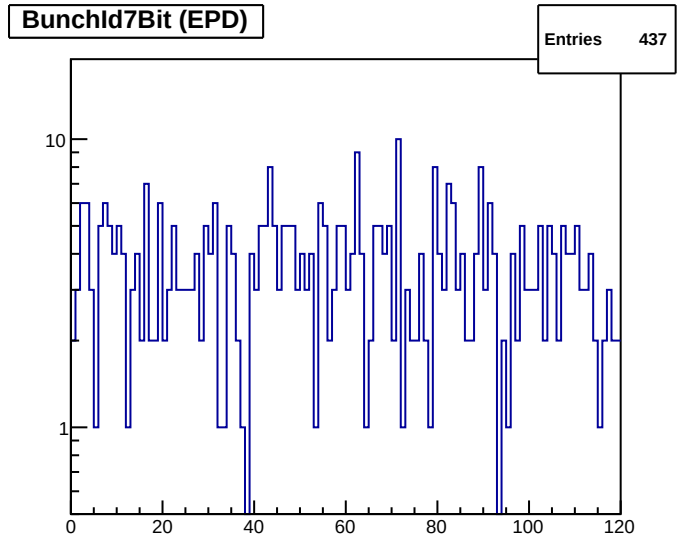

EM201E102JPId
Entries 311587
×10^3
JP partial Id

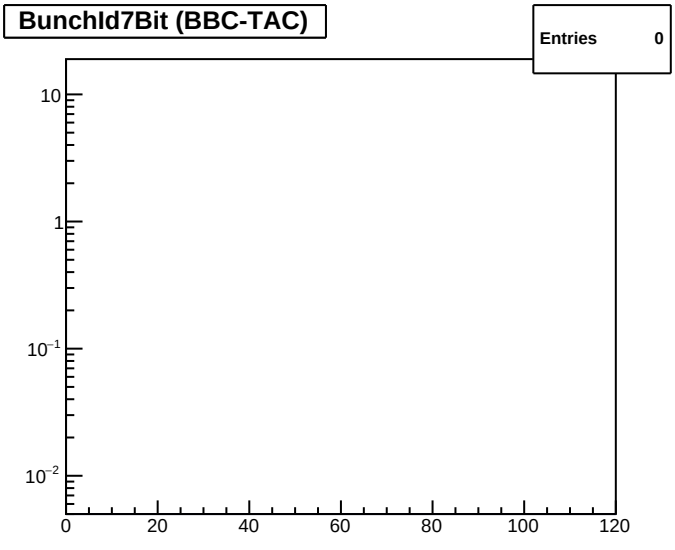

EM201E102JPId
Entries 0
simu
data

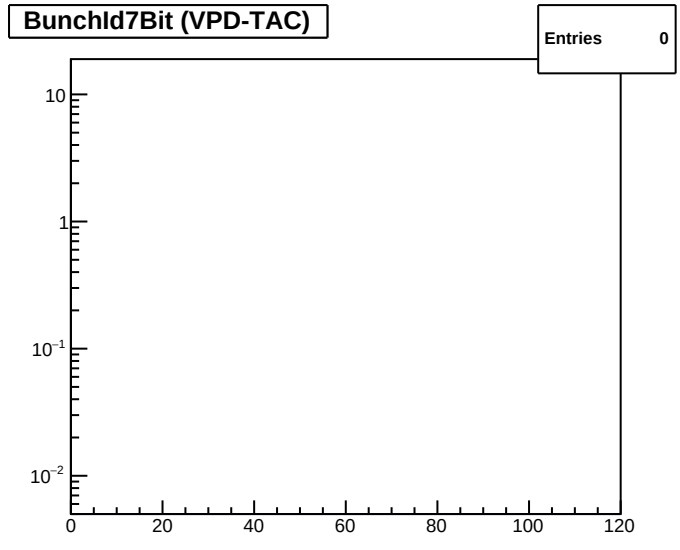

EM201E102Bits
Entries 1557935
160166
311587
311587
311587
311587
151421
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

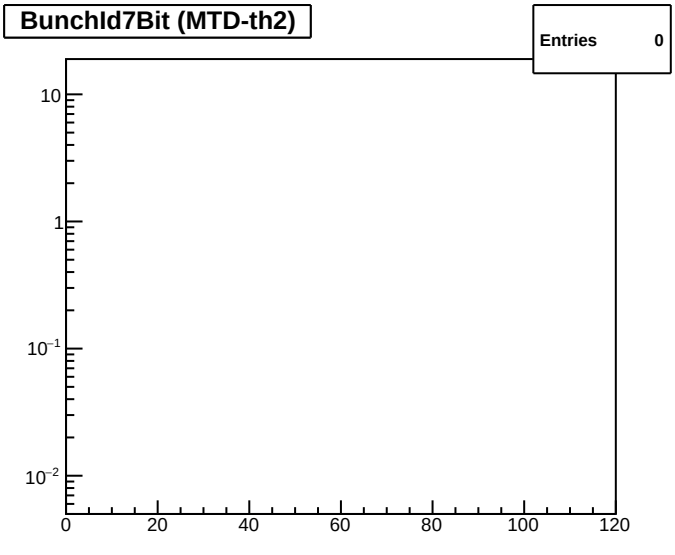

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

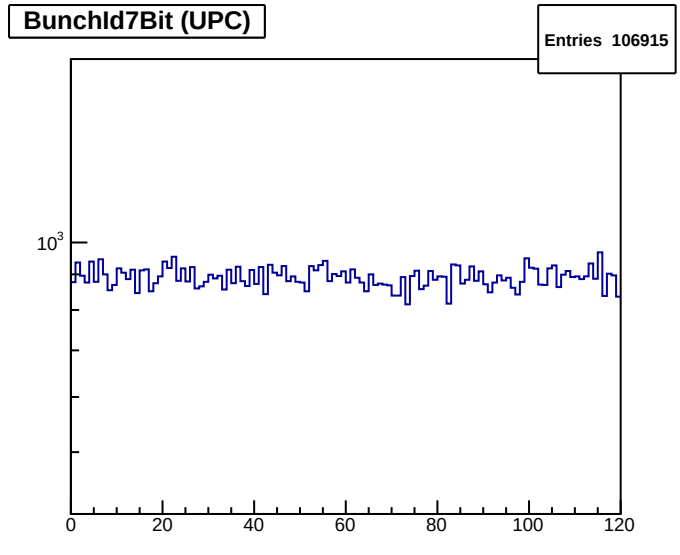

BunchId7Bit (BHT1)
Entries 311587
0
20
40
60
80
100
120

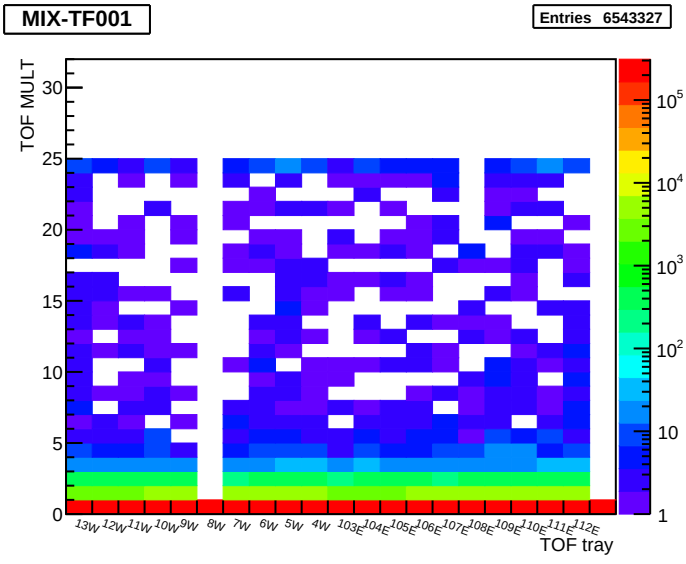

BunchId7Bit (EPD)
Entries 437
10
1
0
20
40
60
80
100
120

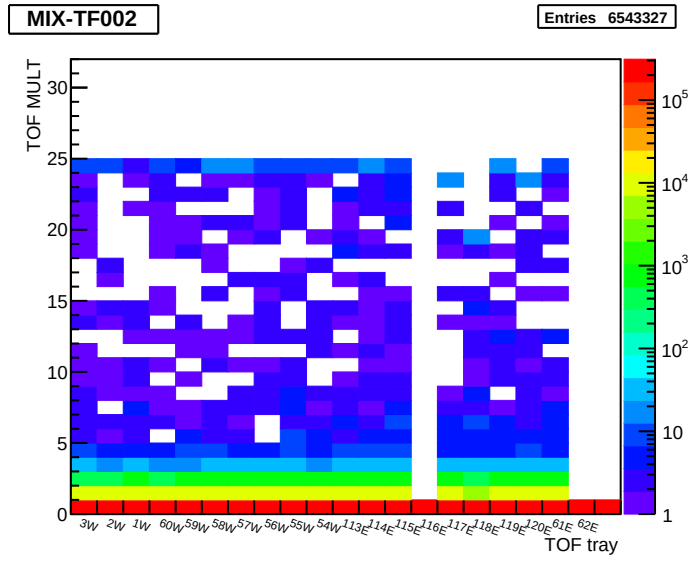

BunchId7Bit (BBC-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

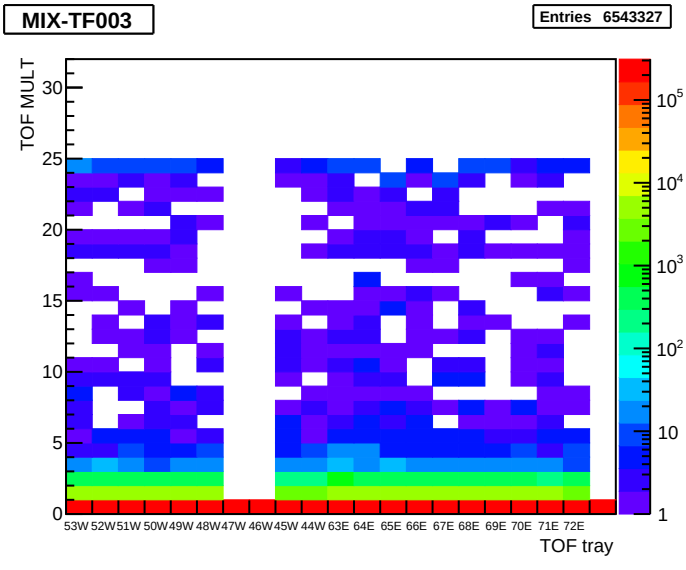

BunchId7Bit (VPD-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

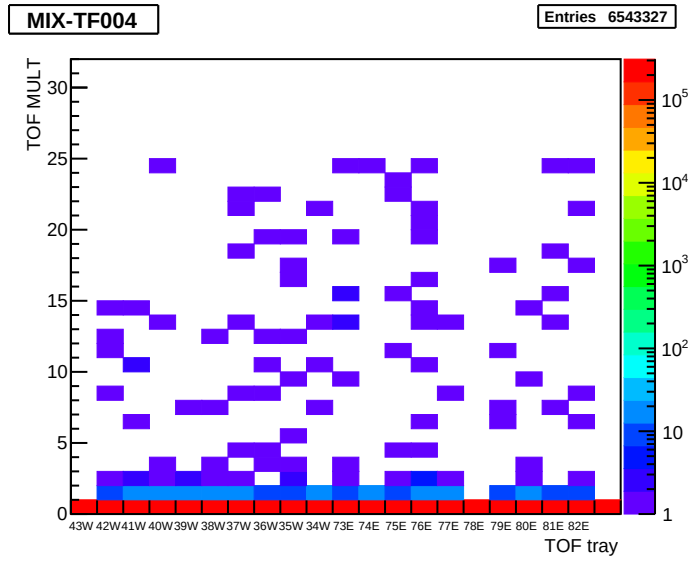

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

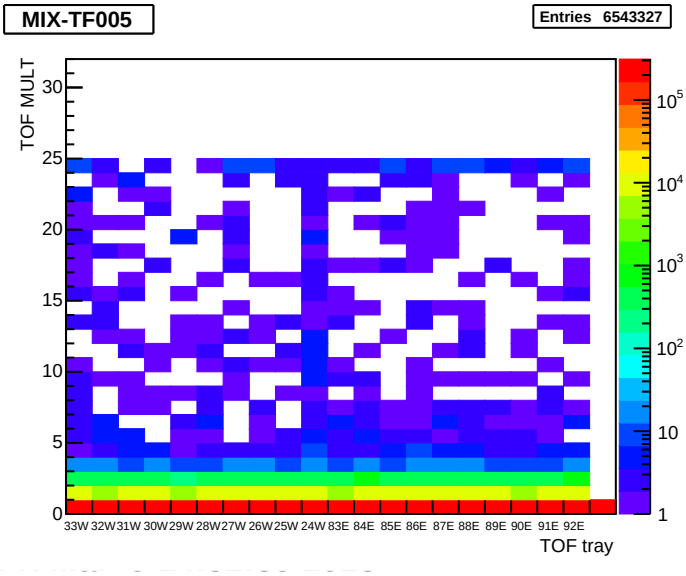

BunchId7Bit (UPC)
Entries 106915
10^{3}
0
20
40
60
80
100
120

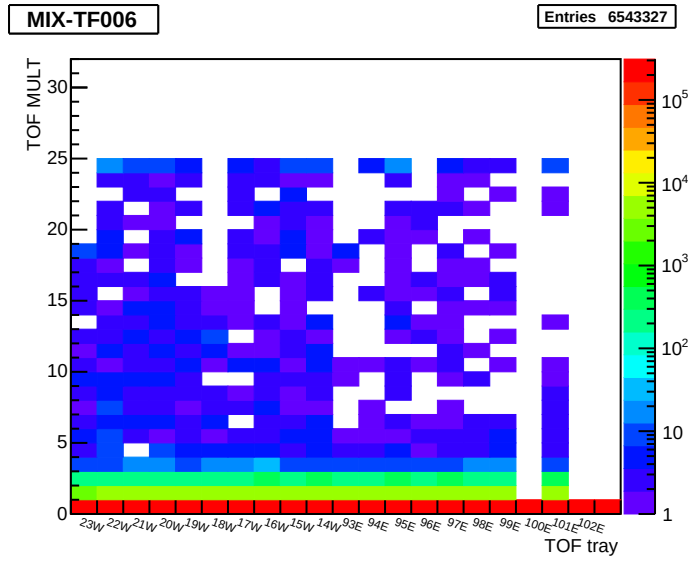

MIX-TF001
Entries 6543327
TOF MULT
TOF tray

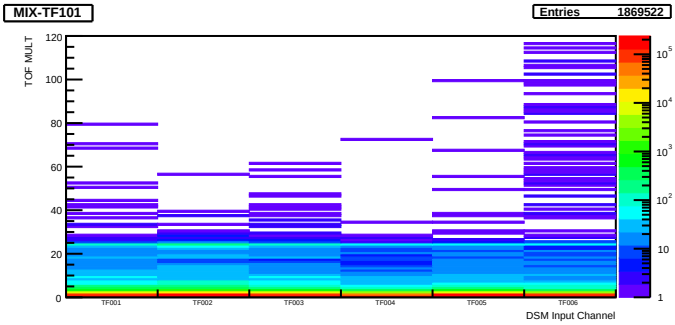

MIX-TF002
Entries 6543327
TOF MULT
TOF tray

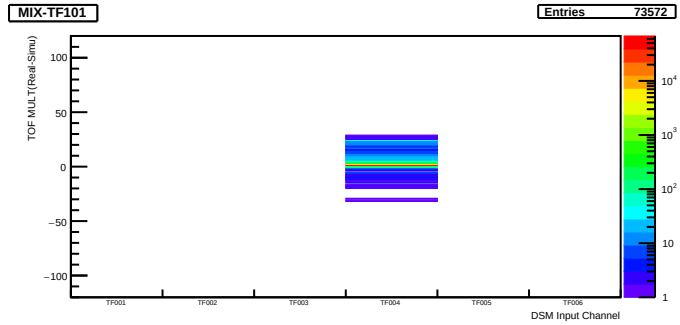

MIX-TF003
Entries 6543327
TOF MULT
TOF tray

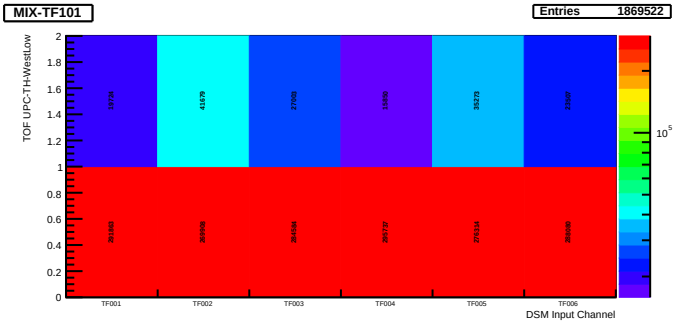

MIX-TF004
Entries 6543327
TOF MULT
TOF tray

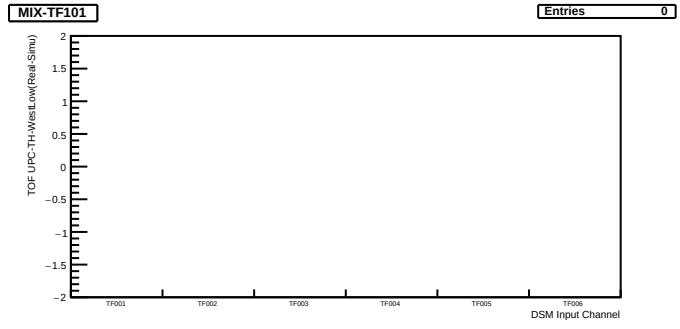

MIX-TF005
Entries 6543327
TOF MULT
TOF tray

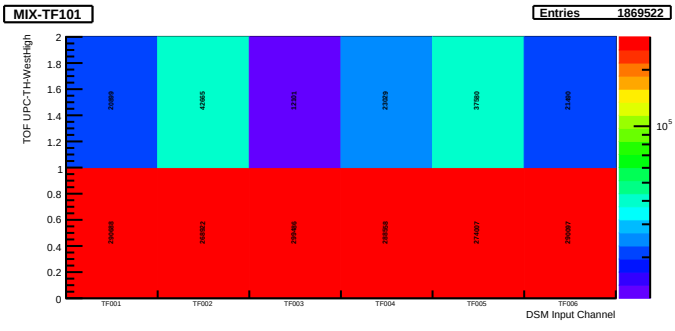

MIX-TF006
Entries 6543327
TOF MULT
TOF tray

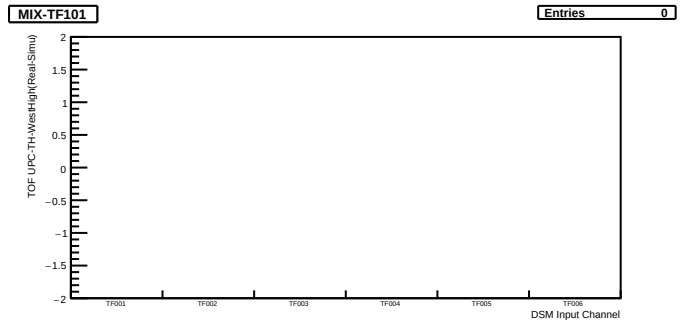

MIX-TF101
Entries 1869522
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

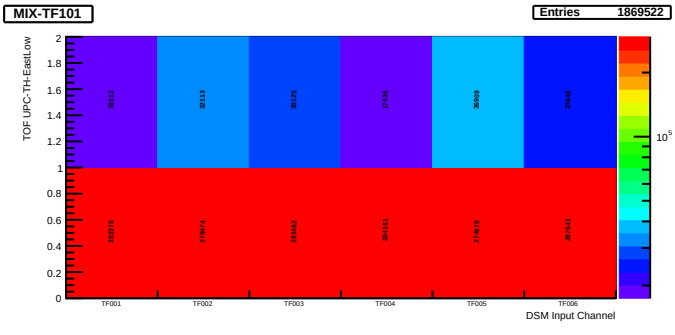

MIX-TF101
Entries 73572
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

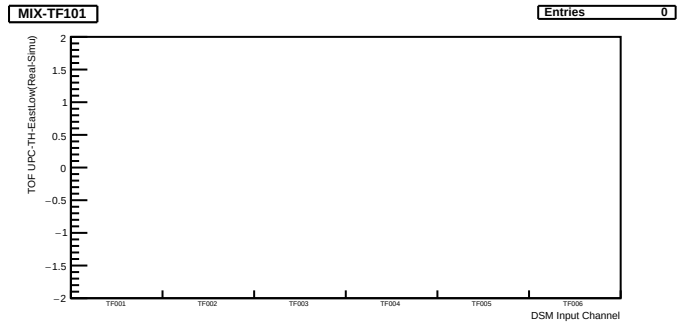

MIX-TF101
Entries 1869522
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

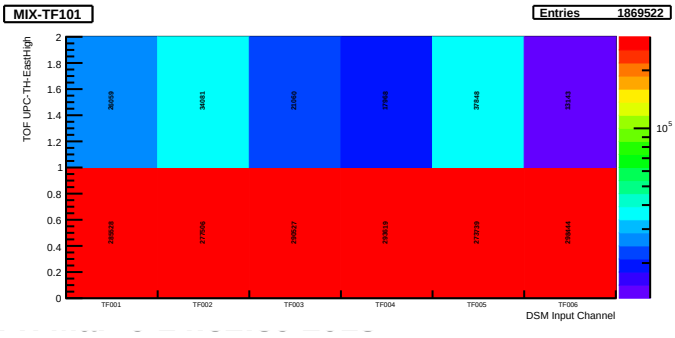

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

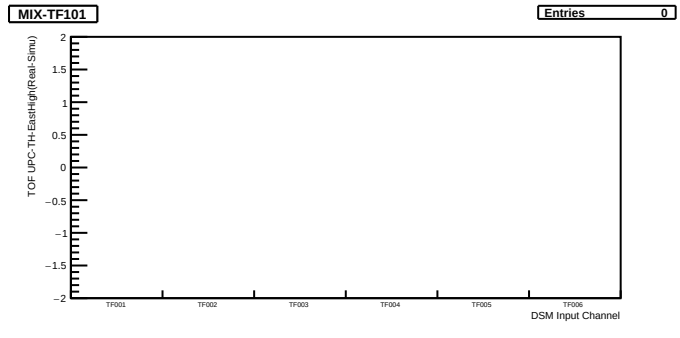

MIX-TF101
Entries 1869522
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

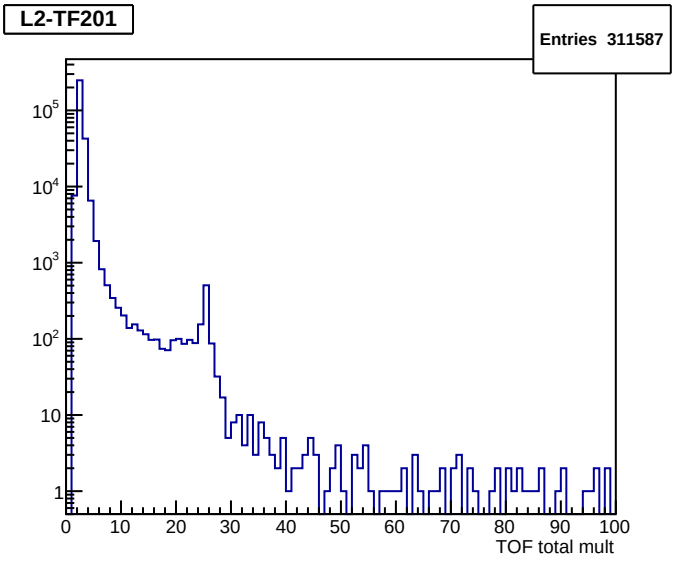

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

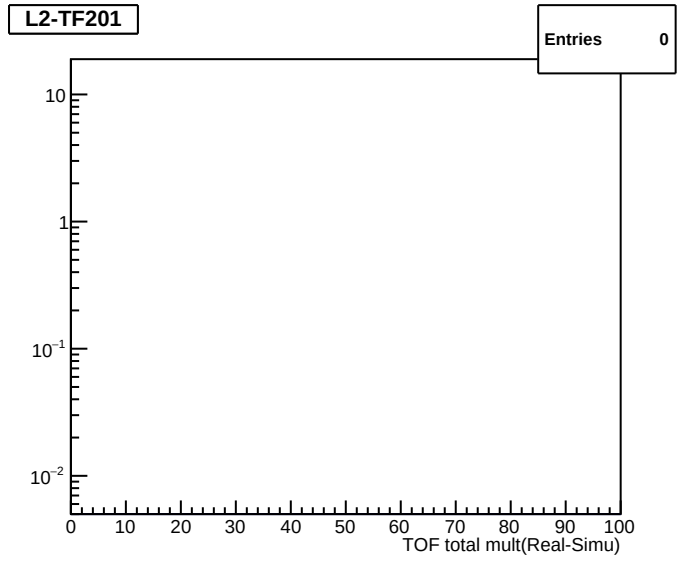

MIX-TF101
Entries 1869522
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

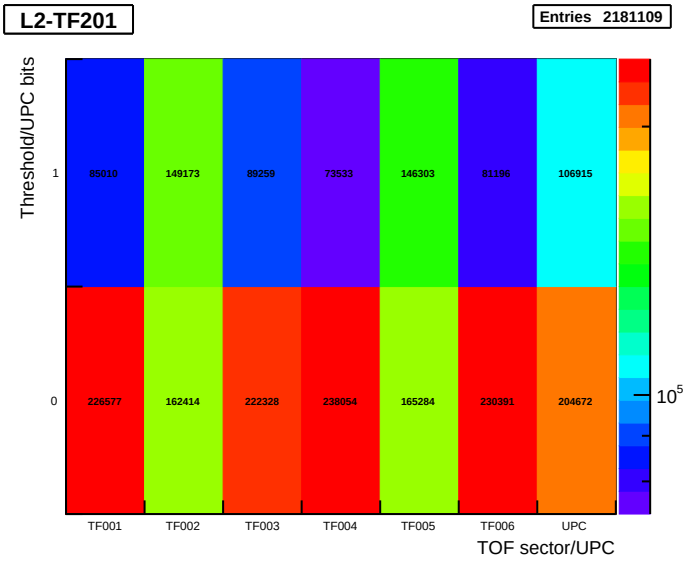

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

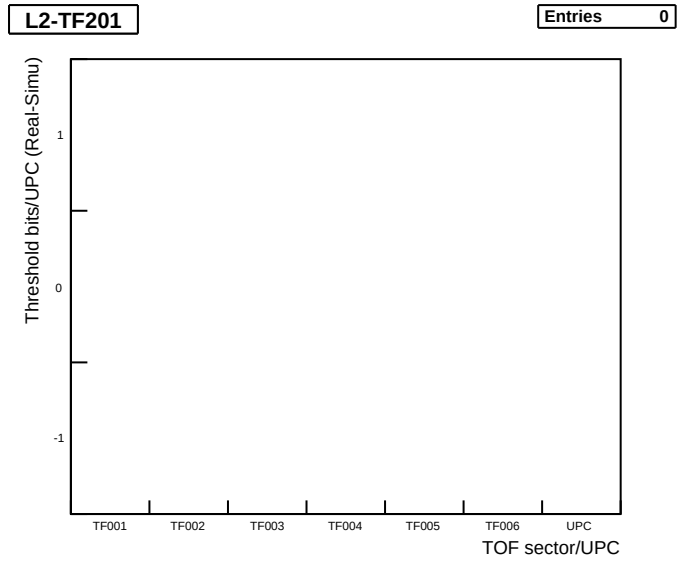

MIX-TF101
Entries 1869522
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

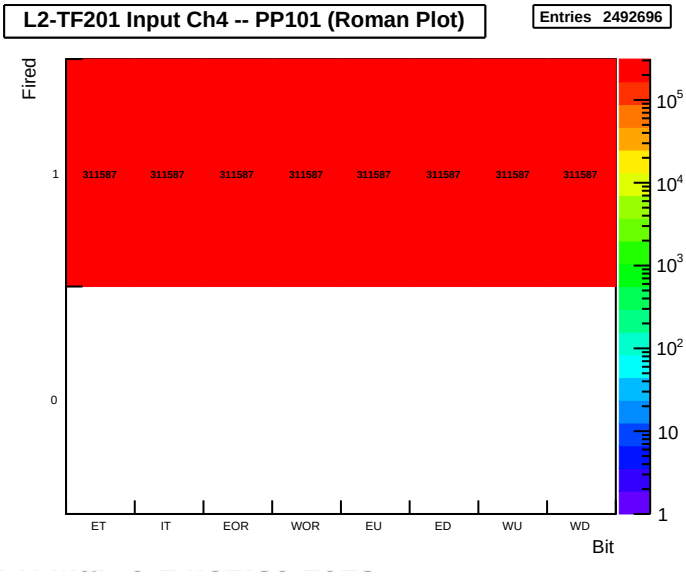

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

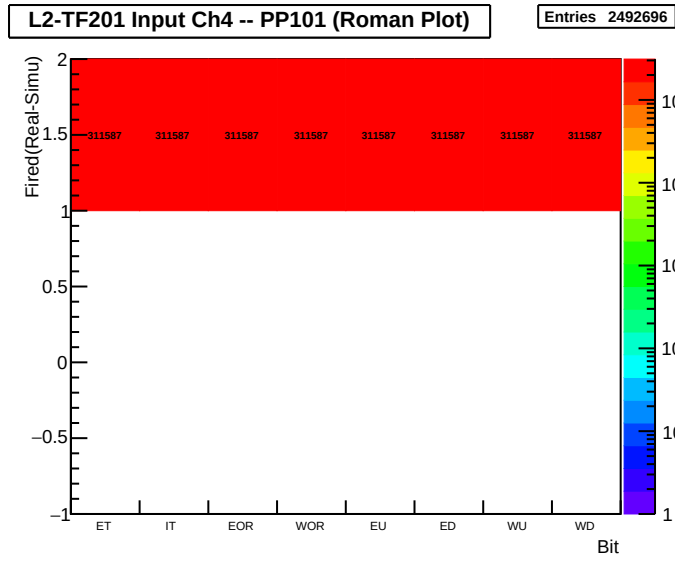

L2-TF201
Entries 311587
TOF total mult

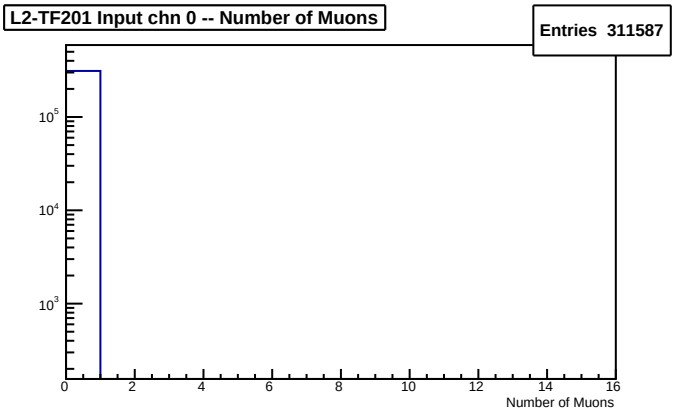

L2-TF201
Entries 0
TOF total mult(Real-Simu)

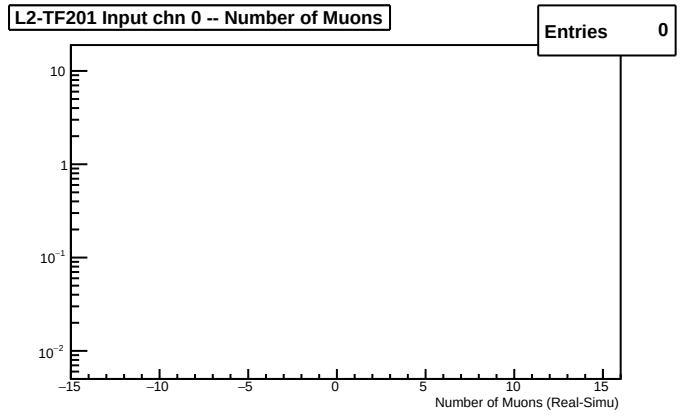

L2-TF201
Entries 2181109
Threshold/UPC bits
85010
149173
89259
73533
146303
81196
106915
226577
162414
222328
238054
165284
230391
204672
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

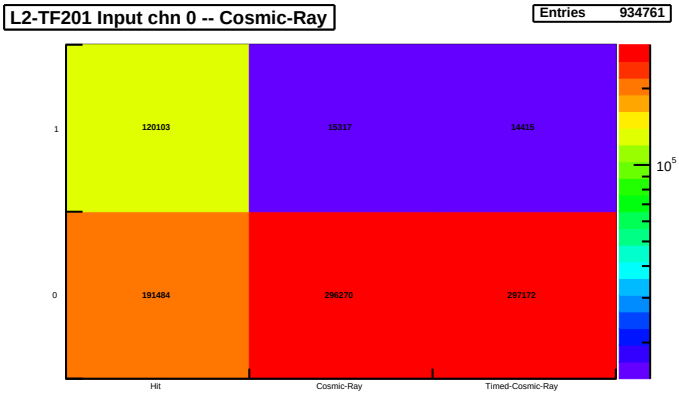

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

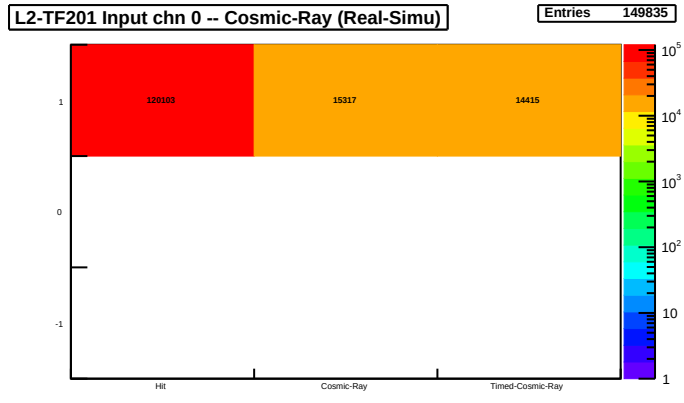

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 2492696
Fired
311587
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

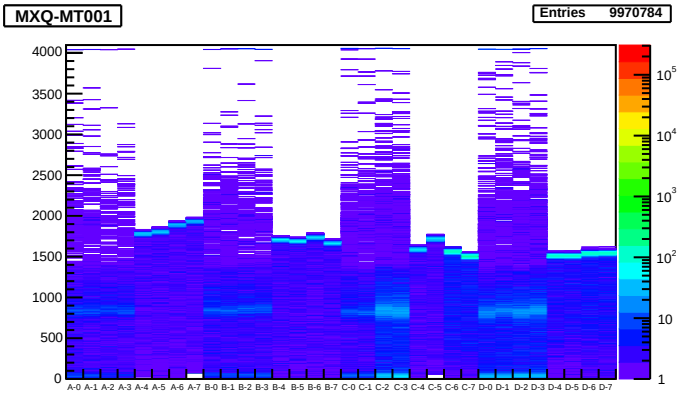

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 2492696
Fired(Real-Simu)
311587
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

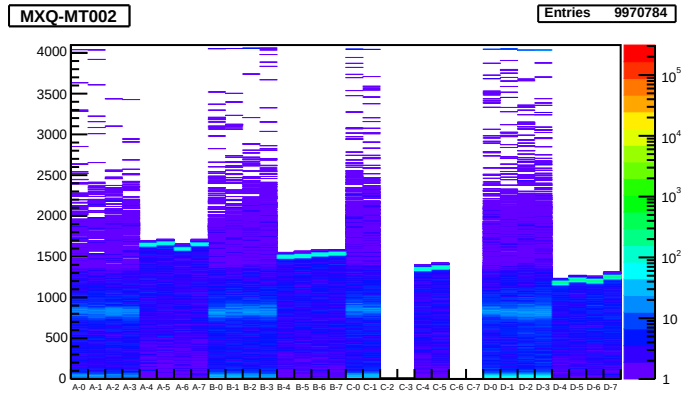

L2-TF201 Input chn 0 -- Number of Muons
Entries 311587
Number of Muons

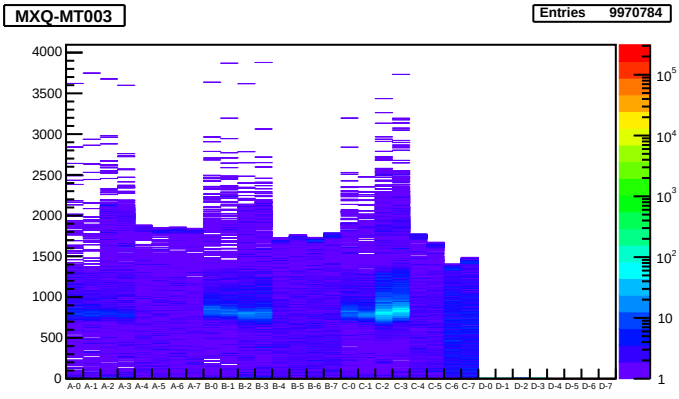

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

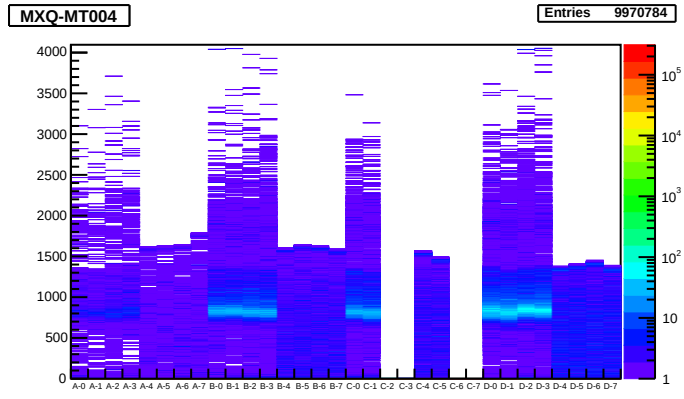

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 934761
120103
15317
14415
191484
296270
297172
Hit
Cosmic-Ray
Timed-Cosmic-Ray

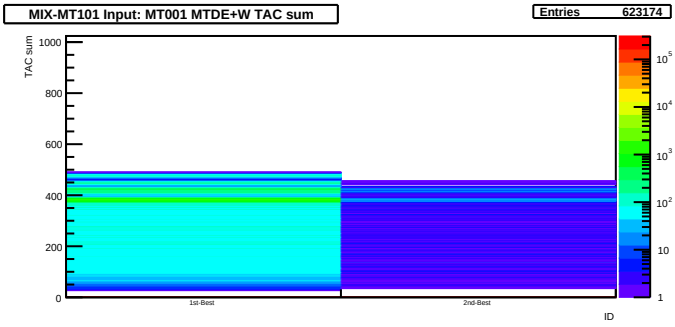

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 149835
120103
15317
14415
Hit
Cosmic-Ray
Timed-Cosmic-Ray

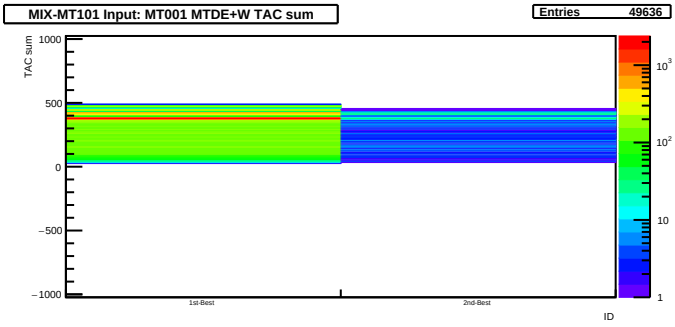

MXQ-MT001
Entries 9970784
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

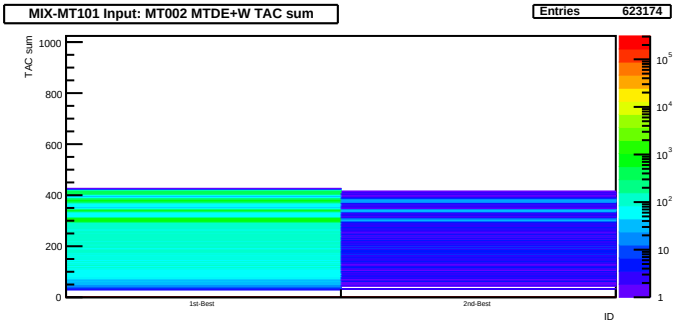

MXQ-MT002
Entries 9970784
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

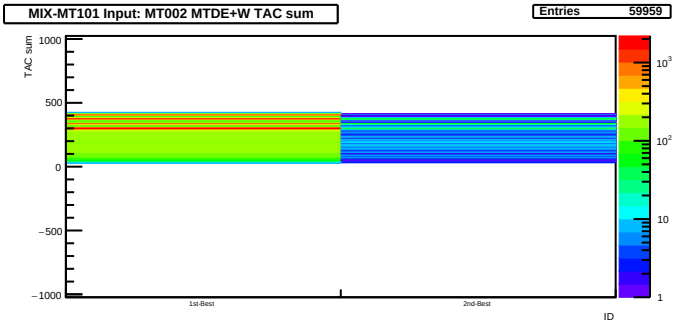

MXQ-MT003
Entries 9970784
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

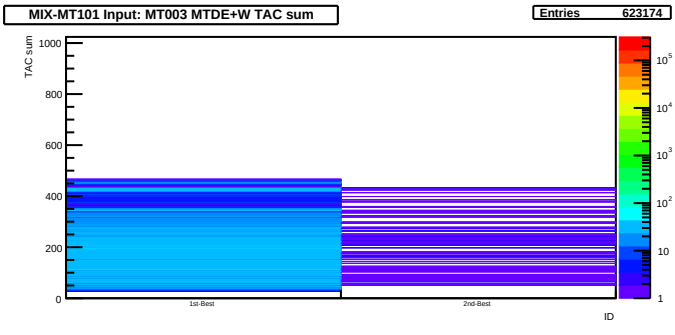

MXQ-MT004
Entries 9970784
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

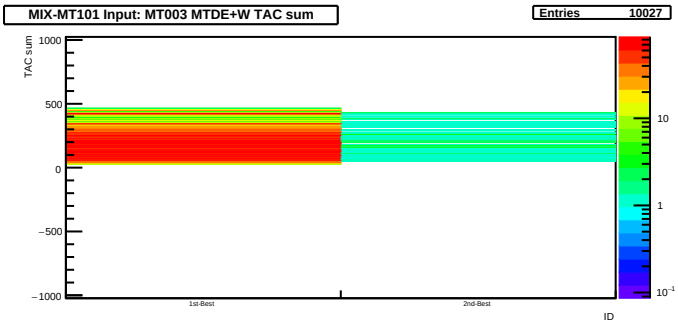

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 623174
TAC sum
1st-Best
2nd-Best
ID

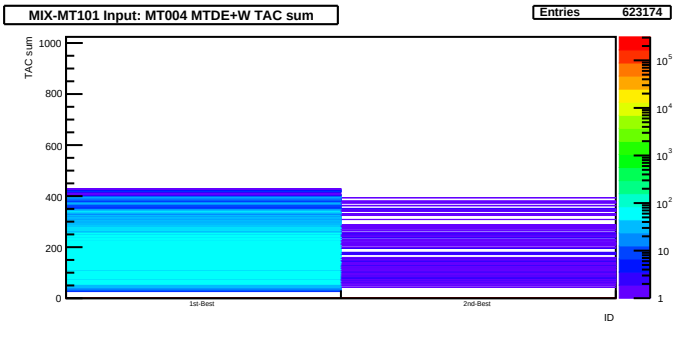

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 49636
TAC sum
1st-Best
2nd-Best
ID

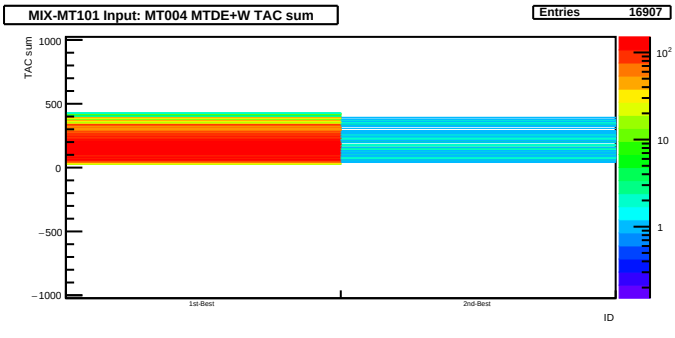

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 623174
TAC sum
1st-Best
2nd-Best
ID

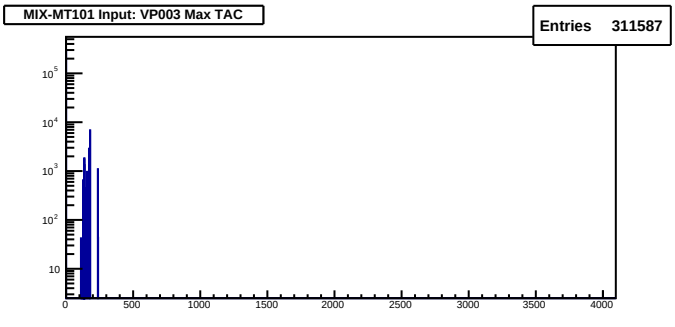

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 59959
TAC sum
1st-Best
2nd-Best
ID

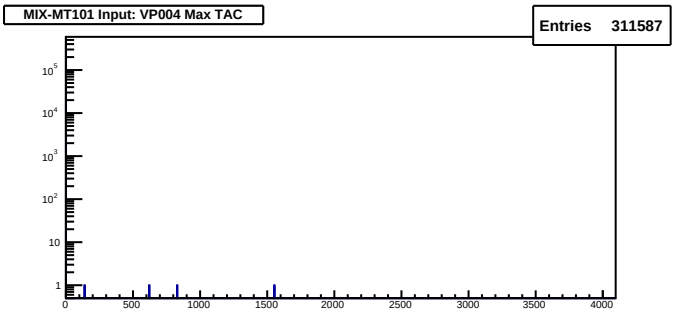

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 623174
TAC sum
1st-Best
2nd-Best
ID

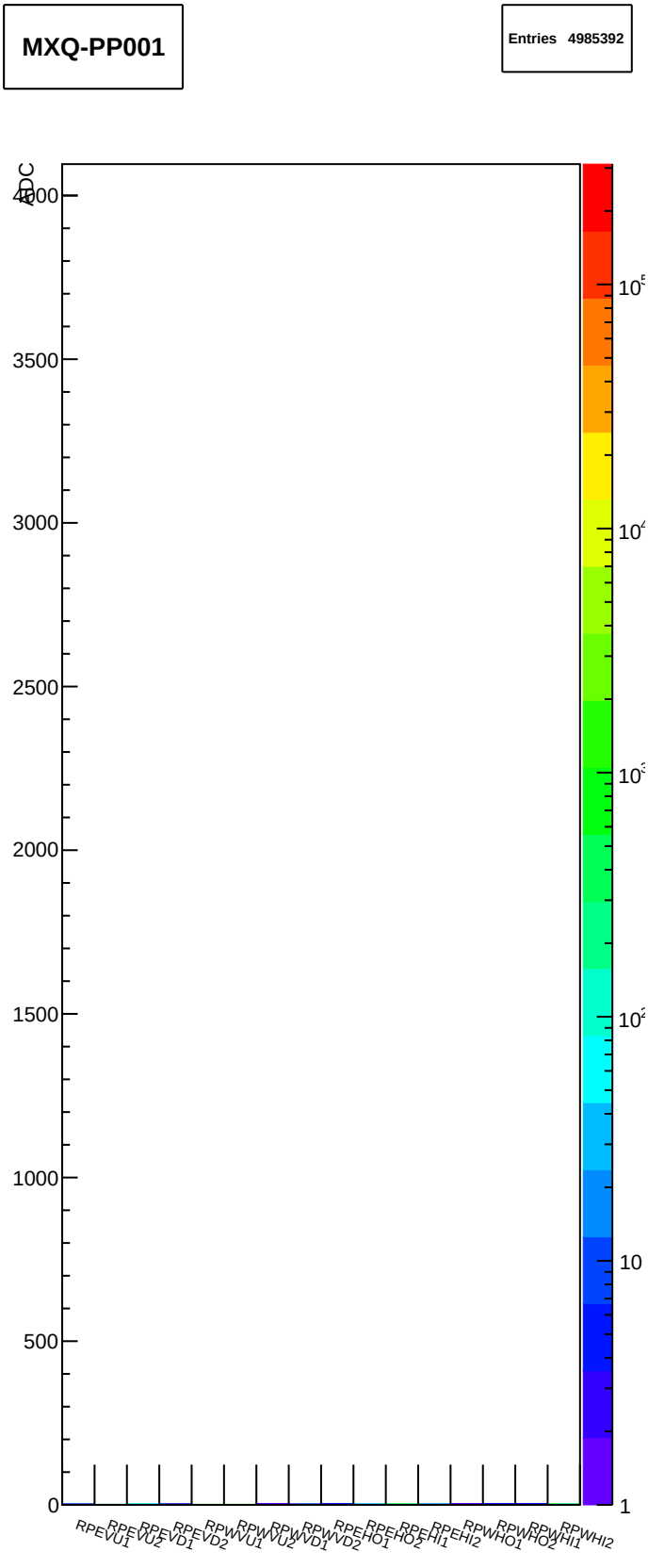

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 10027
TAC sum
1st-Best
2nd-Best
ID


MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 623174
TAC sum
1st-Best
2nd-Best
ID


MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 16907
TAC sum
1st-Best
2nd-Best
ID


MIX-MT101 Input: VP003 Max TAC
Entries 311587


MIX-MT101 Input: VP004 Max TAC
Entries 311587


MXQ-PP001
Entries 4985392
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
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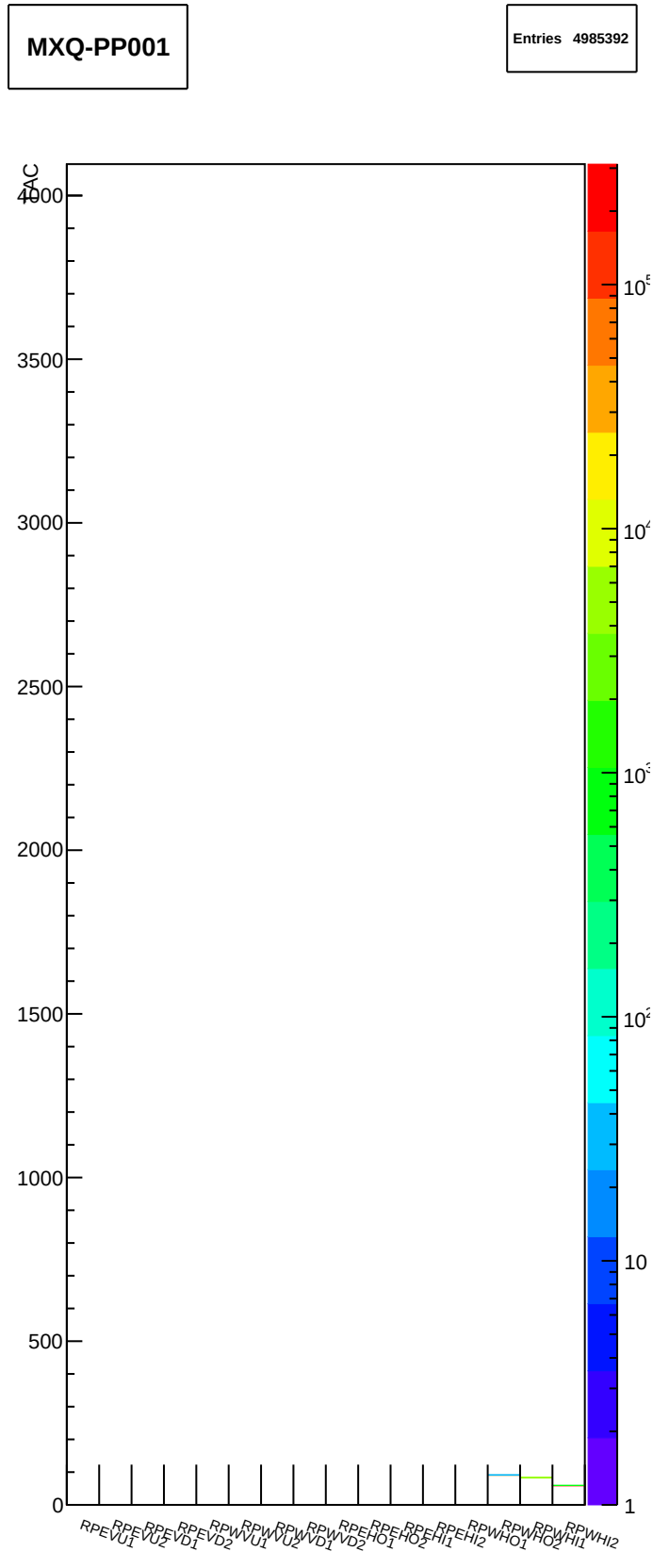
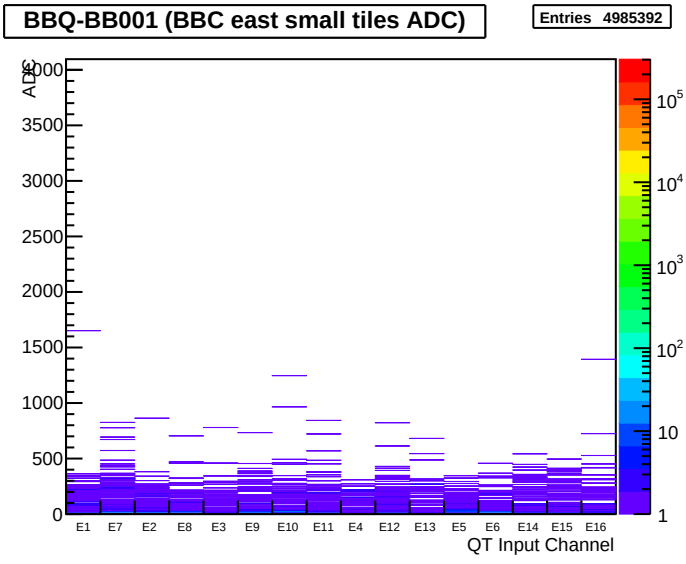
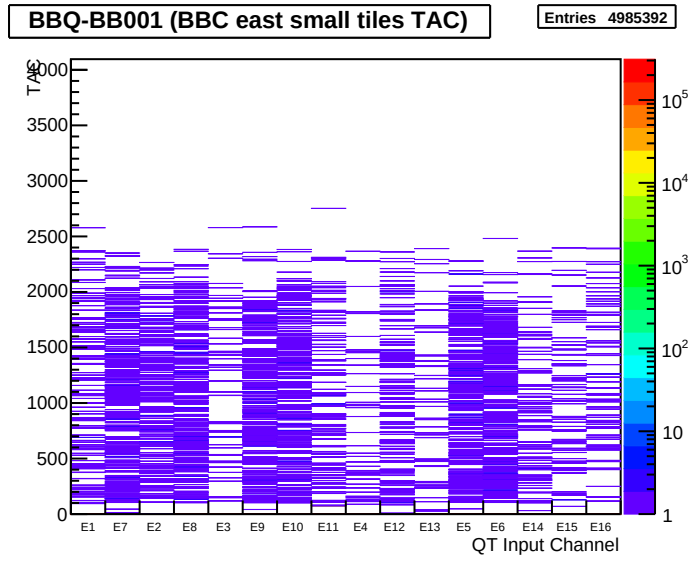
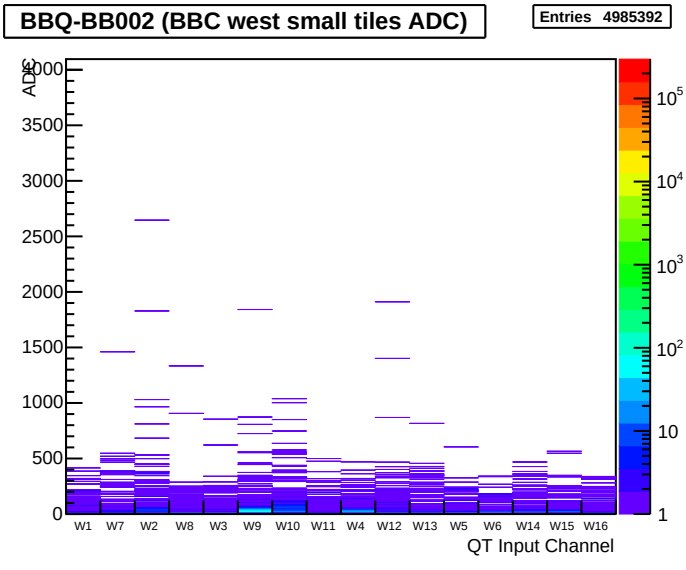
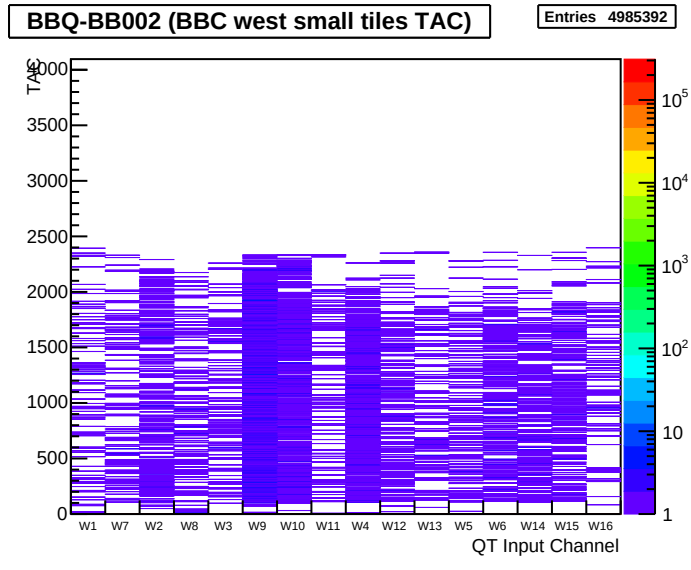
RPWHO2
RPWHI1
RPWHI2
10
1

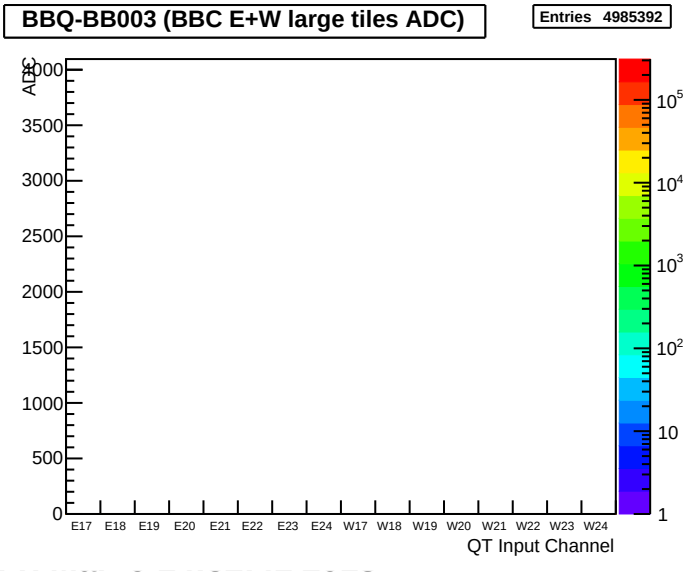

MXQ-PP001
Entries 4985392
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

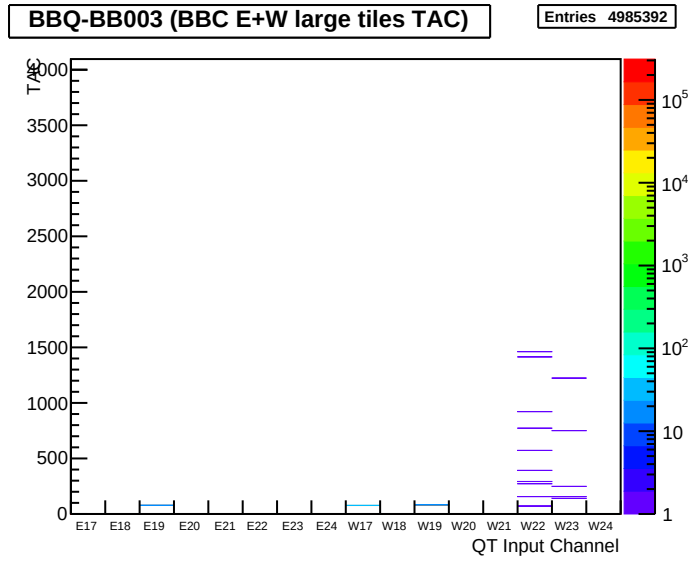

BBQ-BB001 (BBC east small tiles ADC)
Entries 4985392
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

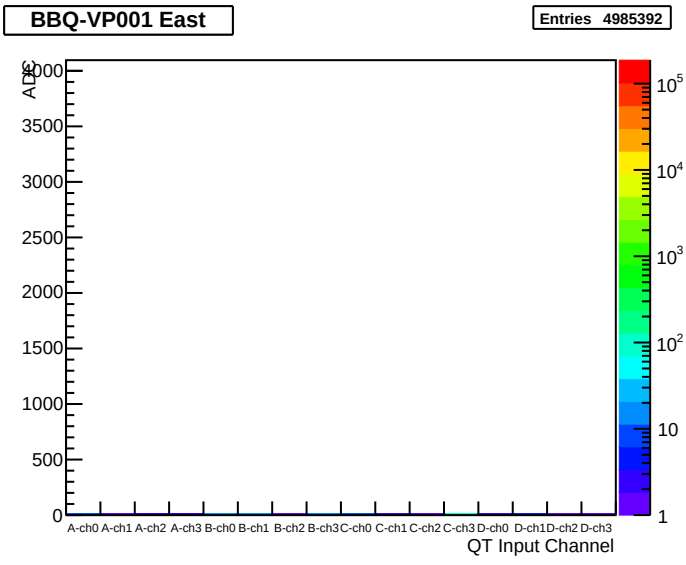

BBQ-BB001 (BBC east small tiles TAC)
Entries 4985392
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

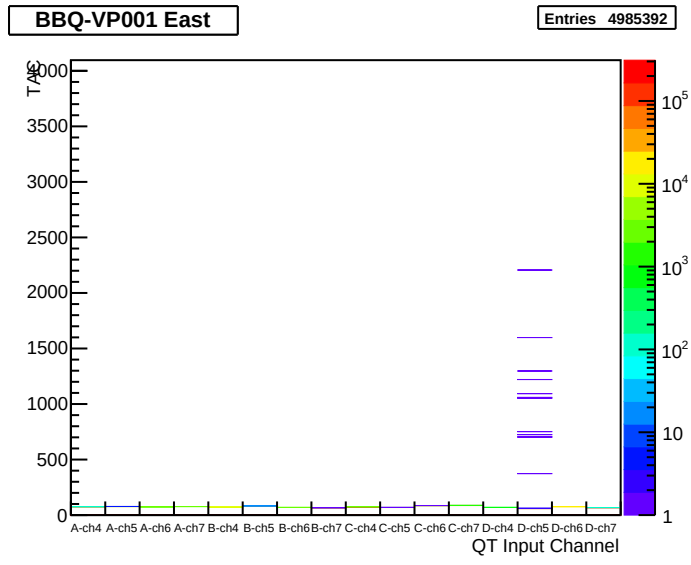

BBQ-BB002 (BBC west small tiles ADC)
Entries 4985392
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

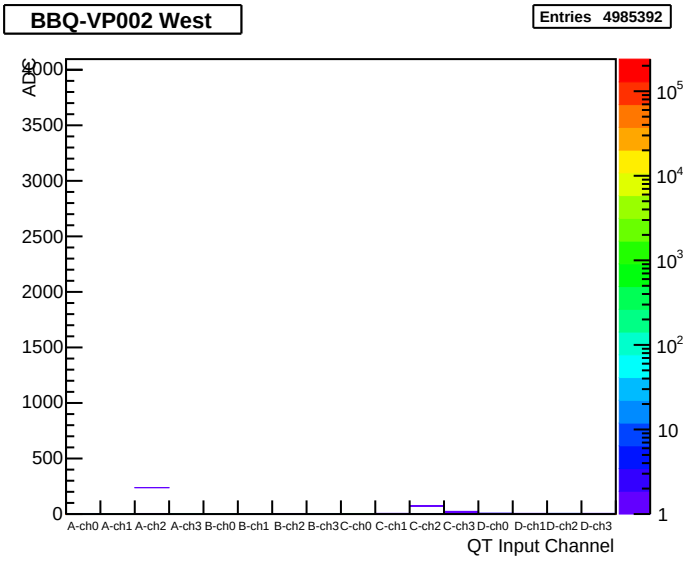

BBQ-BB002 (BBC west small tiles TAC)
Entries 4985392
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

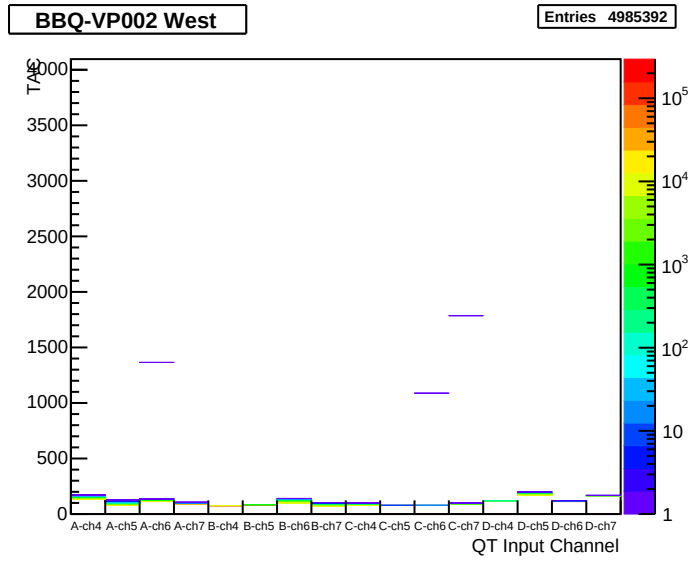

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4985392
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

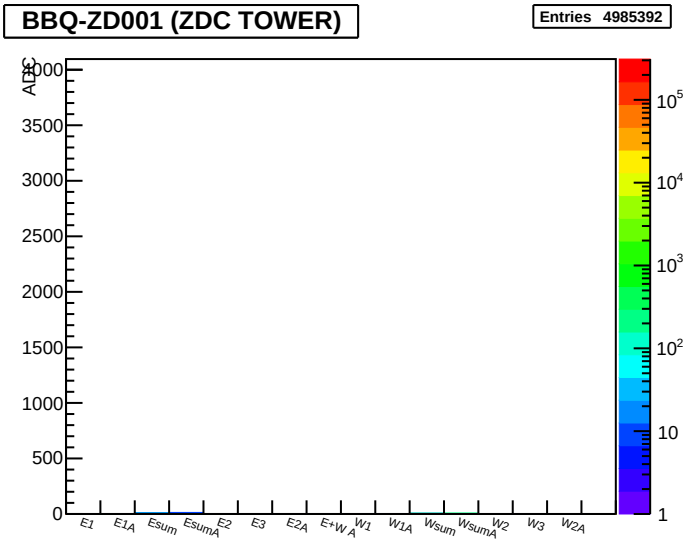

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4985392
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

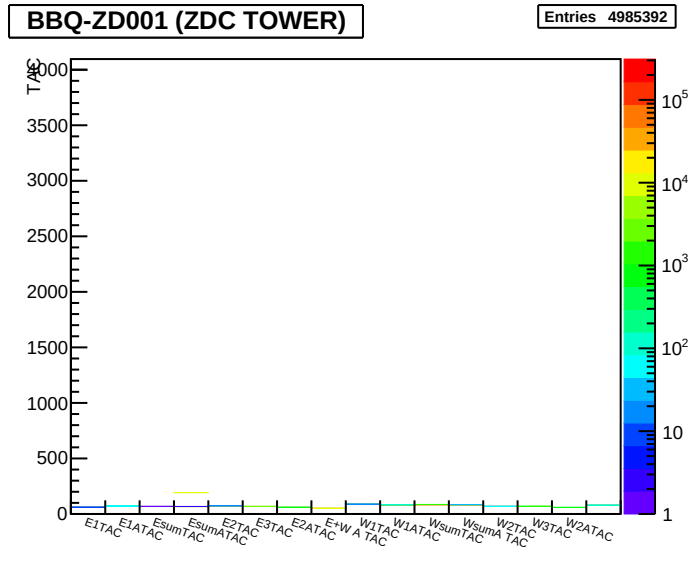

BBQ-VP001 East
Entries 4985392
ADC
QT Input Channel

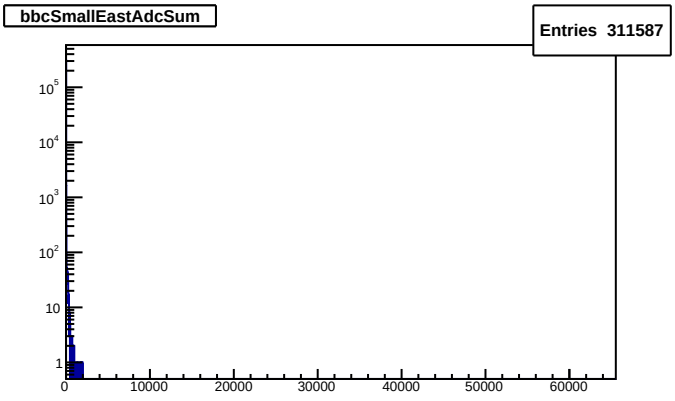

BBQ-VP001 East
Entries 4985392
TAC
QT Input Channel

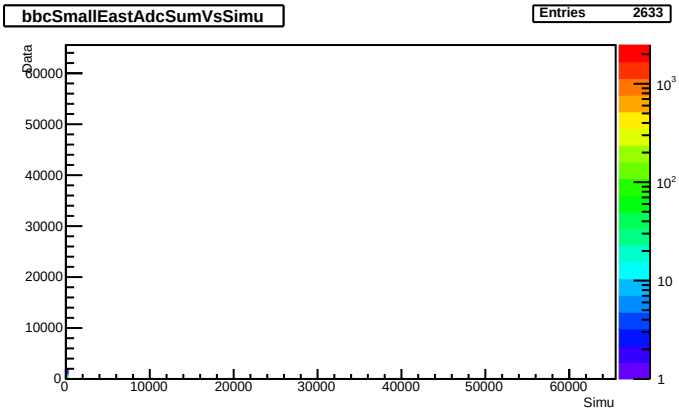

BBQ-VP002 West
Entries 4985392
ADC
QT Input Channel

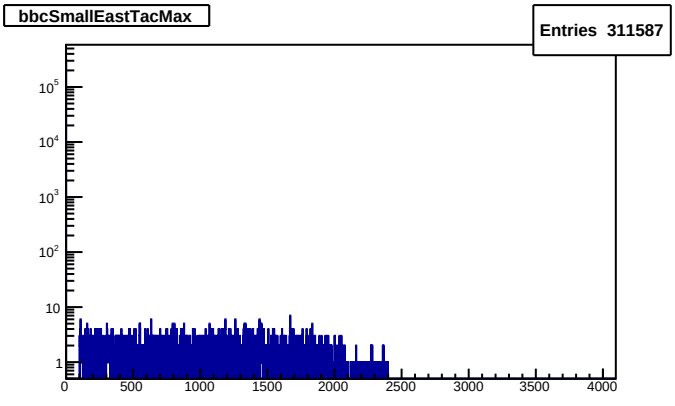

BBQ-VP002 West
Entries 4985392
TAC
QT Input Channel

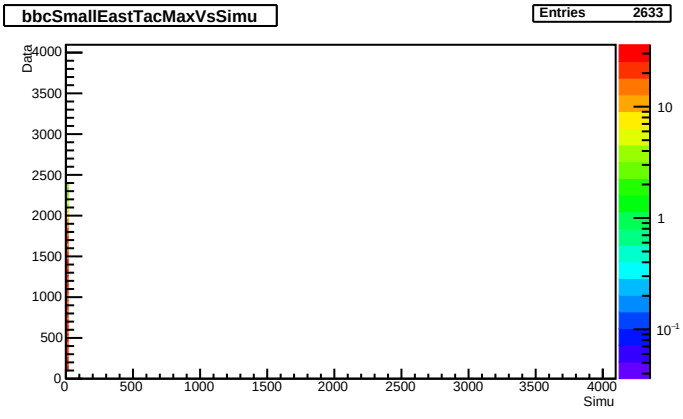

BBQ-ZD001 (ZDC TOWER)
Entries 4985392
ADC

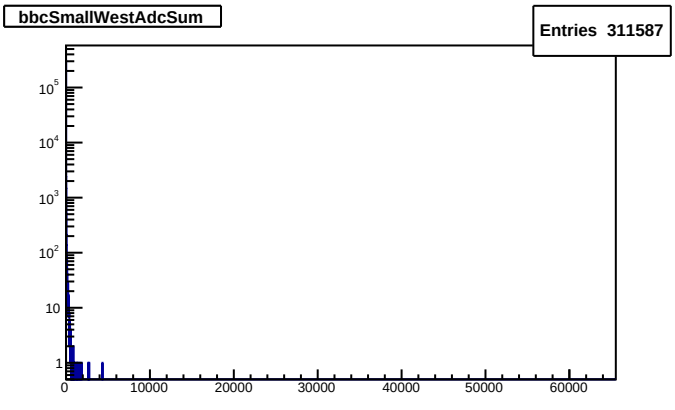

BBQ-ZD001 (ZDC TOWER)
Entries 4985392
TAC

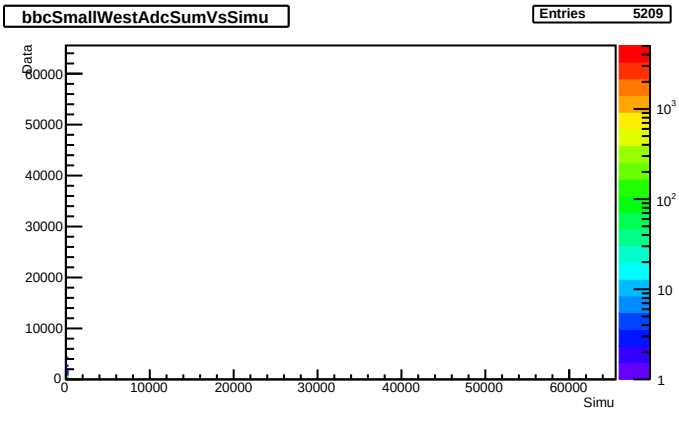

bbcSmallEastAdcSum
Entries 311587

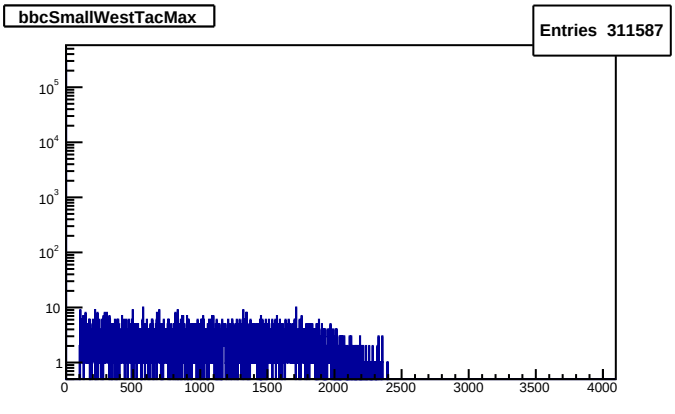

bbcSmallEastAdcSumVsSimu
Entries 2633
Data
Simu

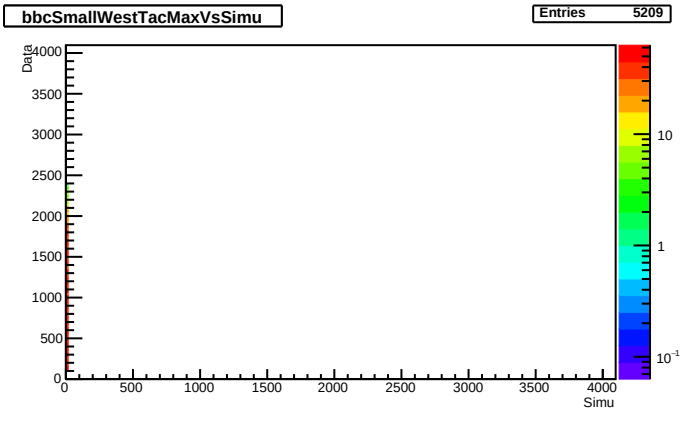

bbcSmallEastTacMax
Entries 311587

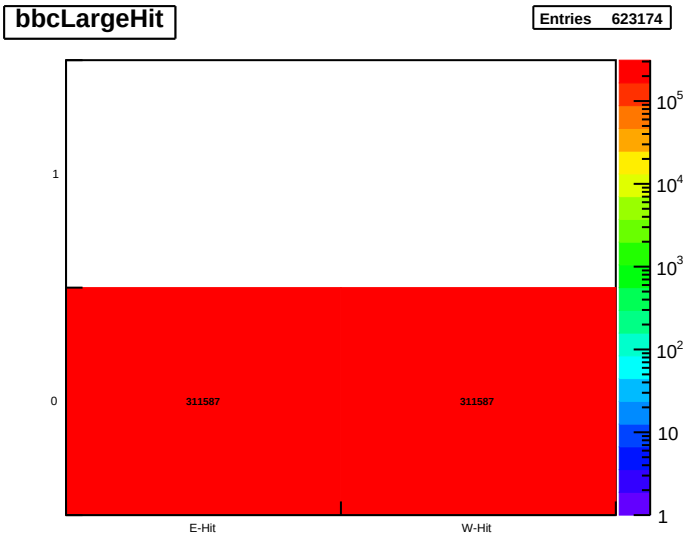

bbcSmallEastTacMaxVsSimu
Entries 2633
Data
Simu

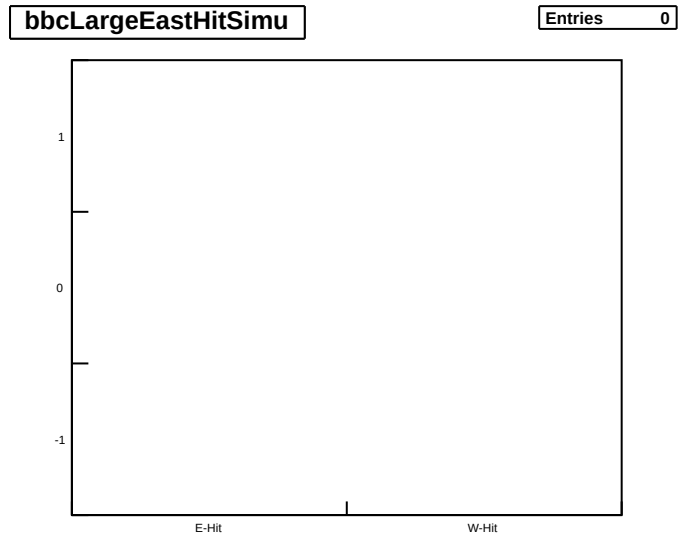

bbcSmallWestAdcSum
Entries 311587

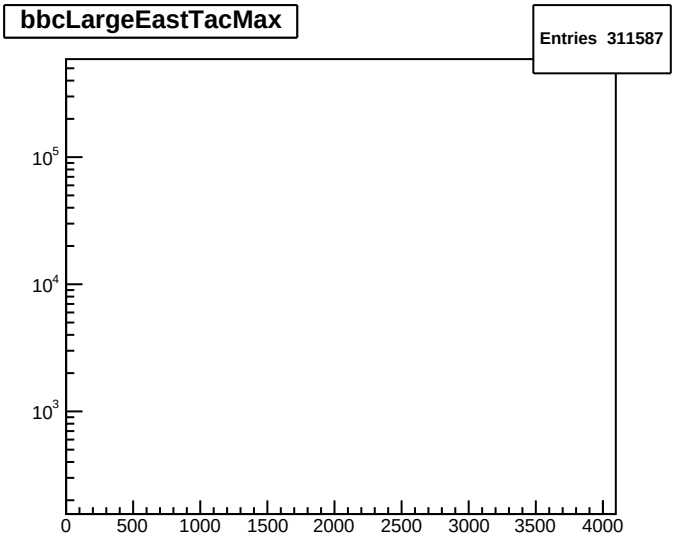

bbcSmallWestAdcSumVsSimu
Entries 5209
Data
Simu

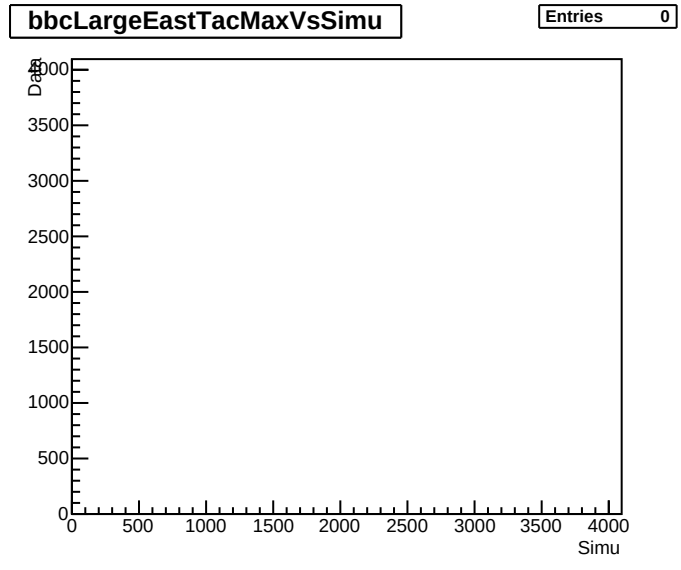

bbcSmallWestTacMax
Entries 311587

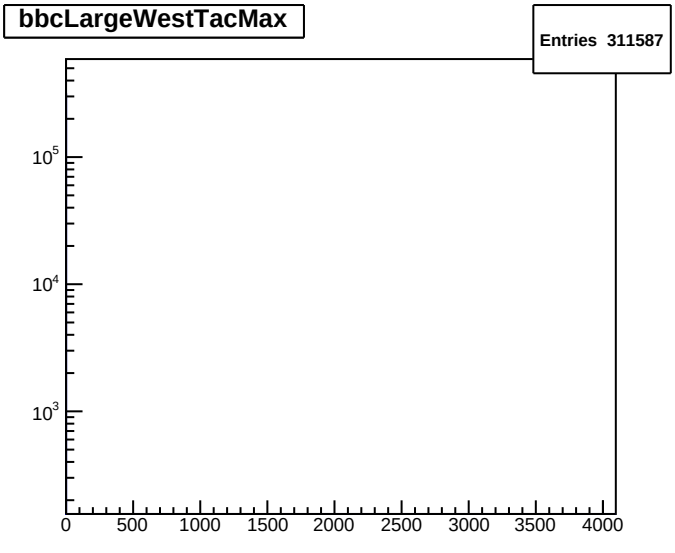

bbcSmallWestTacMaxVsSimu
Entries 5209
Data
Simu

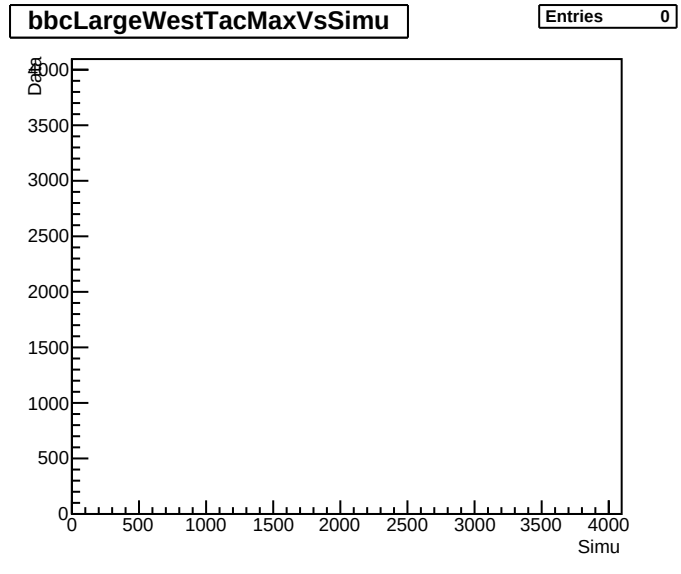

bbcLargeHit
Entries 623174
311587
311587
E-Hit
W-Hit

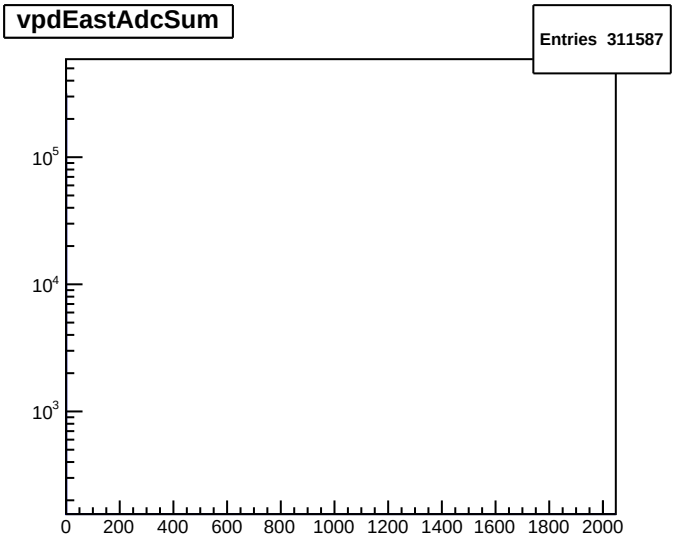

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

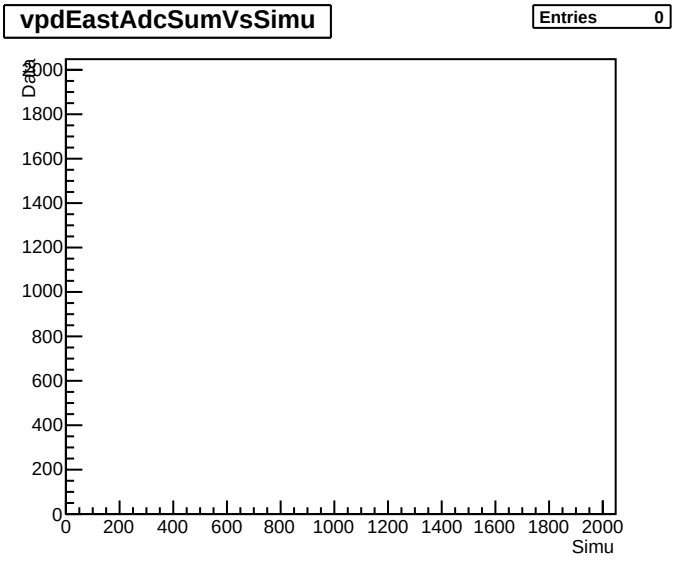

bbcLargeEastTacMax
Entries 311587

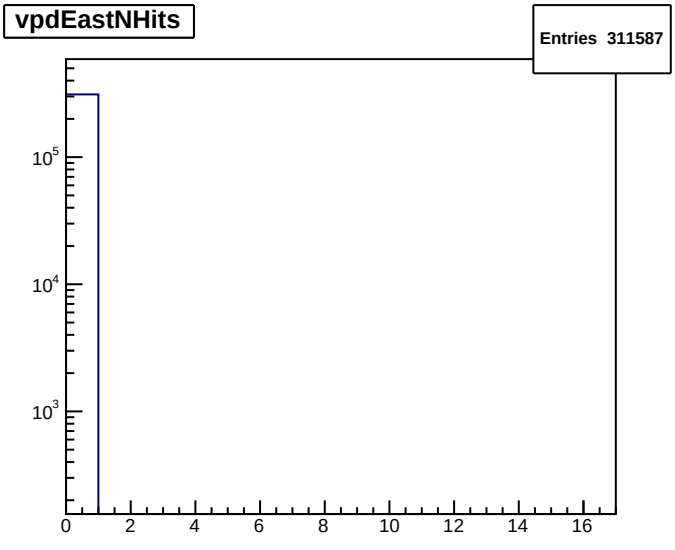

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

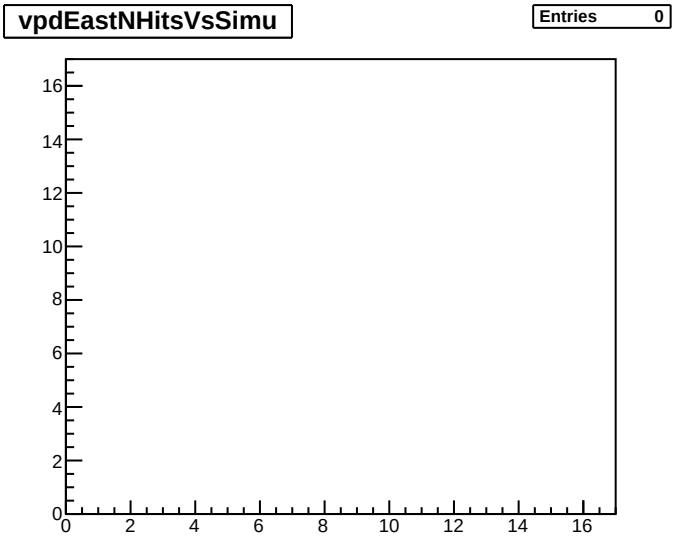

bbcLargeWestTacMax
Entries 311587

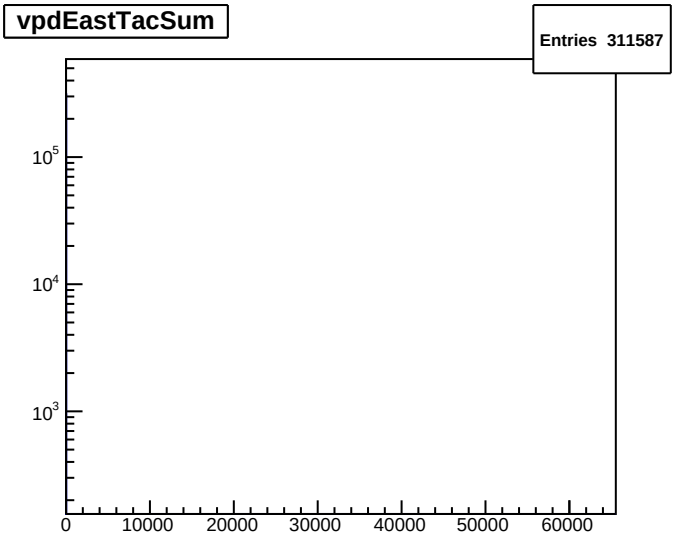

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

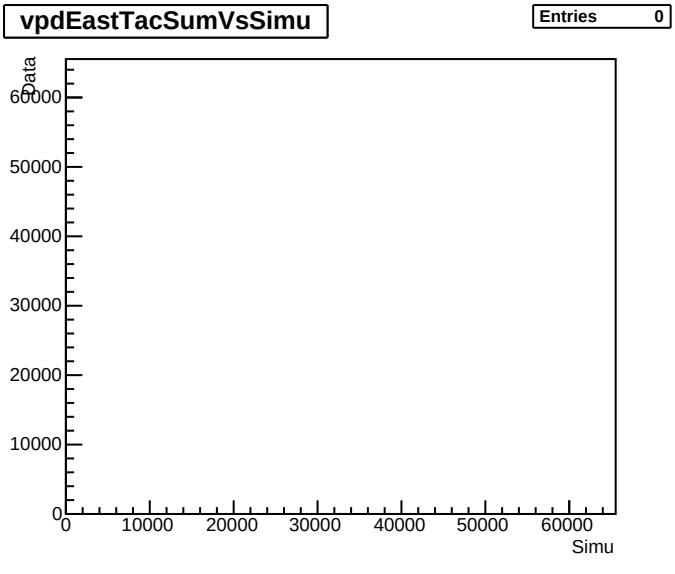

vpdEastAdcSum
Entries 311587
10^5
10^4
10^3
0 200 400 600 800 1000 1200 1400 1600 1800 2000

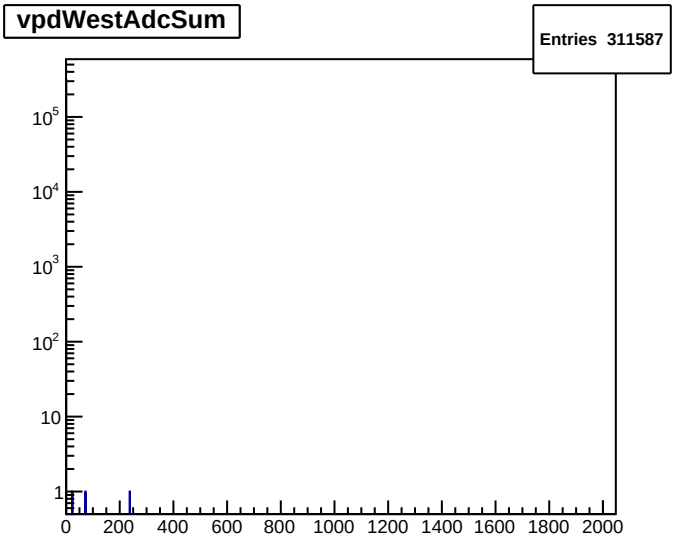

vpdEastAdcSumVsSimu
Entries 0
Data
2000 1800 1600 1400 1200 1000 800 600 400 200 0
0 200 400 600 800 1000 1200 1400 1600 1800 2000
Simu

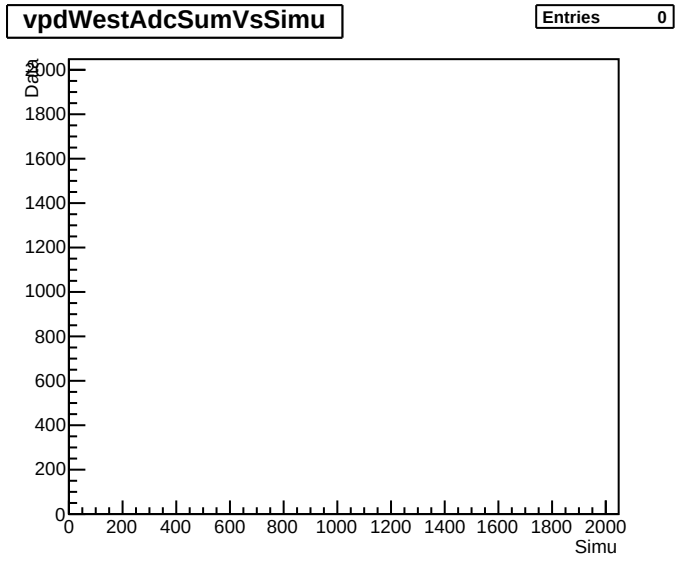

vpdEastNHits
Entries 311587
10^5
10^4
10^3
0 2 4 6 8 10 12 14 16

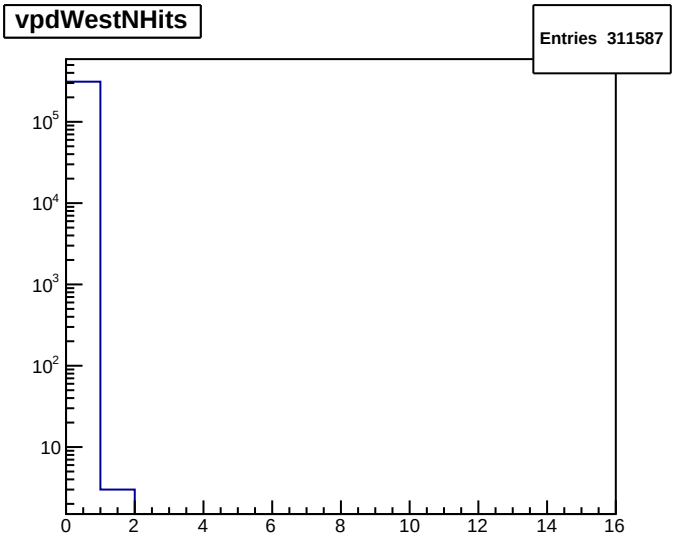

vpdEastNHitsVsSimu
Entries 0
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16

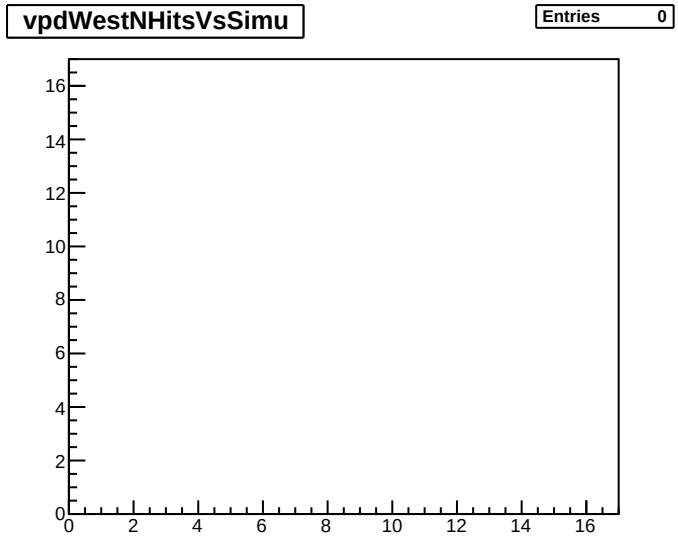

vpdEastTacSum
Entries 311587
10^5
10^4
10^3
0 10000 20000 30000 40000 50000 60000

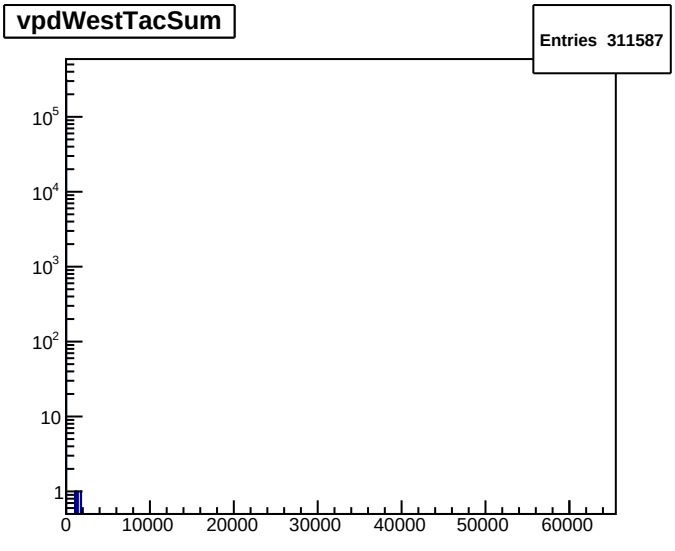

vpdEastTacSumVsSimu
Entries 0
Data
60000 50000 40000 30000 20000 10000 0
0 10000 20000 30000 40000 50000 60000
Simu

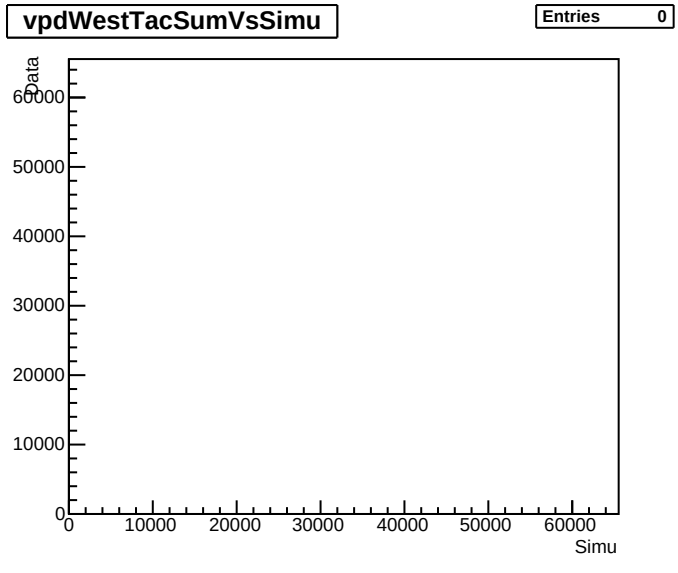

vpdWestAdcSum
Entries 311587

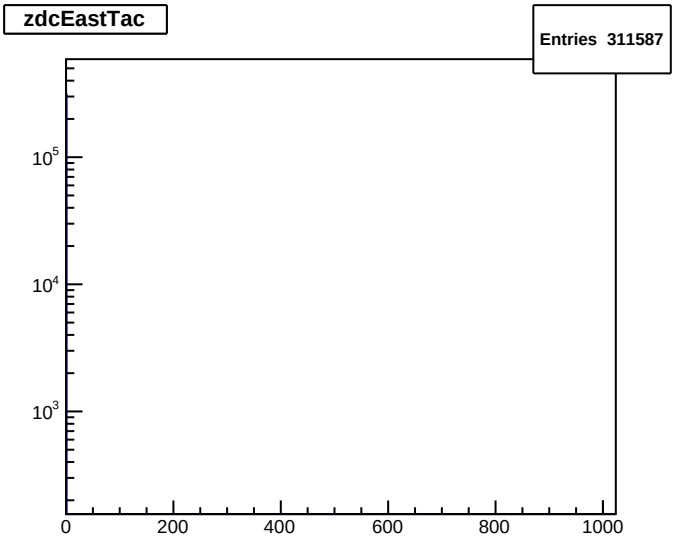

vpdWestAdcSumVsSimu
Entries 0
Data
Simu

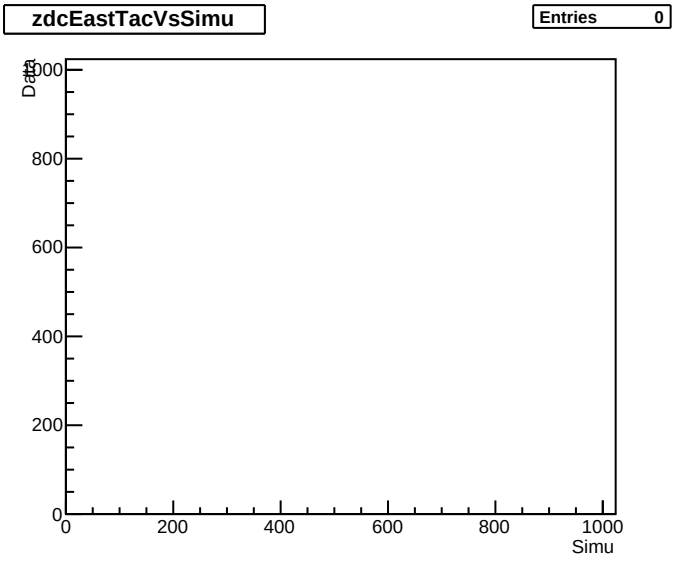

vpdWestNHits
Entries 311587

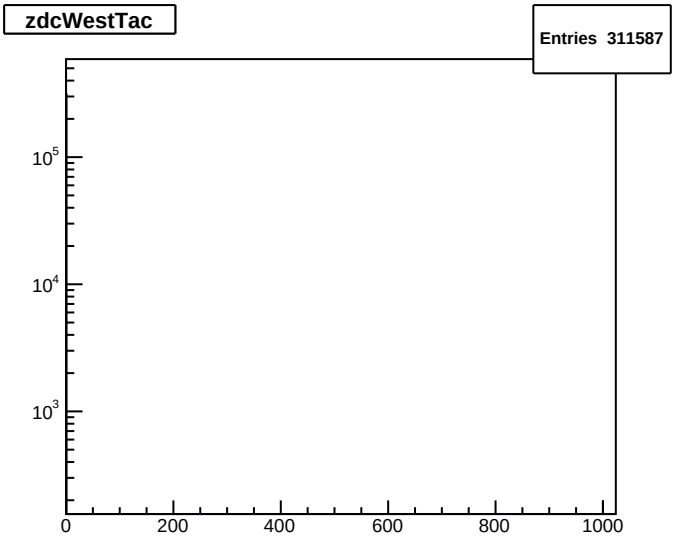

vpdWestNHitsVsSimu
Entries 0

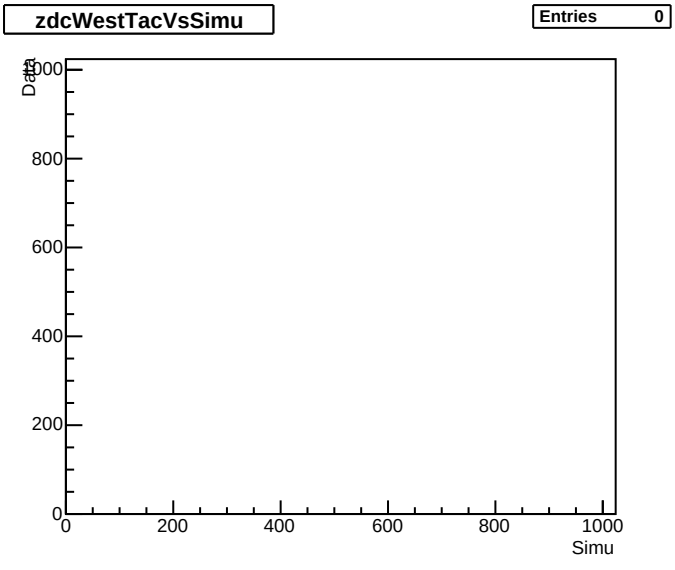

vpdWestTacSum
Entries 311587

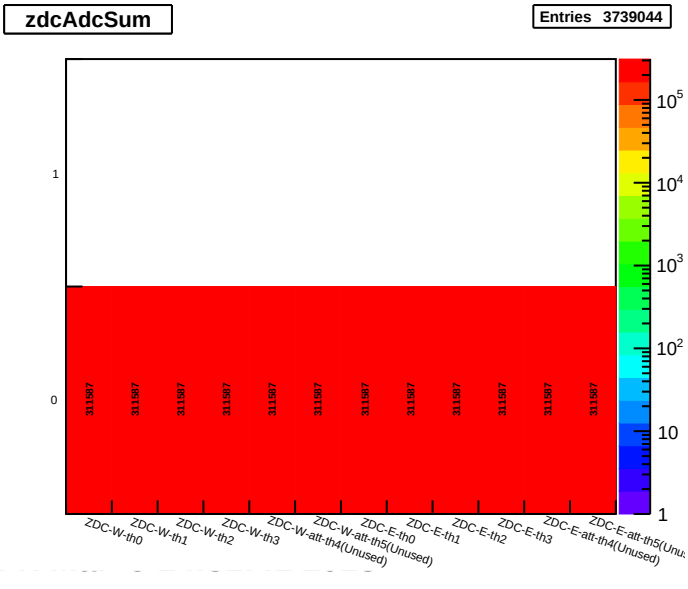

vpdWestTacSumVsSimu
Entries 0
Data
Simu

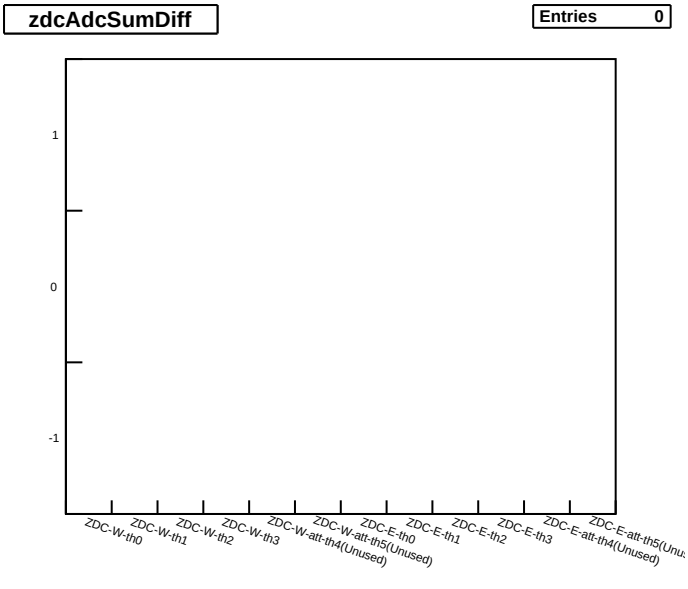

zdcEastTac
Entries 311587
10^5
10^4
10^3
0
200
400
600
800
1000

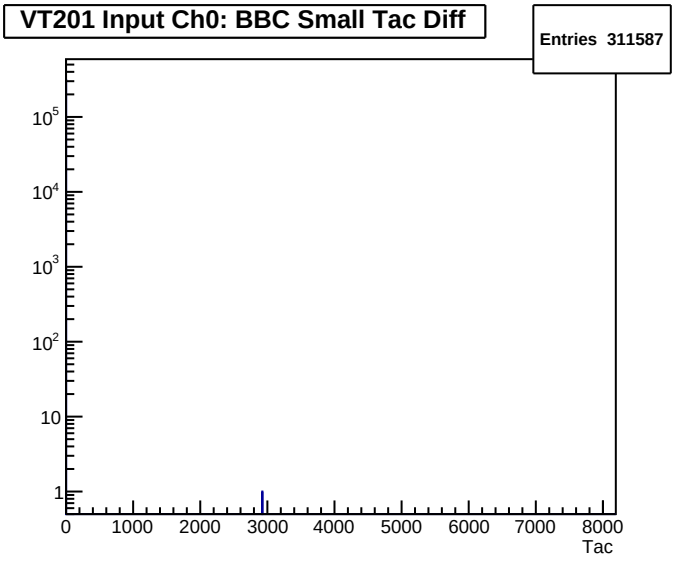

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

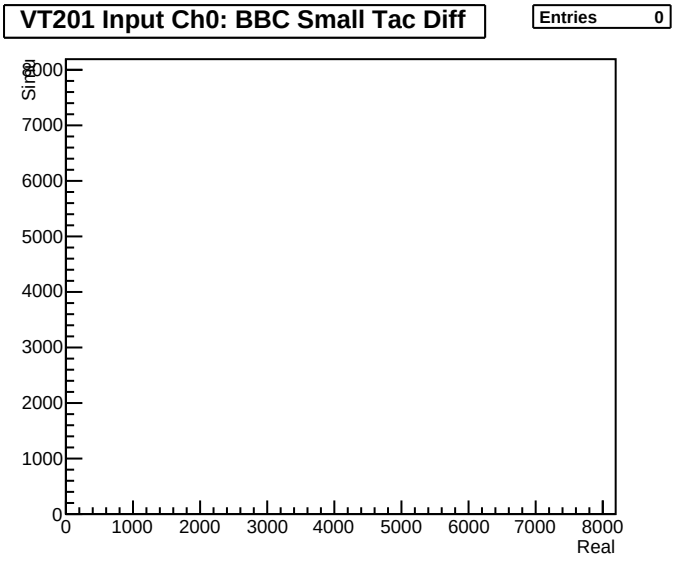

zdcWestTac
Entries 311587
10^5
10^4
10^3
0
200
400
600
800
1000

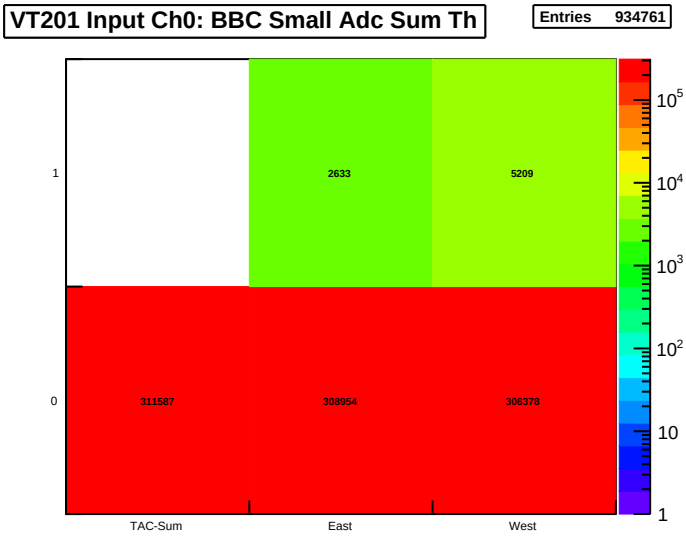

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

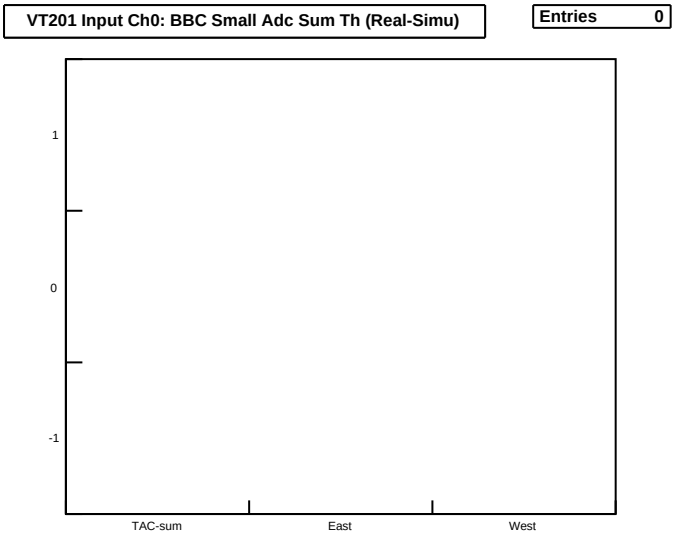

zdcAdcSum
Entries 3739044
1
0
311587
10^5
10^4
10^3
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

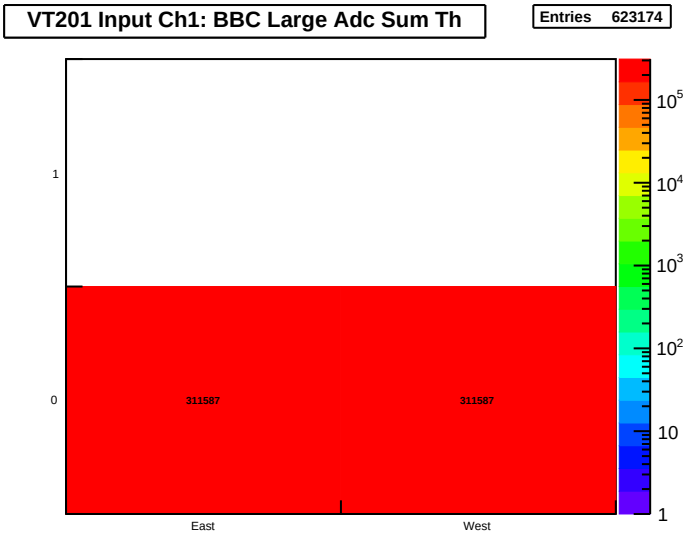

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

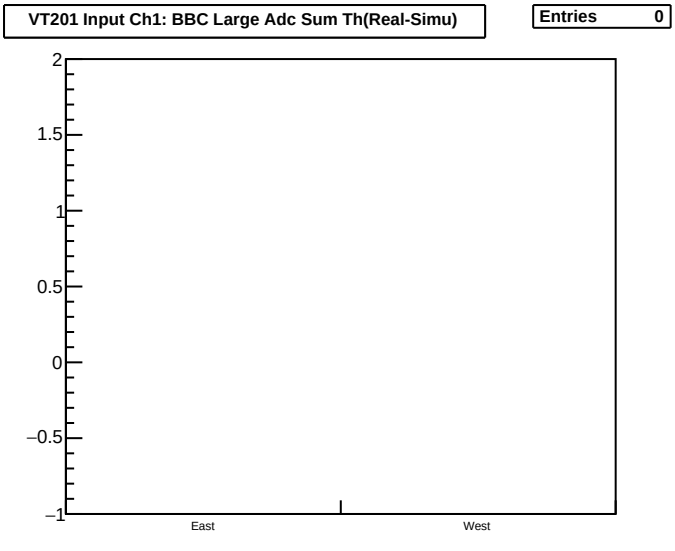

VT201 Input Ch0: BBC Small Tac Diff
Entries 311587
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

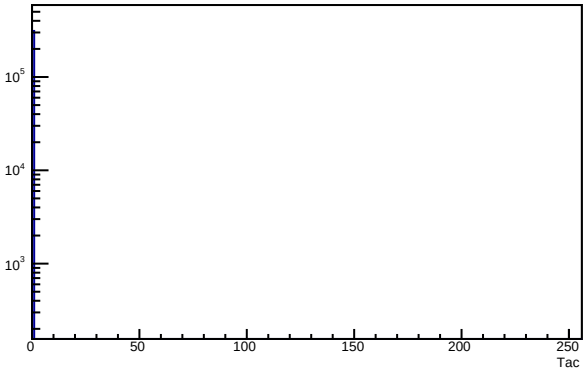

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 934761
2633
5209
311587
308954
306378
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

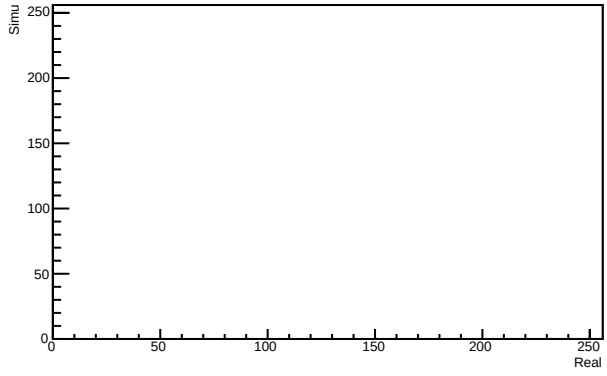

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 623174
311587
311587
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

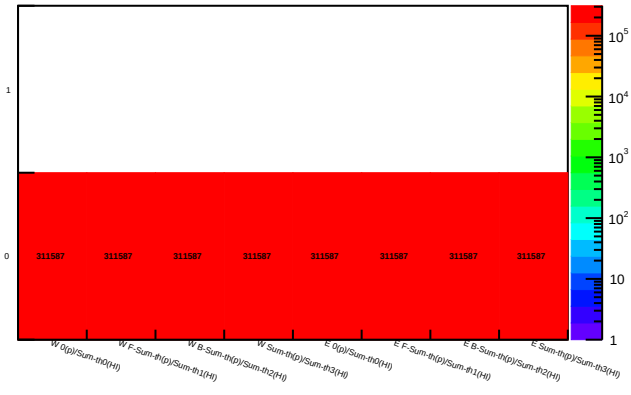
VT201 Input Ch2: ZDC Tac Diff



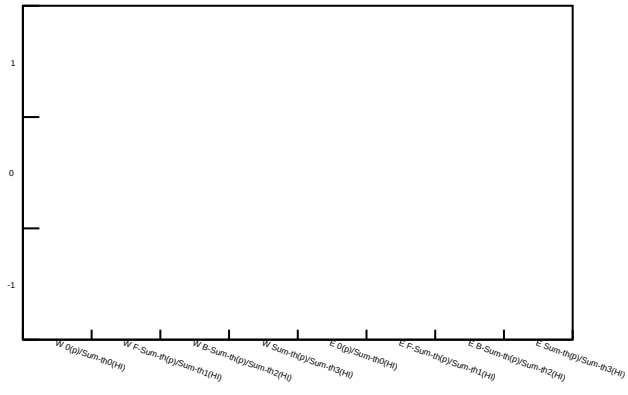
VT201 Input Ch2: ZDC Tac Diff



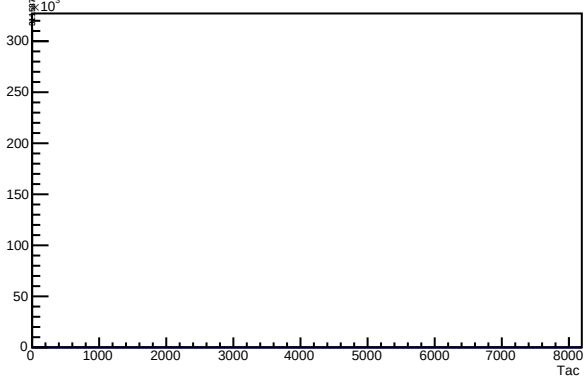
VT201 Input Ch2: ZDC Adc Sum Th



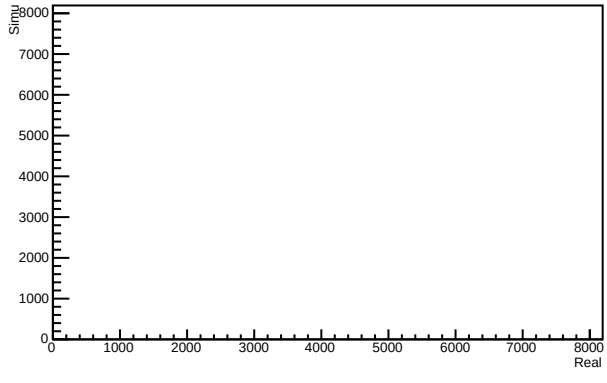
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



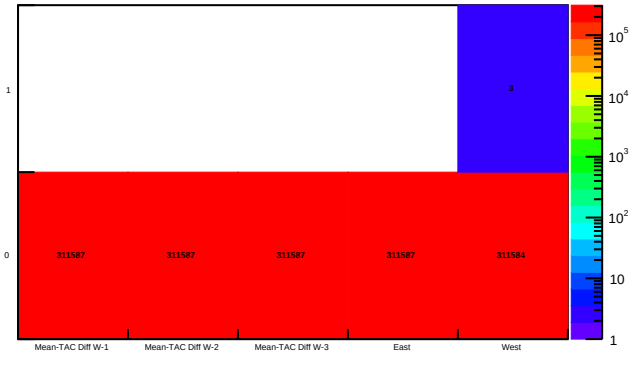
VT201 Input Ch4: VPD Tac Diff



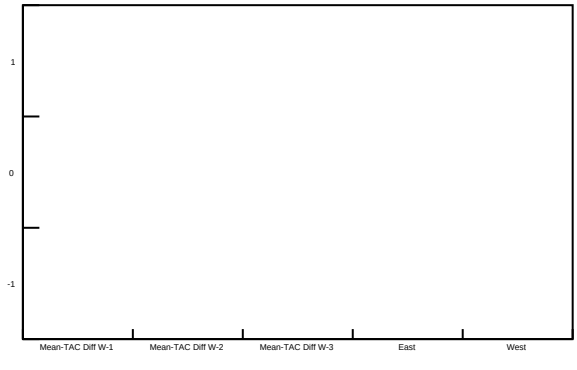
VT201 Input Ch4: VPD Tac Diff

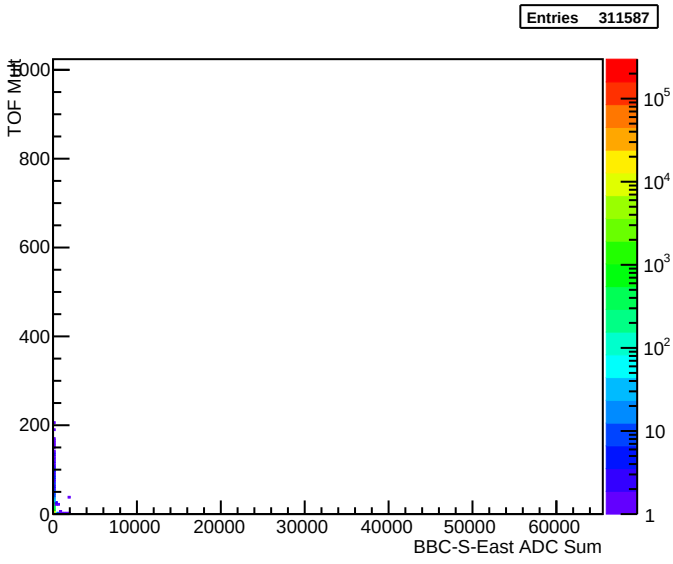


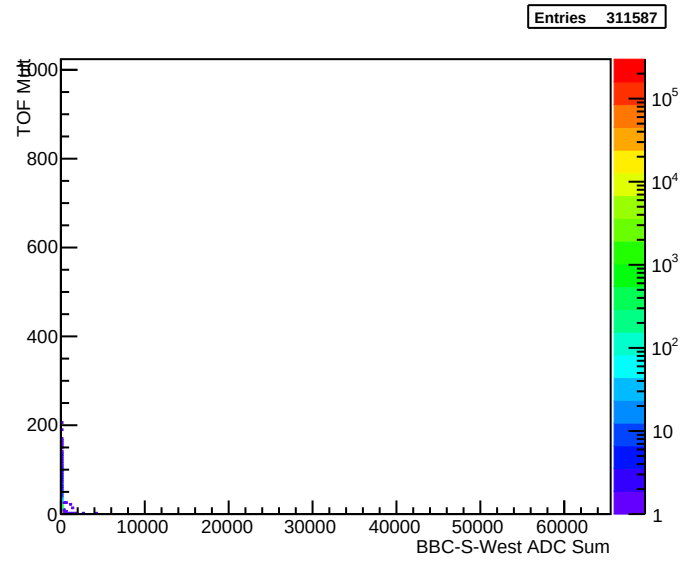
VT201 Input Ch4: VPD Adc Sum Th

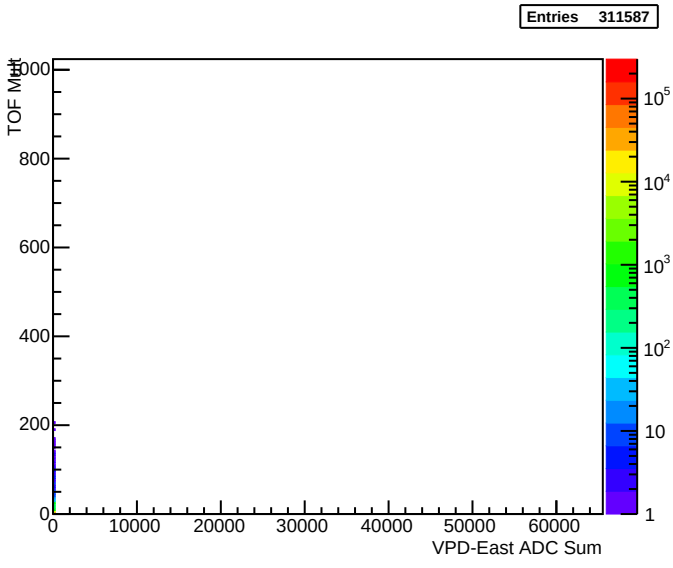


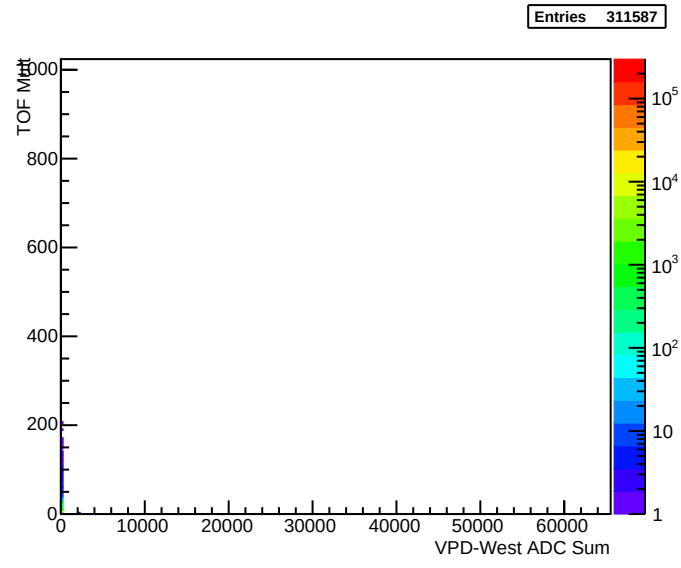
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

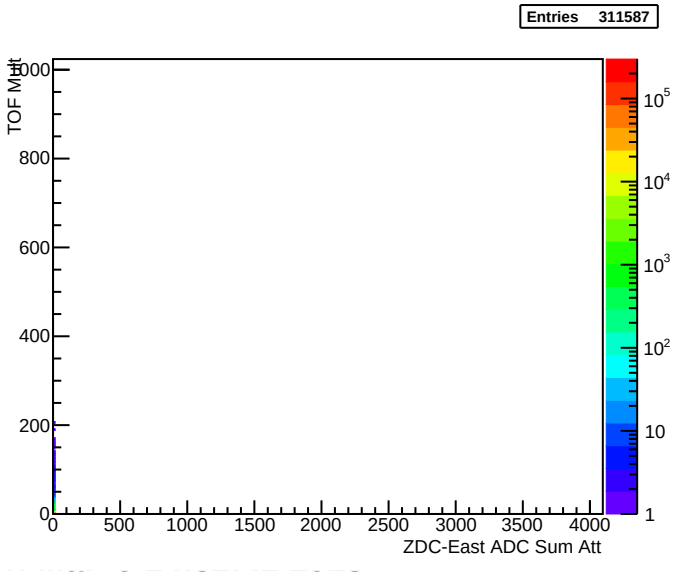


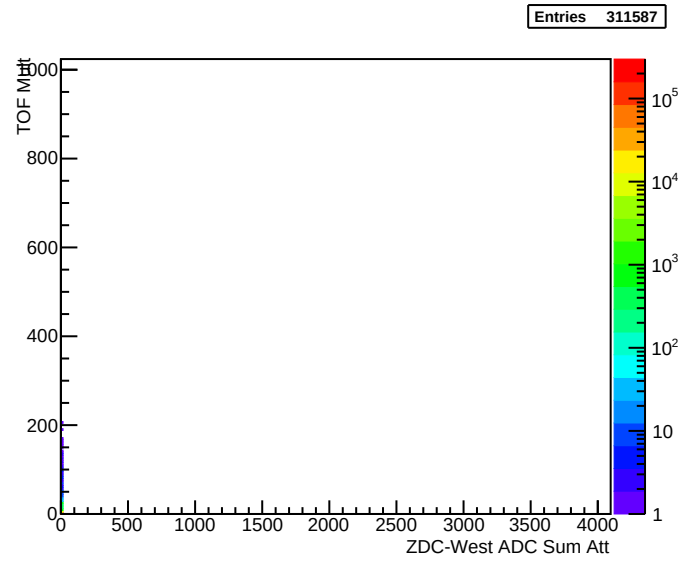

Entries 311587
TOF Mult
BBC-S-East ADC Sum

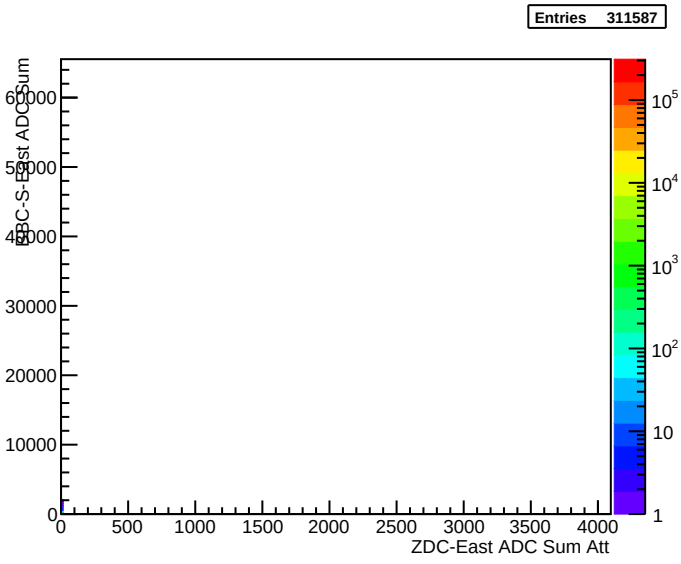

Entries 311587
TOF Mult
BBC-S-West ADC Sum

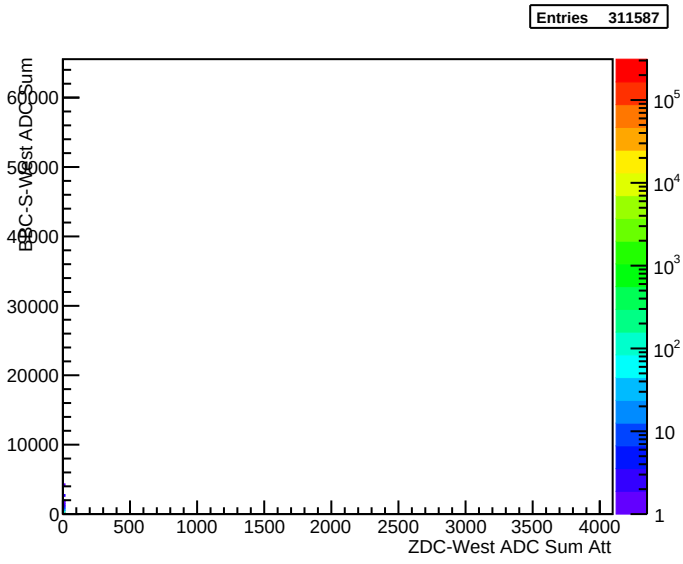

Entries 311587
TOF Mult
VPD-East ADC Sum

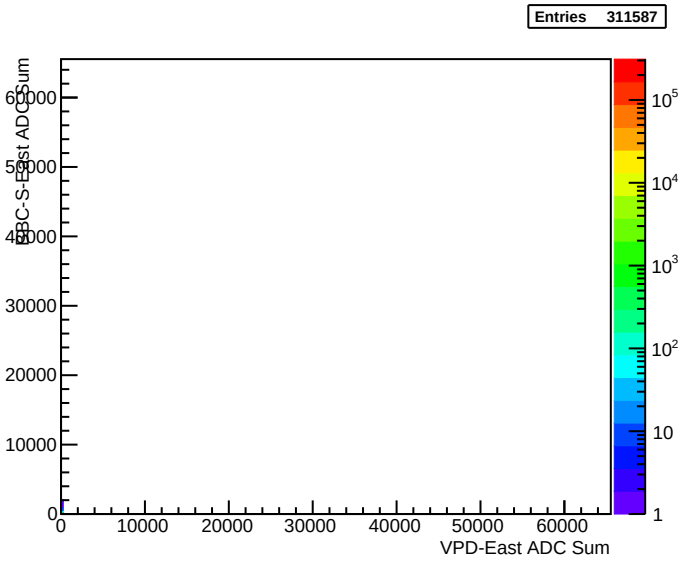

Entries 311587
TOF Mult
VPD-West ADC Sum

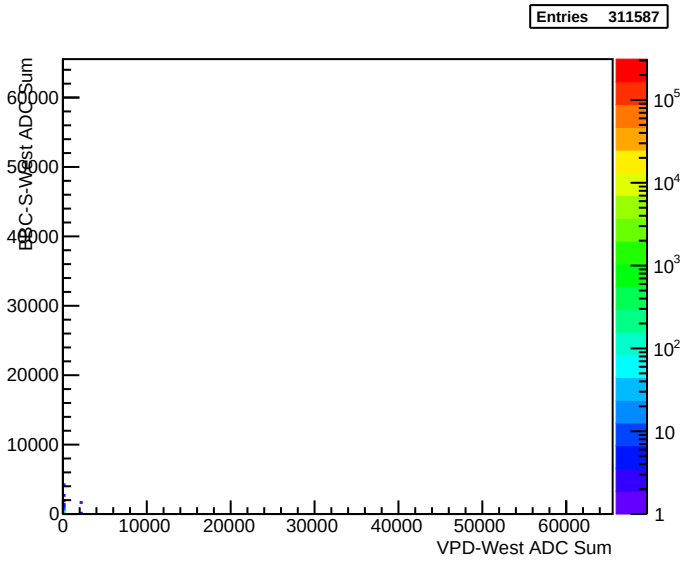

Entries 311587
TOF Mult
ZDC-East ADC Sum Att

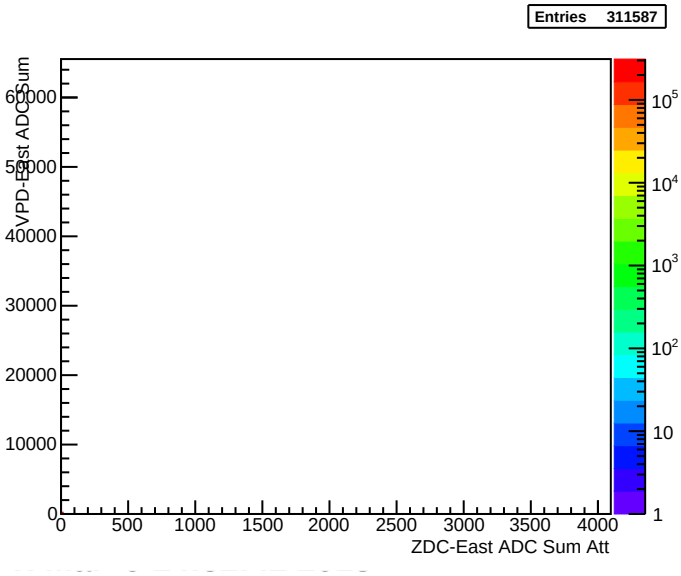

Entries 311587
TOF Mult
ZDC-West ADC Sum Att

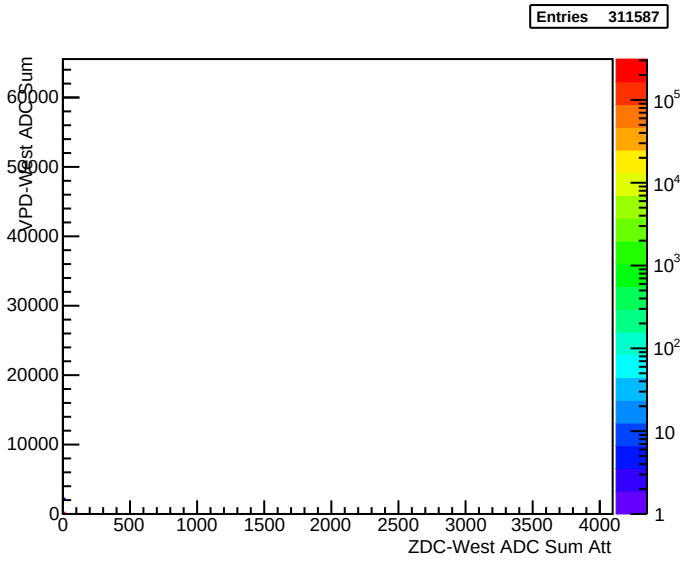

Entries 311587
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

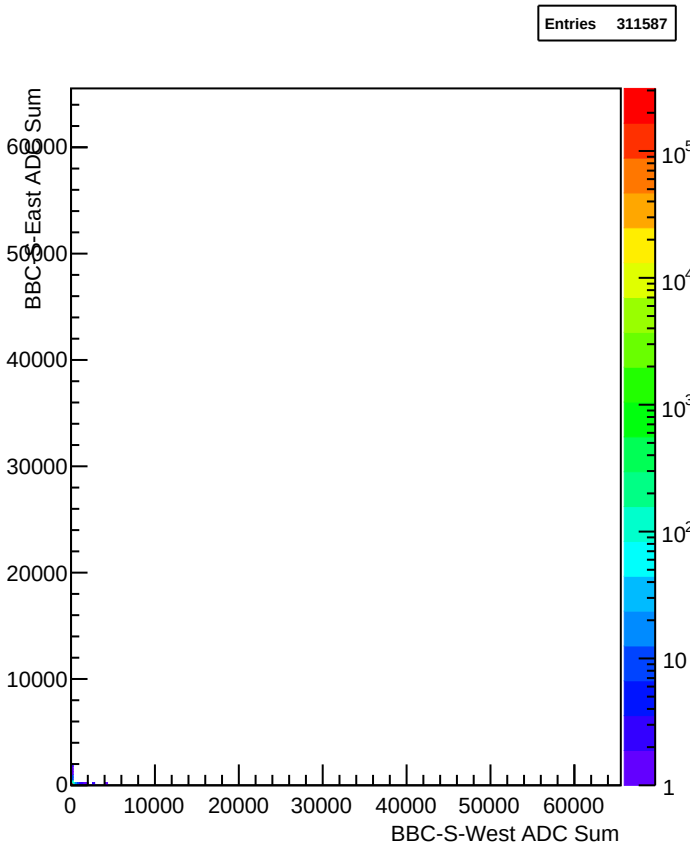

Entries 311587
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

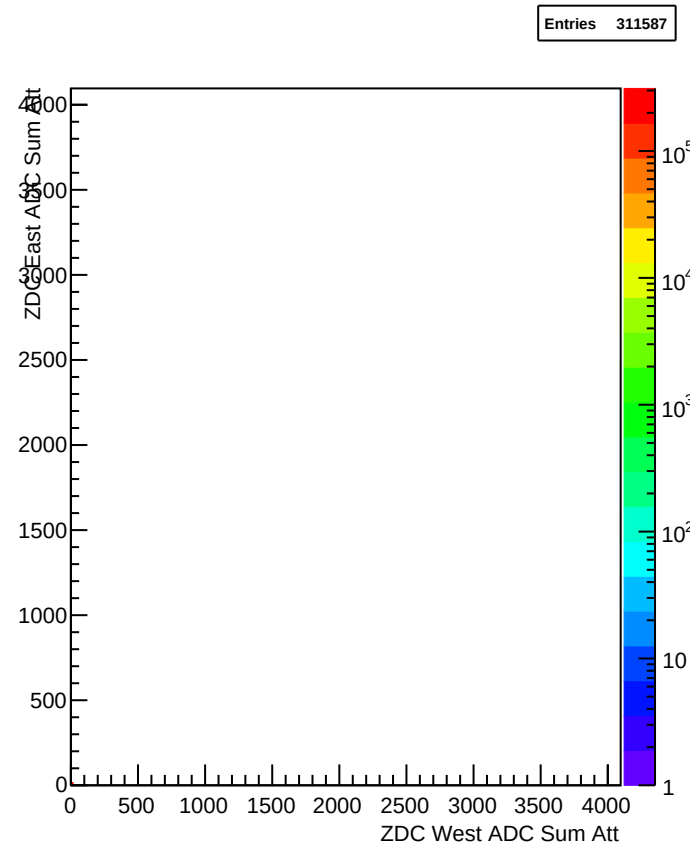

Entries 311587
BBC-S-East ADC Sum
VPD-East ADC Sum

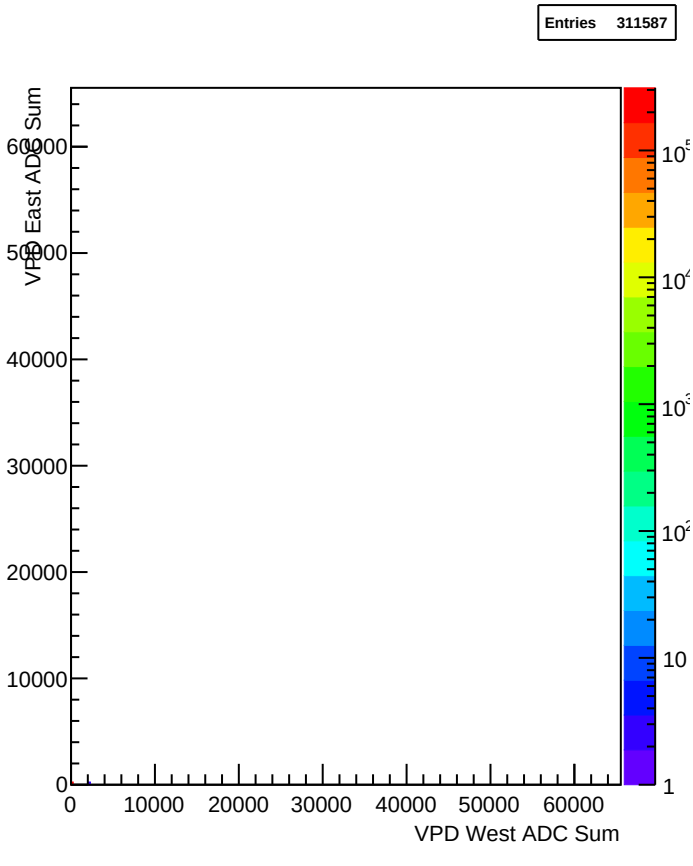

Entries 311587
BBC-S-West ADC Sum
VPD-West ADC Sum

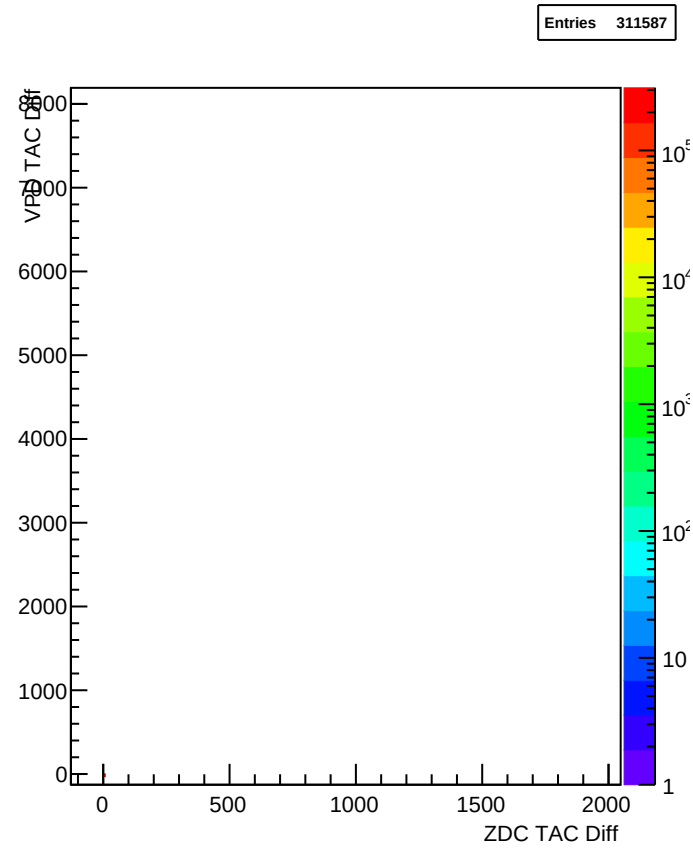

Entries 311587
VPD-East ADC Sum
ZDC-East ADC Sum Att

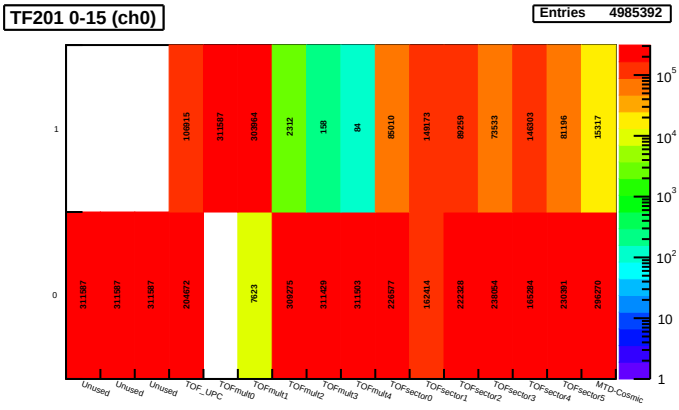

Entries 311587
VPD-West ADC Sum
ZDC-West ADC Sum Att

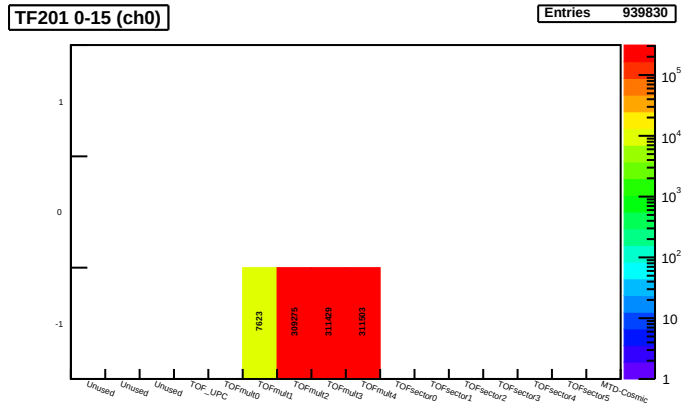

Entries 311587
BBC-S-East ADC Sum
BBC-S-West ADC Sum

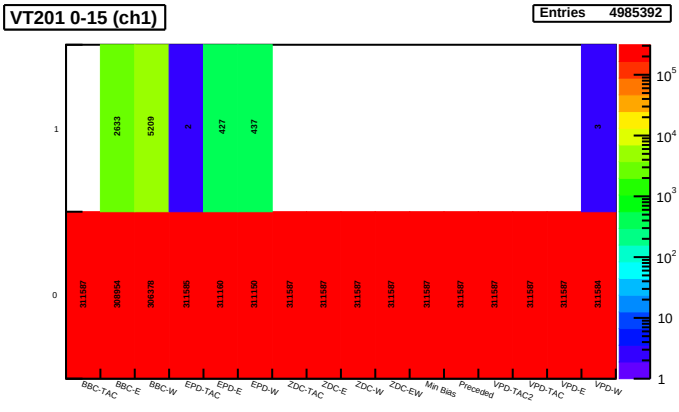

Entries 311587
ZDC East ADC Sum Att
ZDC West ADC Sum Att

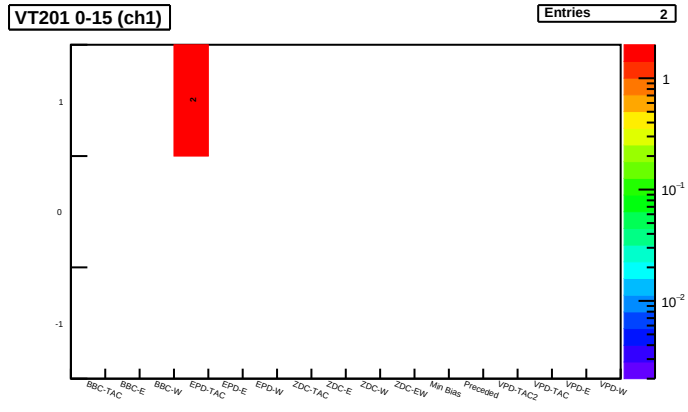

Entries 311587
VPD East ADC Sum
VPD West ADC Sum

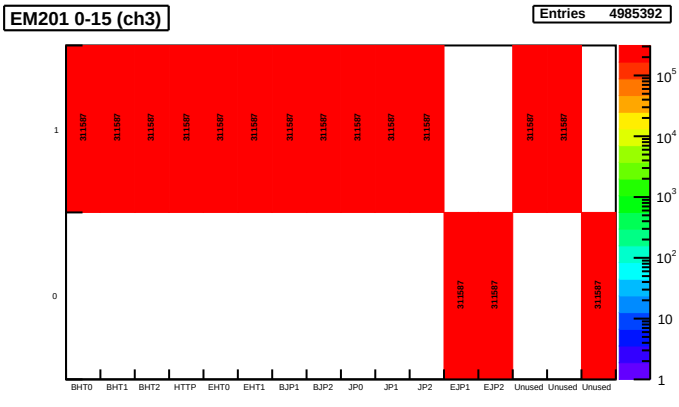

Entries 311587
VPD TAC Diff
ZDC TAC Diff

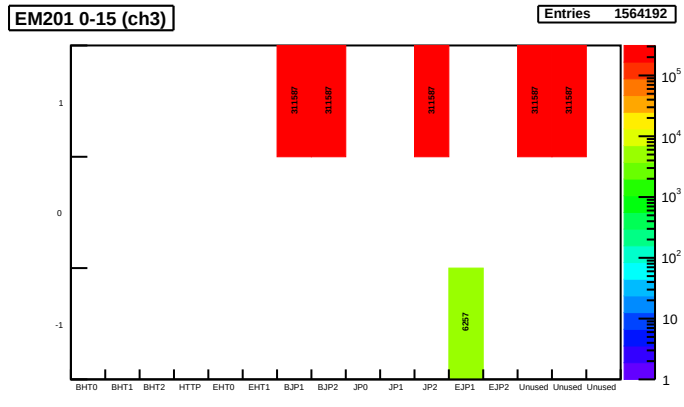

TF201 0-15 (ch0)
Entries 4985392
106915 311587 303864 2312 198 84 85010 149173 89259 72533 146303 81196 15317
311587 311587 311587 204672 7623 309275 311429 311503 226577 162414 222328 238064 165284 230301 209270
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

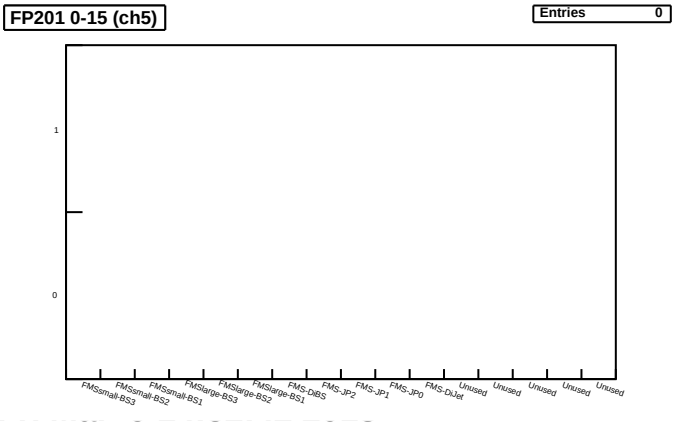

TF201 0-15 (ch0)
Entries 939830
7623 309275 311429 311503
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 4985392
2633 5209 2 427 437 3
311587 308954 306378 311585 311180 311150 311587 311587 311587 311587 311587 311587 311587 311587 311587 311584
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 2
2
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 4985392
311587 311587 311587 311587 311587 311587 311587 311587 311587 311587 311587 311587
311587 311587 311587
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 1564192
311587 311587 311587 311587 311587
6257
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused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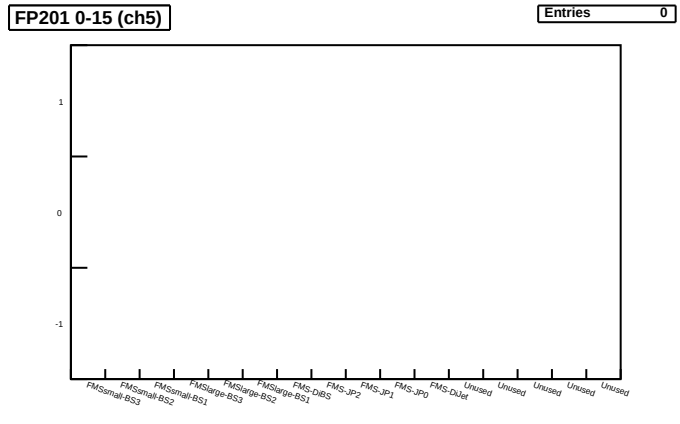
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