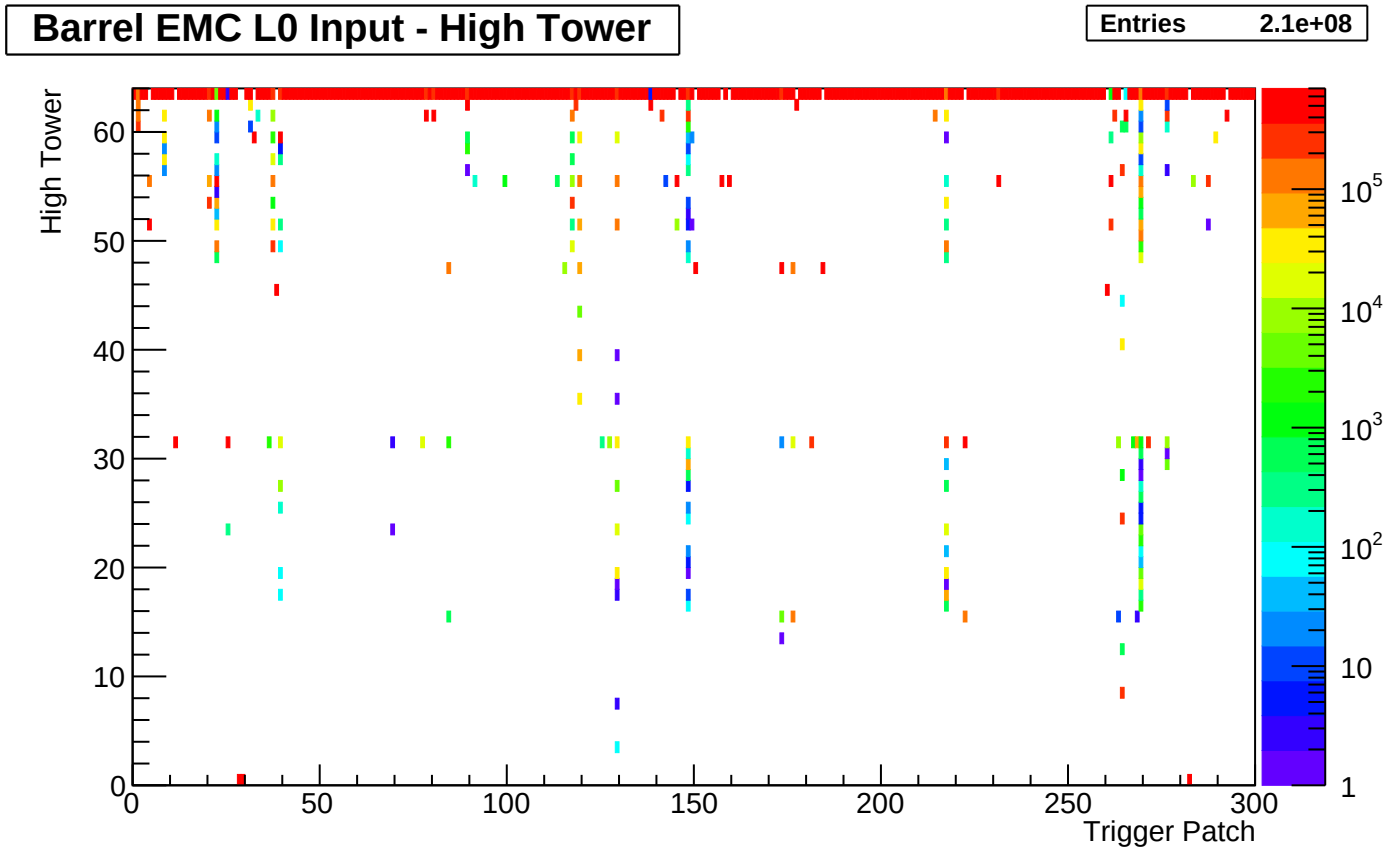
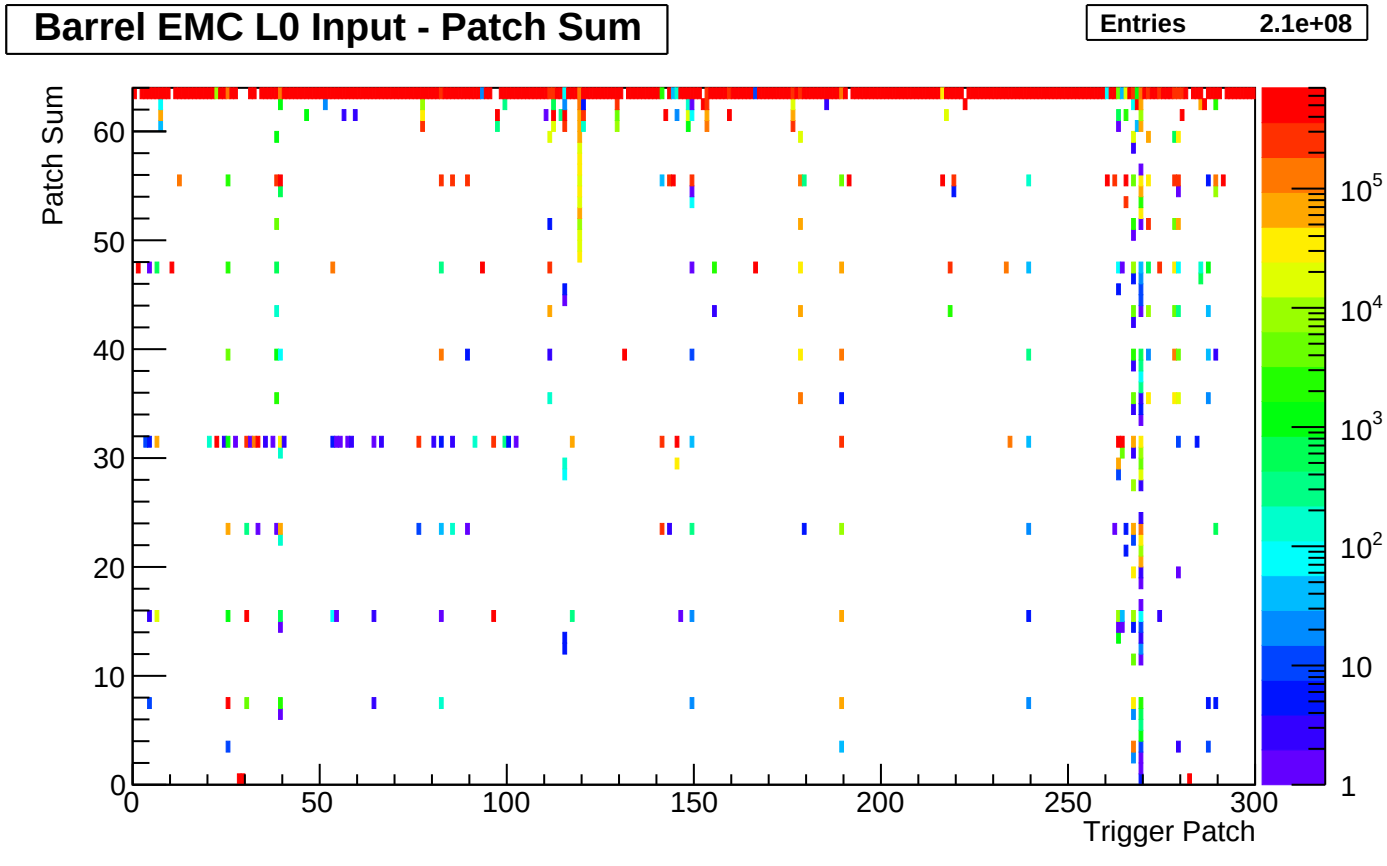
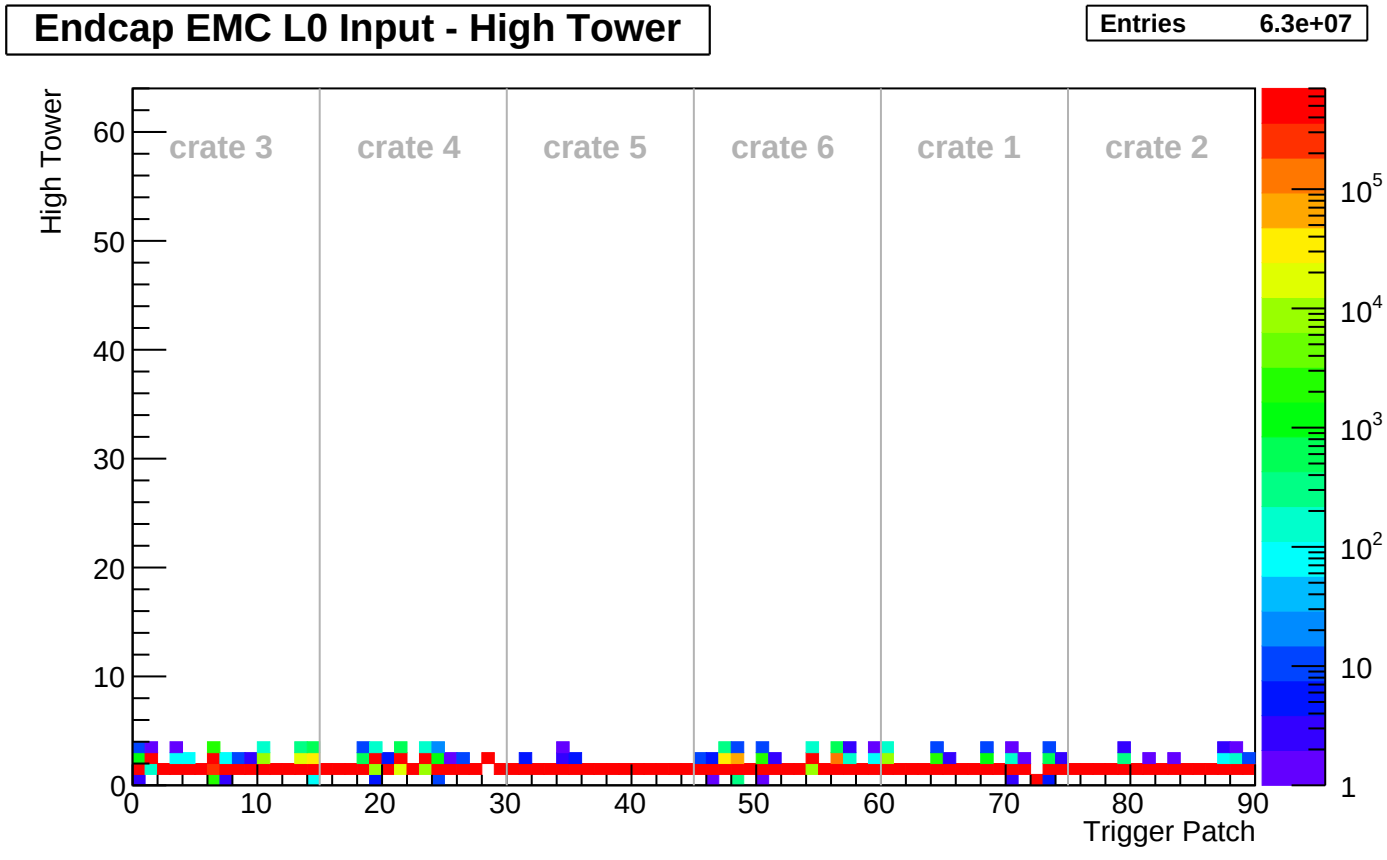
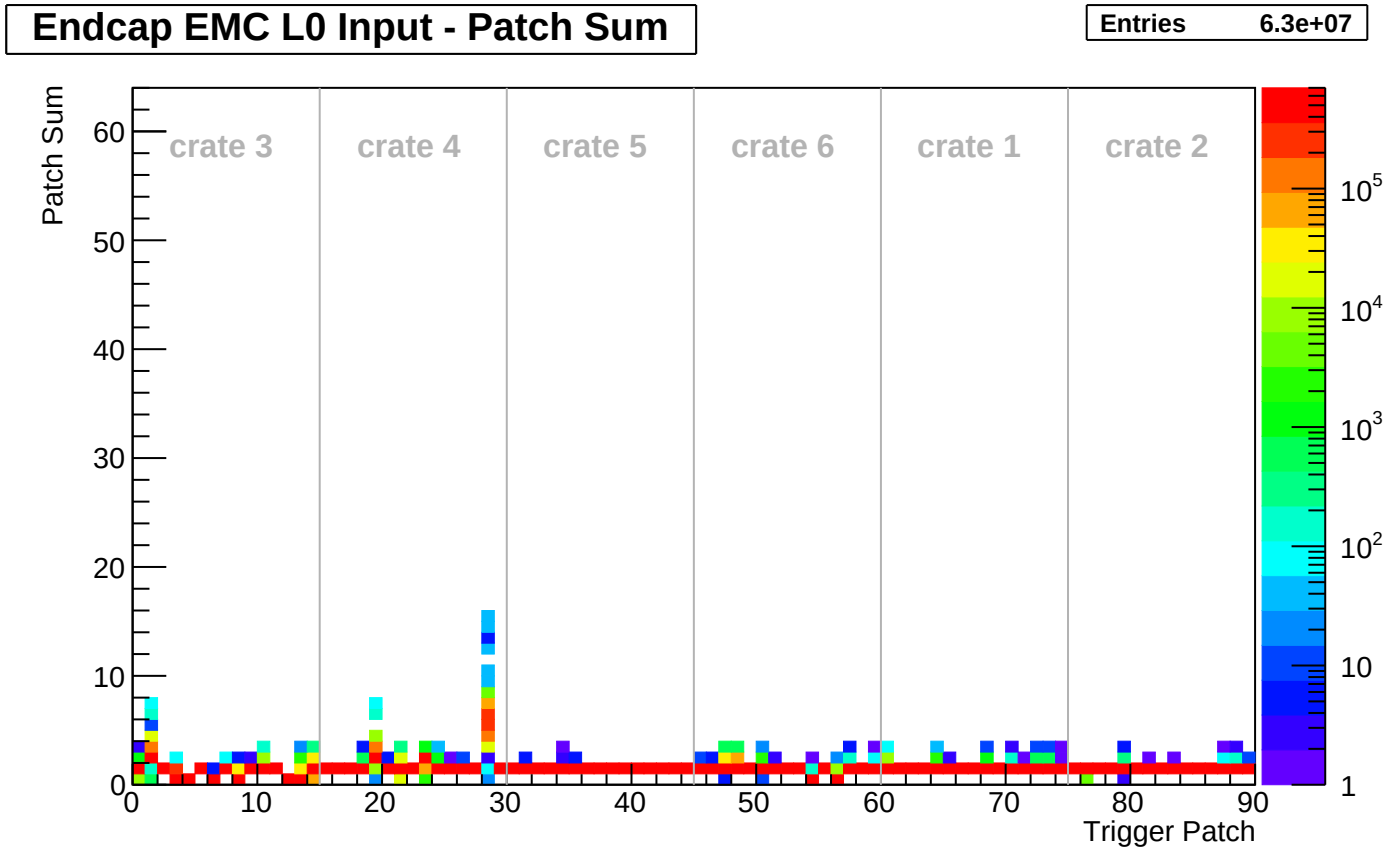
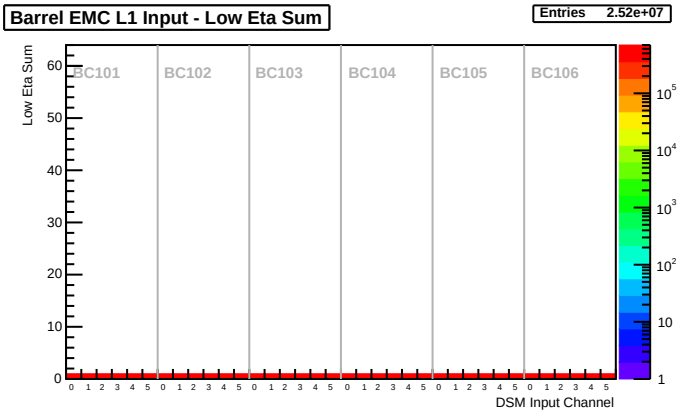
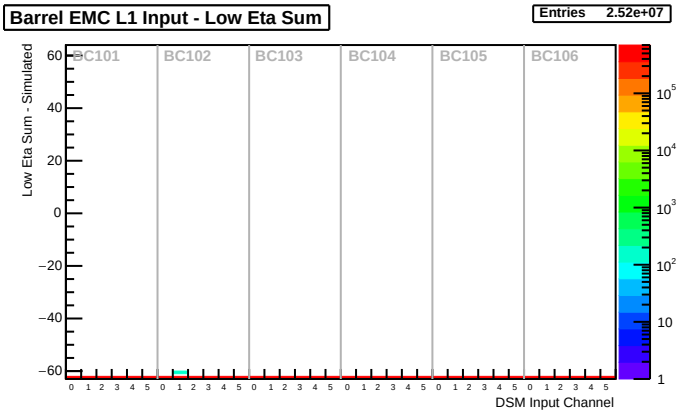

Barrel EMC L0 Input - High Tower
Entries 2.1e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

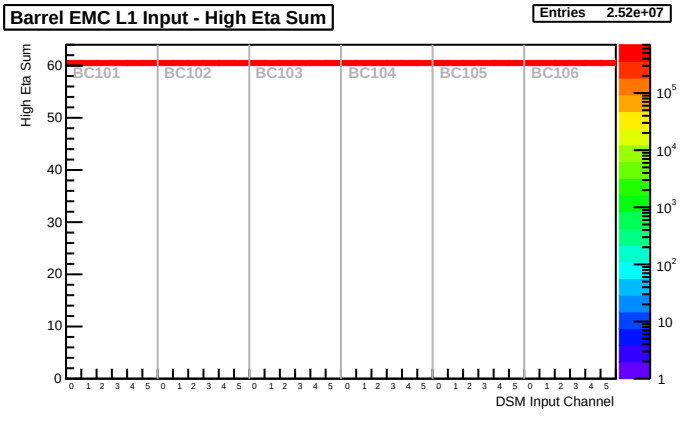

Barrel EMC L0 Input - Patch Sum
Entries 2.1e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

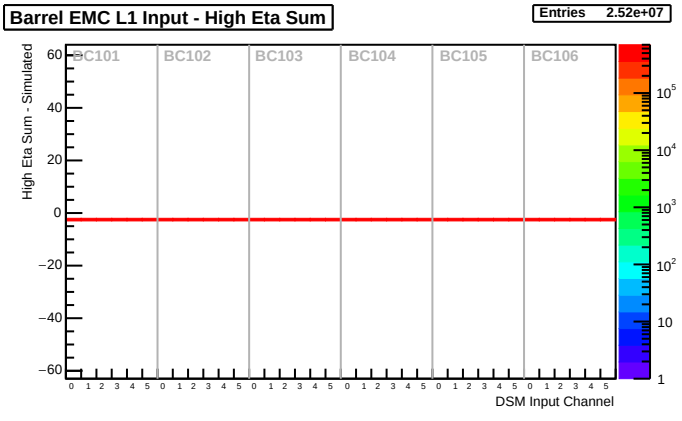

Endcap EMC L0 Input - High Tower
Entries 6.3e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

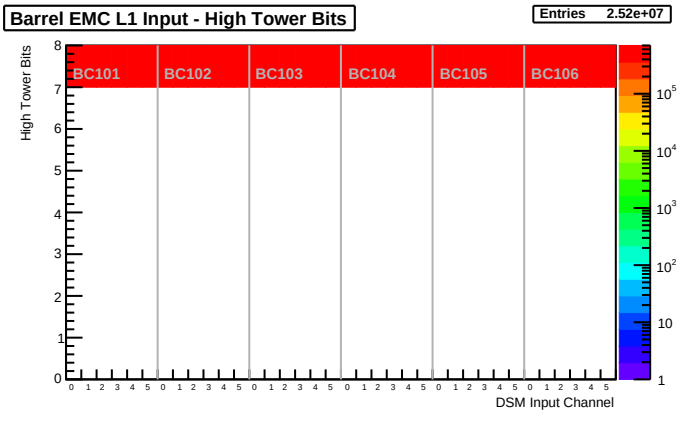

Endcap EMC L0 Input - Patch Sum
Entries 6.3e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

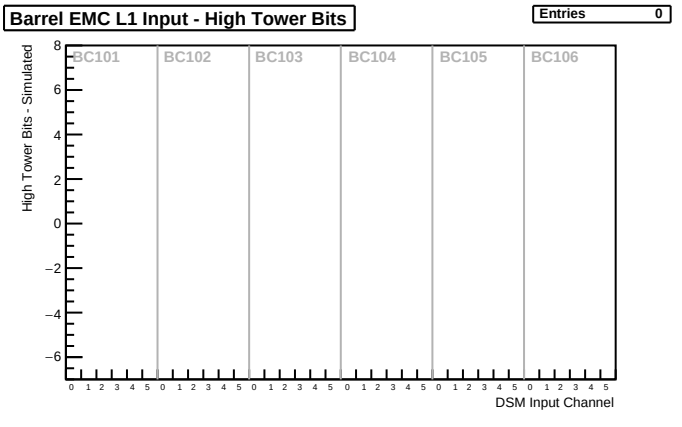

Barrel EMC L1 Input - Low Eta Sum
Entries 2.52e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

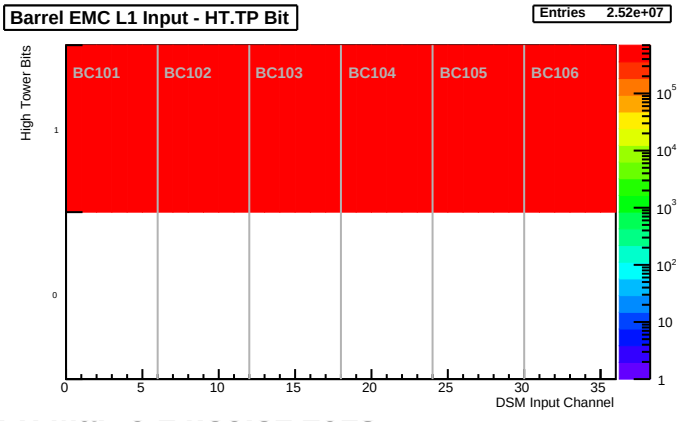

Barrel EMC L1 Input - Low Eta Sum
Entries 2.52e+07
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

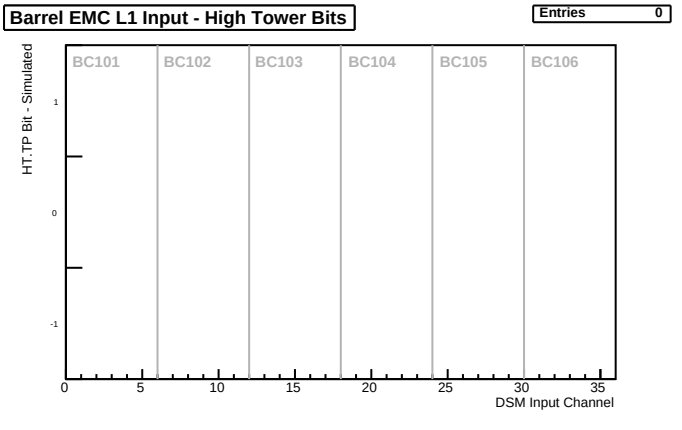

Barrel EMC L1 Input - High Eta Sum
Entries 2.52e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

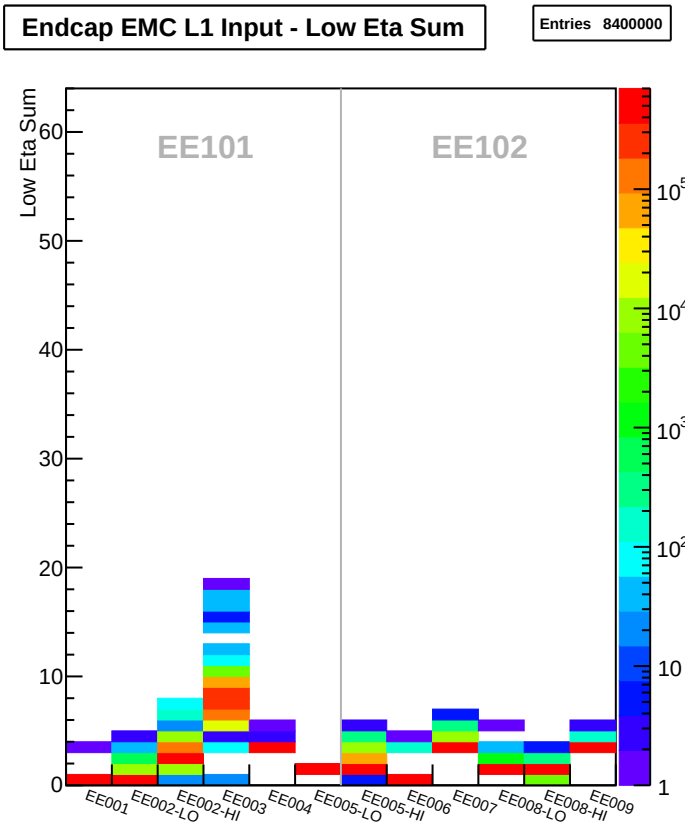

Barrel EMC L1 Input - High Eta Sum
Entries 2.52e+07
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

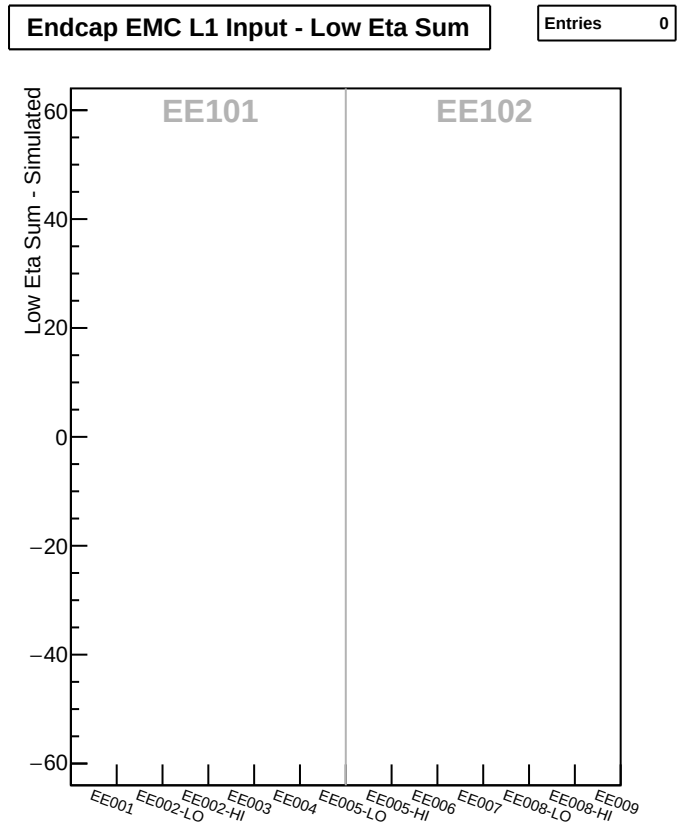

Barrel EMC L1 Input - High Tower Bits
Entries 2.52e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

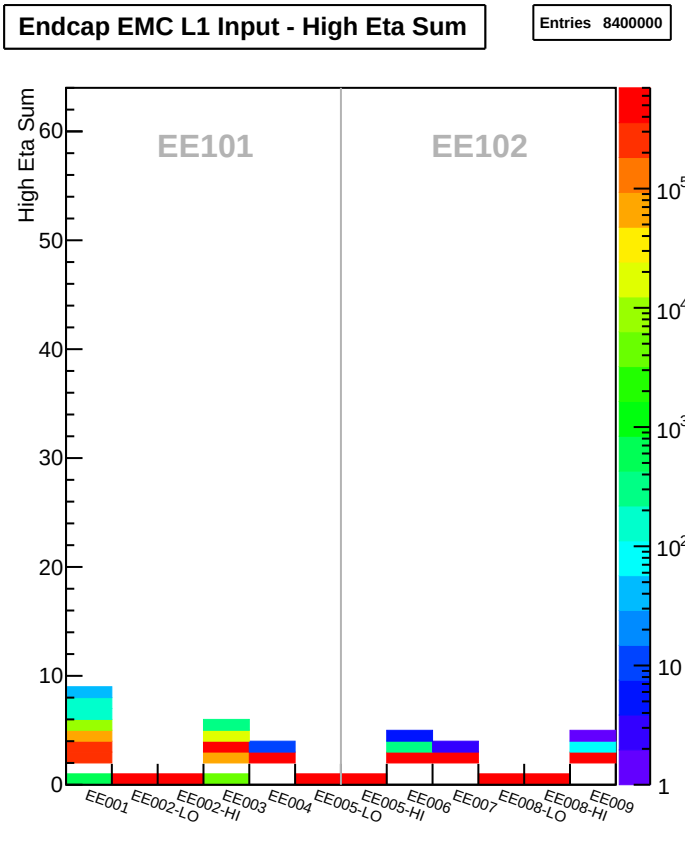

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

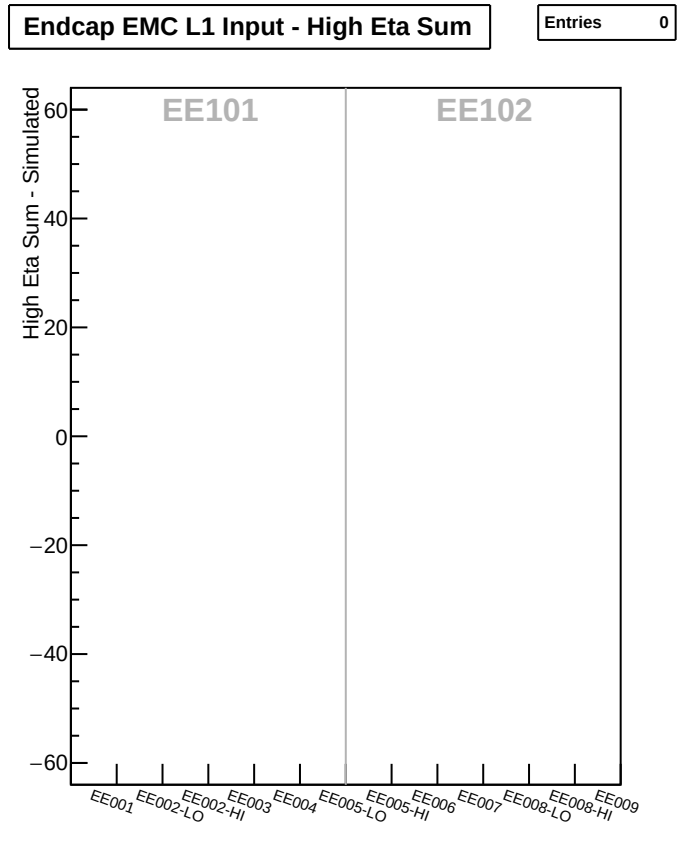

Barrel EMC L1 Input - HT.TP Bit
Entries 2.52e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

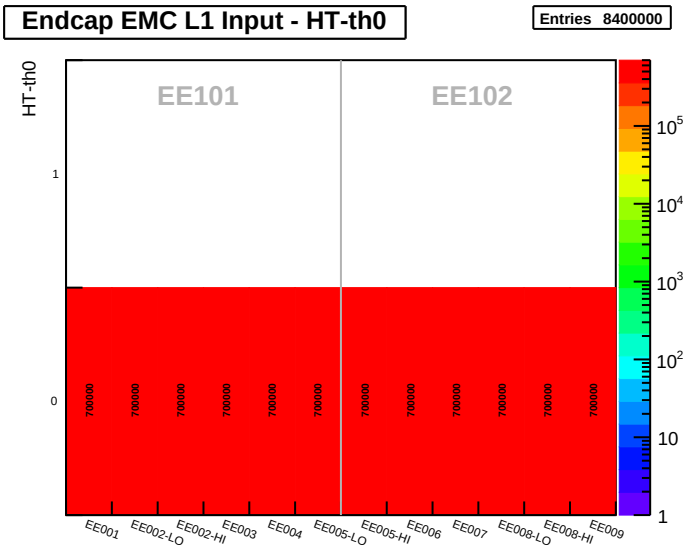

Barrel EMC L1 Input - High Tower Bits
Entries 0
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

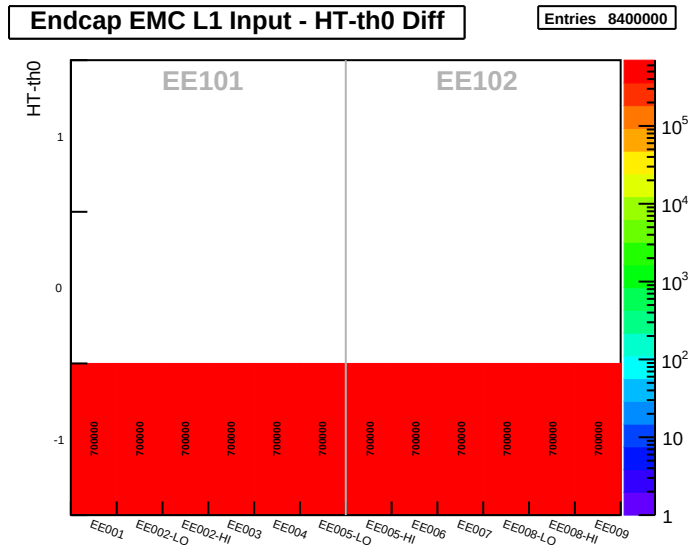

Endcap EMC L1 Input - Low Eta Sum
Entries 8400000
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

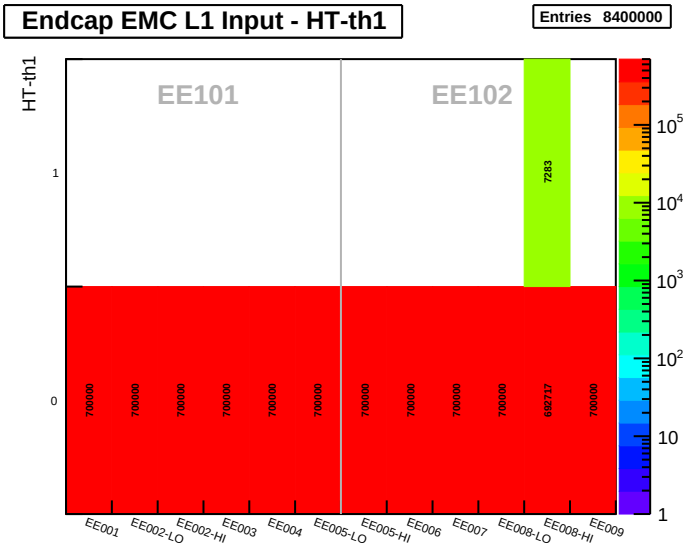

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

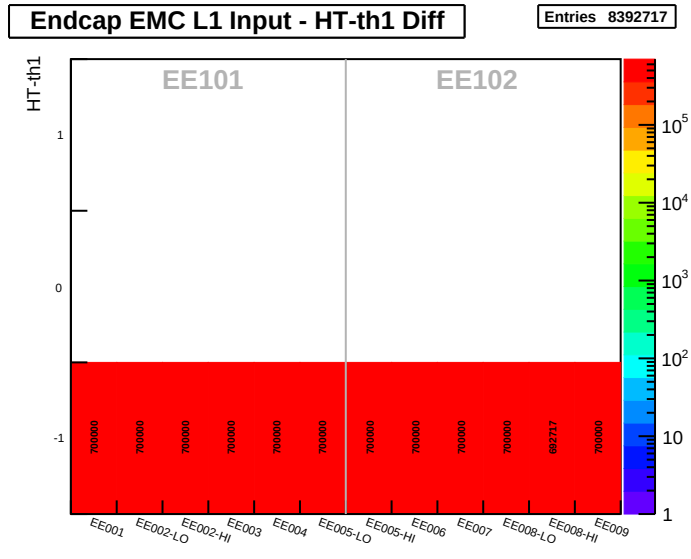

Endcap EMC L1 Input - High Eta Sum
Entries 8400000
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

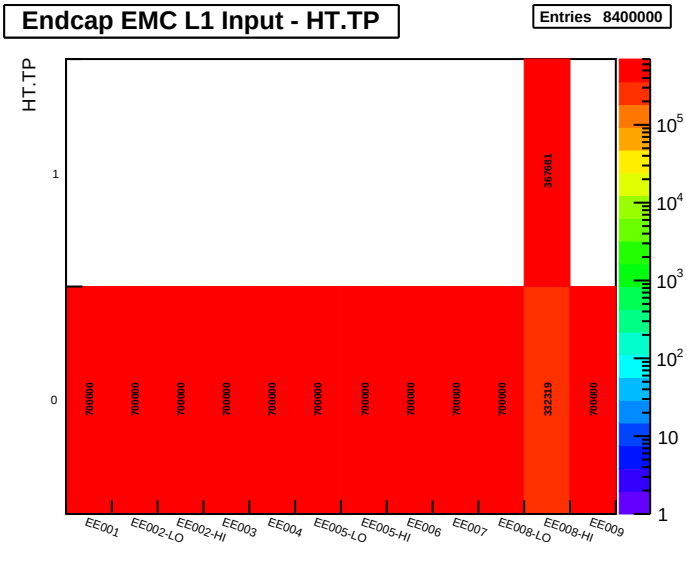

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

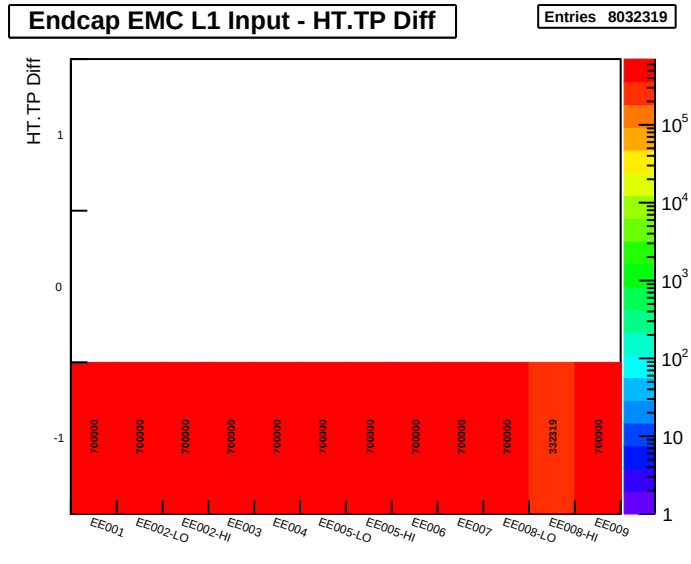

Endcap EMC L1 Input - HT-th0
Entries 8400000
HT-th0
EE101
EE102
1
0
700000
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
$10^5$
$10^4$
$10^3$
$10^2$
10
1

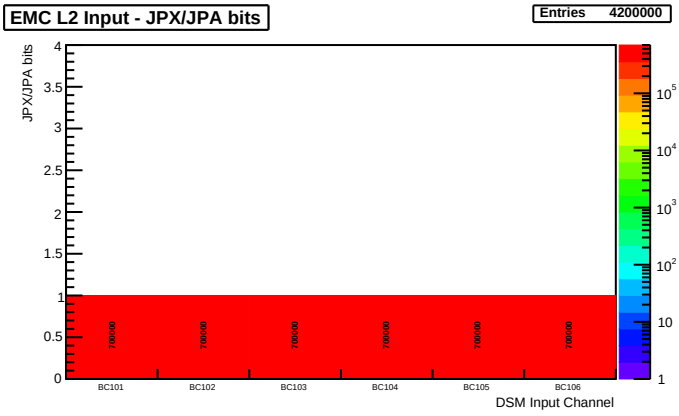

Endcap EMC L1 Input - HT-th0 Diff
Entries 8400000
HT-th0
EE101
EE102
1
0
-1
700000
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
$10^5$
$10^4$
$10^3$
$10^2$
10
1

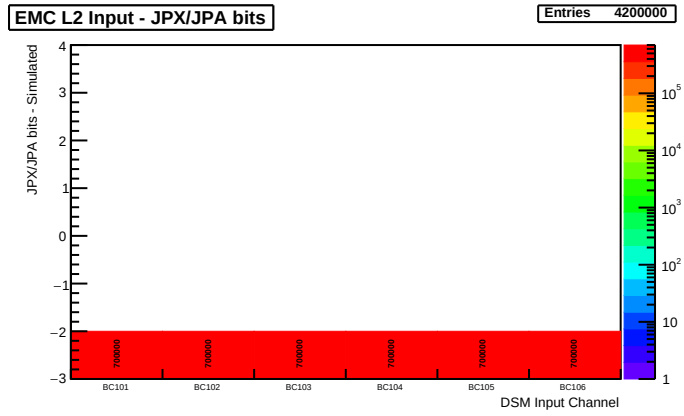

Endcap EMC L1 Input - HT-th1
Entries 8400000
HT-th1
EE101
EE102
1
0
7283
700000
692717
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
$10^5$
$10^4$
$10^3$
$10^2$
10
1

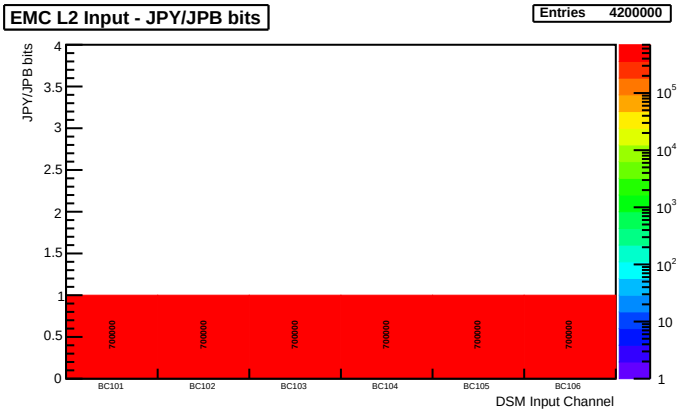

Endcap EMC L1 Input - HT-th1 Diff
Entries 8392717
HT-th1
EE101
EE102
1
0
-1
700000
692717
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
$10^5$
$10^4$
$10^3$
$10^2$
10
1

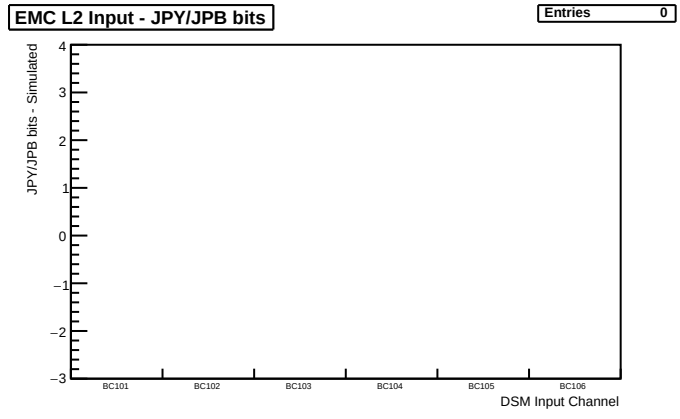

Endcap EMC L1 Input - HT.TP
Entries 8400000
HT.TP
1
0
367681
332319
700000
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
$10^5$
$10^4$
$10^3$
$10^2$
10
1

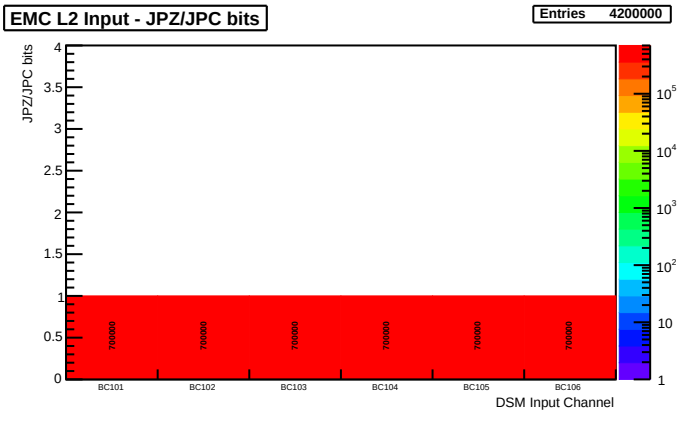

Endcap EMC L1 Input - HT.TP Diff
Entries 8032319
HT.TP Diff
1
0
-1
332319
700000
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
$10^5$
$10^4$
$10^3$
$10^2$
10
1

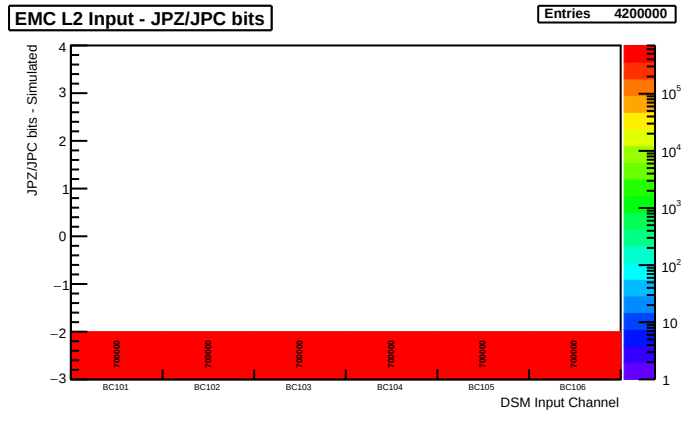

EMC L2 Input - JPX/JPA bits
Entries 4200000
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
700000

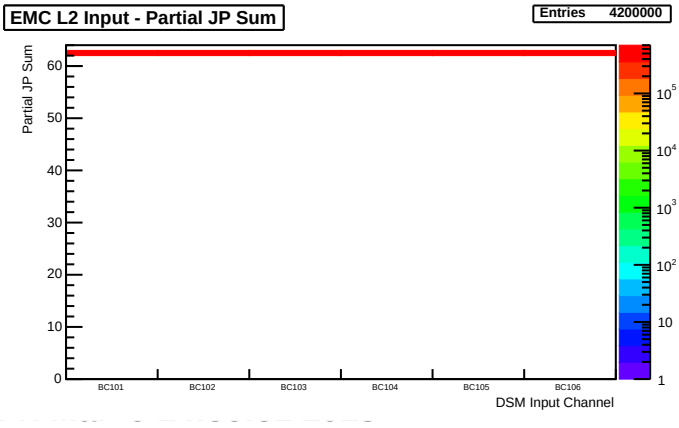

EMC L2 Input - JPX/JPA bits
Entries 4200000
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
700000

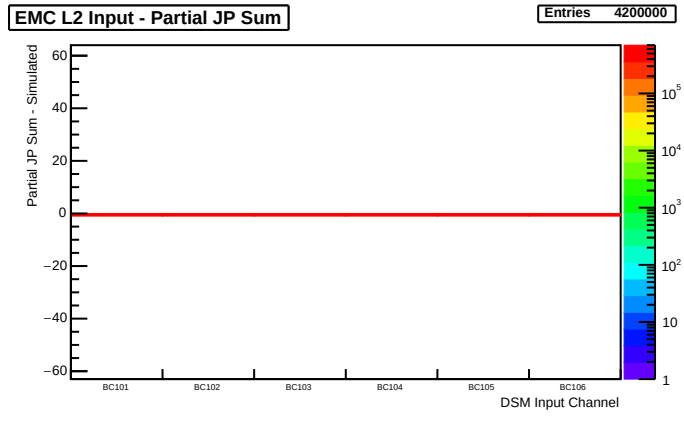

EMC L2 Input - JPY/JPB bits
Entries 4200000
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
700000

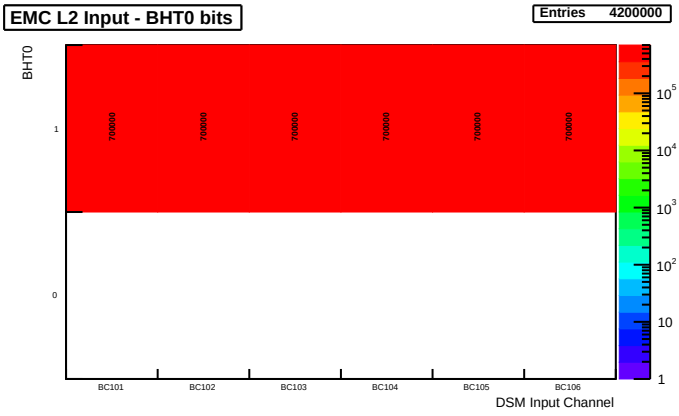

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

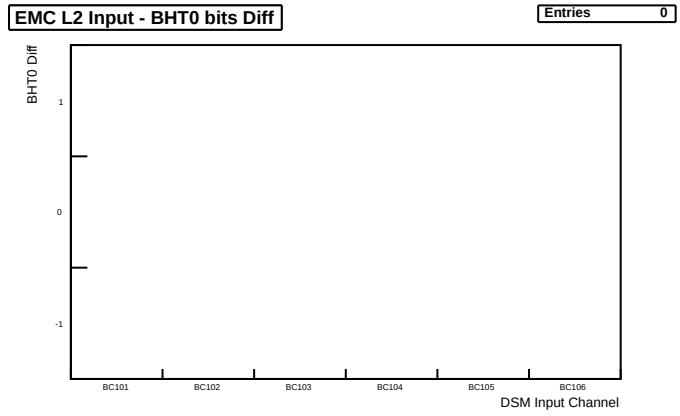

EMC L2 Input - JPZ/JPC bits
Entries 4200000
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
700000

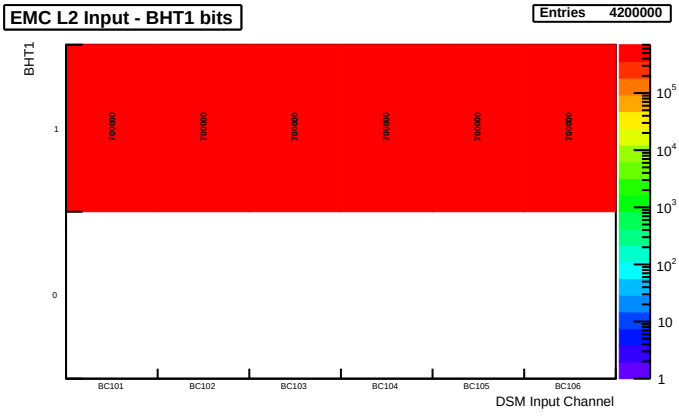

EMC L2 Input - JPZ/JPC bits
Entries 4200000
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
700000

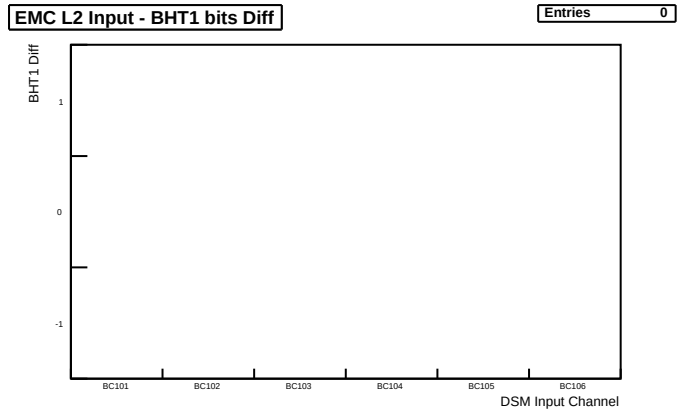

EMC L2 Input - Partial JP Sum
Entries 4200000
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

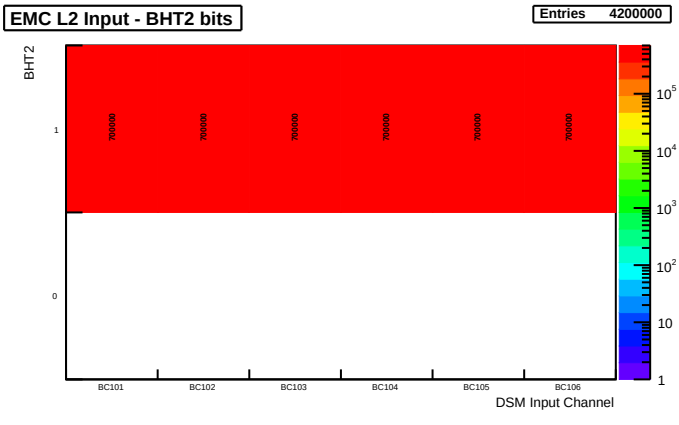

EMC L2 Input - Partial JP Sum
Entries 4200000
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

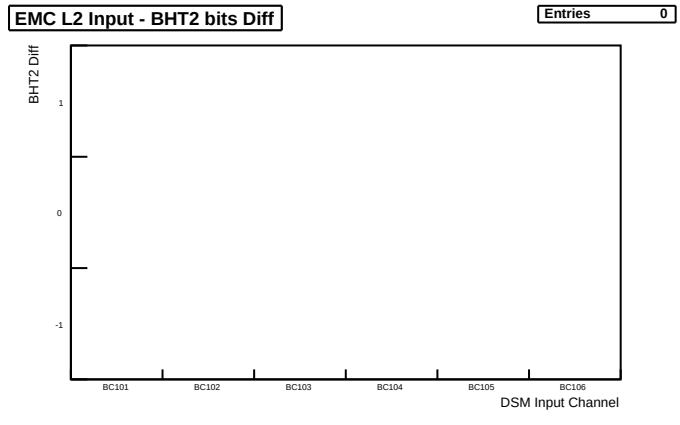

EMC L2 Input - BHT0 bits
Entries 4200000
BHT0
700000
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

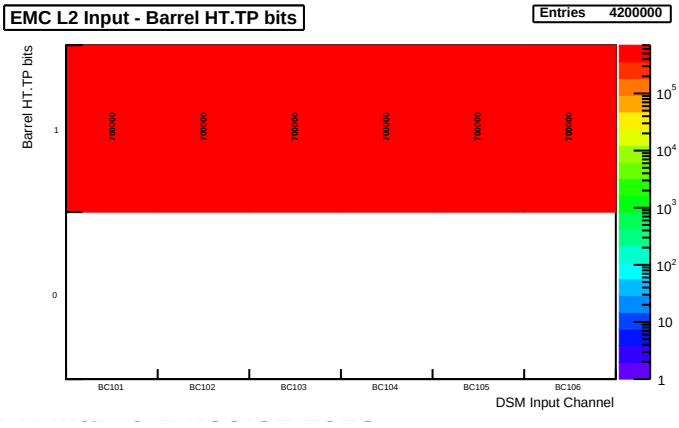

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

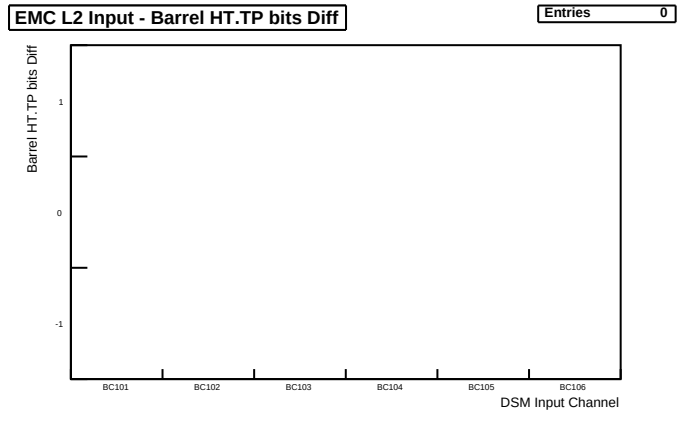

EMC L2 Input - BHT1 bits
Entries 4200000
BHT1
700000
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

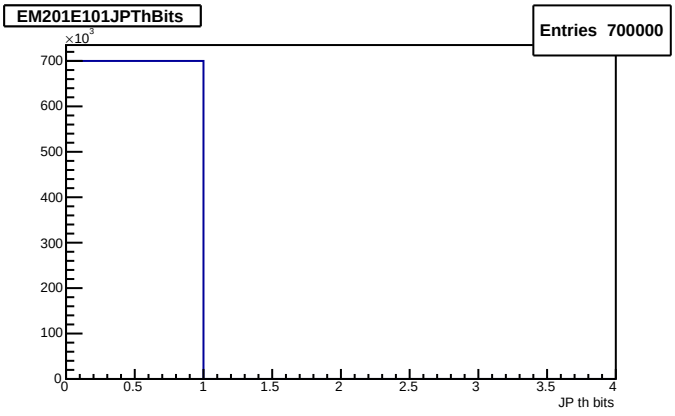

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

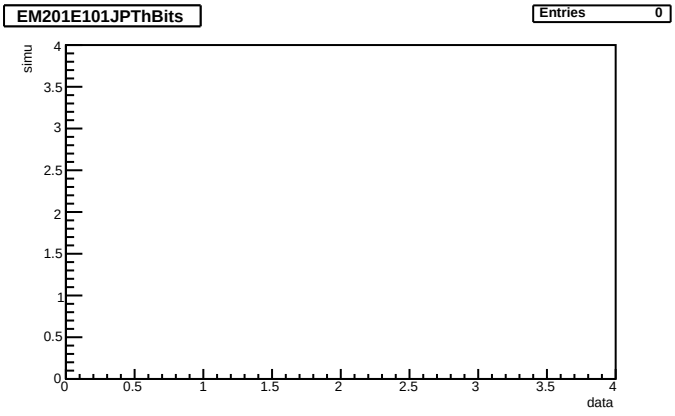

EMC L2 Input - BHT2 bits
Entries 4200000
BHT2
700000
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

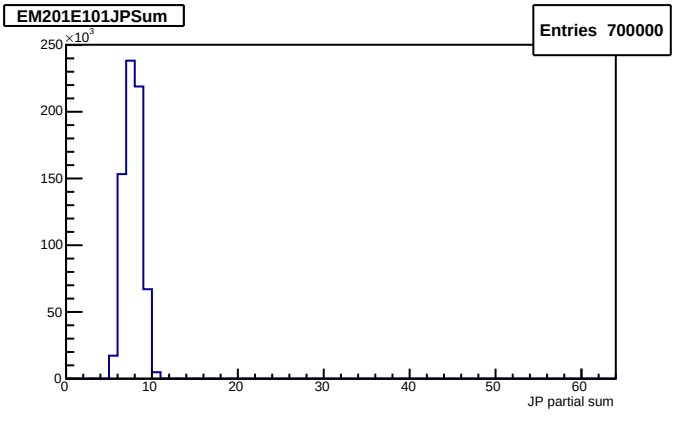

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

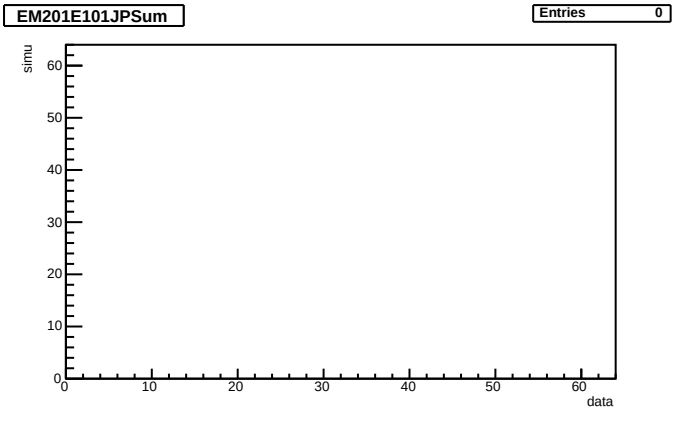

EMC L2 Input - Barrel HT.TP bits
Entries 4200000
Barrel HT.TP bits
700000
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

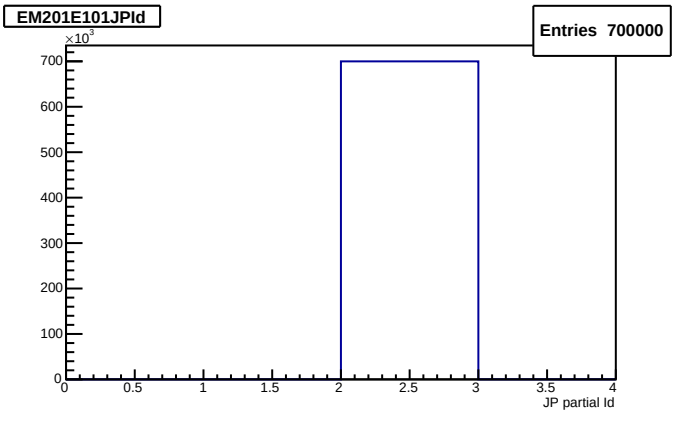

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

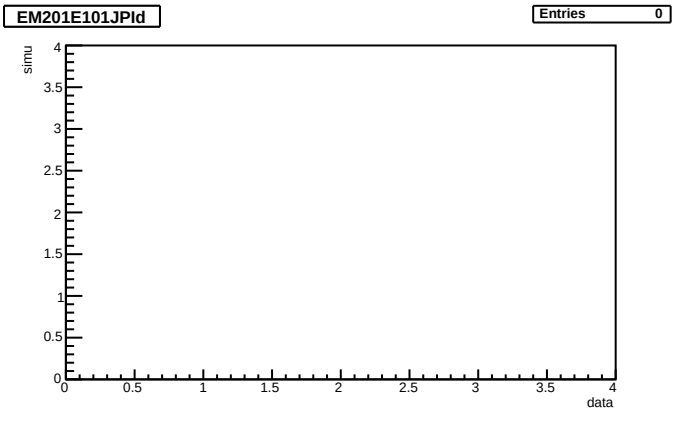

EM201E101JPThBits
Entries 700000
×10^3
JP th bits

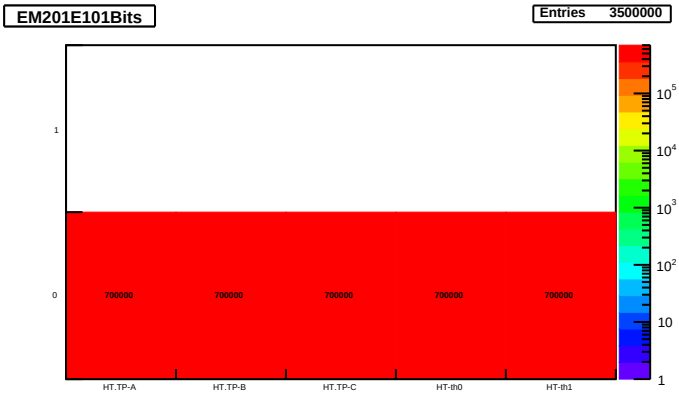

EM201E101JPThBits
Entries 0
simu
data

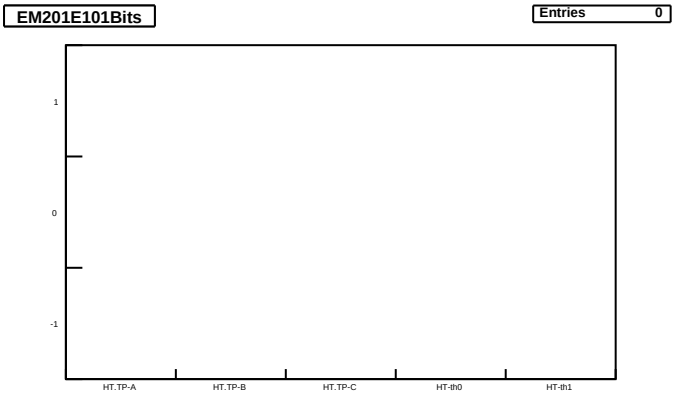

EM201E101JPSum
Entries 700000
×10^3
JP partial sum

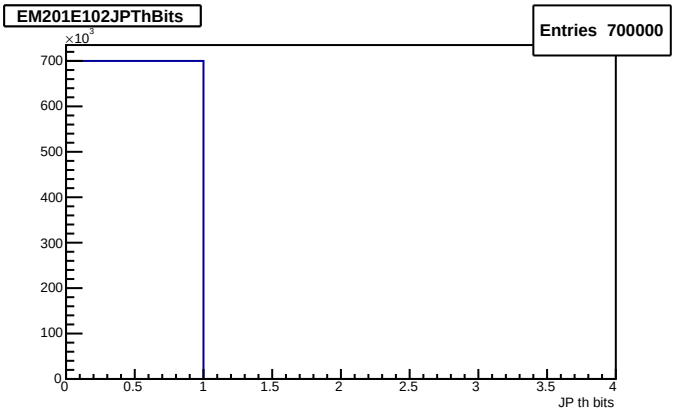

EM201E101JPSum
Entries 0
simu
data

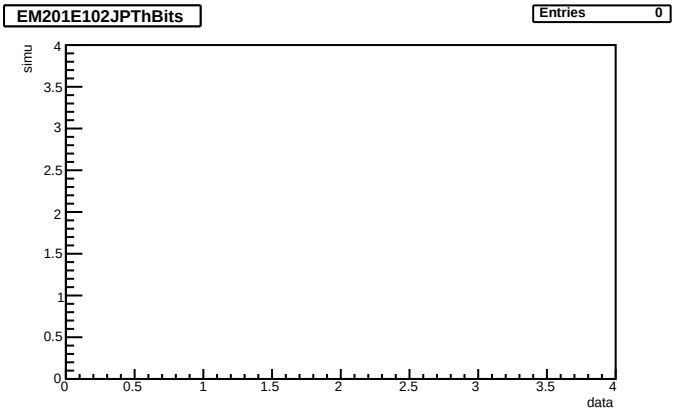

EM201E101JPId
Entries 700000
×10^3
JP partial Id

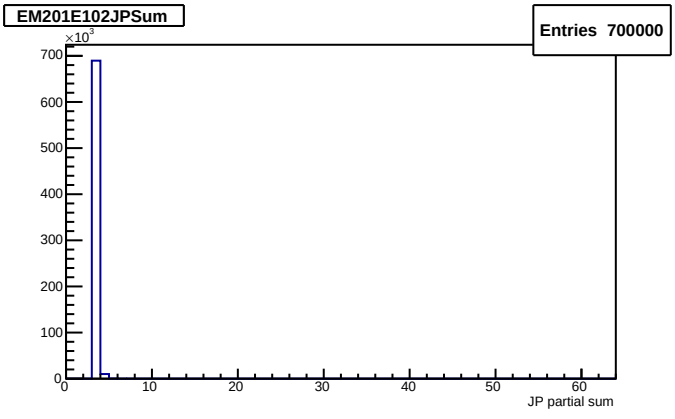

EM201E101JPId
Entries 0
simu
data

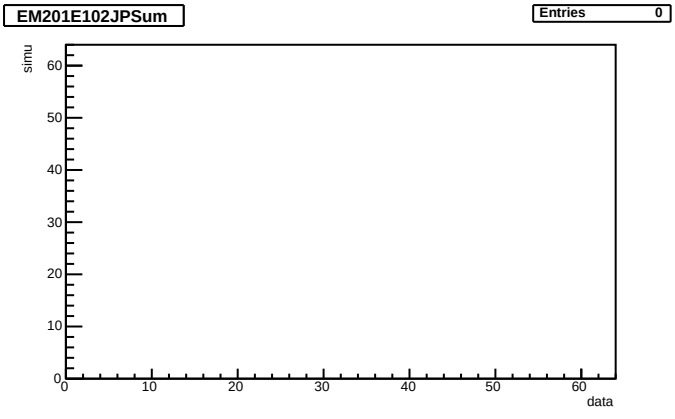

EM201E101Bits
Entries 3500000
700000
700000
700000
700000
700000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

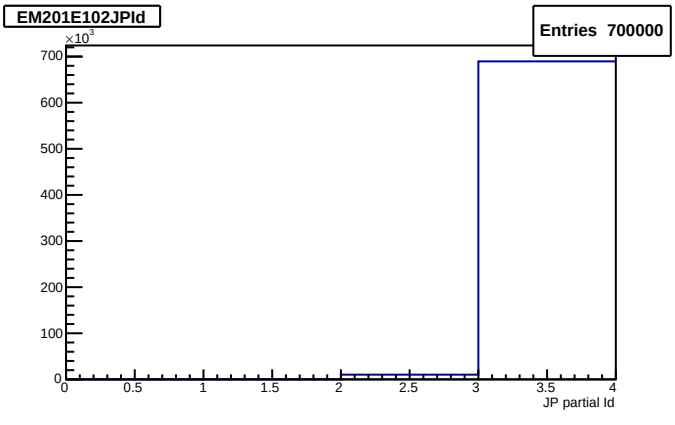

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

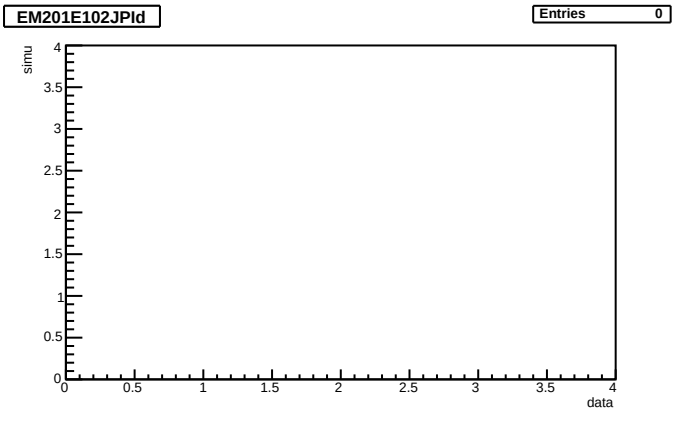

EM201E102JPThBits
Entries 700000
×10^3
700
600
500
400
300
200
100
0
0 0.5 1 1.5 2 2.5 3 3.5 4
JP th bits

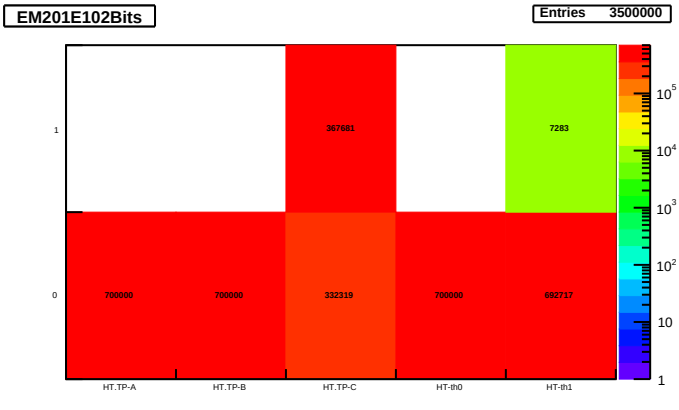

EM201E102JPThBits
Entries 0
simu
4
3.5
3
2.5
2
1.5
1
0.5
0
0 0.5 1 1.5 2 2.5 3 3.5 4
data

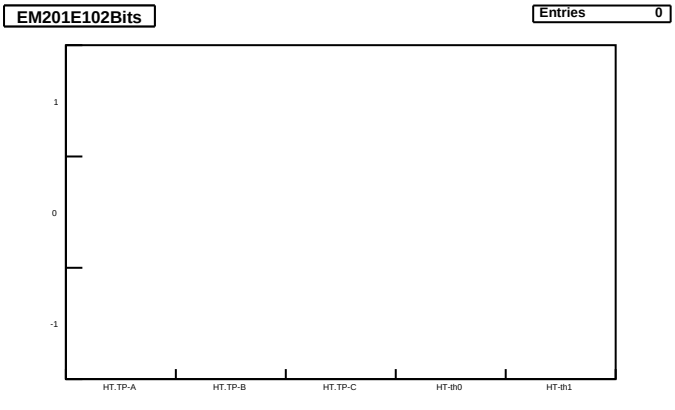

EM201E102JPSum
Entries 700000
×10^3
700
600
500
400
300
200
100
0
0 10 20 30 40 50 60
JP partial sum

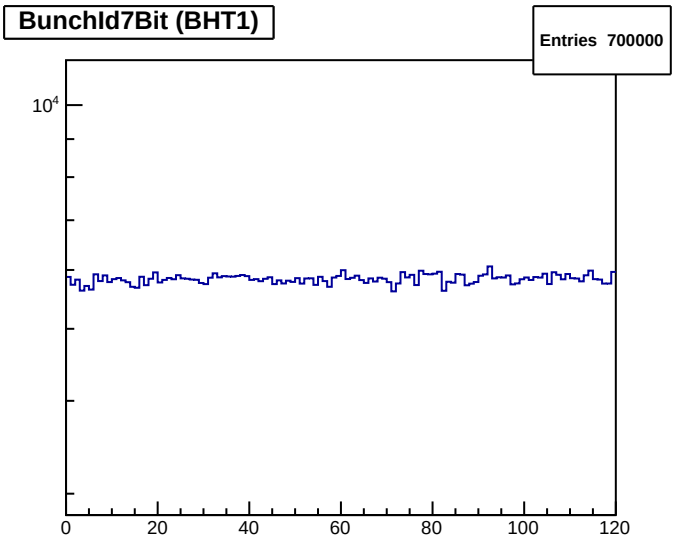

EM201E102JPSum
Entries 0
simu
60
50
40
30
20
10
0
0 10 20 30 40 50 60
data

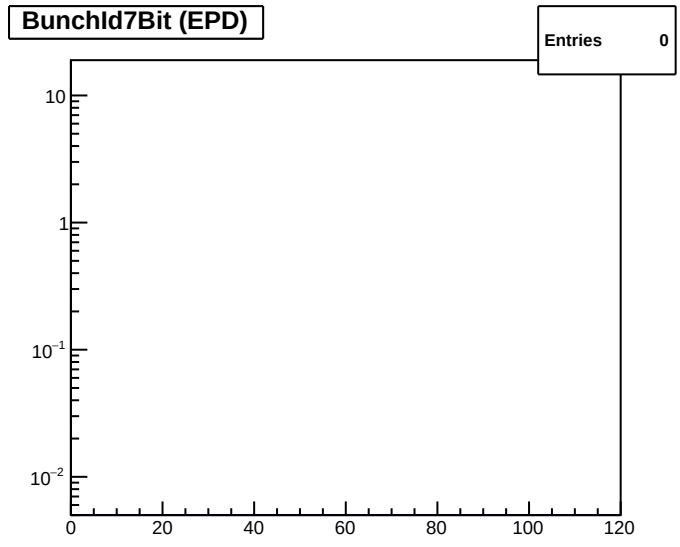

EM201E102JPId
Entries 700000
×10^3
700
600
500
400
300
200
100
0
0 0.5 1 1.5 2 2.5 3 3.5 4
JP partial Id

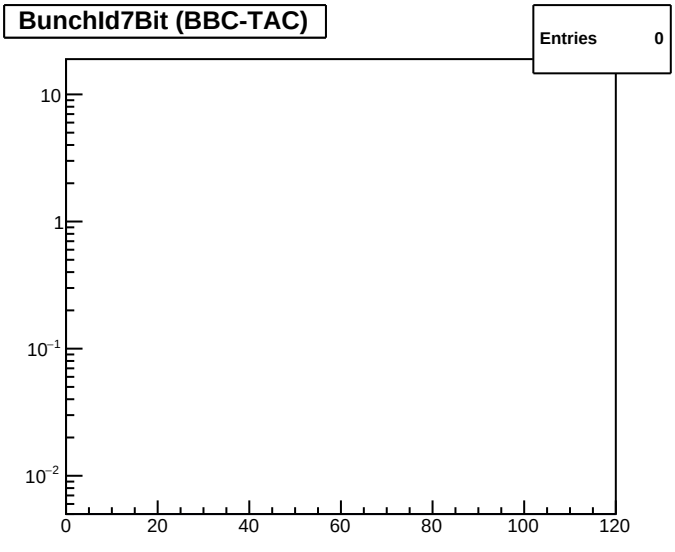

EM201E102JPId
Entries 0
simu
4
3.5
3
2.5
2
1.5
1
0.5
0
0 0.5 1 1.5 2 2.5 3 3.5 4
data

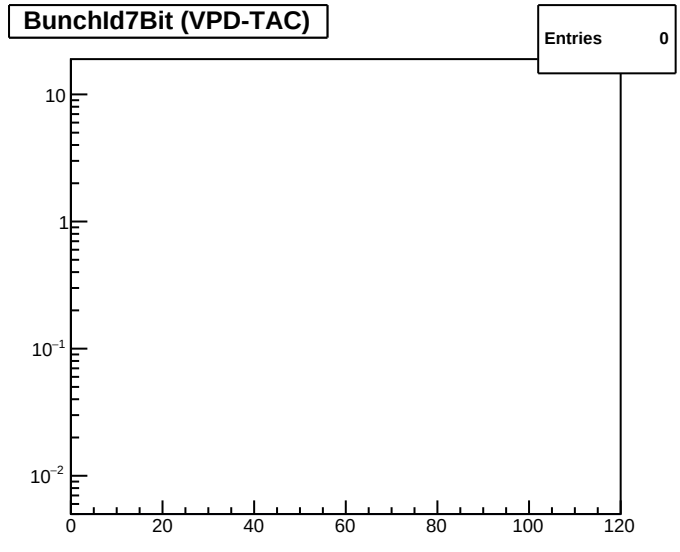

EM201E102Bits
Entries 3500000
1
0
367681
7283
700000
700000
332319
700000
692717
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1
10^5
10^4
10^3
10^2
10
1

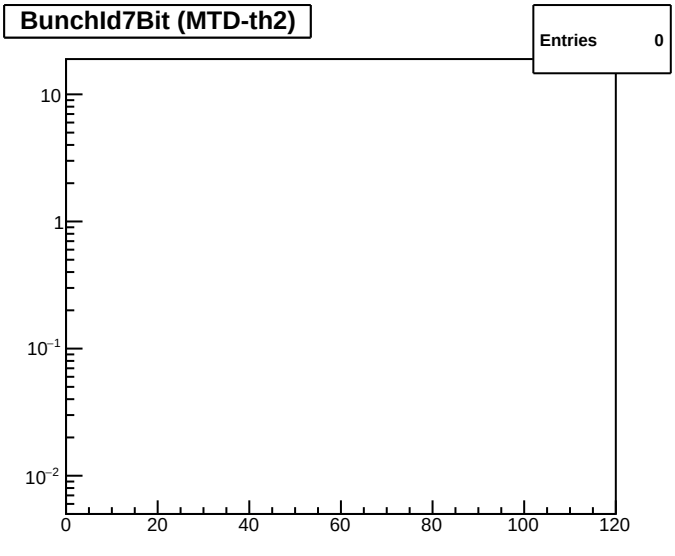

EM201E102Bits
Entries 0
1
0
-1
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

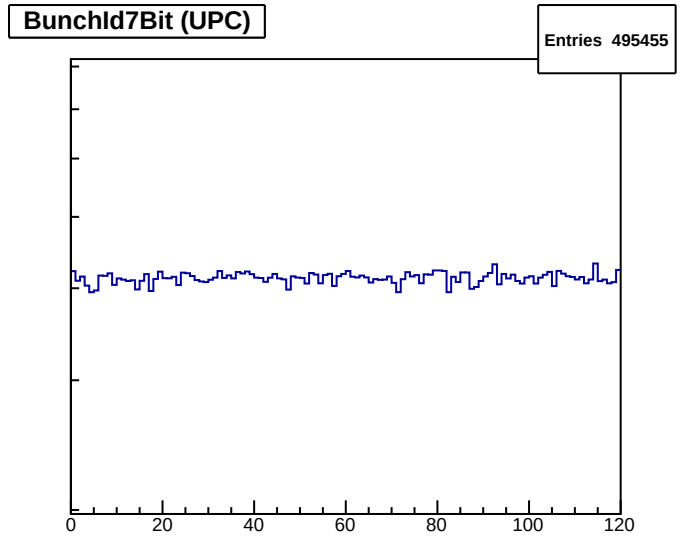

BunchId7Bit (BHT1)
Entries 700000
$10^4$
0
20
40
60
80
100
120

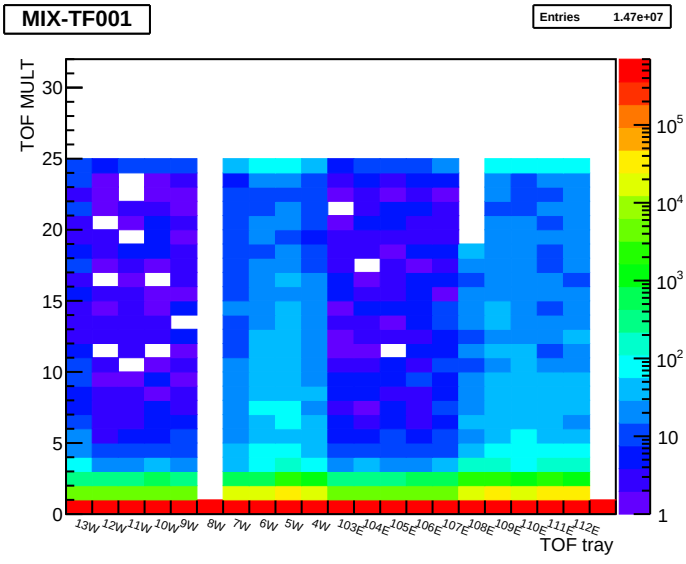

BunchId7Bit (EPD)
Entries 0
10
1
$10^{-1}$
$10^{-2}$
0
20
40
60
80
100
120

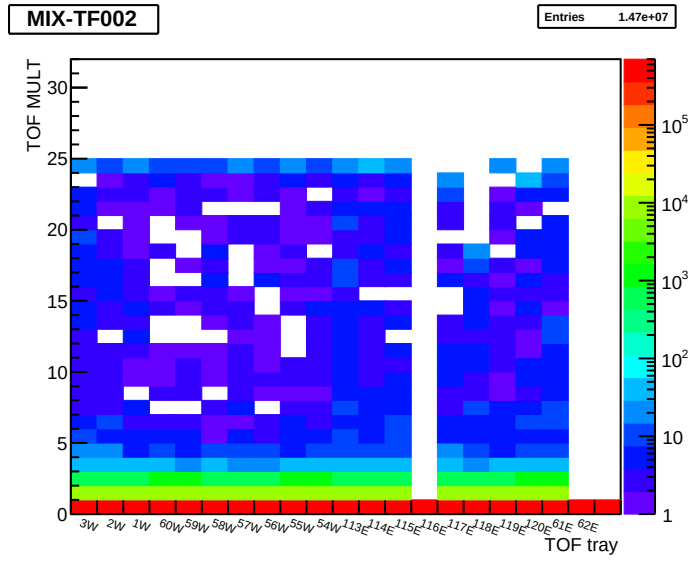

BunchId7Bit (BBC-TAC)
Entries 0
10
1
$10^{-1}$
$10^{-2}$
0
20
40
60
80
100
120

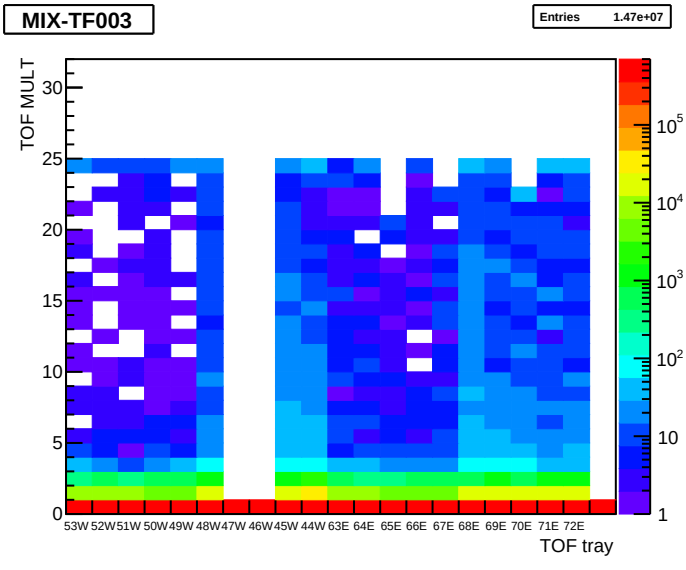

BunchId7Bit (VPD-TAC)
Entries 0
10
1
$10^{-1}$
$10^{-2}$
0
20
40
60
80
100
120

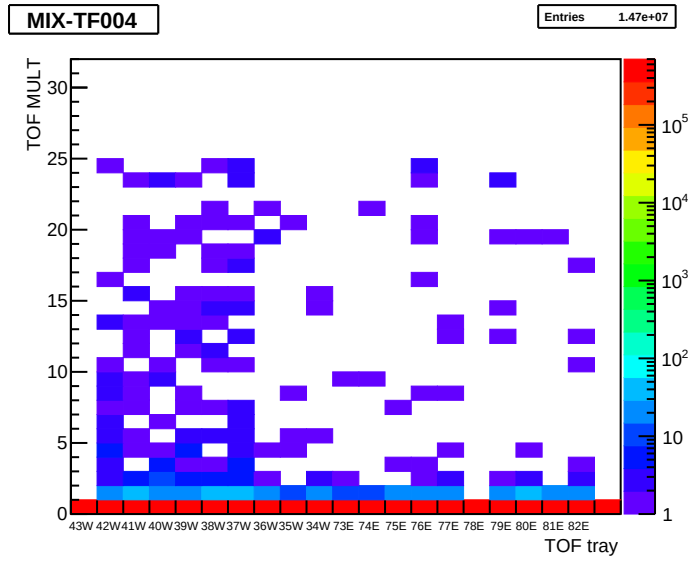

BunchId7Bit (MTD-th2)
Entries 0
10
1
$10^{-1}$
$10^{-2}$
0
20
40
60
80
100
120

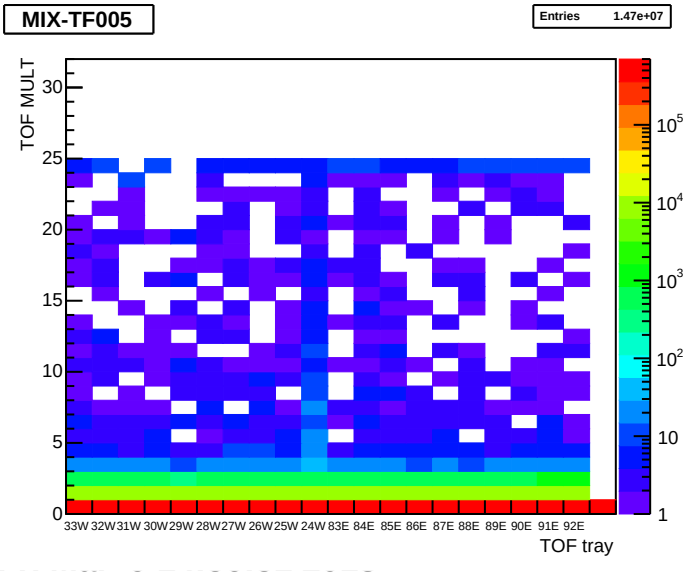

BunchId7Bit (UPC)
Entries 495455
0
20
40
60
80
100
120

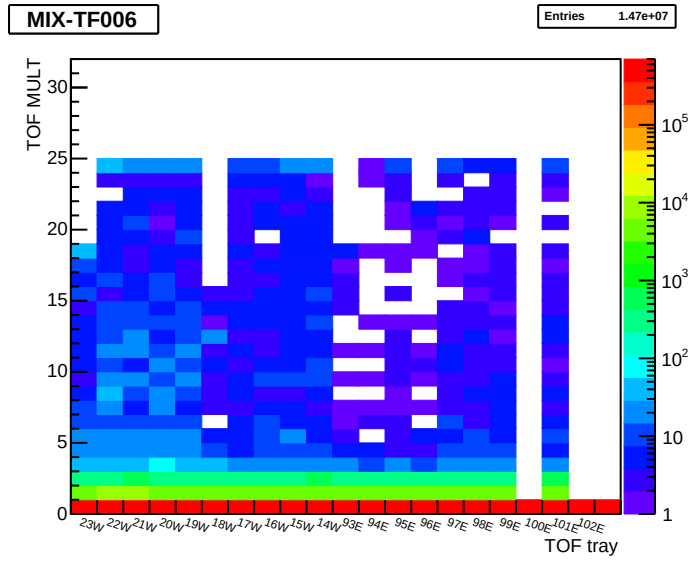

MIX-TF001
Entries 1.47e+07
TOF MULT
TOF tray

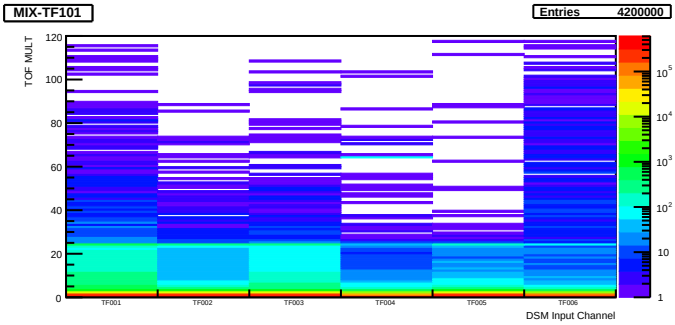

MIX-TF002
Entries 1.47e+07
TOF MULT
TOF tray

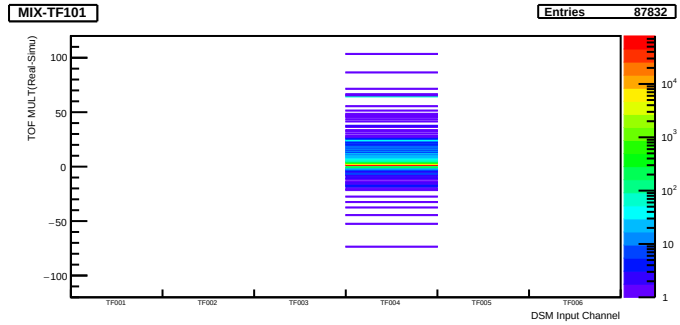

MIX-TF003
Entries 1.47e+07
TOF MULT
TOF tray

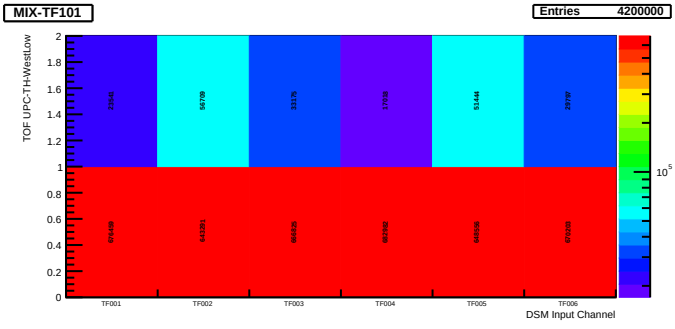

MIX-TF004
Entries 1.47e+07
TOF MULT
TOF tray

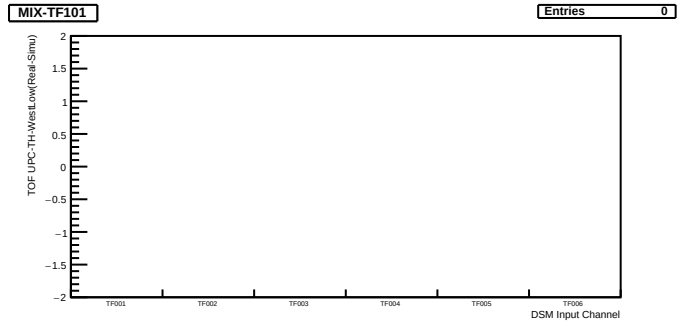

MIX-TF005
Entries 1.47e+07
TOF MULT
TOF tray

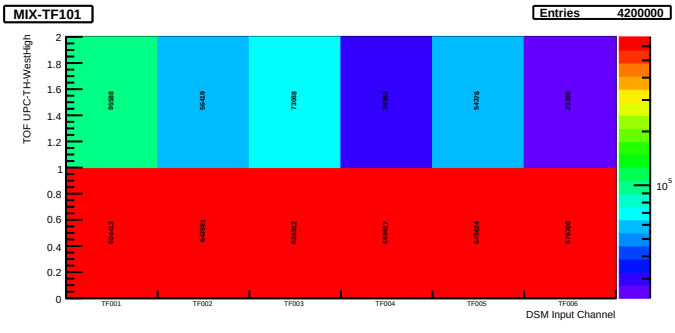

MIX-TF006
Entries 1.47e+07
TOF MULT
TOF tray

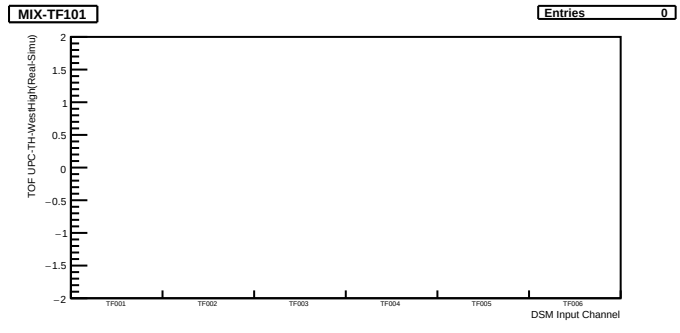

MIX-TF101
Entries 4200000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

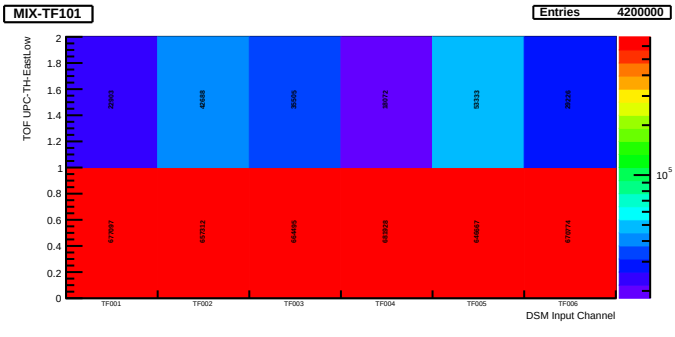

MIX-TF101
Entries 87832
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

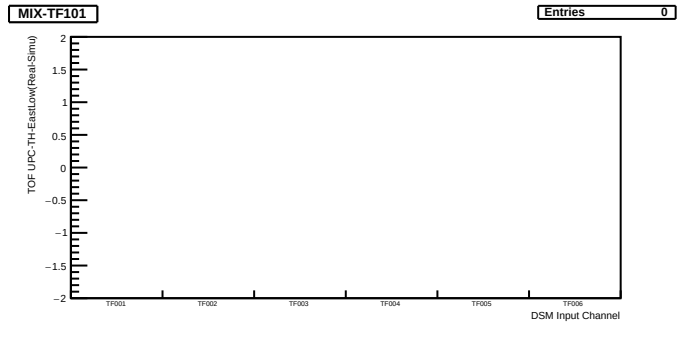

MIX-TF101
Entries 4200000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

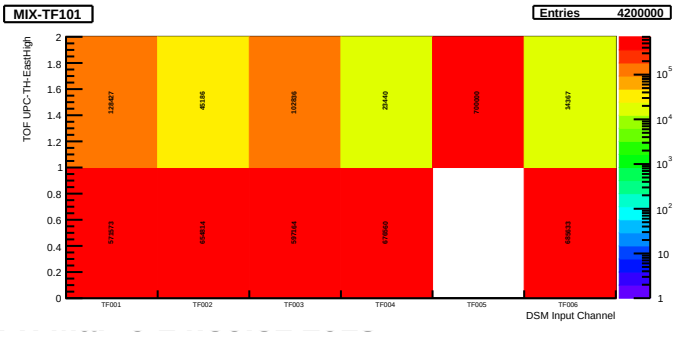

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

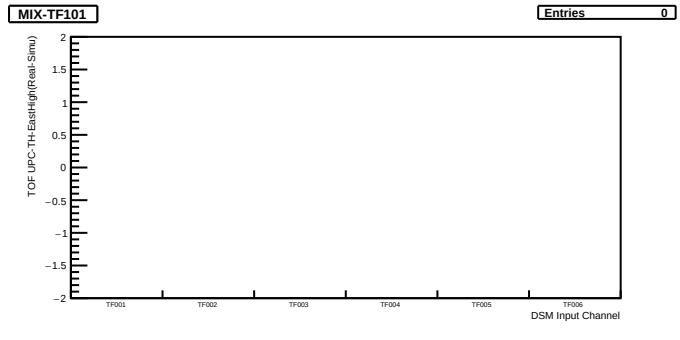

MIX-TF101
Entries 4200000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

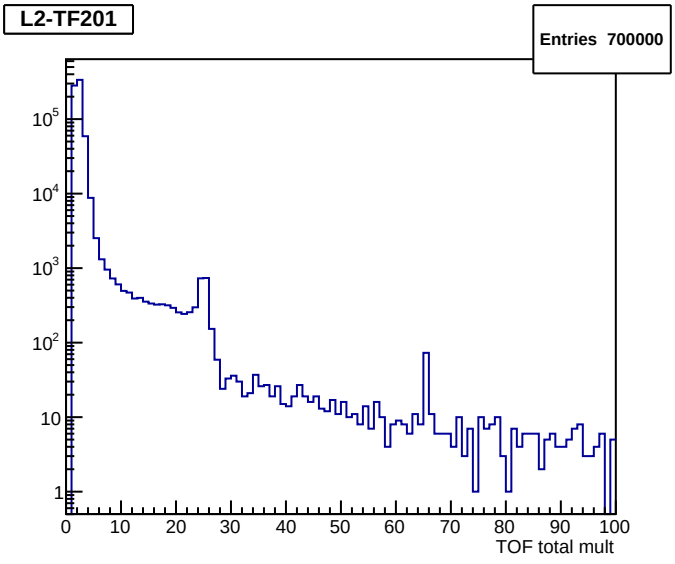

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

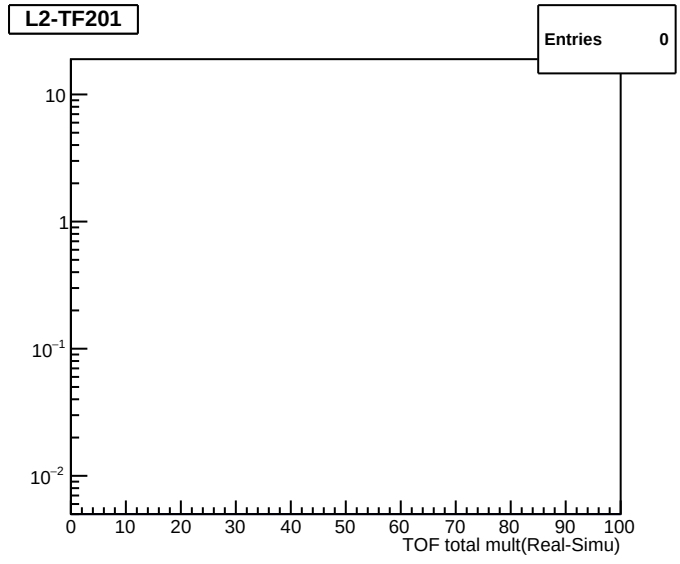

MIX-TF101
Entries 4200000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

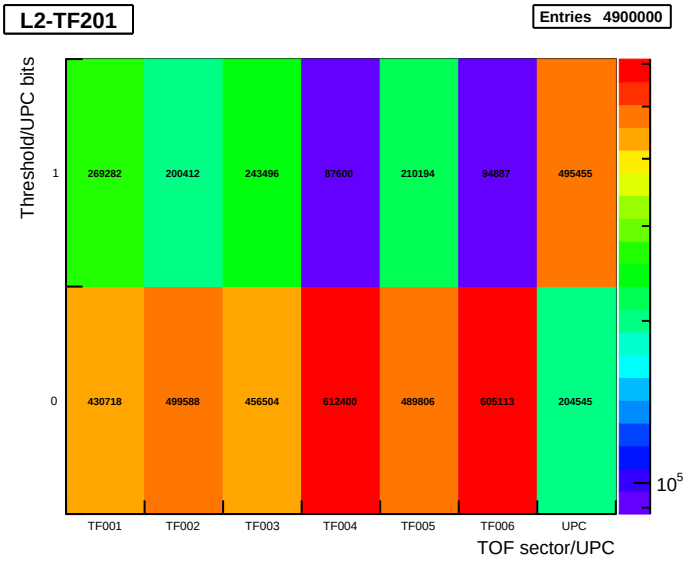

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

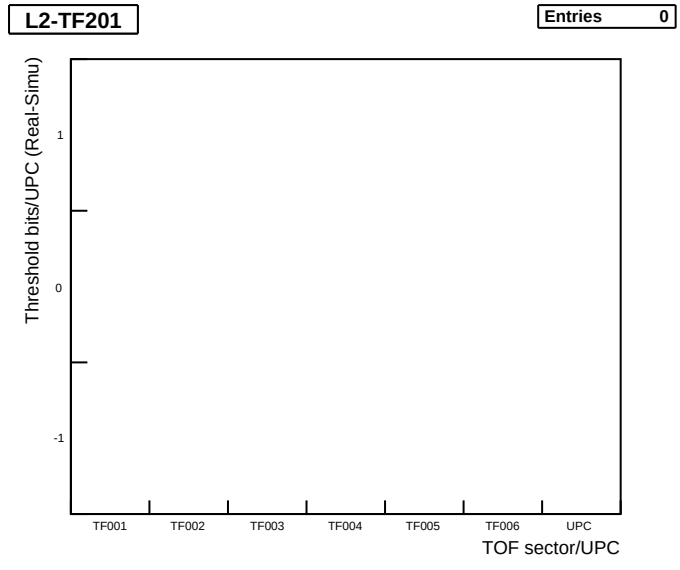

MIX-TF101
Entries 4200000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

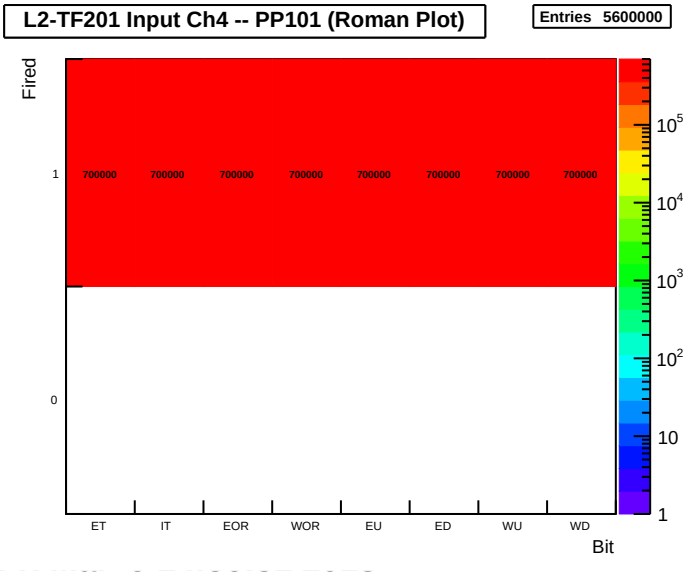

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

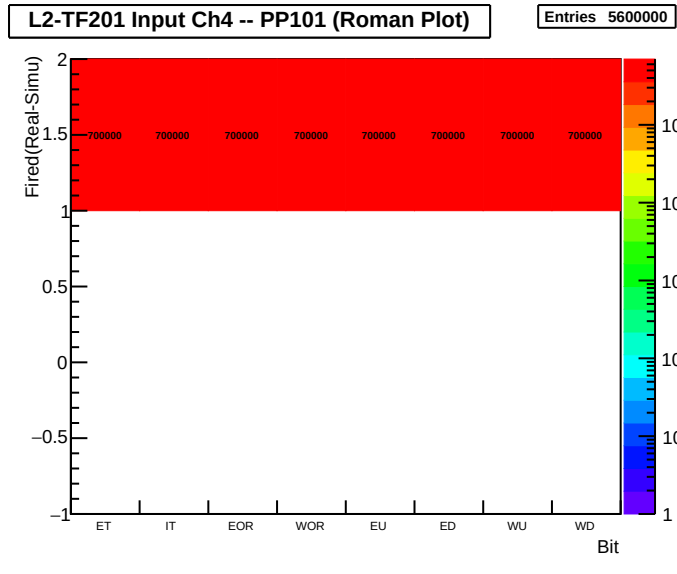

L2-TF201
Entries 700000
TOF total mult

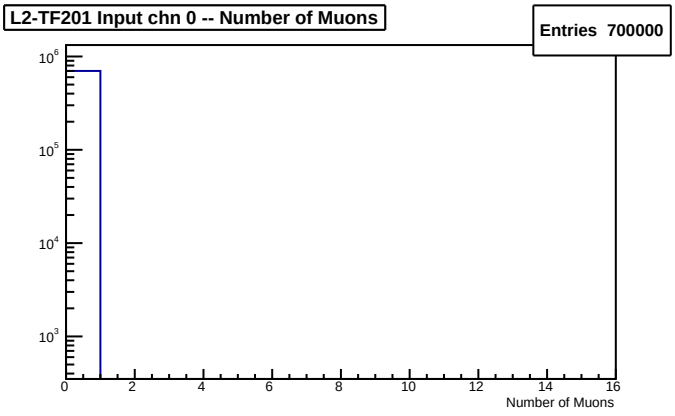

L2-TF201
Entries 0
TOF total mult(Real-Simu)

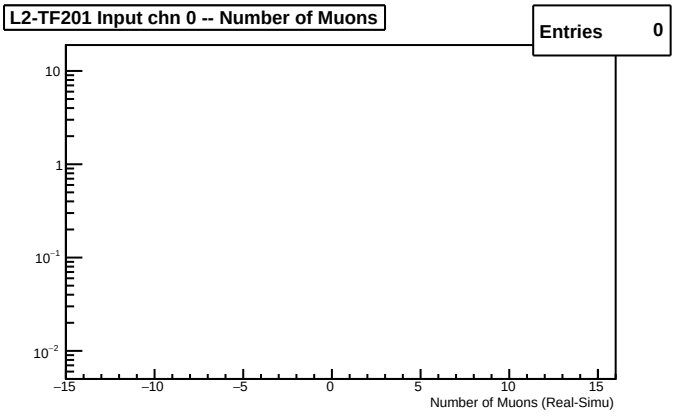

L2-TF201
Entries 4900000
Threshold/UPC bits
269282
200412
243496
87600
210194
94887
495455
430718
499588
456504
612400
489806
605113
204545
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

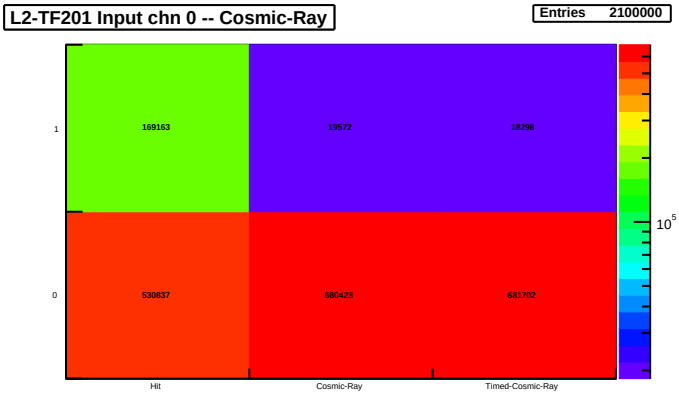

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

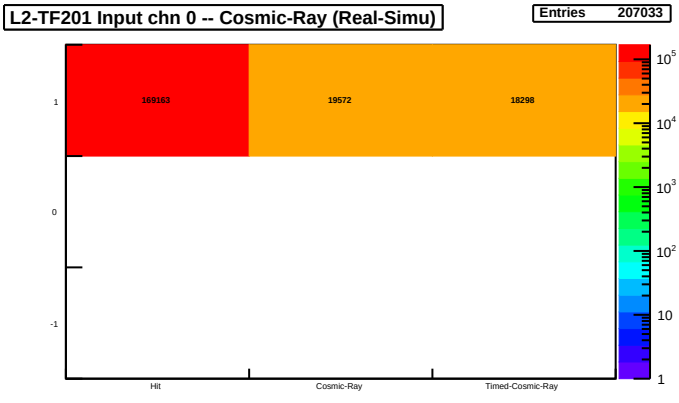

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 5600000
Fired
700000
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

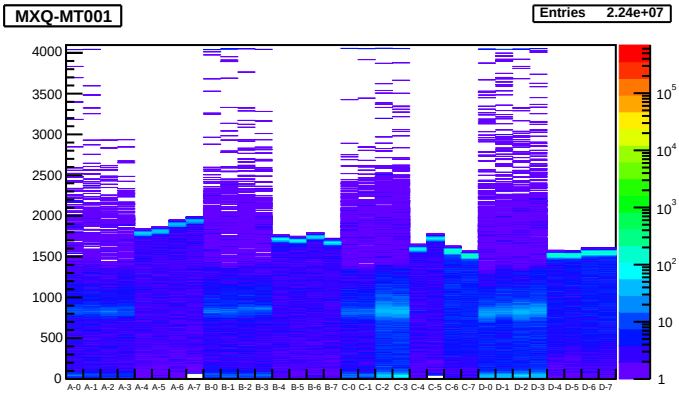

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 5600000
Fired(Real-Simu)
700000
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

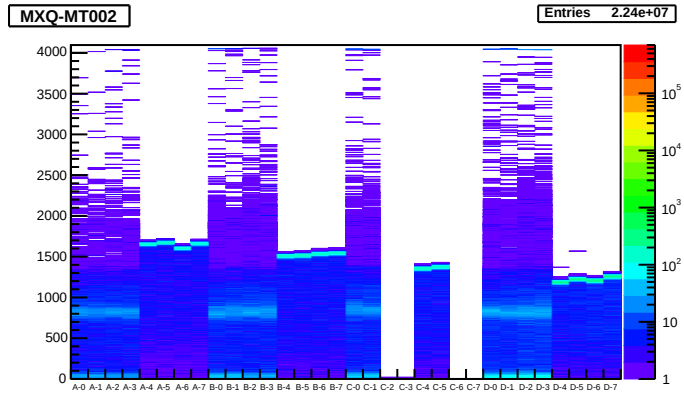

L2-TF201 Input chn 0 -- Number of Muons
Entries 700000
Number of Muons


L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)


L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 2100000
169163
19572
18298
530837
680428
681702
Hit
Cosmic-Ray
Timed-Cosmic-Ray


L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 207033
169163
19572
18298
Hit
Cosmic-Ray
Timed-Cosmic-Ray


MXQ-MT001
Entries 2.24e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
$10^5$
$10^4$
$10^3$
$10^2$
10
1


MXQ-MT002
Entries 2.24e+07
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3500
3000
2500
2000
1500
1000
500
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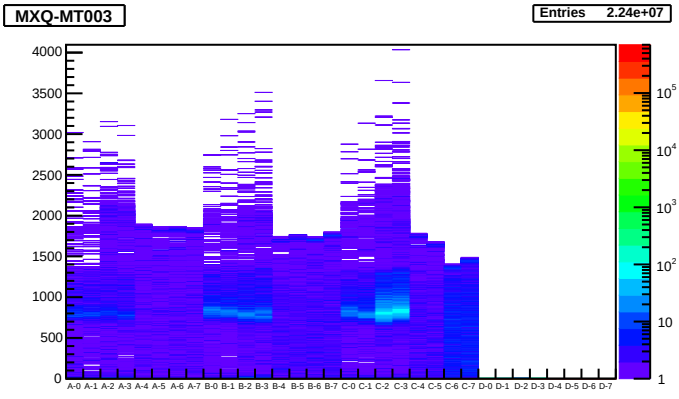
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
$10^5$
$10^4$
$10^3$
$10^2$
10
1


MXQ-MT003
Entries 2.24e+07
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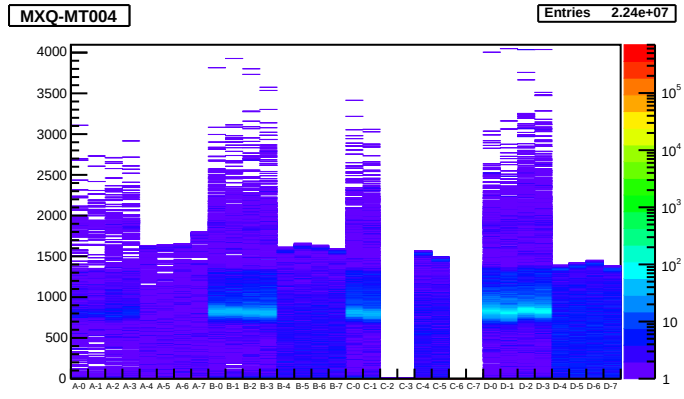
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
$10^5$
$10^4$
$10^3$
$10^2$
10
1


MXQ-MT004
Entries 2.24e+07
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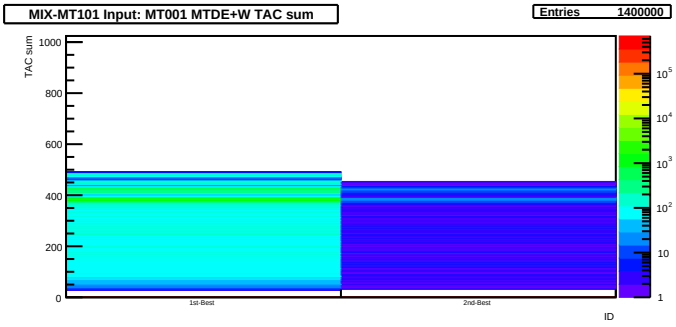
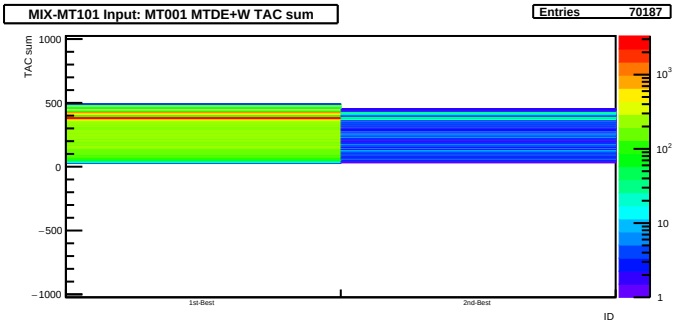
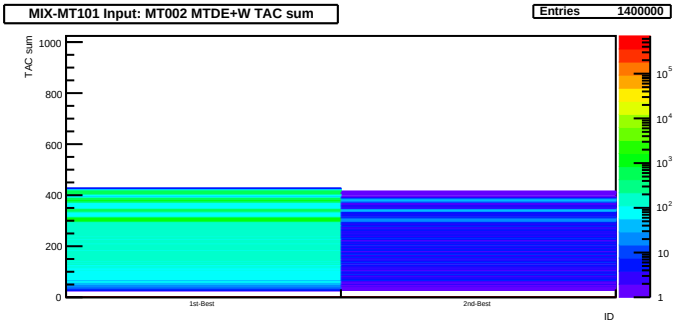
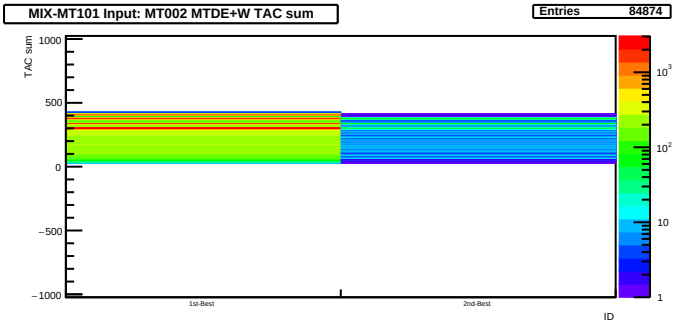
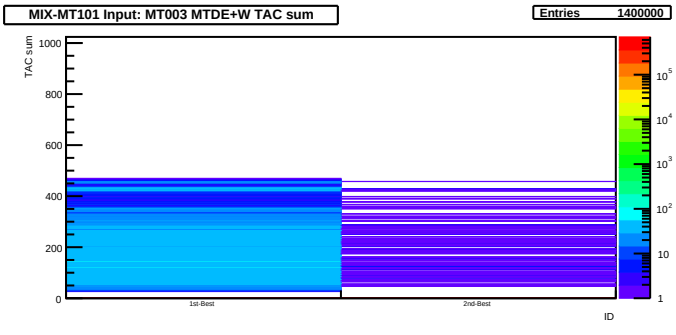
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
$10^5$
$10^4$
$10^3$
$10^2$
10
1

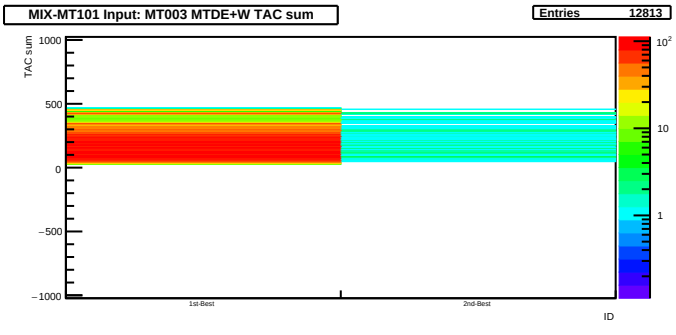

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 1400000
TAC sum
ID
1st-Best
2nd-Best

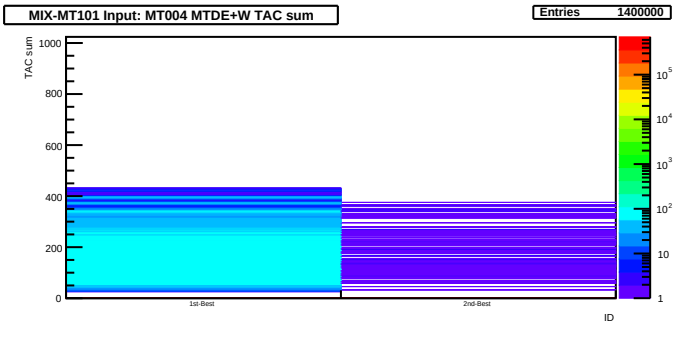

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 70187
TAC sum
ID
1st-Best
2nd-Best

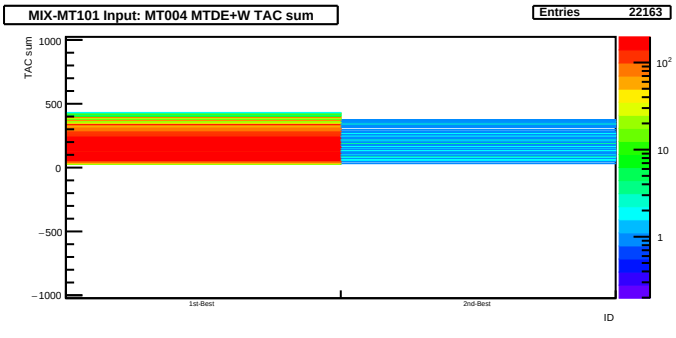

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 1400000
TAC sum
ID
1st-Best
2nd-Best

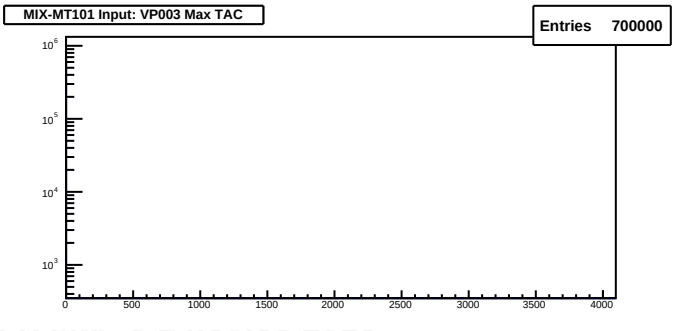

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 84874
TAC sum
ID
1st-Best
2nd-Best

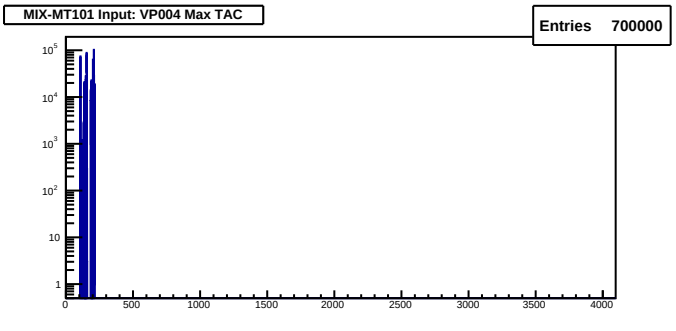

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 1400000
TAC sum
ID
1st-Best
2nd-Best

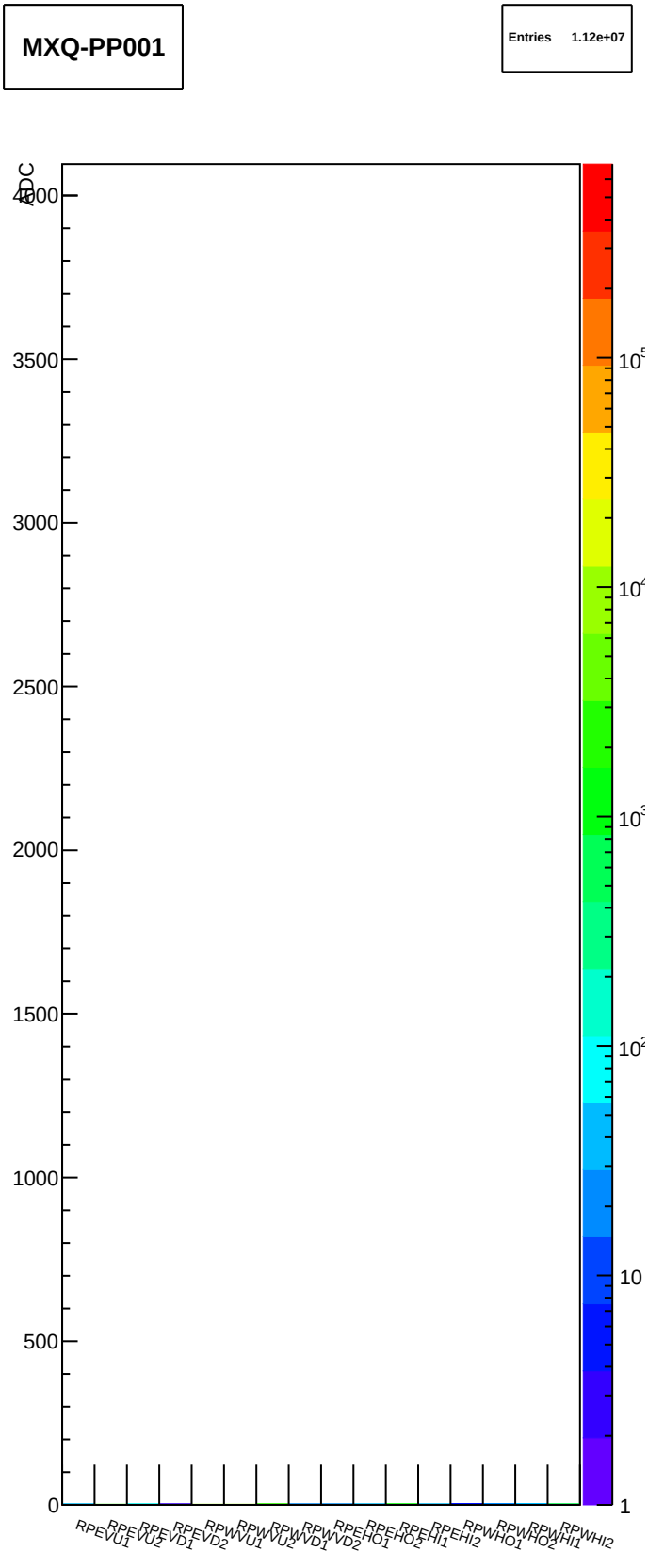

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 12813
TAC sum
ID
1st-Best
2nd-Best

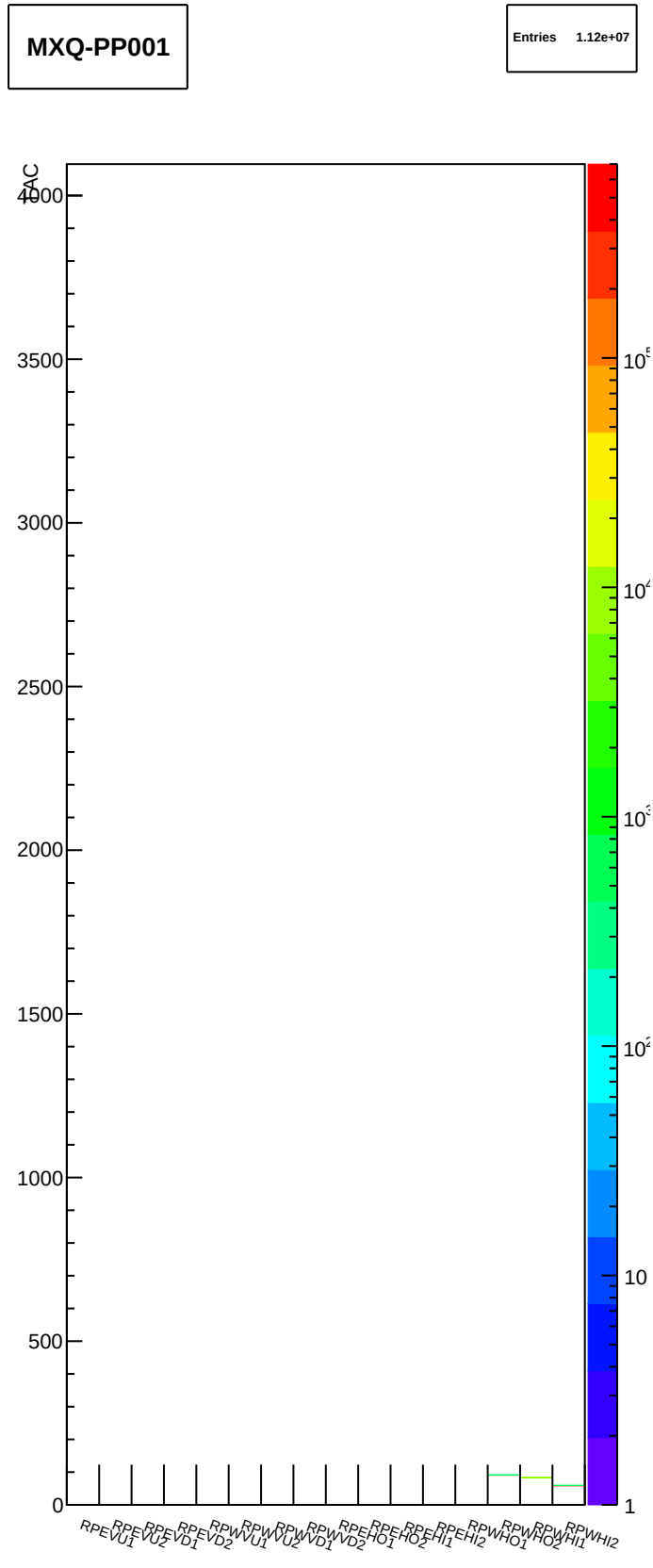

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 1400000
TAC sum
ID
1st-Best
2nd-Best

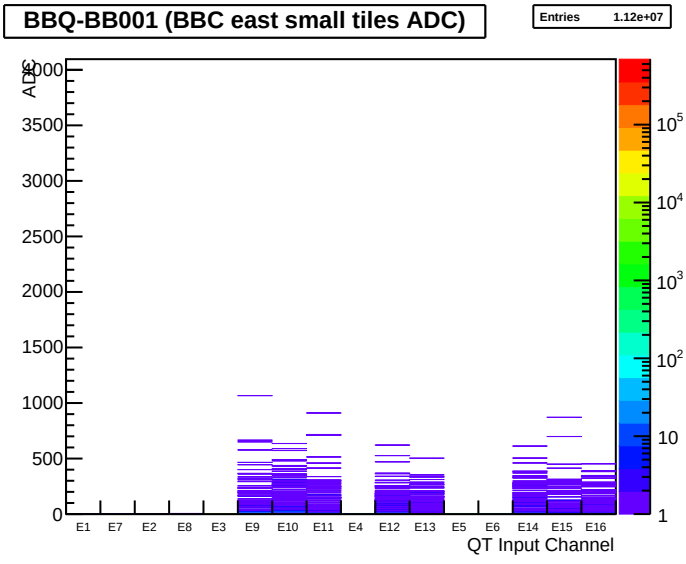

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 22163
TAC sum
ID
1st-Best
2nd-Best

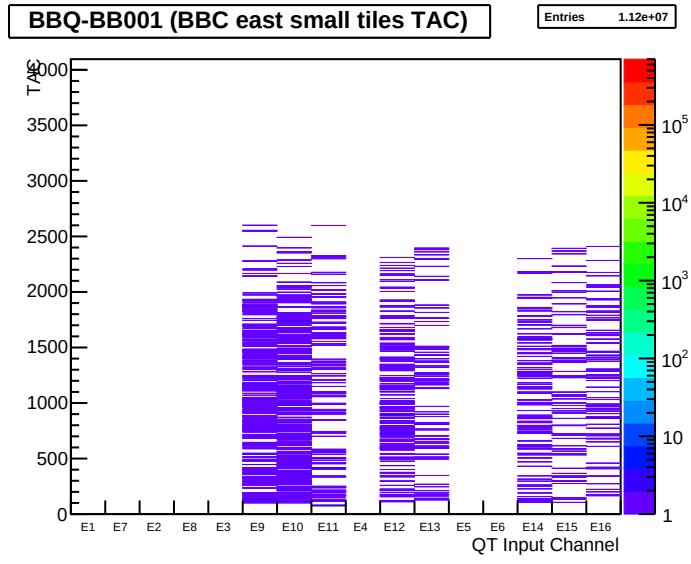

MIX-MT101 Input: VP003 Max TAC
Entries 700000

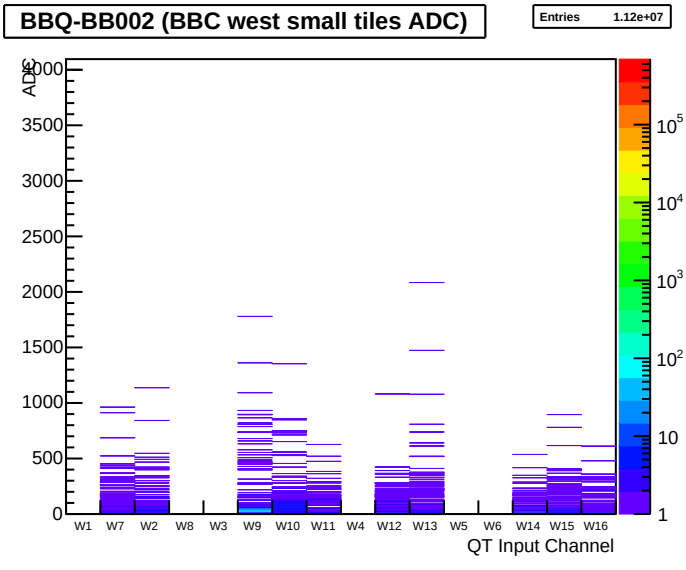

MIX-MT101 Input: VP004 Max TAC
Entries 700000

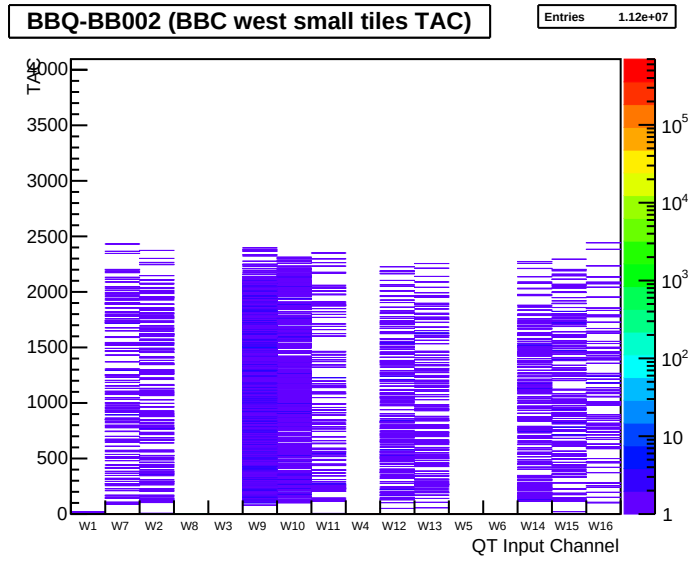

MXQ-PP001
Entries 1.12e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

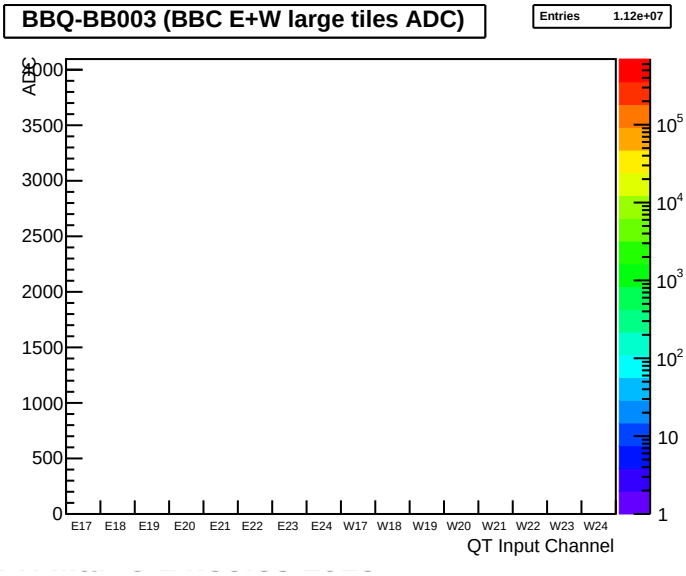

MXQ-PP001
Entries 1.12e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

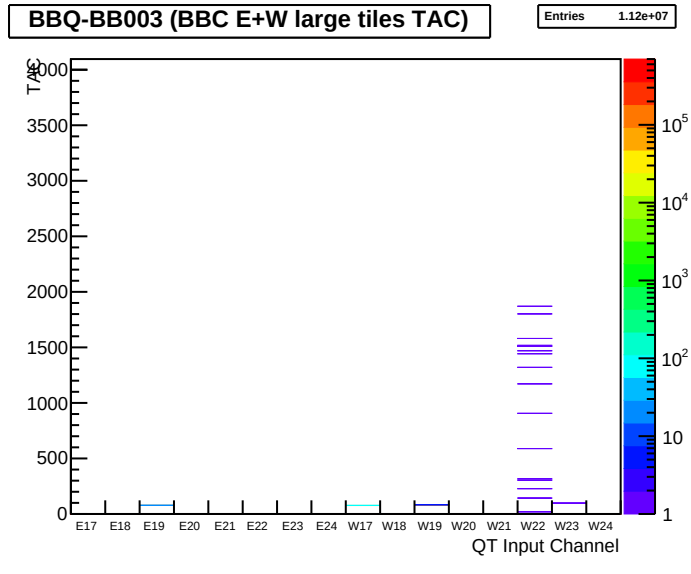

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.12e+07
ADC
QT Input Channel

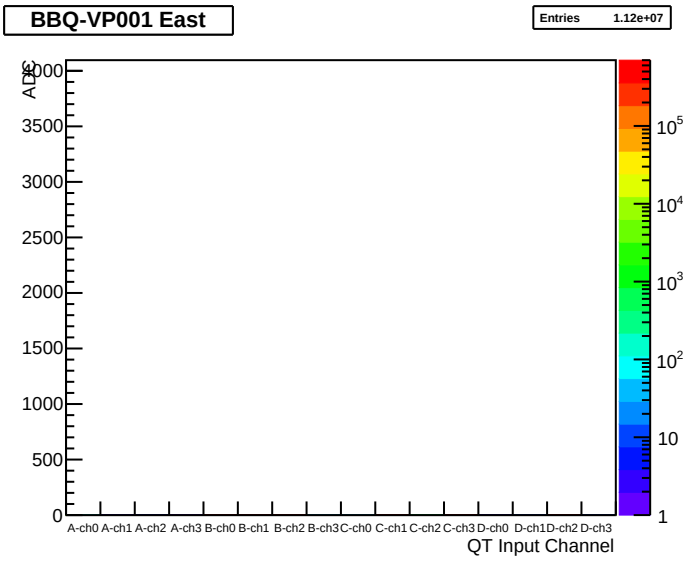

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.12e+07
TAC
QT Input Channel

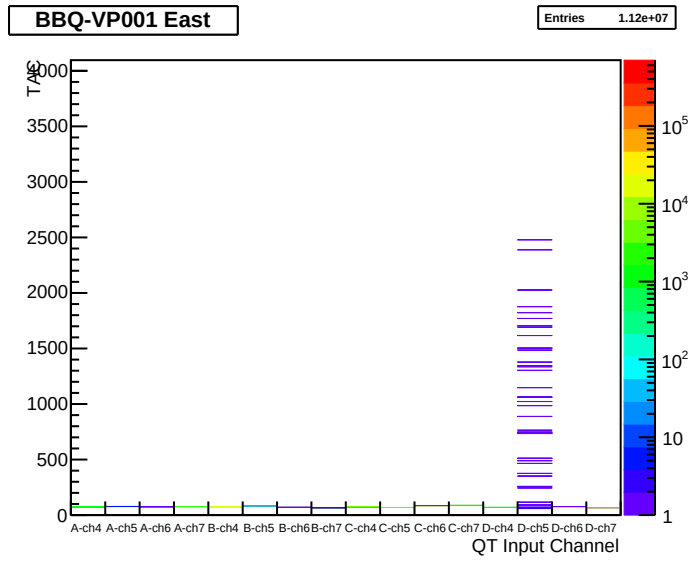

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.12e+07
ADC
QT Input Channel

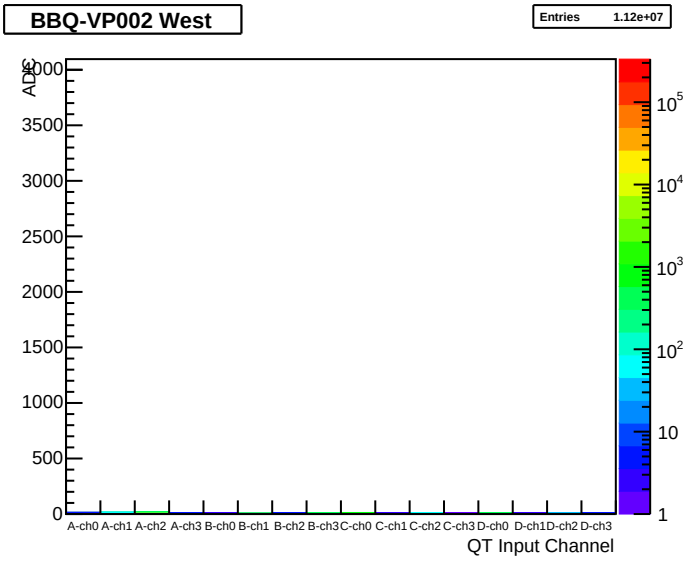

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.12e+07
TAC
QT Input Channel

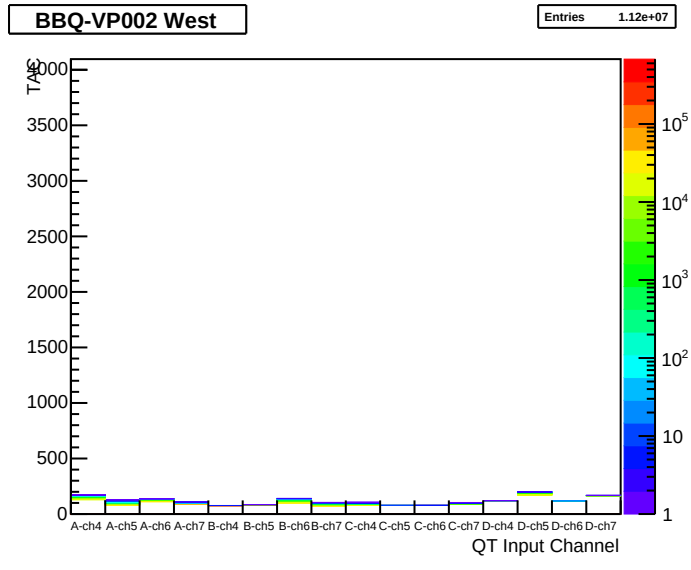

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.12e+07
ADC
QT Input Channel

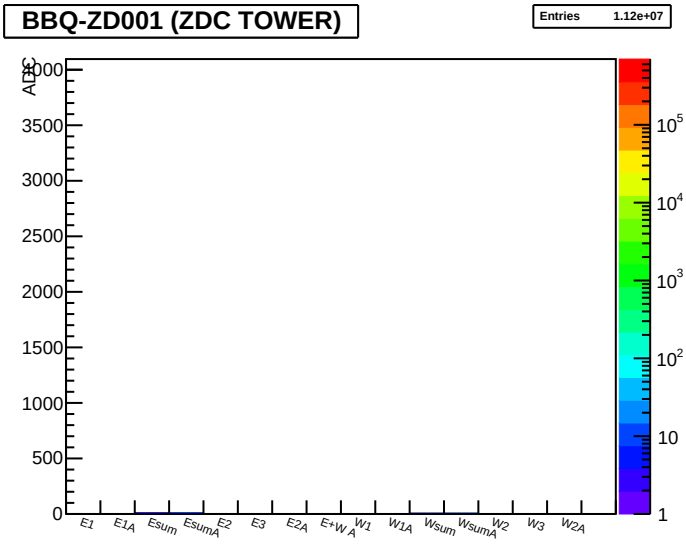

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.12e+07
TAC
QT Input Channel

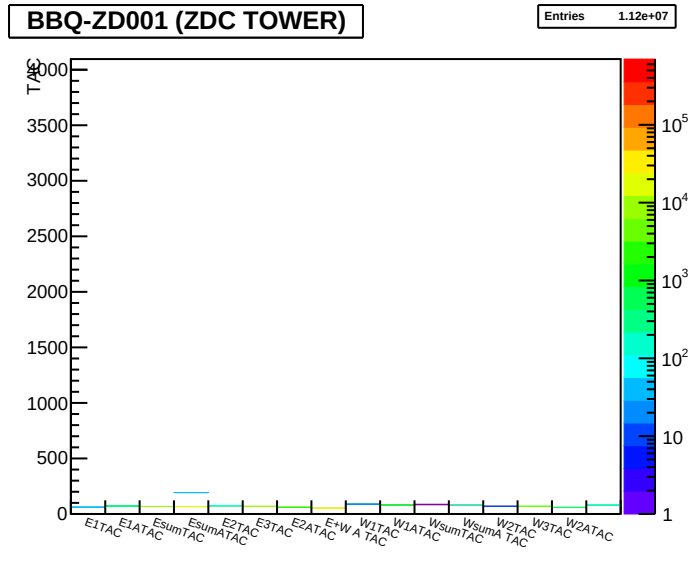

BBQ-VP001 East
Entries 1.12e+07
ADC
A-ch0 A-ch1 A-ch2 A-ch3 B-ch0 B-ch1 B-ch2 B-ch3 C-ch0 C-ch1 C-ch2 C-ch3 D-ch0 D-ch1 D-ch2 D-ch3
QT Input Channel

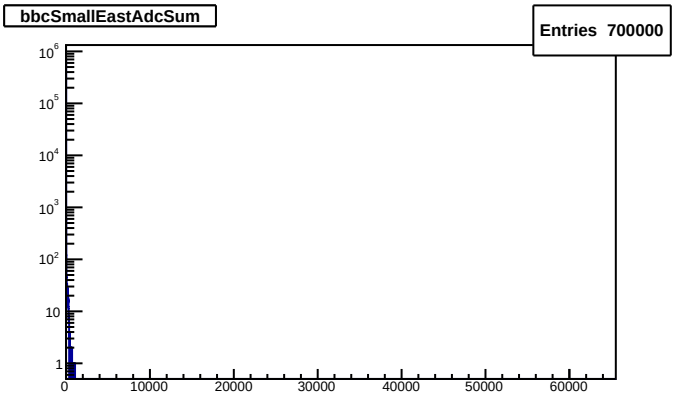

BBQ-VP001 East
Entries 1.12e+07
TAC
A-ch4 A-ch5 A-ch6 A-ch7 B-ch4 B-ch5 B-ch6 B-ch7 C-ch4 C-ch5 C-ch6 C-ch7 D-ch4 D-ch5 D-ch6 D-ch7
QT Input Channel

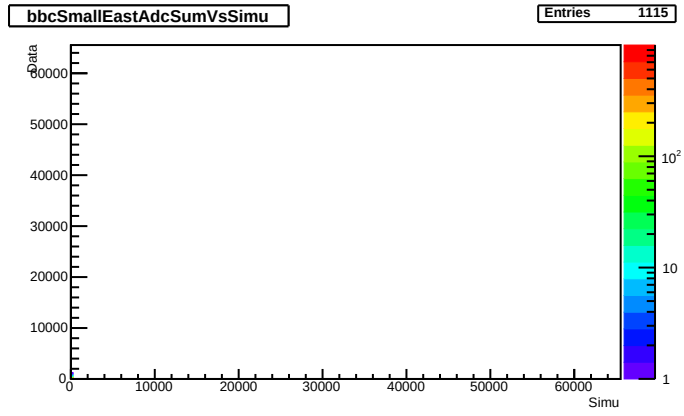

BBQ-VP002 West
Entries 1.12e+07
ADC
A-ch0 A-ch1 A-ch2 A-ch3 B-ch0 B-ch1 B-ch2 B-ch3 C-ch0 C-ch1 C-ch2 C-ch3 D-ch0 D-ch1 D-ch2 D-ch3
QT Input Channel

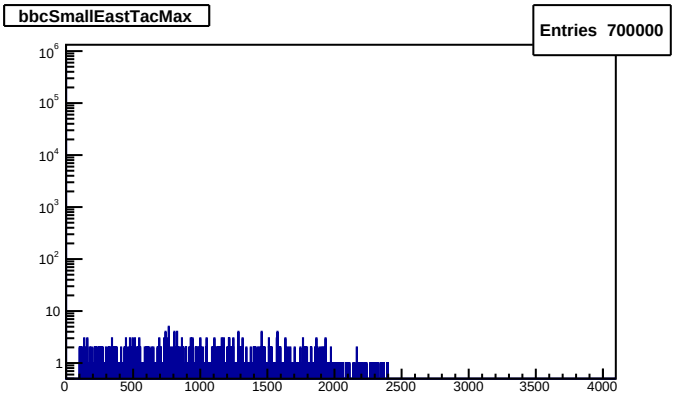

BBQ-VP002 West
Entries 1.12e+07
TAC
A-ch4 A-ch5 A-ch6 A-ch7 B-ch4 B-ch5 B-ch6 B-ch7 C-ch4 C-ch5 C-ch6 C-ch7 D-ch4 D-ch5 D-ch6 D-ch7
QT Input Channel

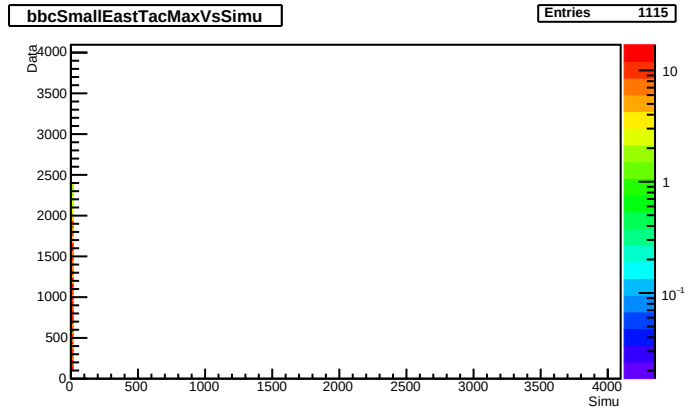

BBQ-ZD001 (ZDC TOWER)
Entries 1.12e+07
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

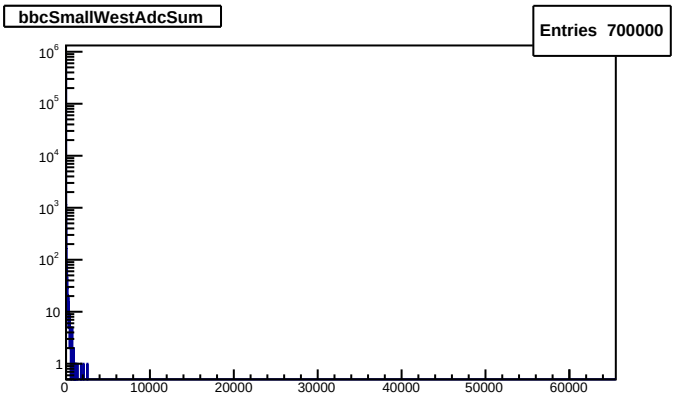

BBQ-ZD001 (ZDC TOWER)
Entries 1.12e+07
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

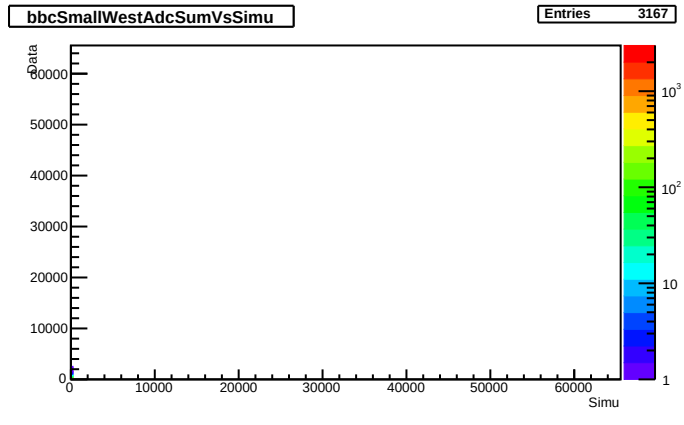

bbcSmallEastAdcSum
Entries 700000

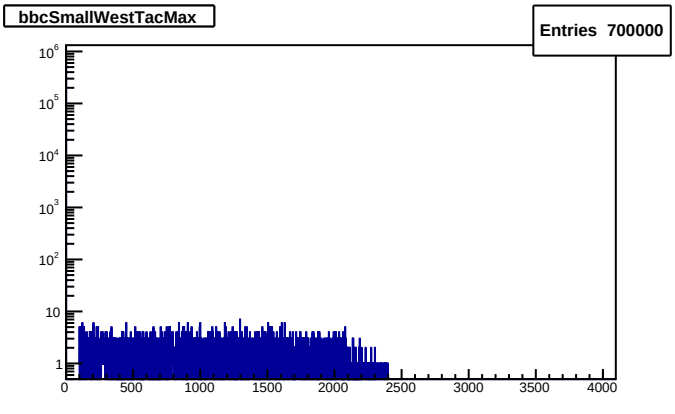

bbcSmallEastAdcSumVsSimu
Entries 1115
Data
Simu

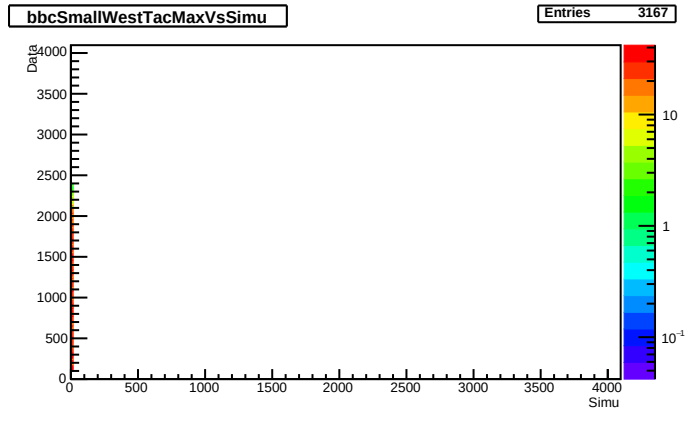

bbcSmallEastTacMax
Entries 700000

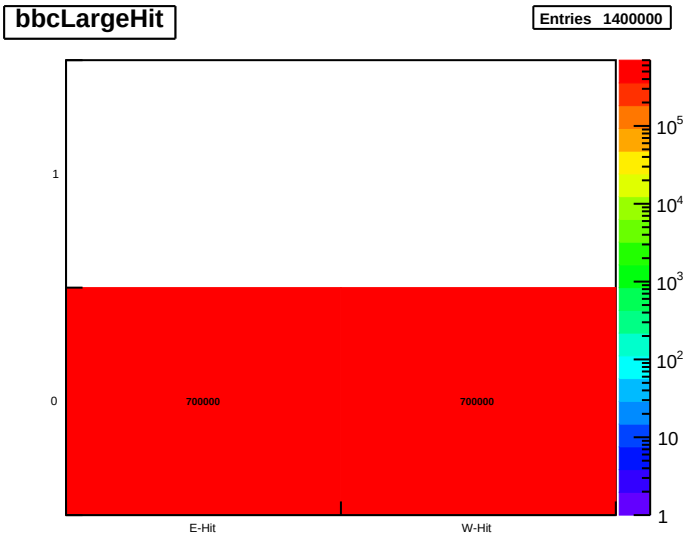

bbcSmallEastTacMaxVsSimu
Entries 1115
Data
Simu

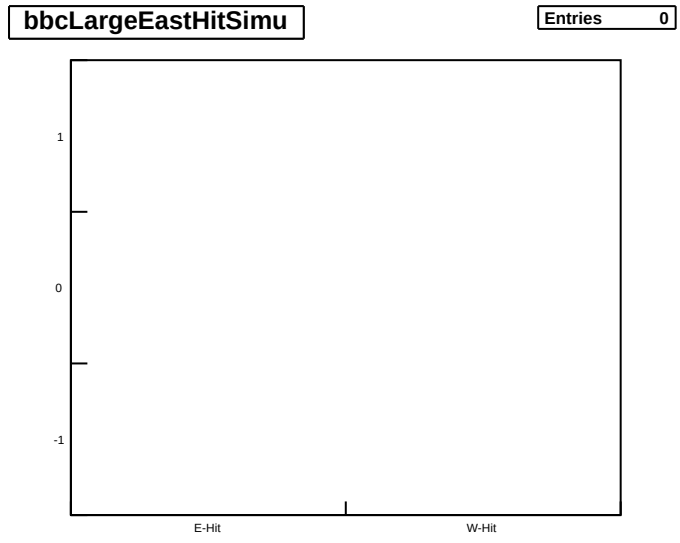

bbcSmallWestAdcSum
Entries 700000

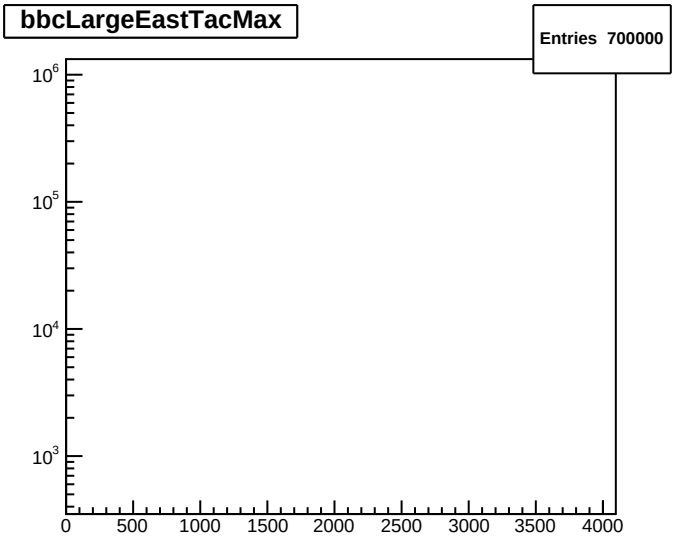

bbcSmallWestAdcSumVsSimu
Entries 3167
Data
Simu

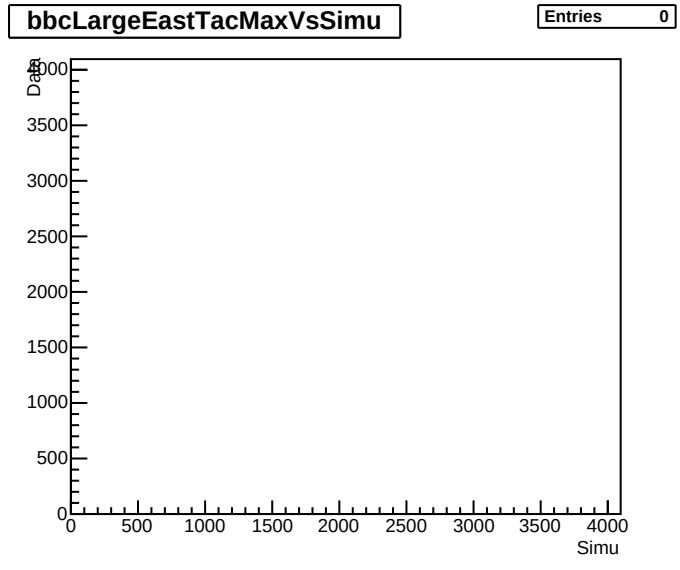

bbcSmallWestTacMax
Entries 700000

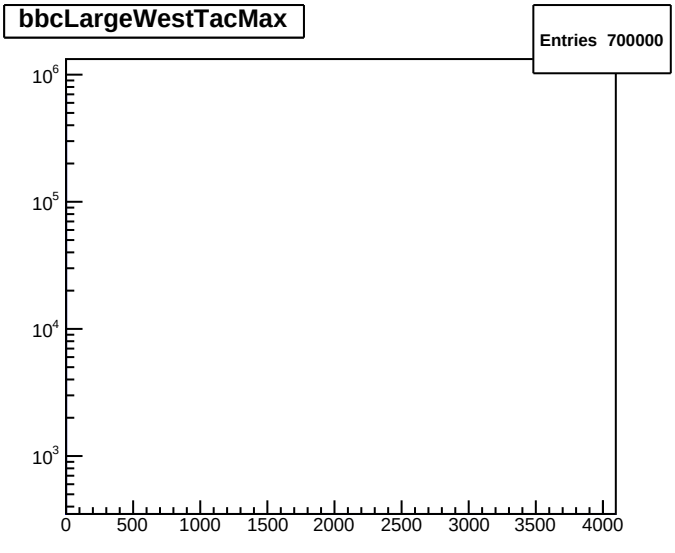

bbcSmallWestTacMaxVsSimu
Entries 3167
Data
Simu

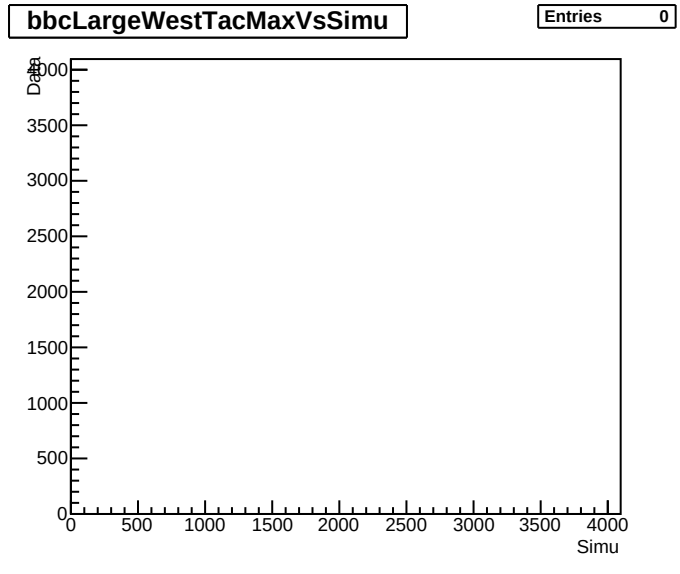

bbcLargeHit
Entries 1400000
700000
700000
E-Hit
W-Hit

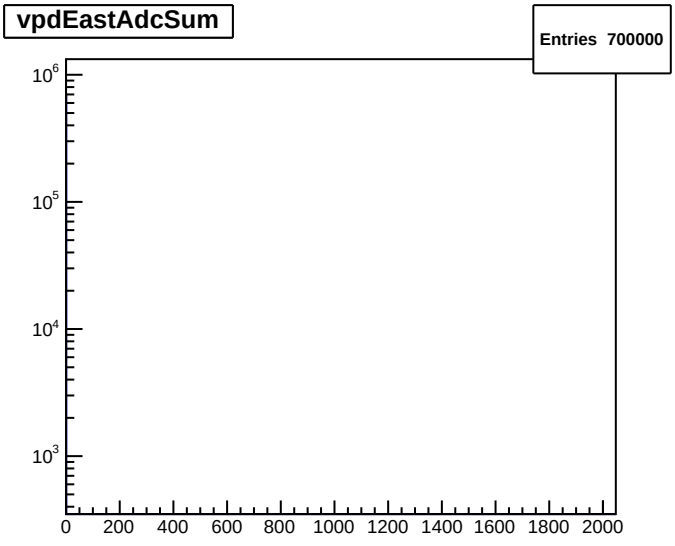

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

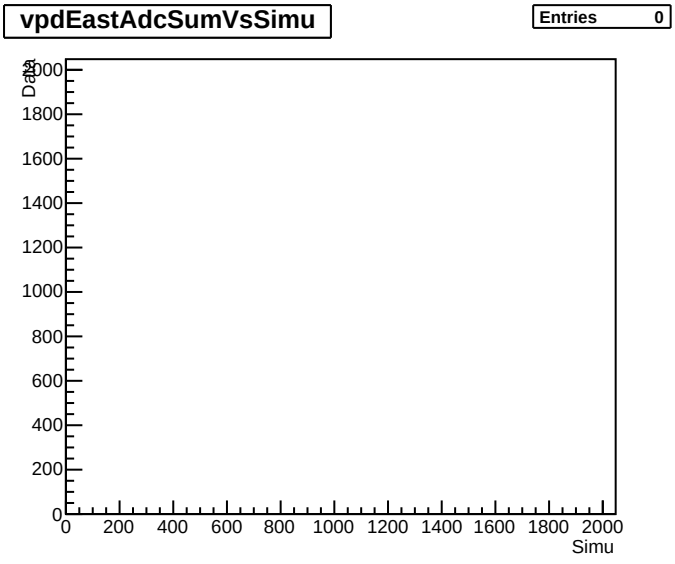

bbcLargeEastTacMax
Entries 700000

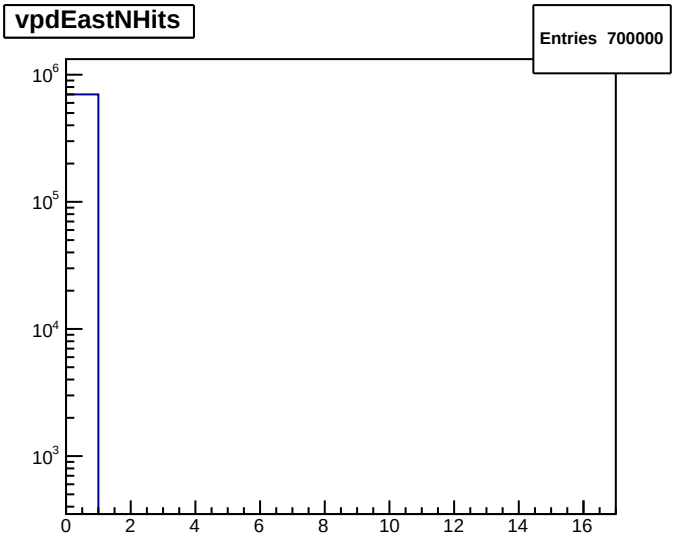

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

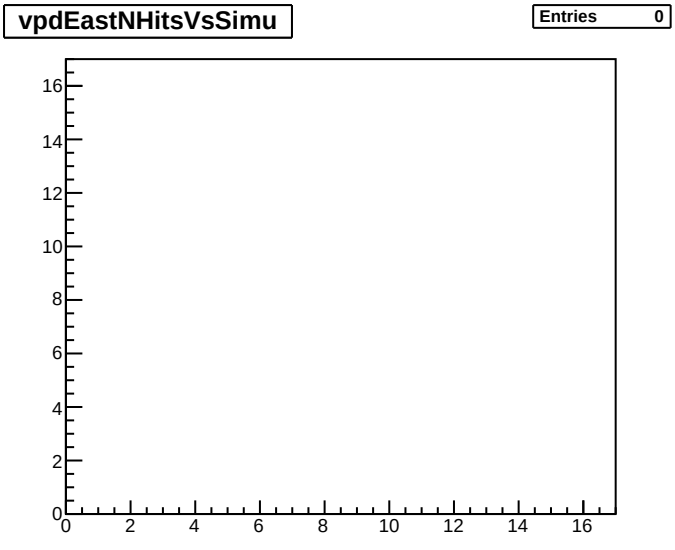

bbcLargeWestTacMax
Entries 700000

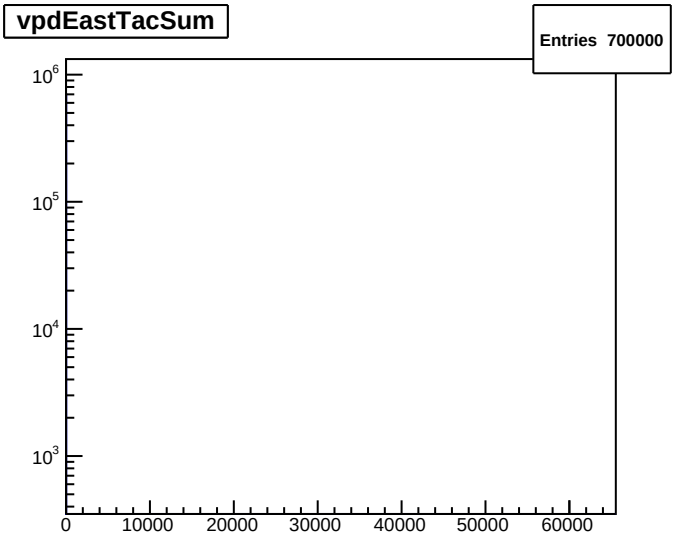

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

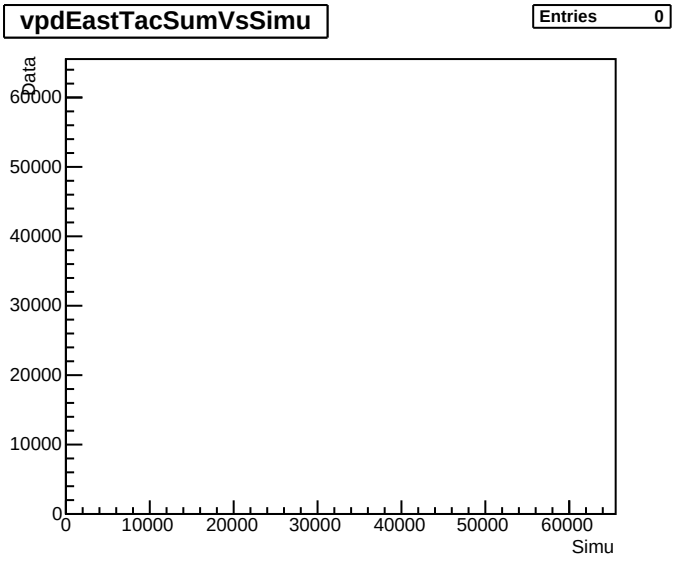

vpdEastAdcSum
Entries 700000
$10^6$
$10^5$
$10^4$
$10^3$
0 200 400 600 800 1000 1200 1400 1600 1800 2000

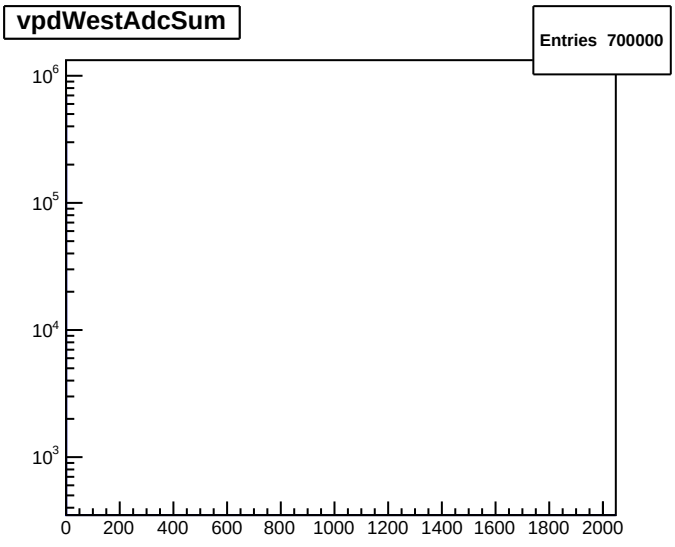

vpdEastAdcSumVsSimu
Entries 0
Data
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000
Simu

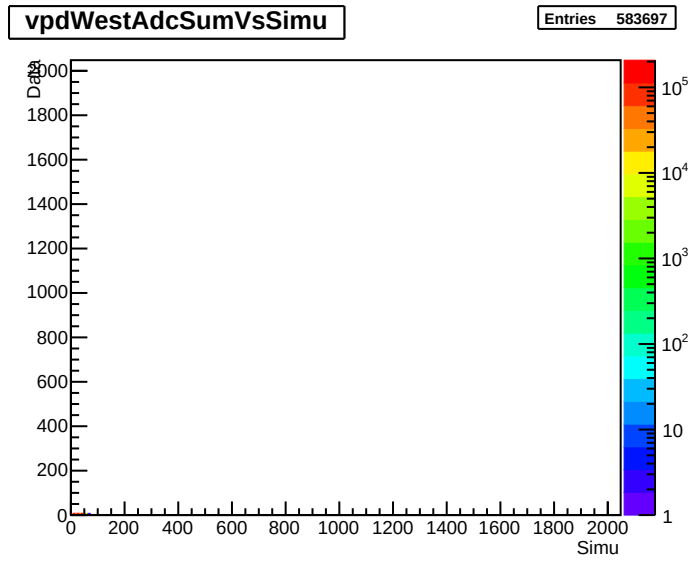

vpdEastNHits
Entries 700000
$10^6$
$10^5$
$10^4$
$10^3$
0 2 4 6 8 10 12 14 16

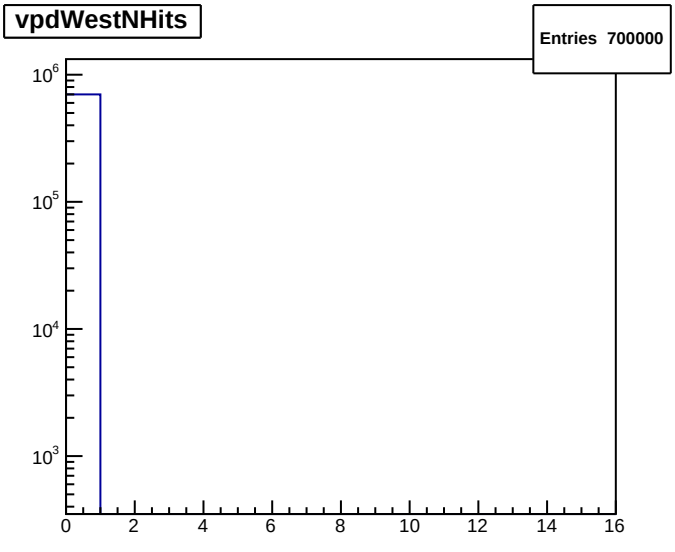

vpdEastNHitsVsSimu
Entries 0
16
14
12
10
8
6
4
2
0
0 2 4 6 8 10 12 14 16

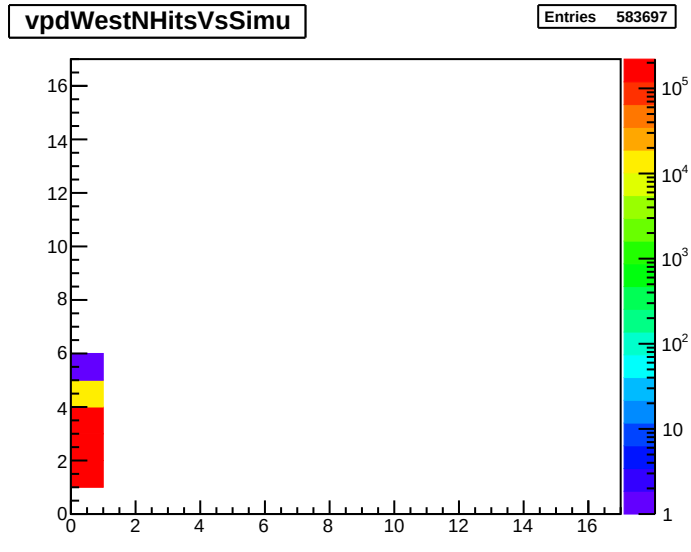

vpdEastTacSum
Entries 700000
$10^6$
$10^5$
$10^4$
$10^3$
0 10000 20000 30000 40000 50000 60000

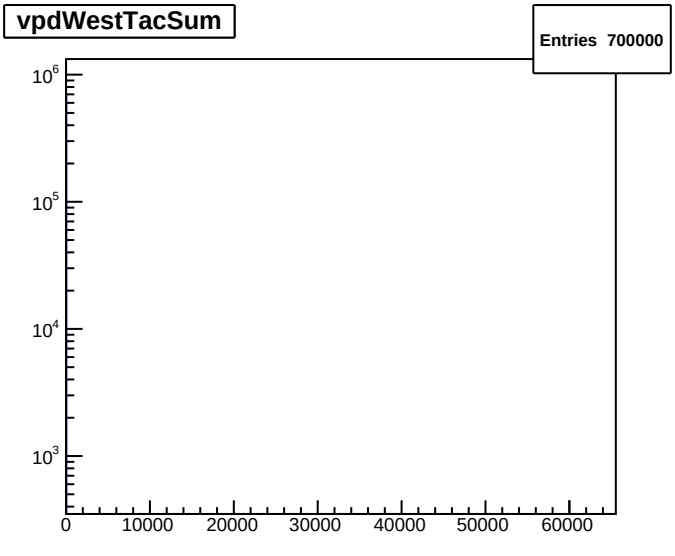

vpdEastTacSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0 10000 20000 30000 40000 50000 60000
Simu

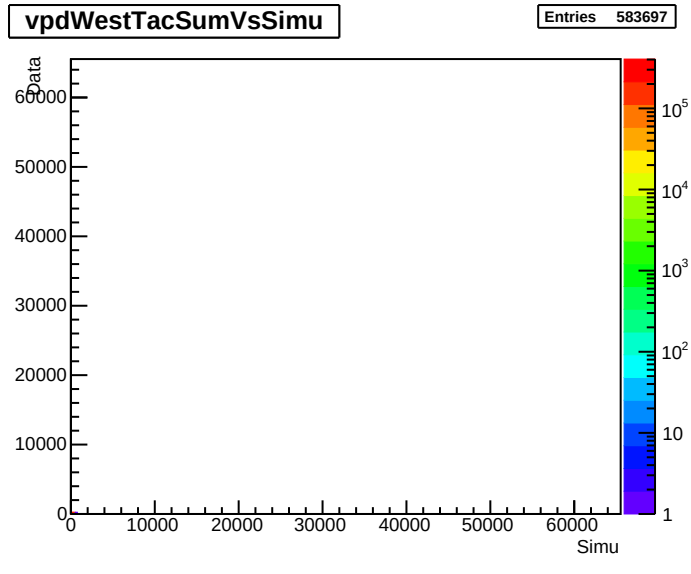

vpdWestAdcSum
Entries 700000

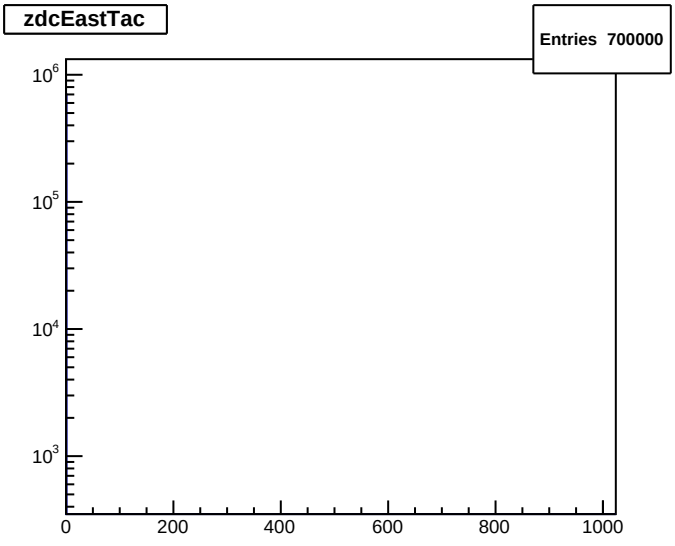

vpdWestAdcSumVsSimu
Entries 583697
Data
Simu

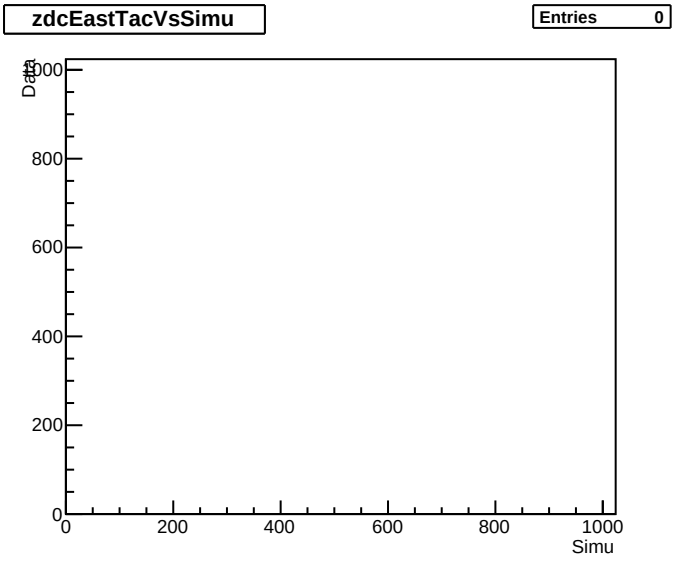

vpdWestNHits
Entries 700000

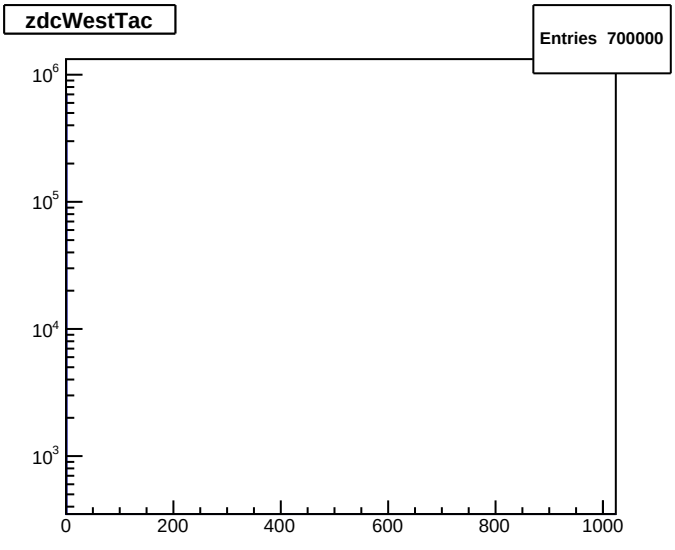

vpdWestNHitsVsSimu
Entries 583697

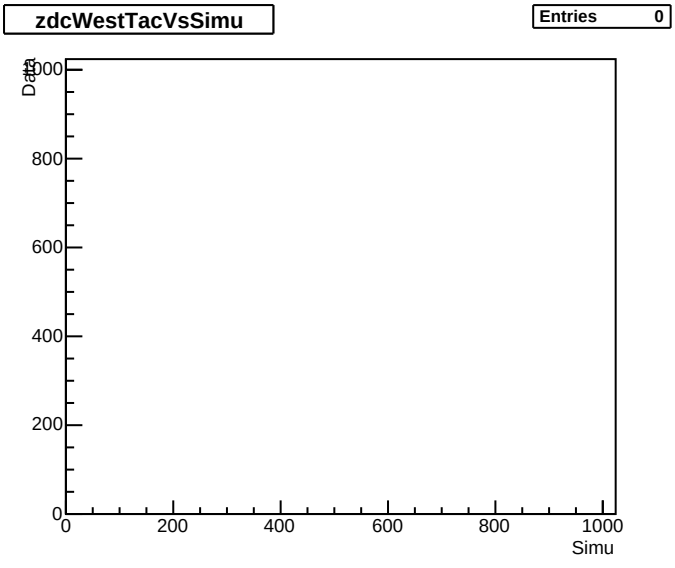

vpdWestTacSum
Entries 700000

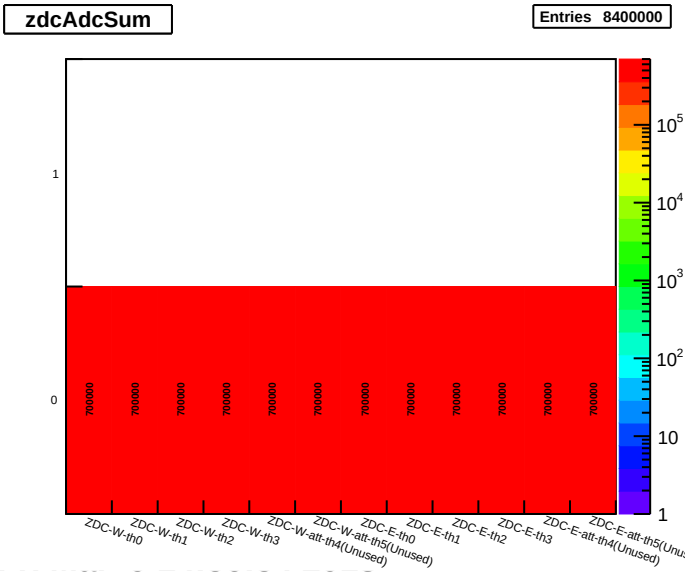

vpdWestTacSumVsSimu
Entries 583697
Data
Simu

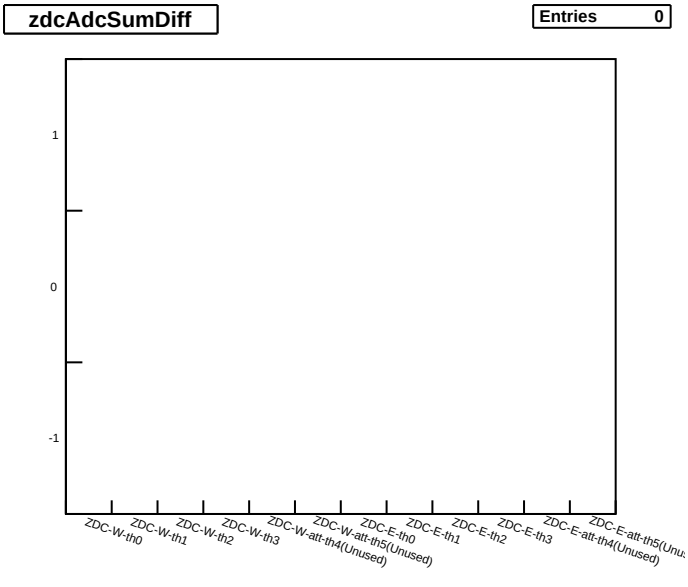

zdcEastTac
Entries 700000
$10^6$
$10^5$
$10^4$
$10^3$
0
200
400
600
800
1000

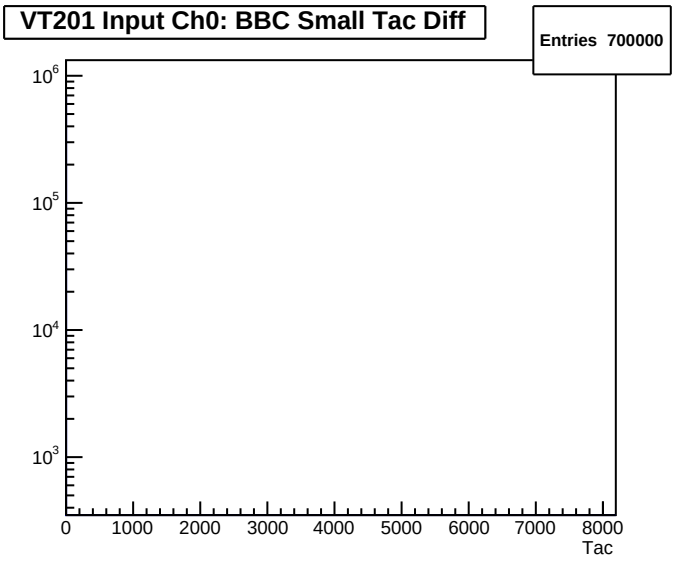

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

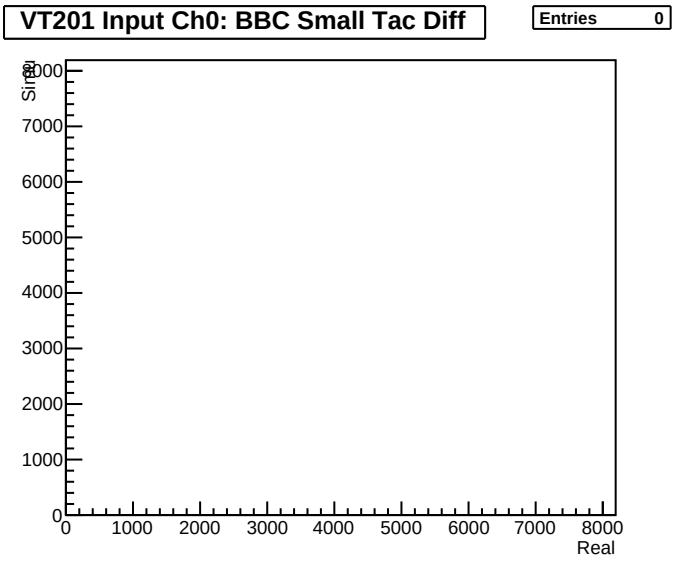

zdcWestTac
Entries 700000
$10^6$
$10^5$
$10^4$
$10^3$
0
200
400
600
800
1000

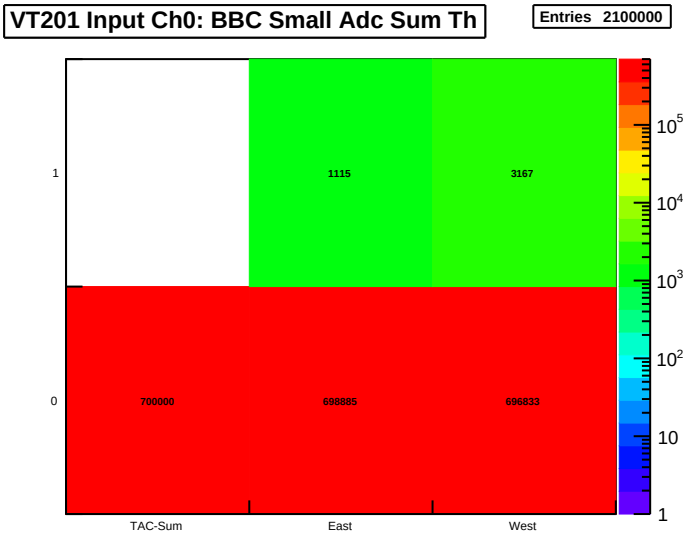

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

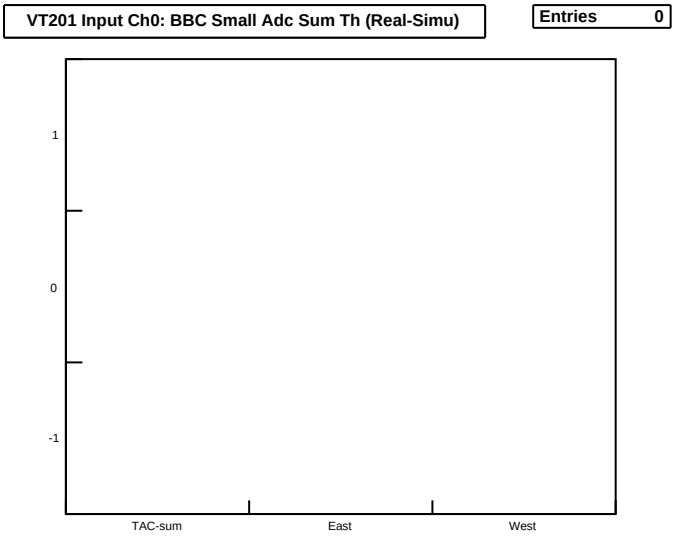

zdcAdcSum
Entries 8400000
1
0
700000
$10^5$
$10^4$
$10^3$
$10^2$
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

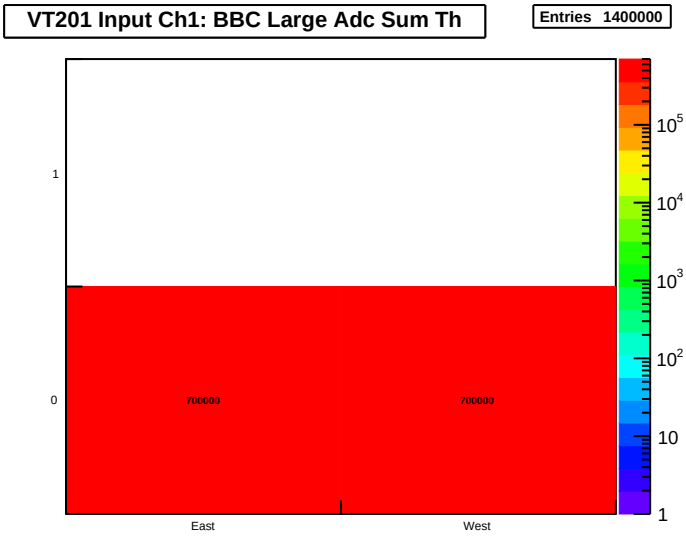

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

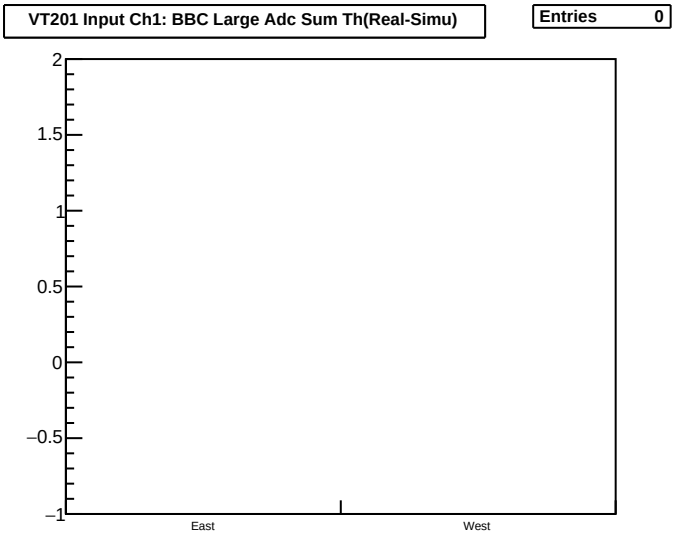

VT201 Input Ch0: BBC Small Tac Diff
Entries 700000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

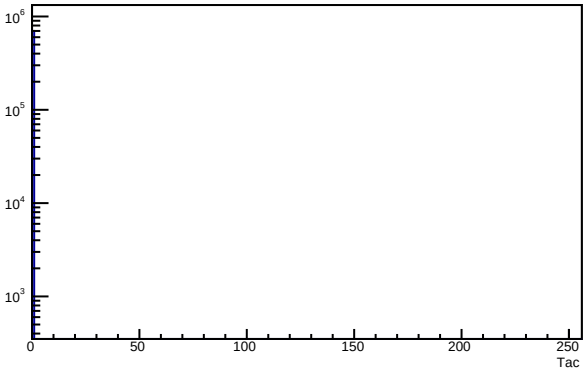

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 2100000
1115
3167
700000
698885
696833
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

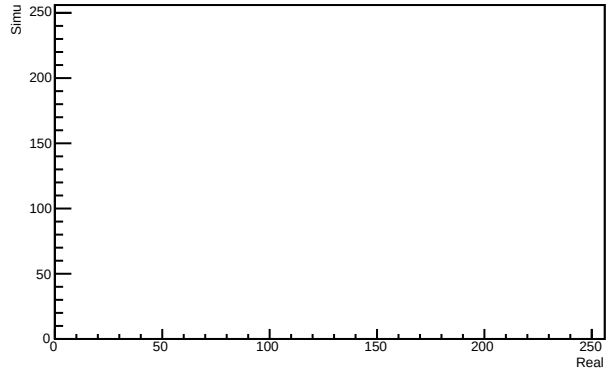

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 1400000
700000
700000
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

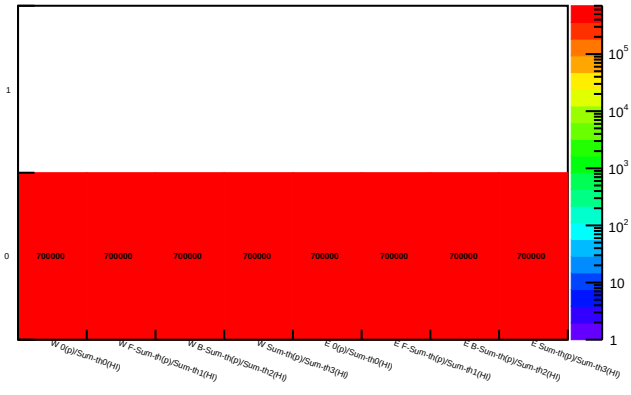
**VT201 Input Ch2: ZDC Tac Diff**



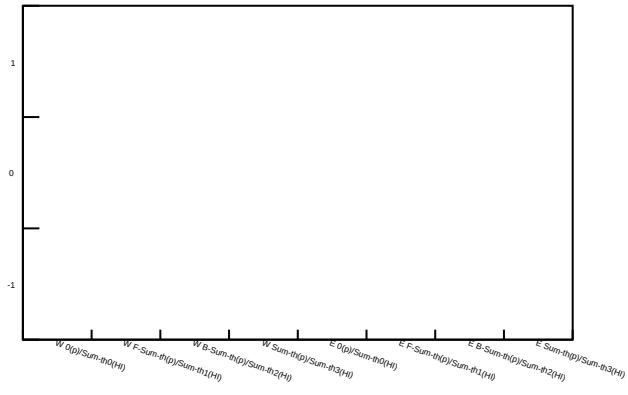

**VT201 Input Ch2: ZDC Tac Diff**



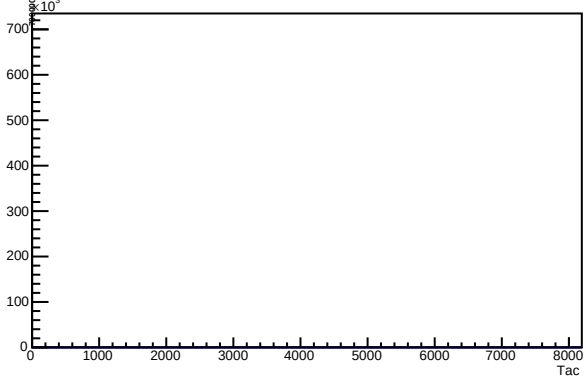

**VT201 Input Ch2: ZDC Adc Sum Th**



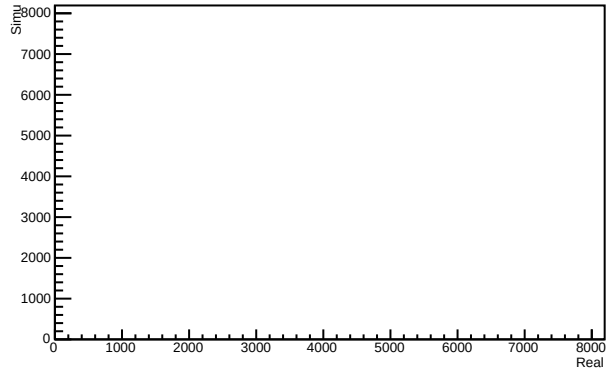

**VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)**



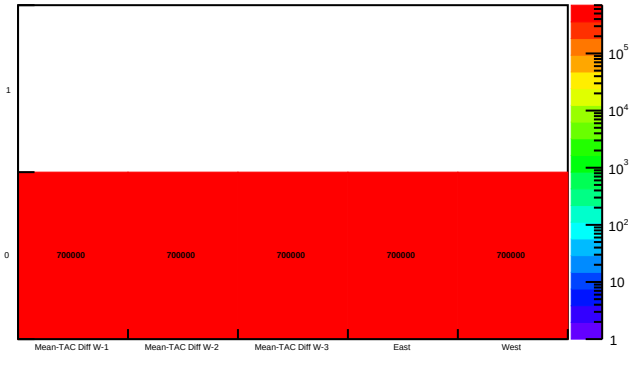

**VT201 Input Ch4: VPD Tac Diff**



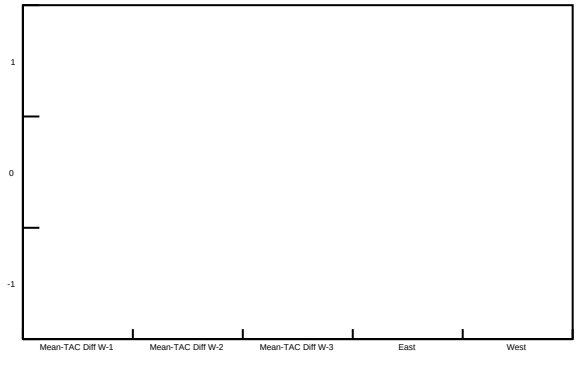

**VT201 Input Ch4: VPD Tac Diff**

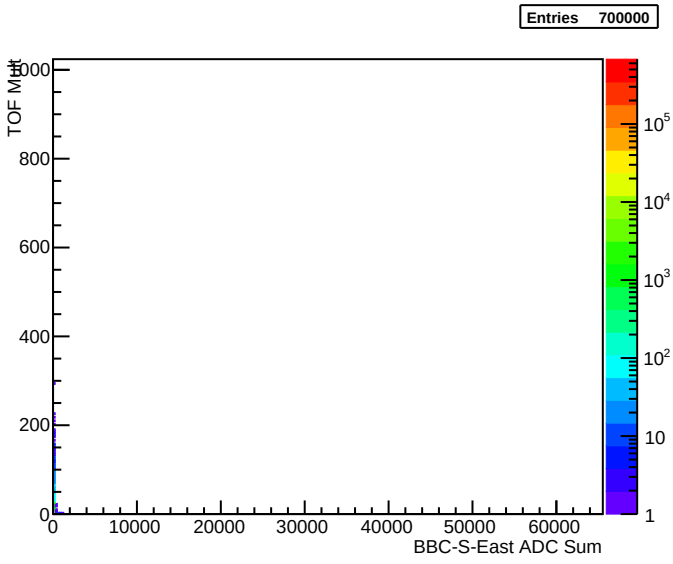


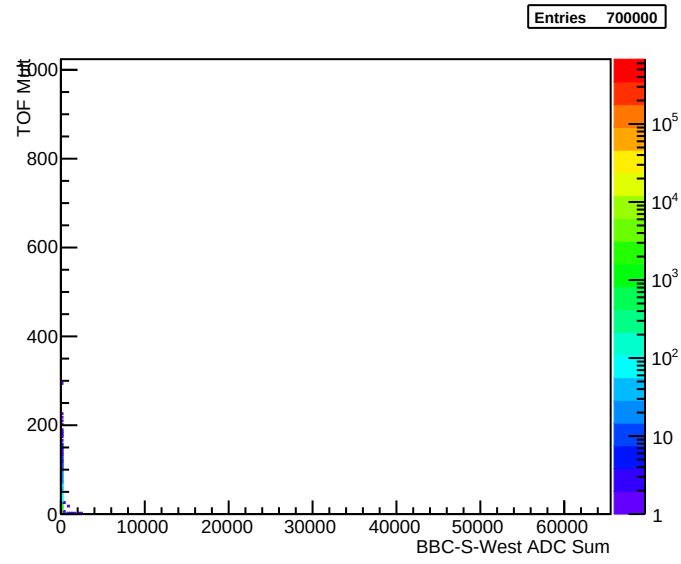

**VT201 Input Ch4: VPD Adc Sum Th**

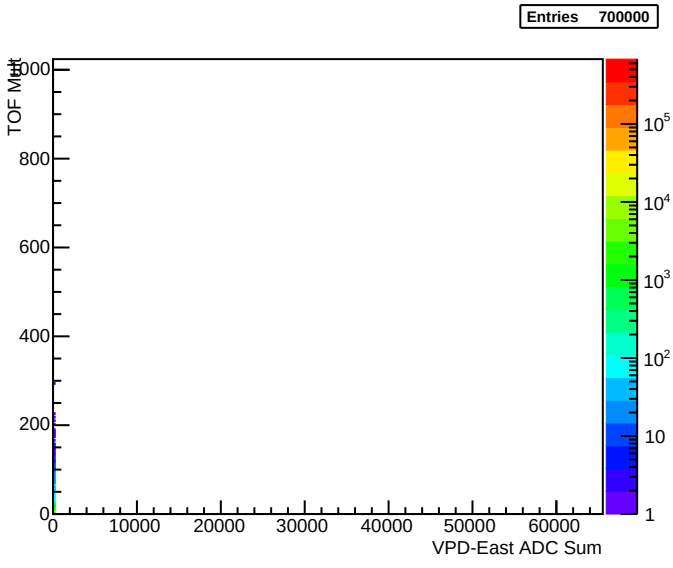


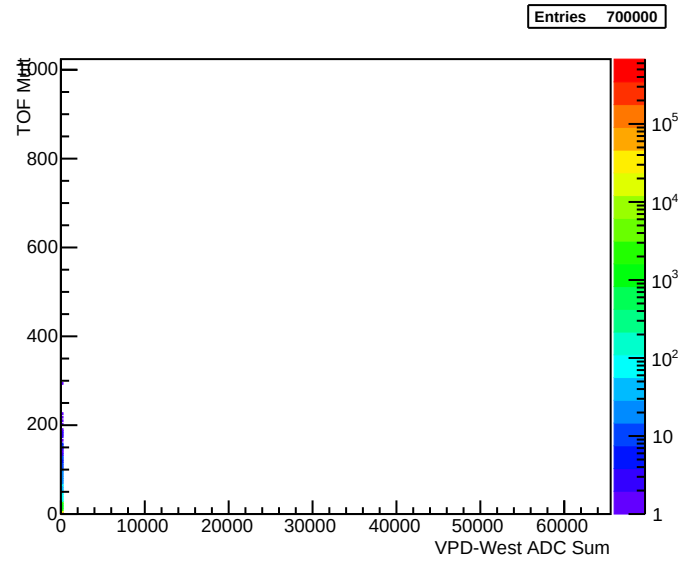

**VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)**

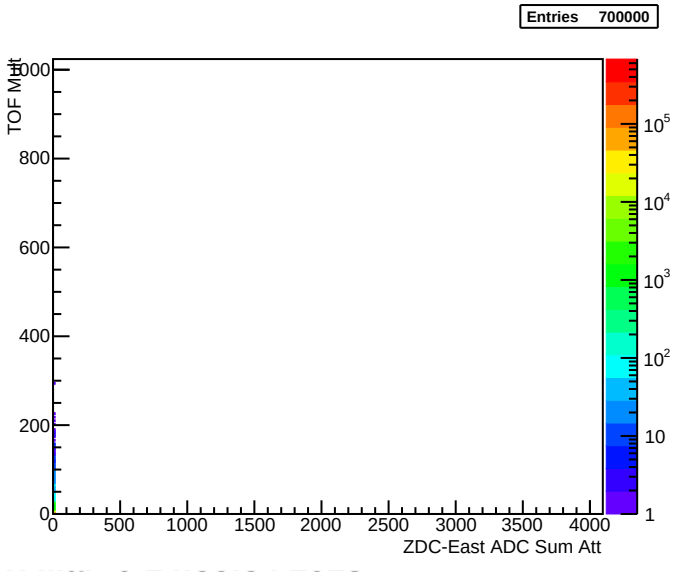


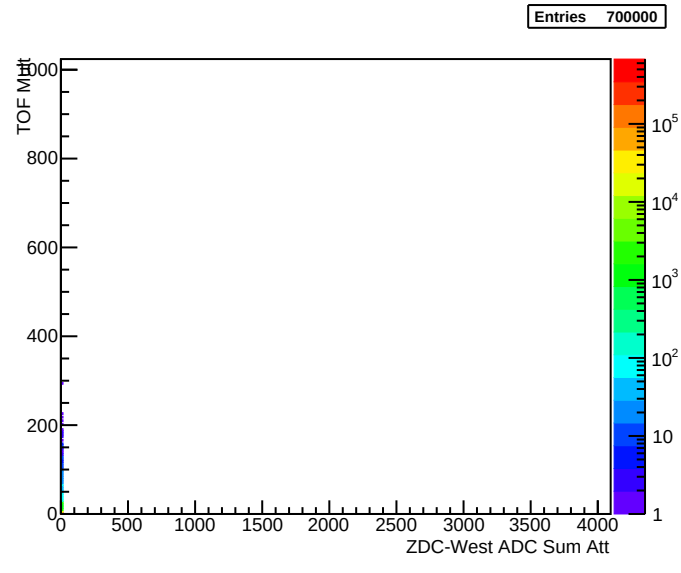

Entries 700000
TOF Mult
BBC-S-East ADC Sum

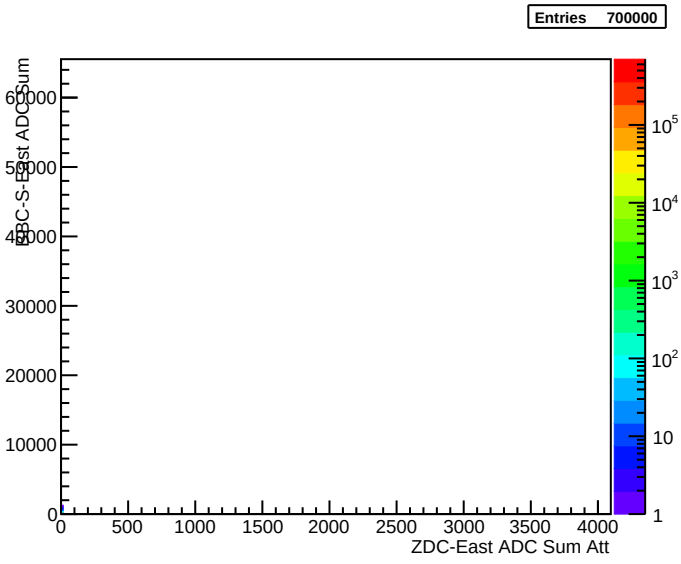

Entries 700000
TOF Mult
BBC-S-West ADC Sum

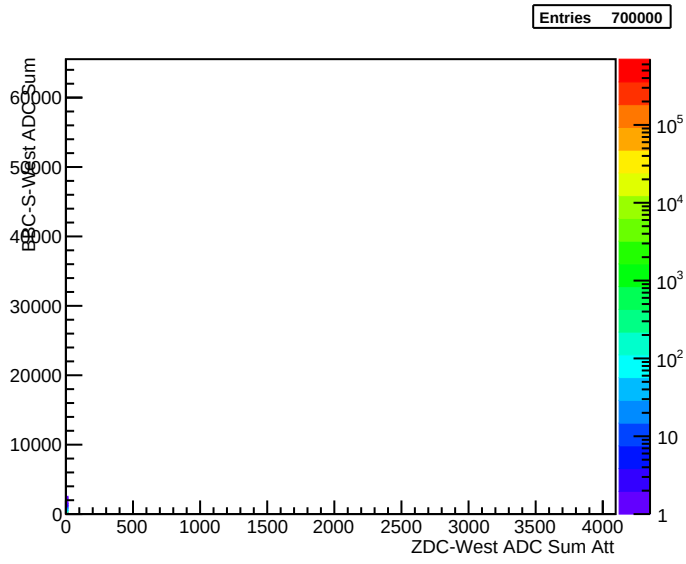

Entries 700000
TOF Mult
VPD-East ADC Sum

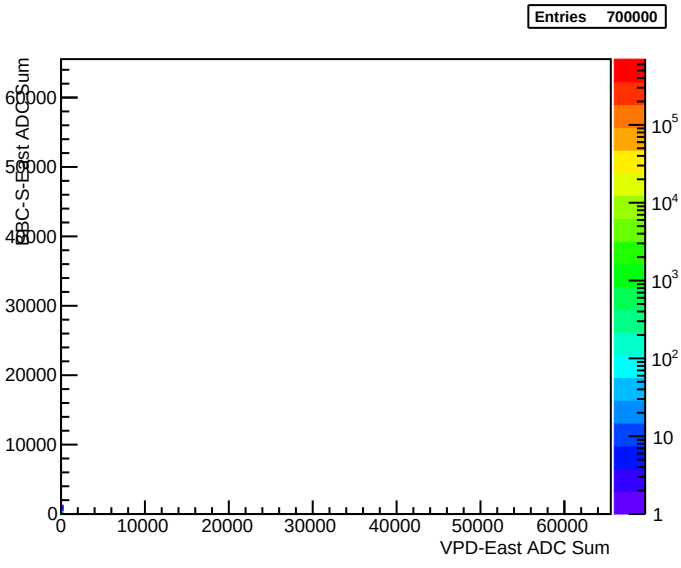

Entries 700000
TOF Mult
VPD-West ADC Sum

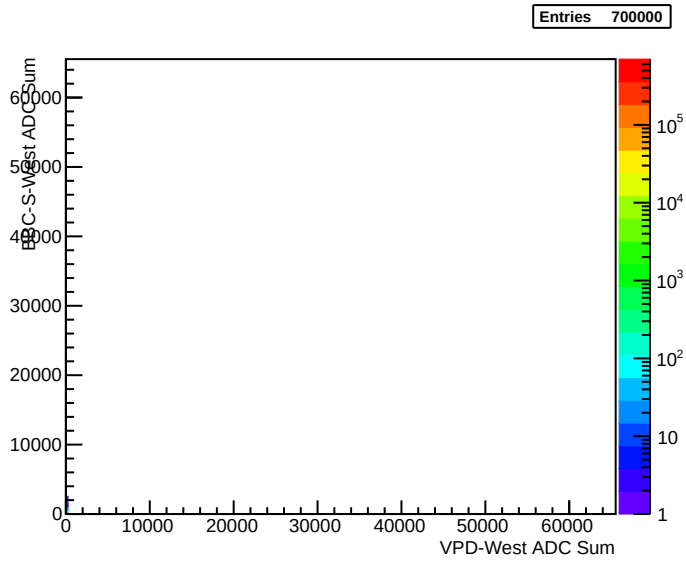

Entries 700000
TOF Mult
ZDC-East ADC Sum Att

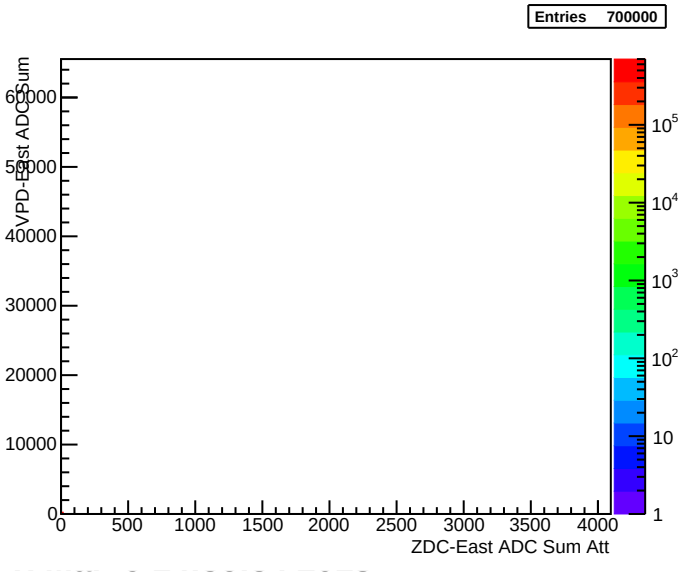

Entries 700000
TOF Mult
ZDC-West ADC Sum Att

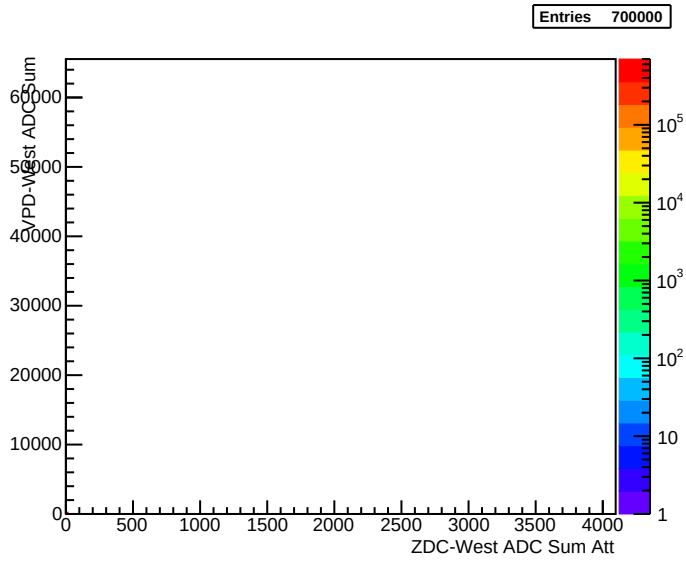

Entries 700000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

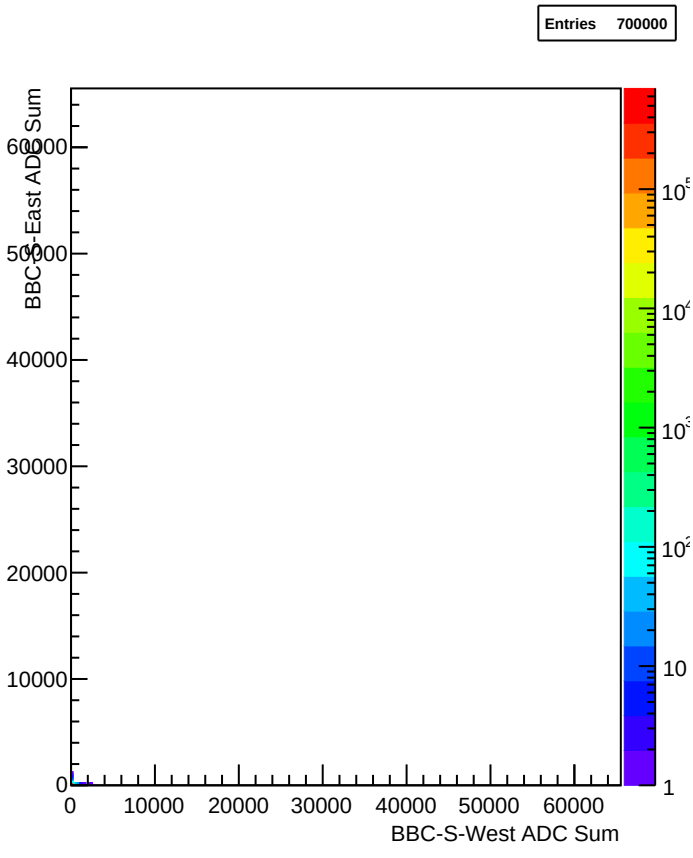

Entries 700000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

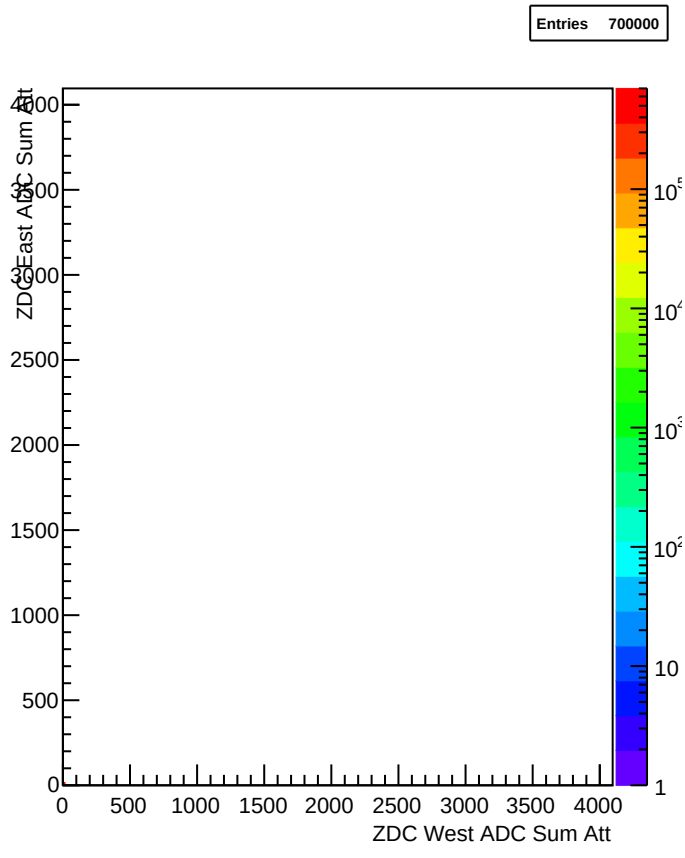

Entries 700000
BBC-S-East ADC Sum
VPD-East ADC Sum

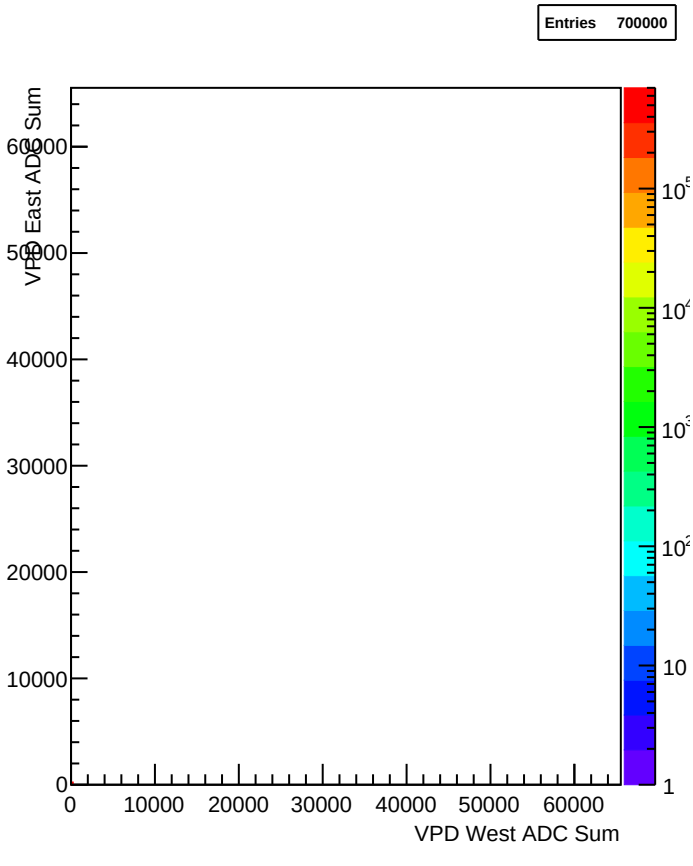

Entries 700000
BBC-S-West ADC Sum
VPD-West ADC Sum

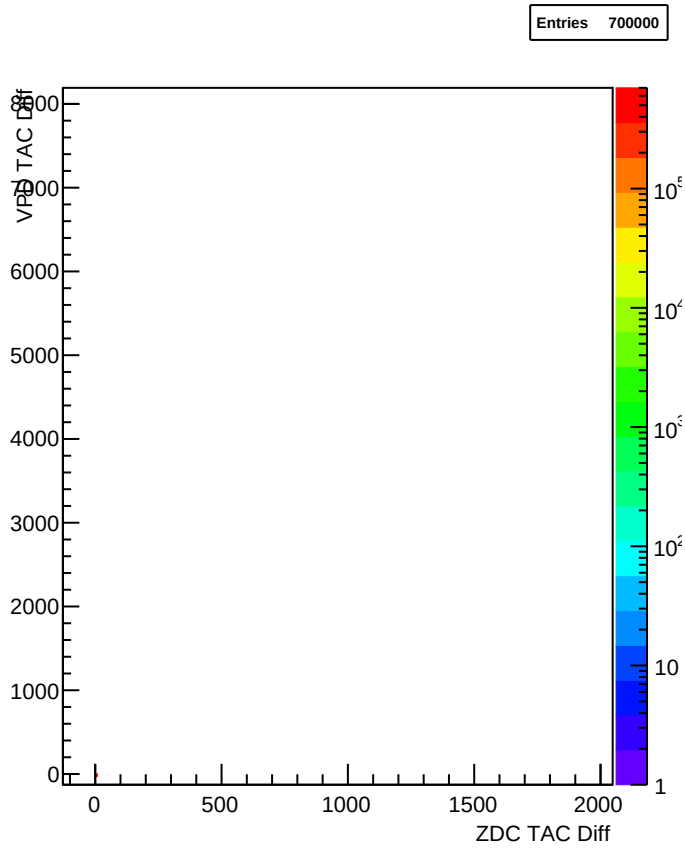

Entries 700000
VPD-East ADC Sum
ZDC-East ADC Sum Att

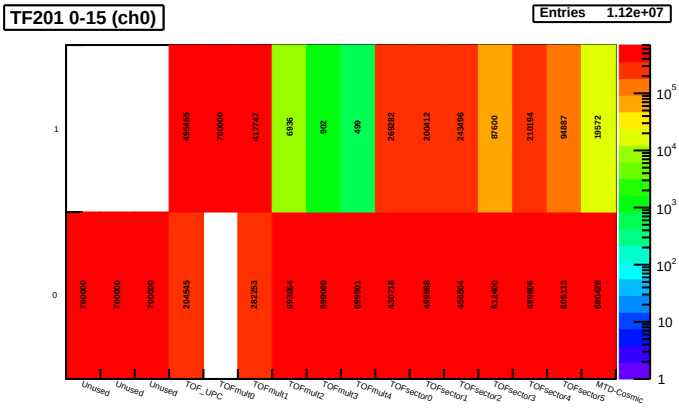

Entries 700000
VPD-West ADC Sum
ZDC-West ADC Sum Att

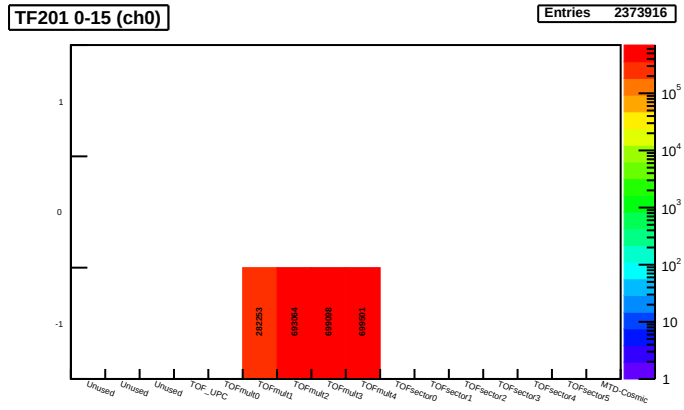

Entries 700000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

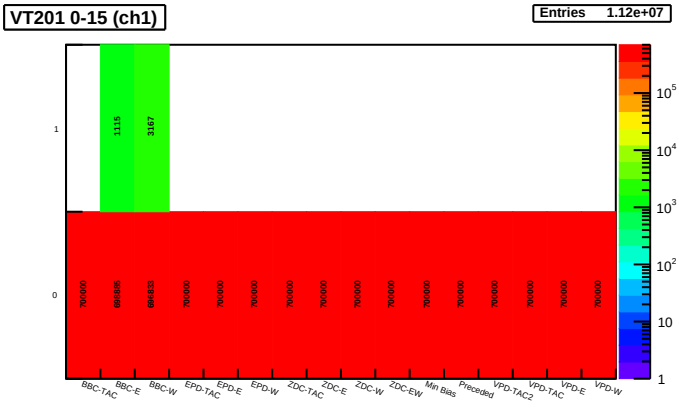

Entries 700000
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 700000
VPD East ADC Sum
VPD West ADC Sum


Entries 700000
VPD TAC Diff
ZDC TAC Diff


TF201 0-15 (ch0)
Entries 1.12e+07
495455 700000 417747 6936 902 499 269292 200412 243496 87600 210104 94887 19572
700000 700000 700000 204545 282253 693064 699098 699901 439718 499588 456904 612409 489806 605113 680428
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 2373916
282253 693064 699098 699901
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1.12e+07
1115 3167
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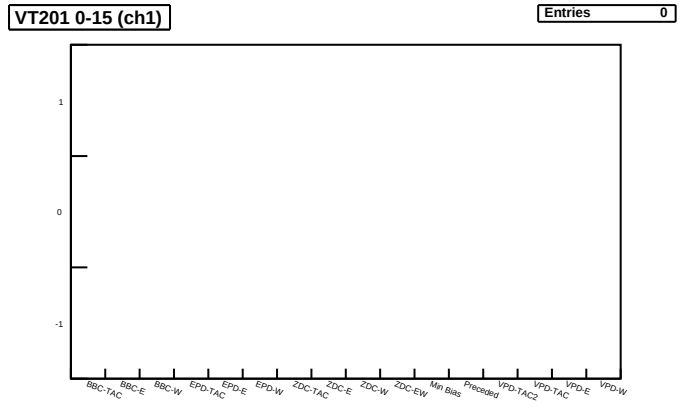
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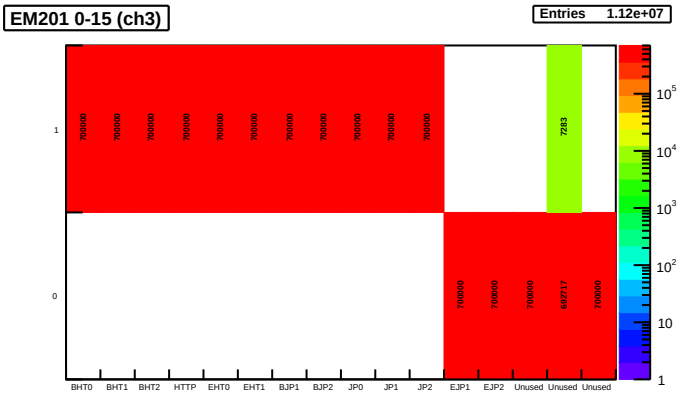
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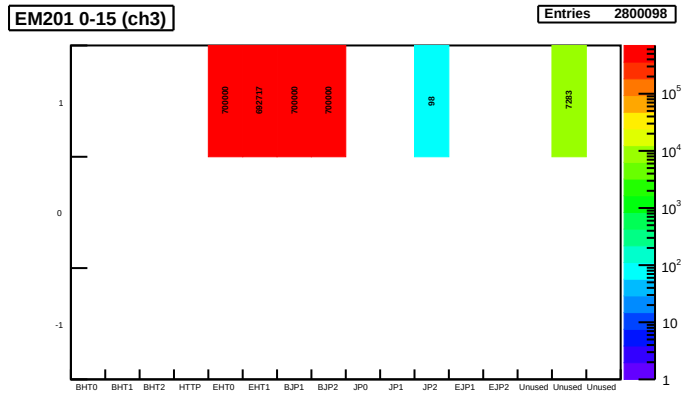
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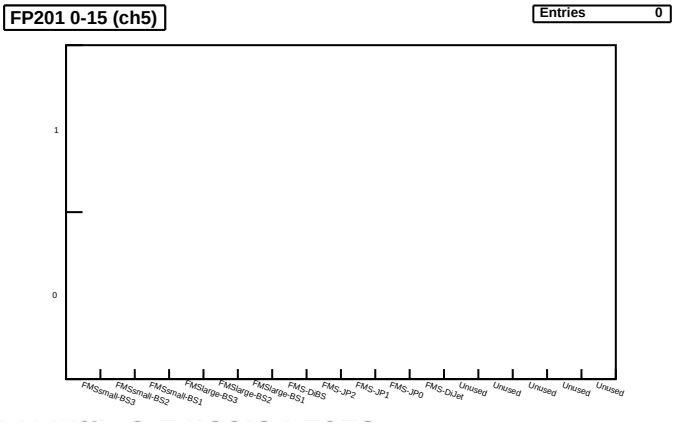
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Entries 0
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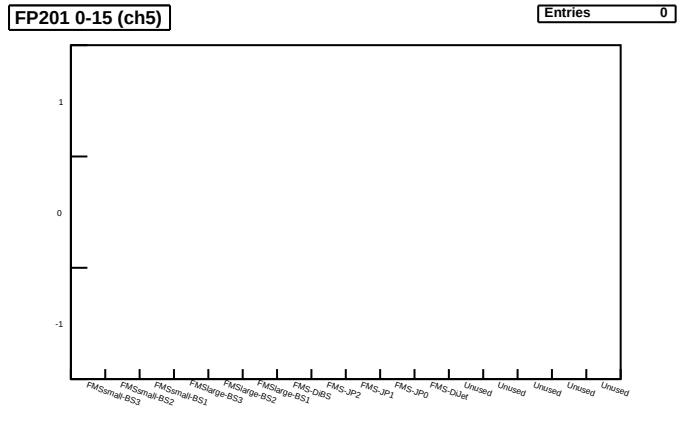
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Entries 0
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