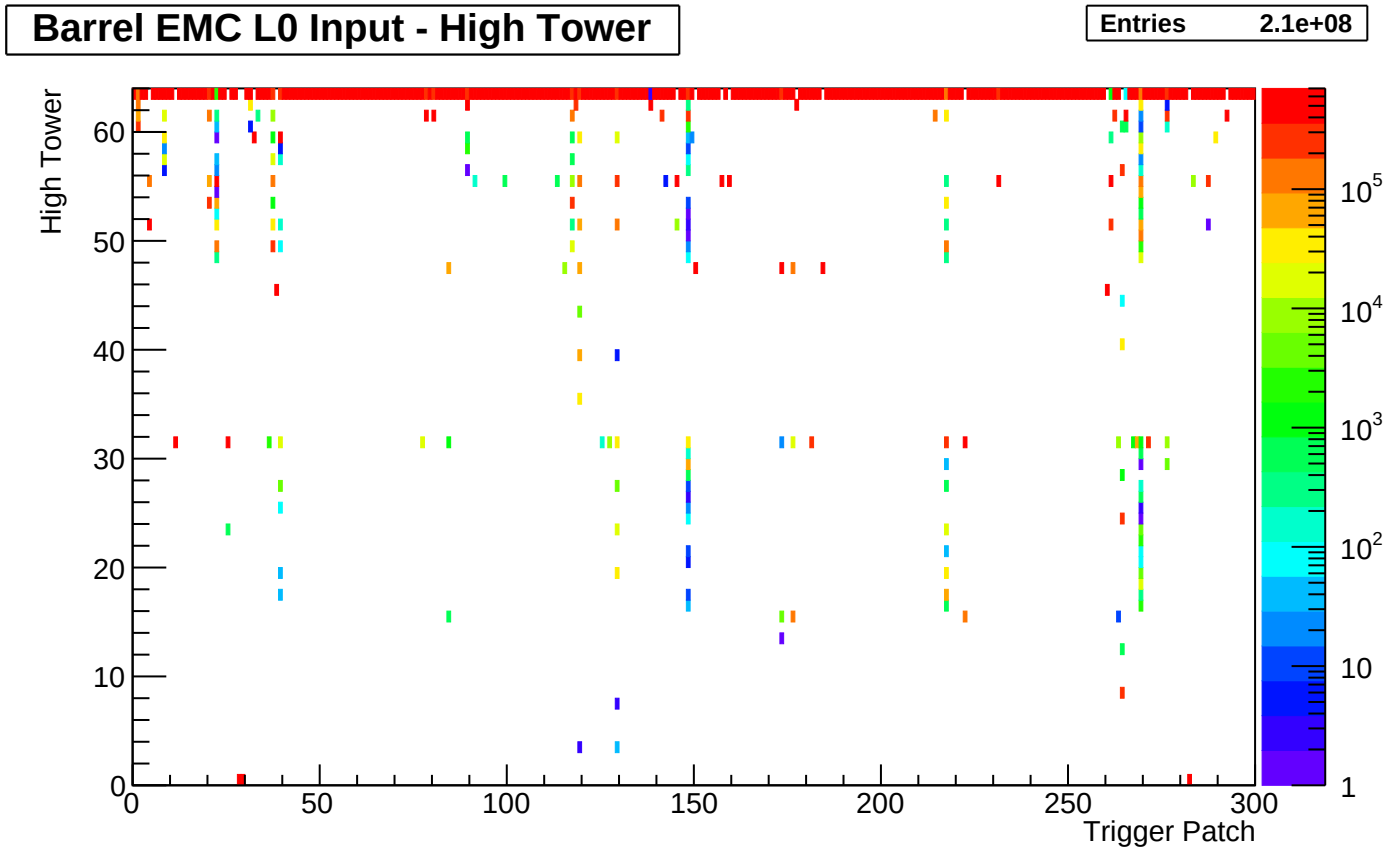
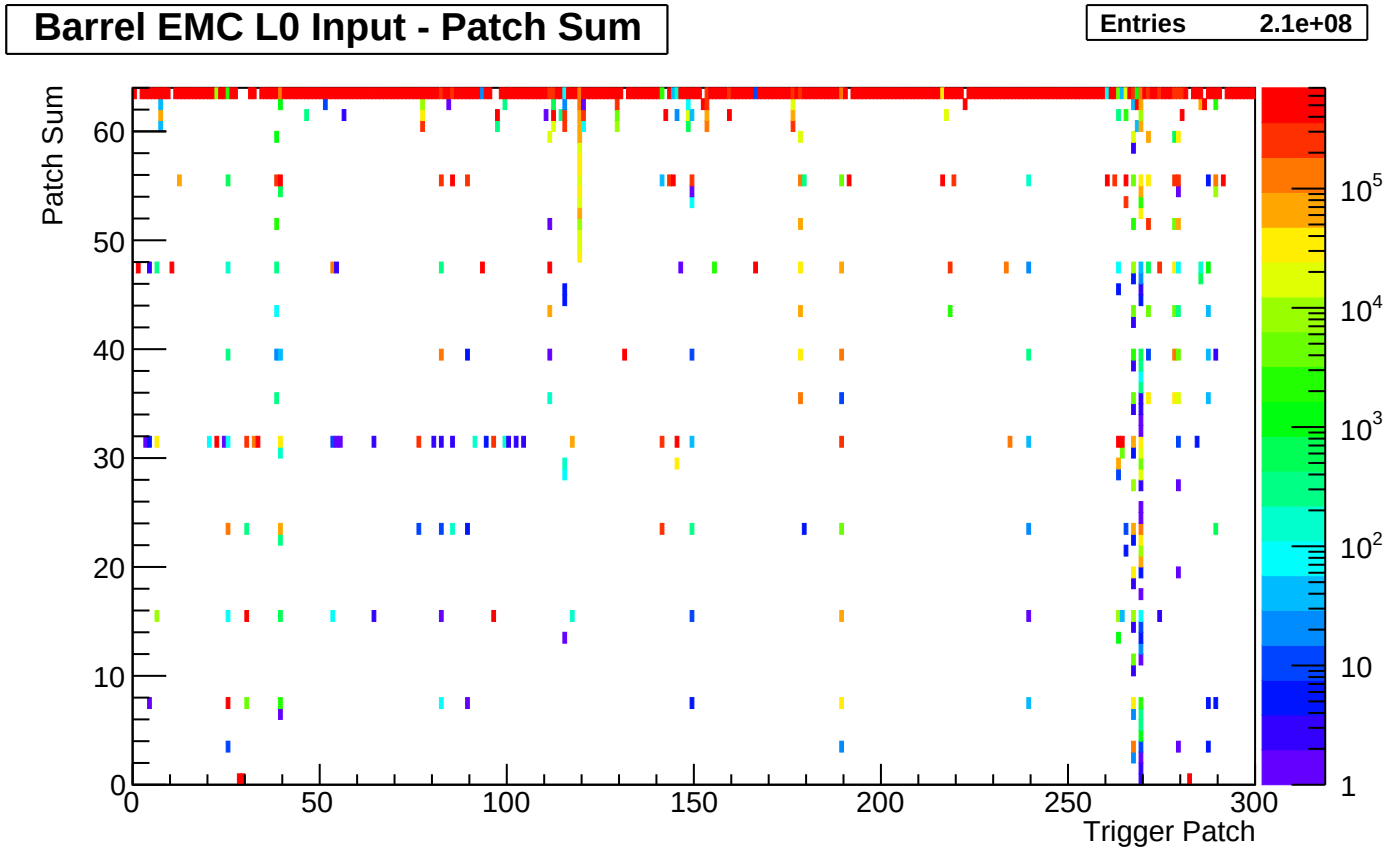
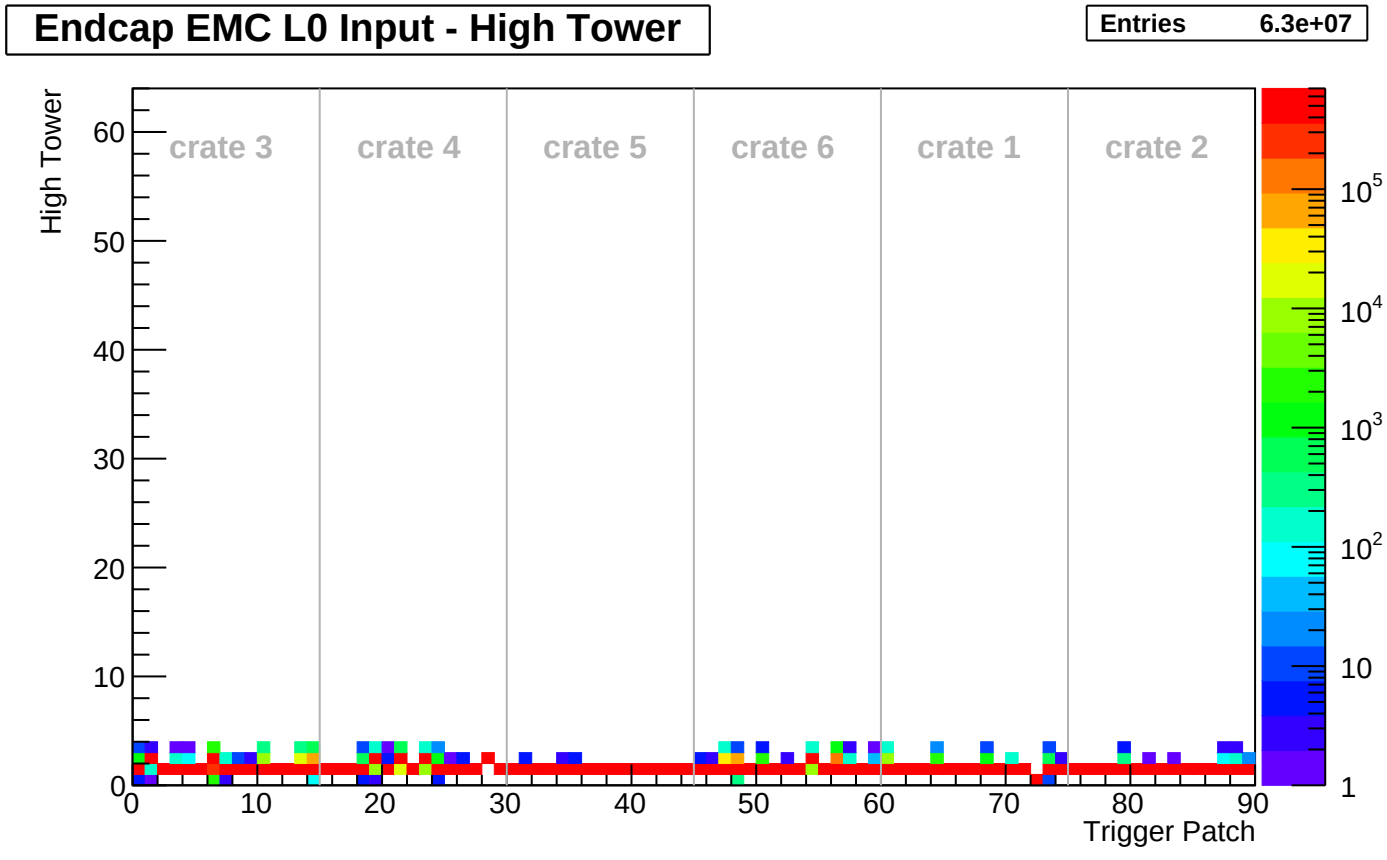
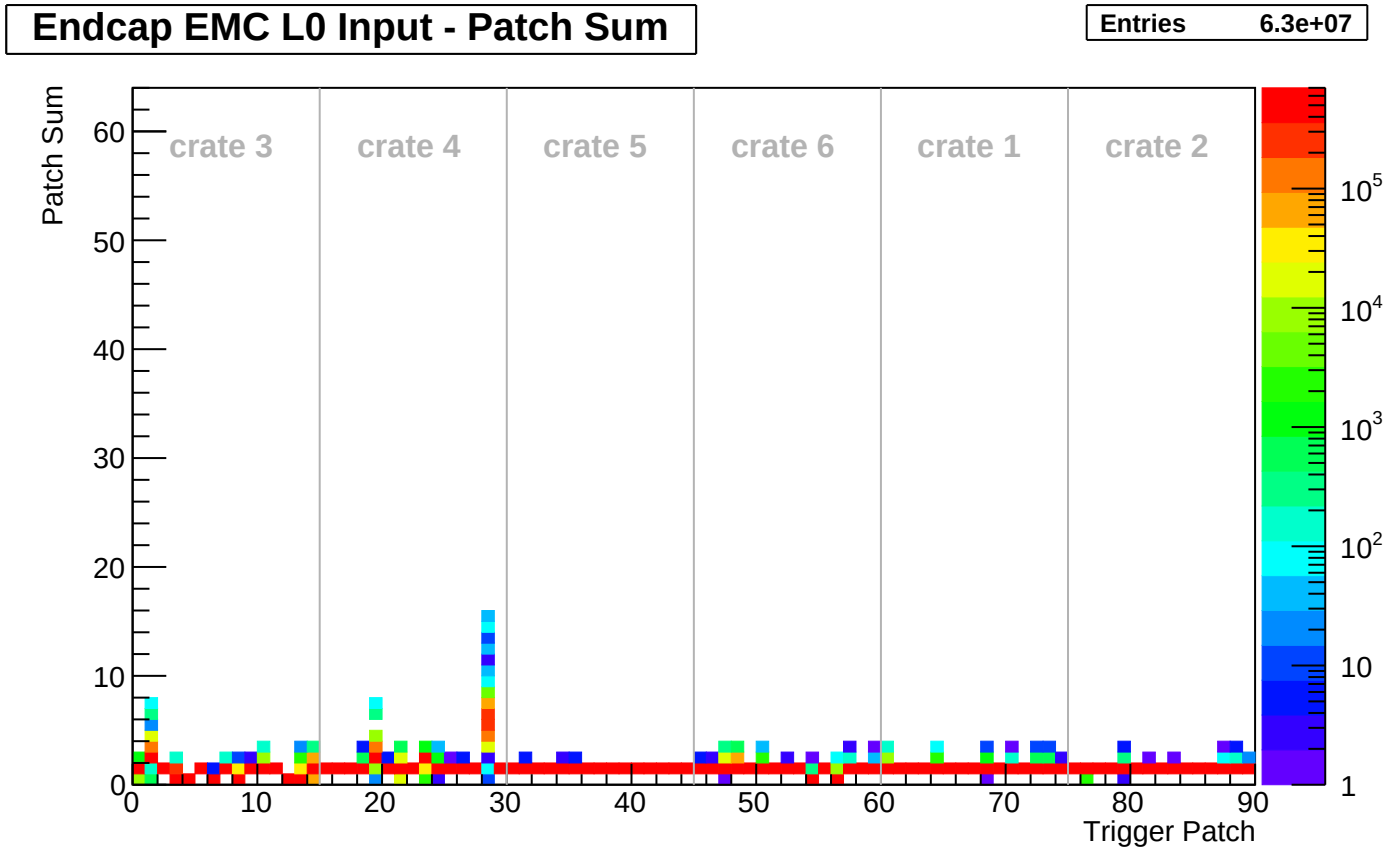
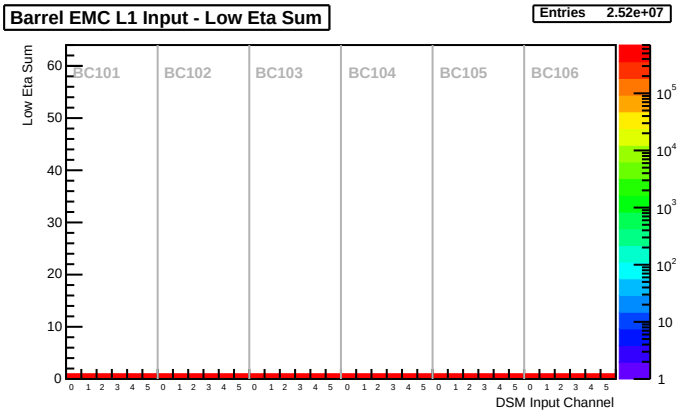
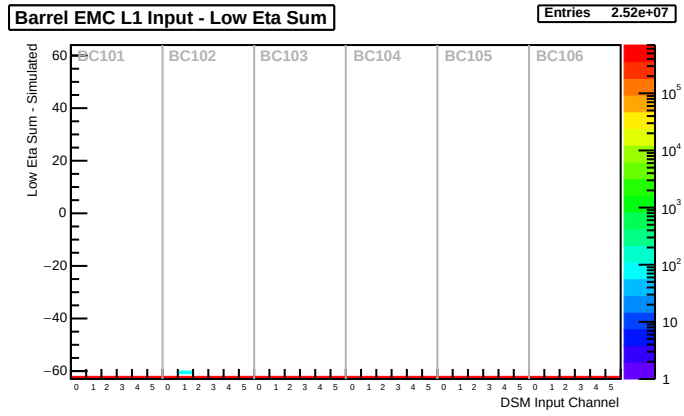

Barrel EMC L0 Input - High Tower
Entries 2.1e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

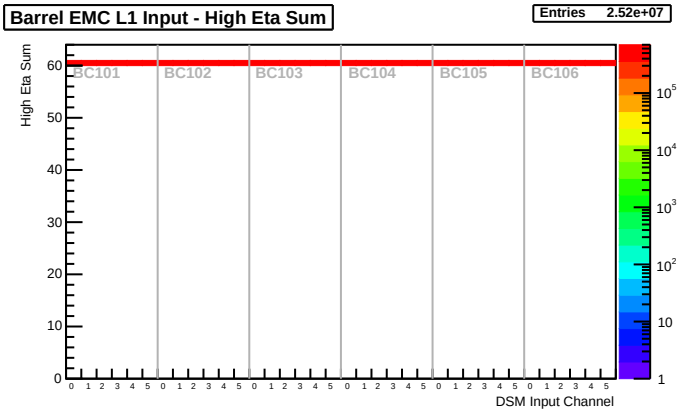

Barrel EMC L0 Input - Patch Sum
Entries 2.1e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

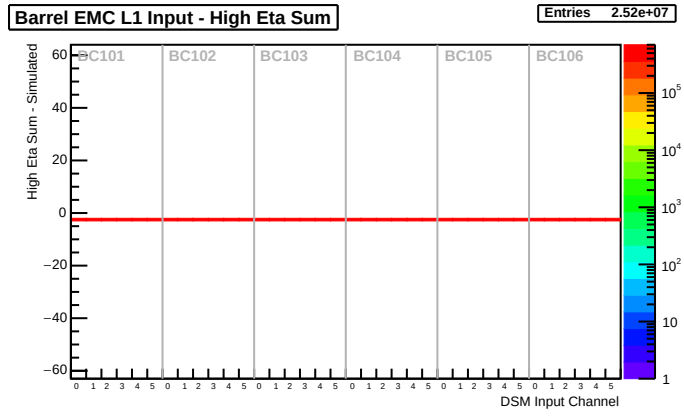

Endcap EMC L0 Input - High Tower
Entries 6.3e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

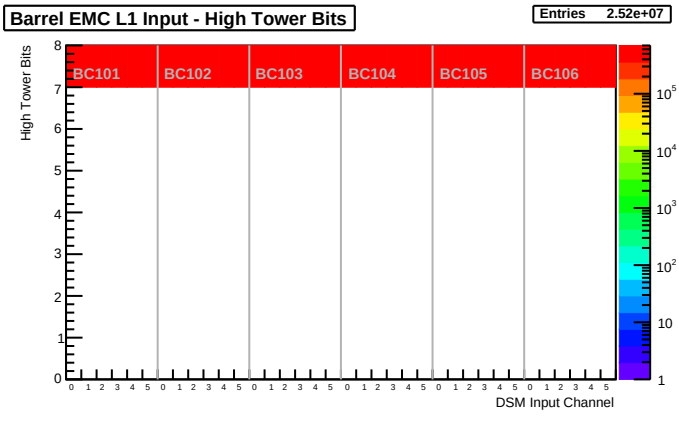

Endcap EMC L0 Input - Patch Sum
Entries 6.3e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

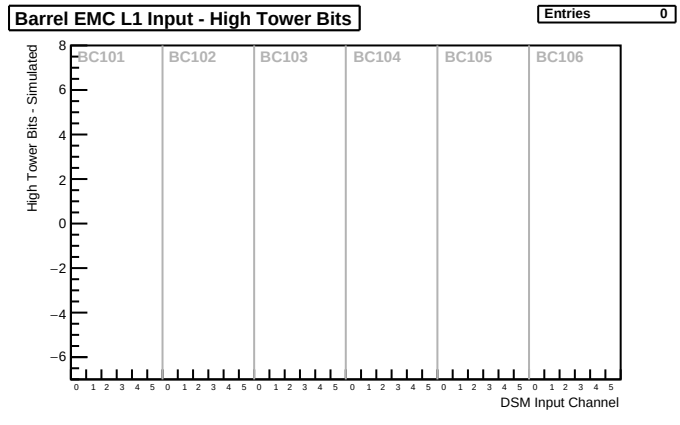

Barrel EMC L1 Input - Low Eta Sum
Entries 2.52e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

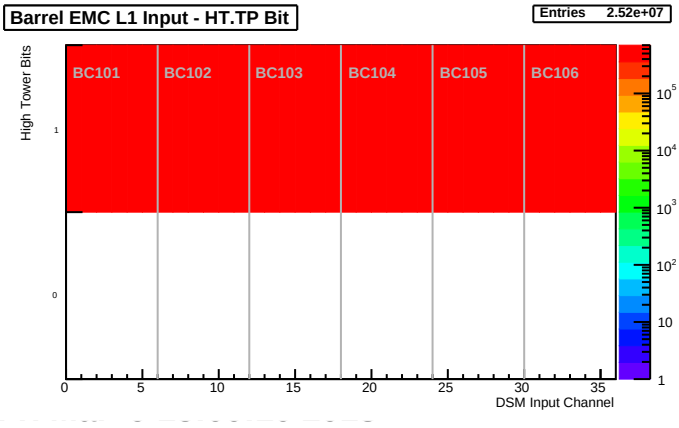

Barrel EMC L1 Input - Low Eta Sum
Entries 2.52e+07
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

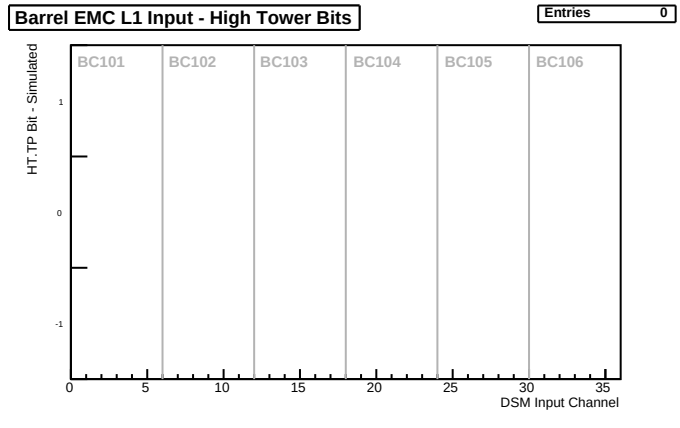

Barrel EMC L1 Input - High Eta Sum
Entries 2.52e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

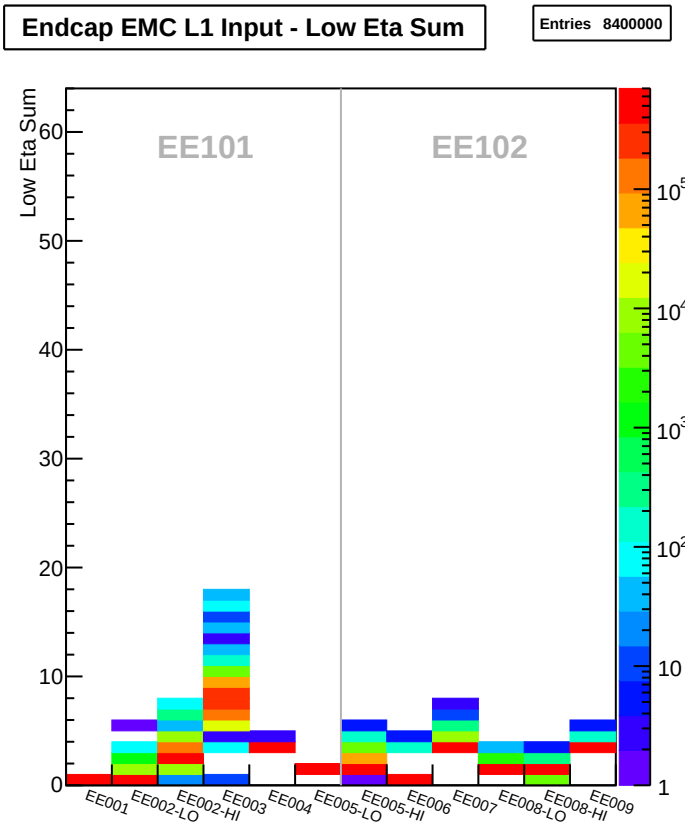

Barrel EMC L1 Input - High Eta Sum
Entries 2.52e+07
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

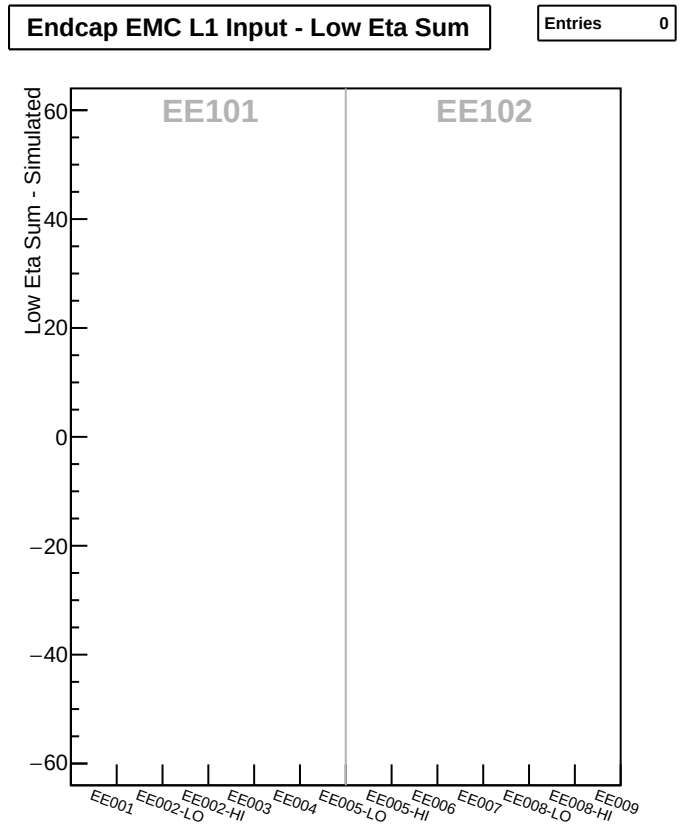

Barrel EMC L1 Input - High Tower Bits
Entries 2.52e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

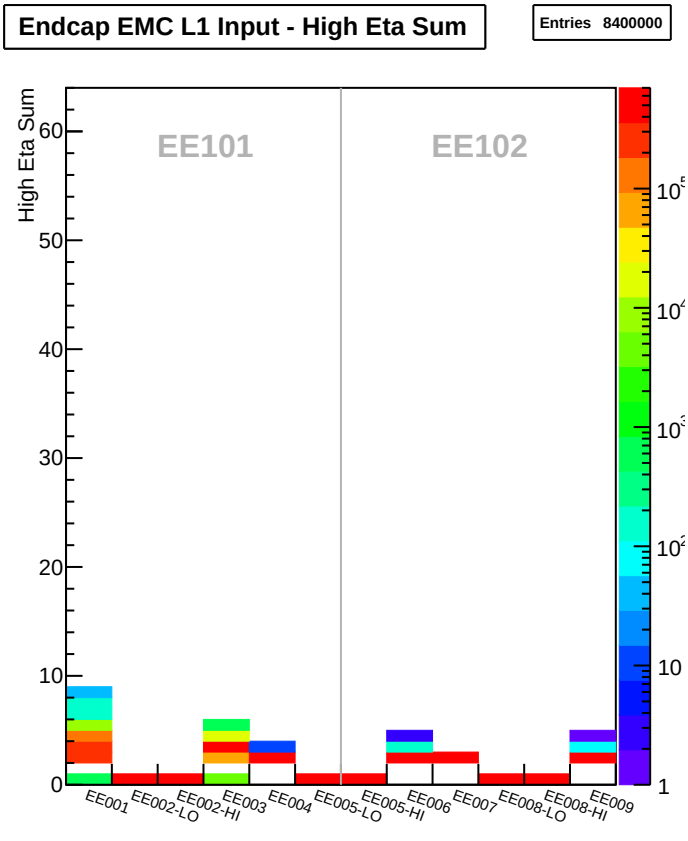

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

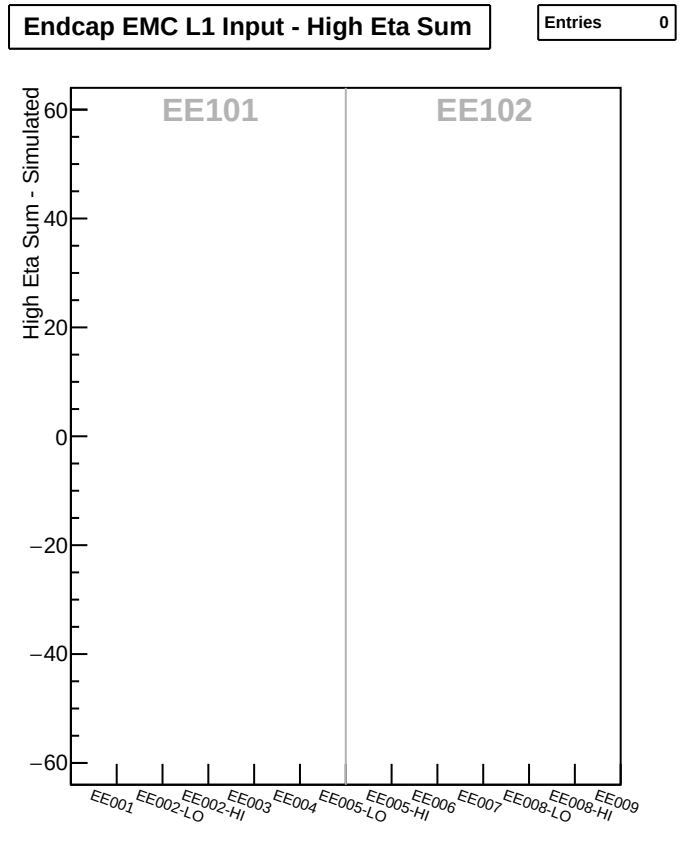

Barrel EMC L1 Input - HT.TP Bit
Entries 2.52e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

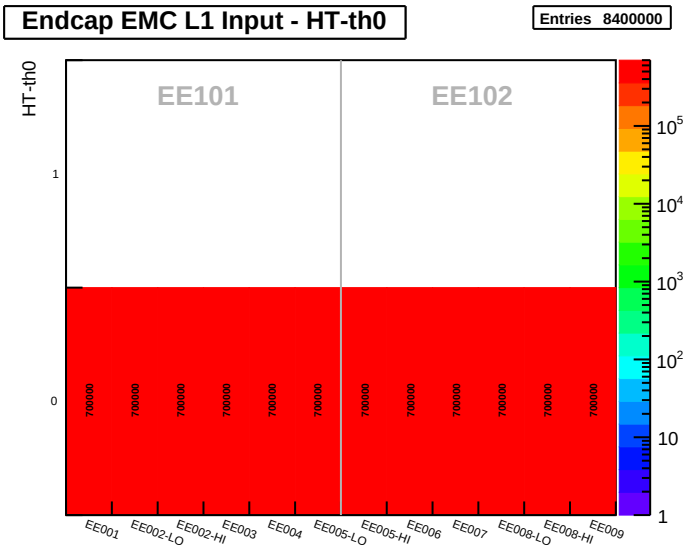

Barrel EMC L1 Input - High Tower Bits
Entries 0
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

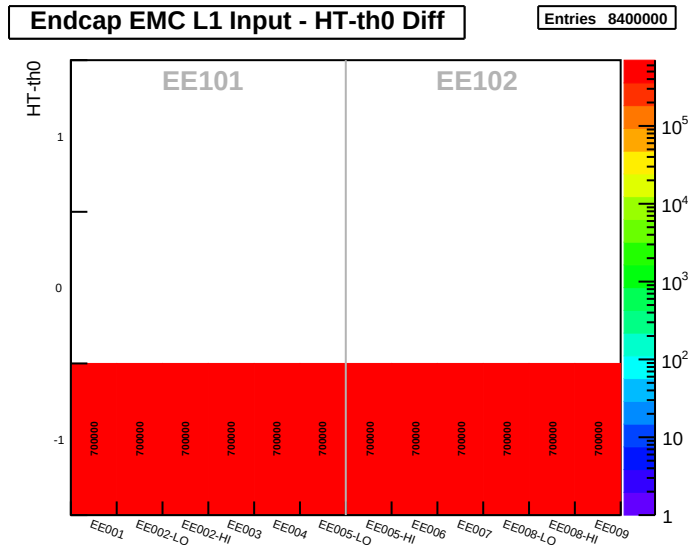

Endcap EMC L1 Input - Low Eta Sum
Entries 8400000
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

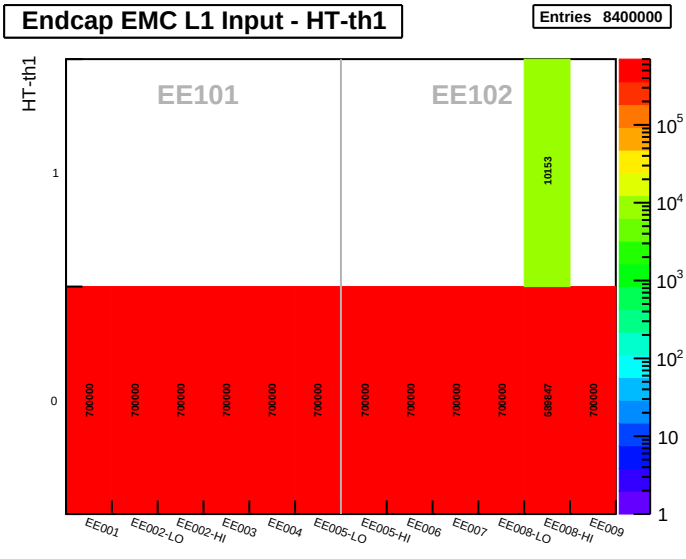

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

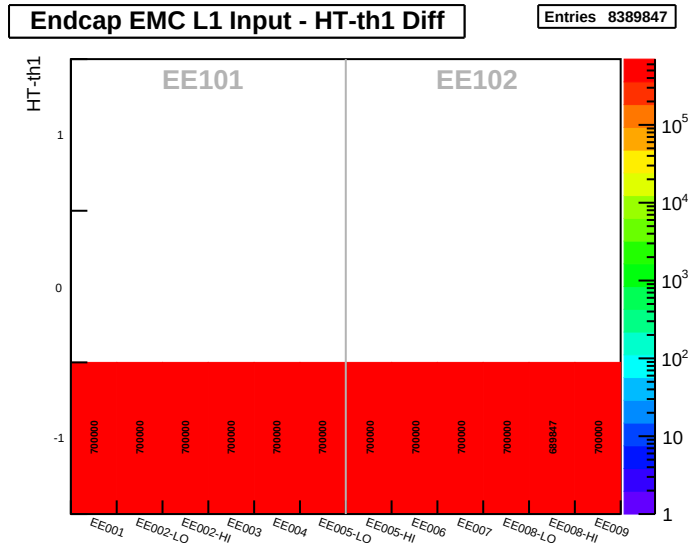

Endcap EMC L1 Input - High Eta Sum
Entries 8400000
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

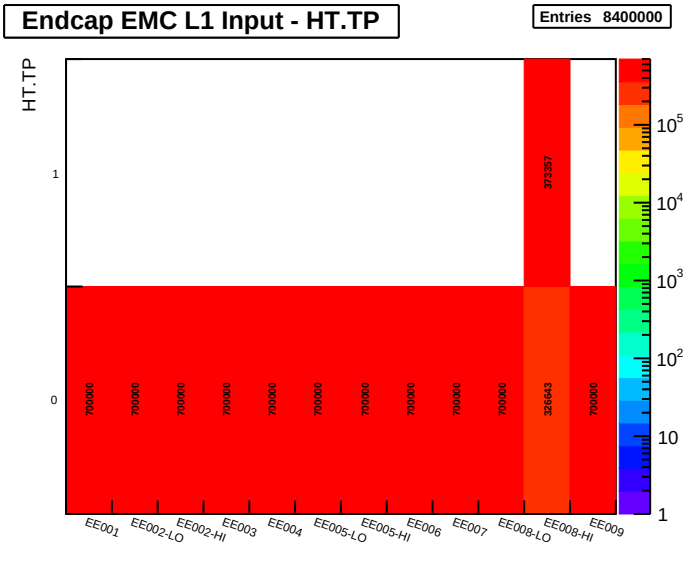

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

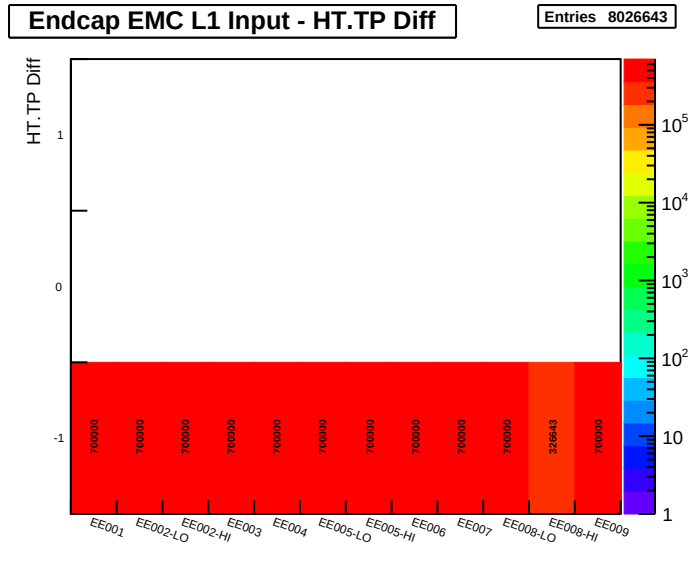

Endcap EMC L1 Input - HT-th0
Entries 8400000
HT-th0
EE101
EE102
700000
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

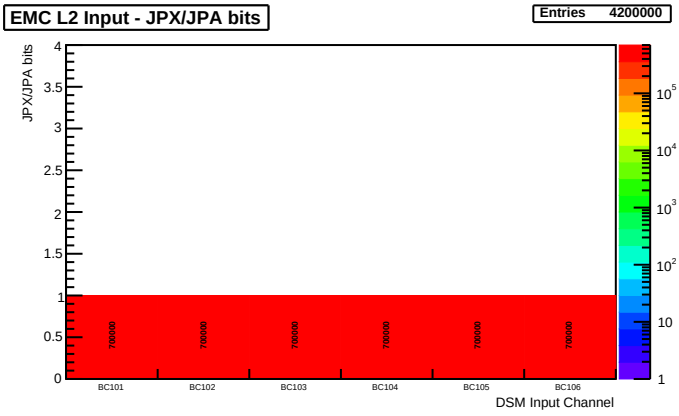

Endcap EMC L1 Input - HT-th0 Diff
Entries 8400000
HT-th0
EE101
EE102
700000
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

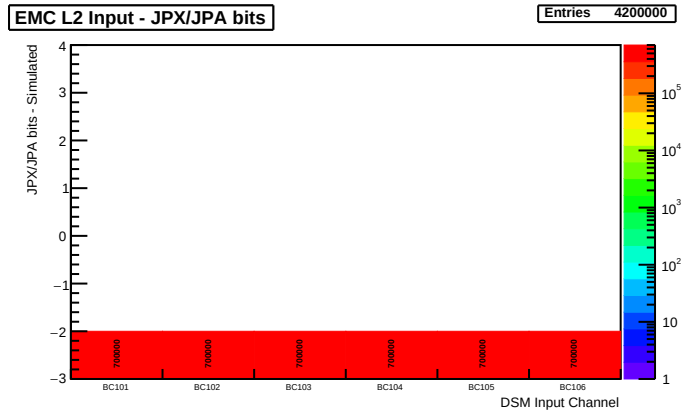

Endcap EMC L1 Input - HT-th1
Entries 8400000
HT-th1
EE101
EE102
10153
700000
689847
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

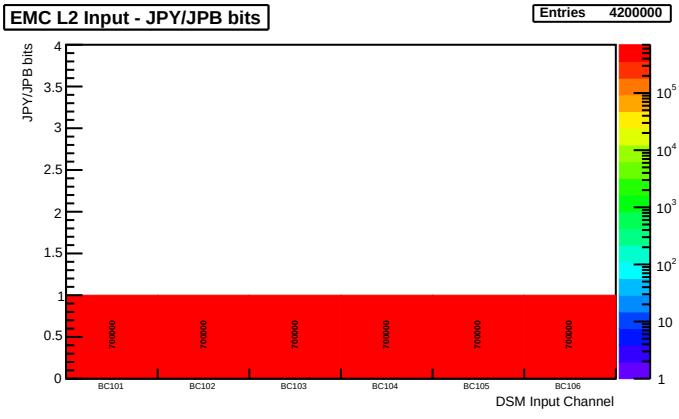

Endcap EMC L1 Input - HT-th1 Diff
Entries 8389847
HT-th1
EE101
EE102
700000
689847
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

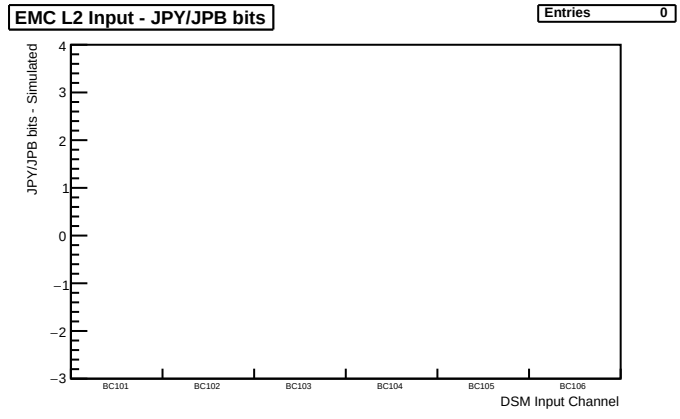

Endcap EMC L1 Input - HT.TP
Entries 8400000
HT.TP
373357
700000
326643
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

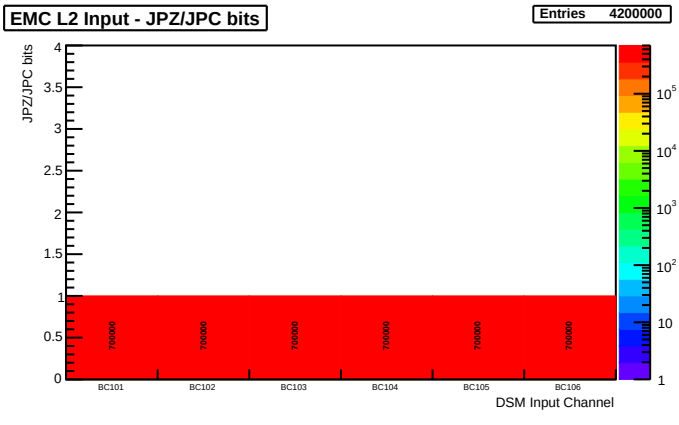

Endcap EMC L1 Input - HT.TP Diff
Entries 8026643
HT.TP Diff
700000
326643
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

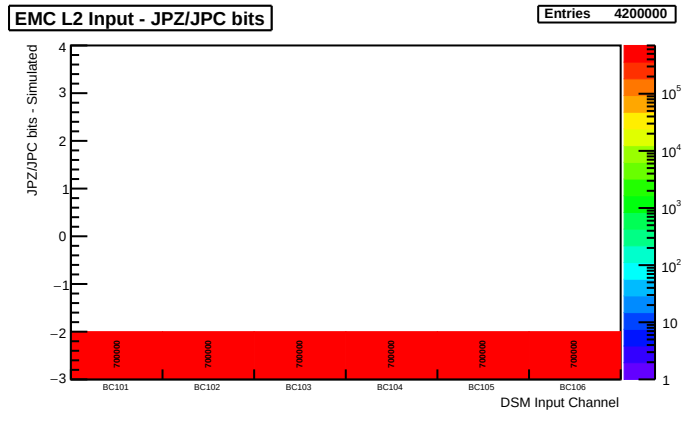

EMC L2 Input - JPX/JPA bits
Entries 4200000
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
700000

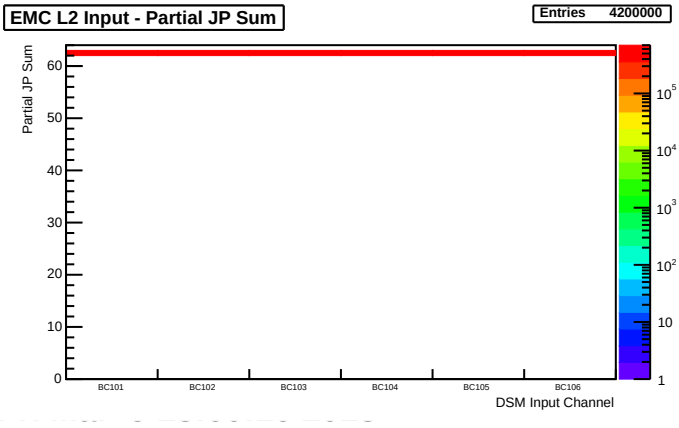

EMC L2 Input - JPX/JPA bits
Entries 4200000
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
700000

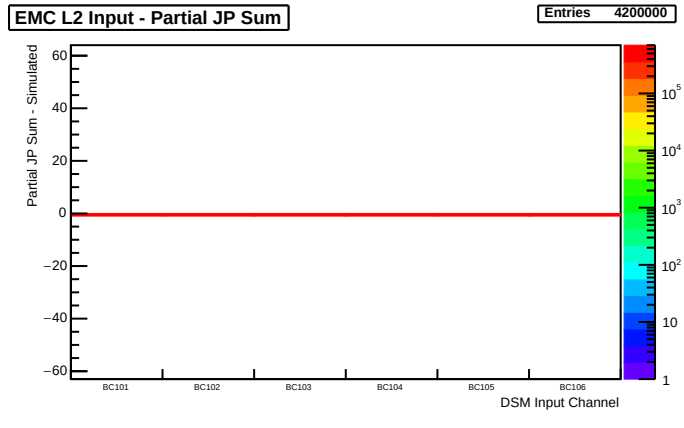

EMC L2 Input - JPY/JPB bits
Entries 4200000
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
700000

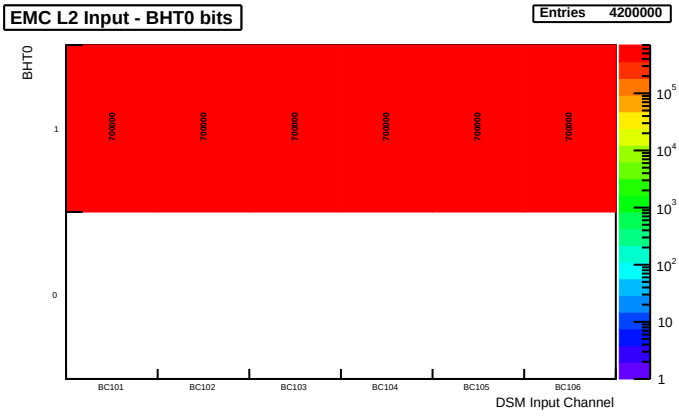

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

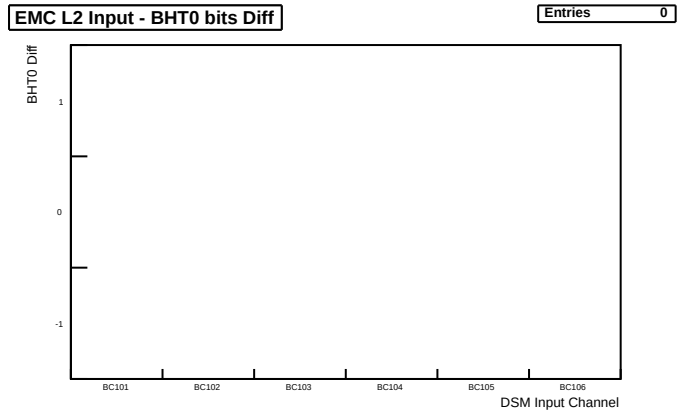

EMC L2 Input - JPZ/JPC bits
Entries 4200000
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
700000

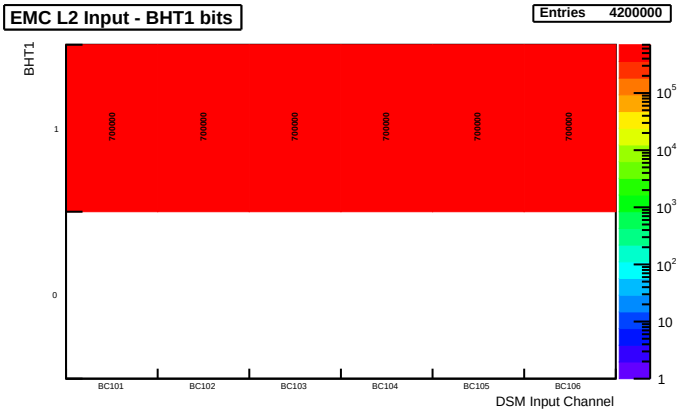

EMC L2 Input - JPZ/JPC bits
Entries 4200000
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
700000

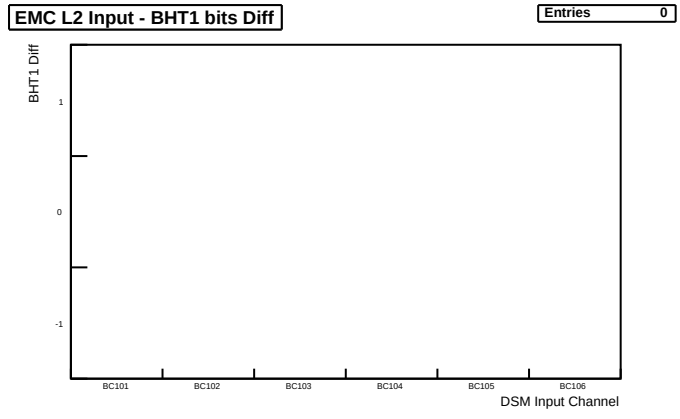

EMC L2 Input - Partial JP Sum
Entries 4200000
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

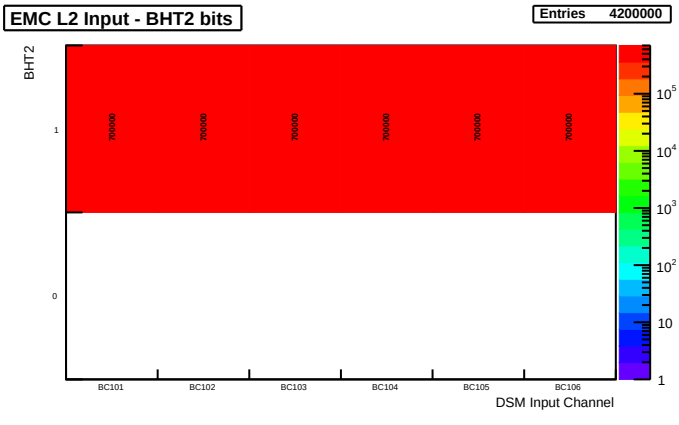

EMC L2 Input - Partial JP Sum
Entries 4200000
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

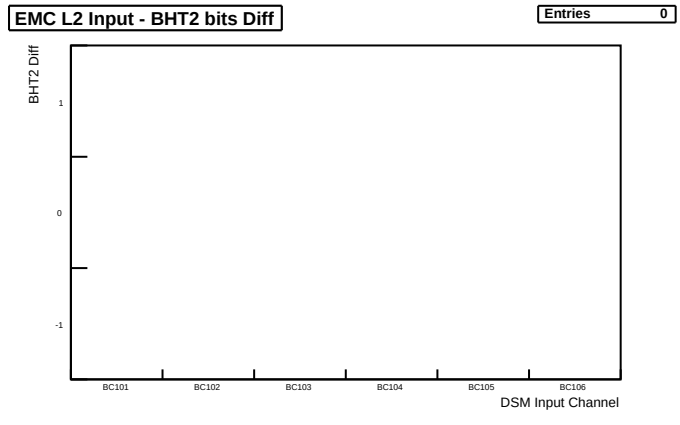

EMC L2 Input - BHT0 bits
Entries 4200000
BHT0
700000
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

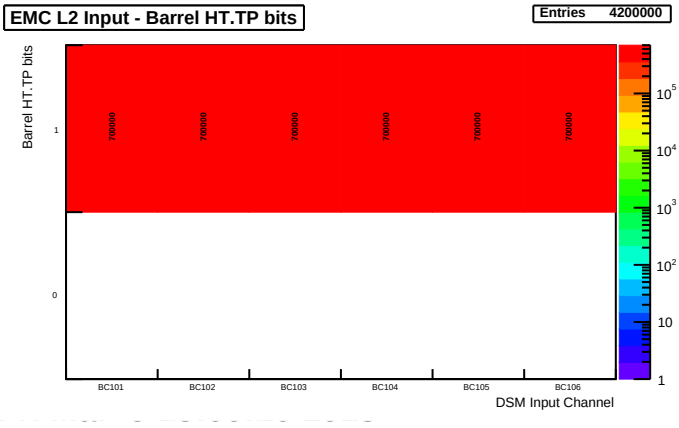

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

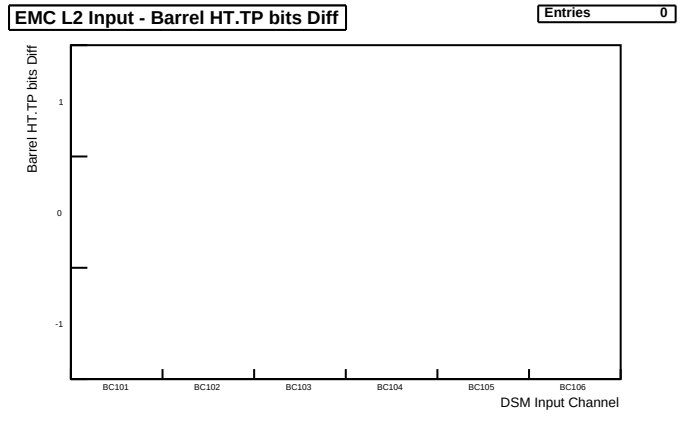

EMC L2 Input - BHT1 bits
Entries 4200000
BHT1
700000
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

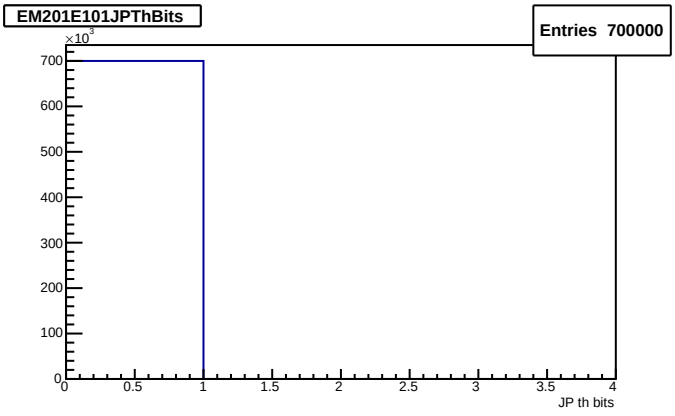

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

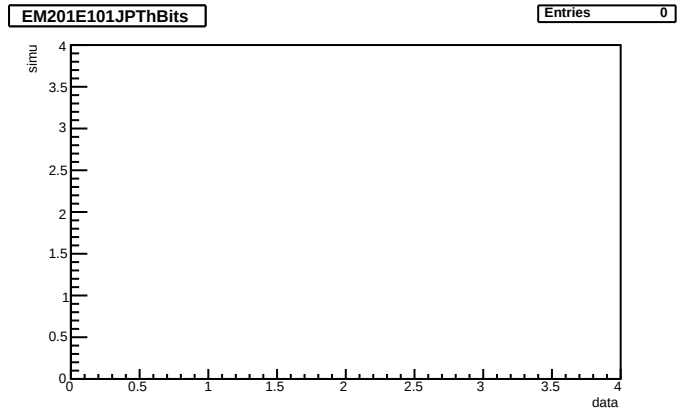

EMC L2 Input - BHT2 bits
Entries 4200000
BHT2
700000
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

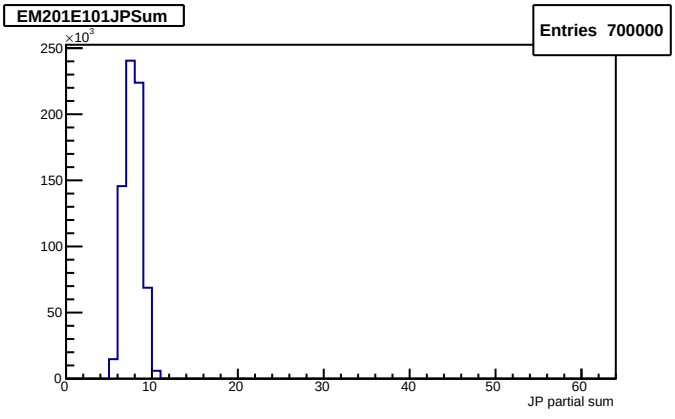

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

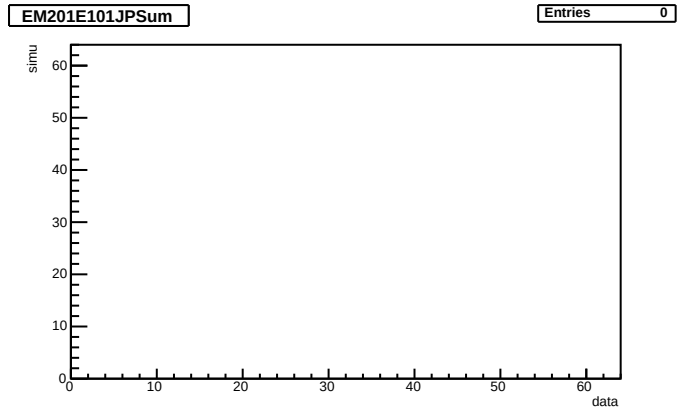

EMC L2 Input - Barrel HT.TP bits
Entries 4200000
Barrel HT.TP bits
700000
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

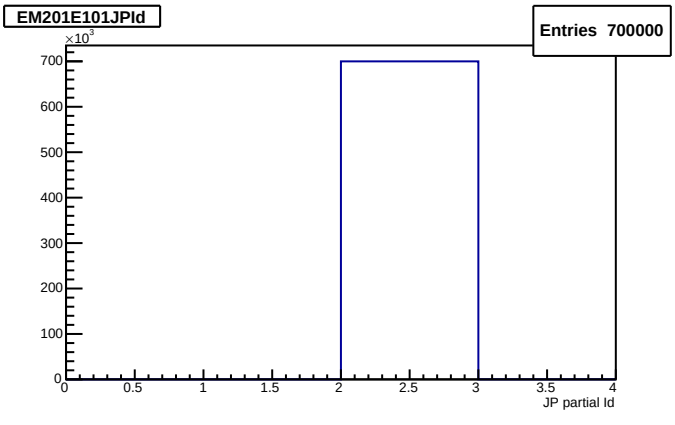

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

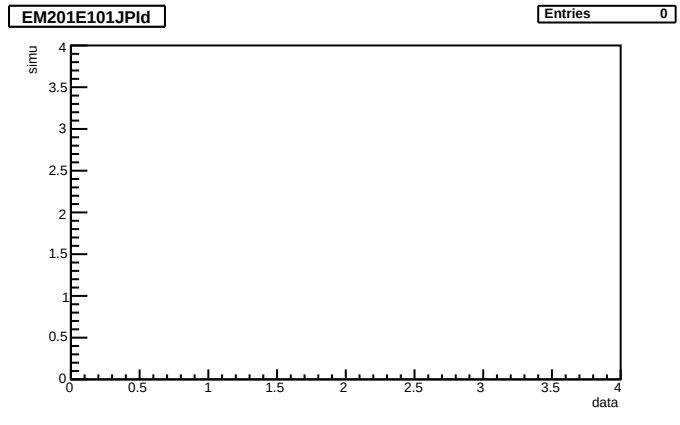

EM201E101JPThBits
Entries 700000
×10^3
JP th bits

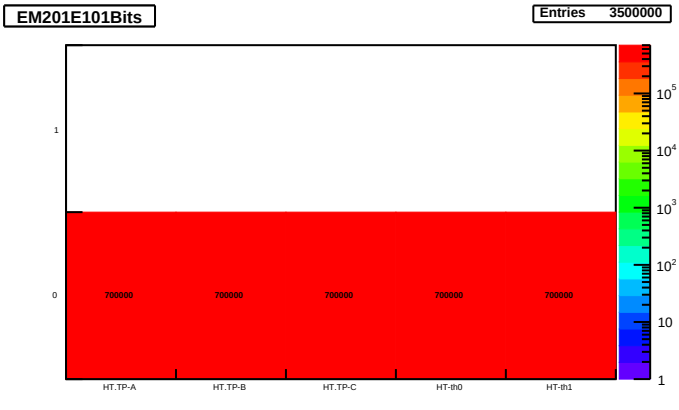

EM201E101JPThBits
Entries 0
simu
data

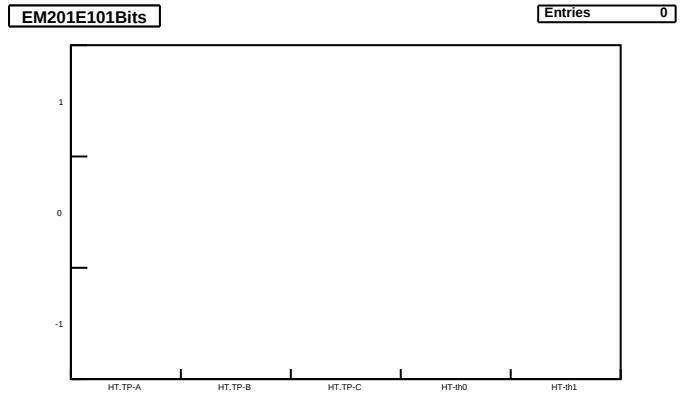

EM201E101JPSum
Entries 700000
×10^3
JP partial sum

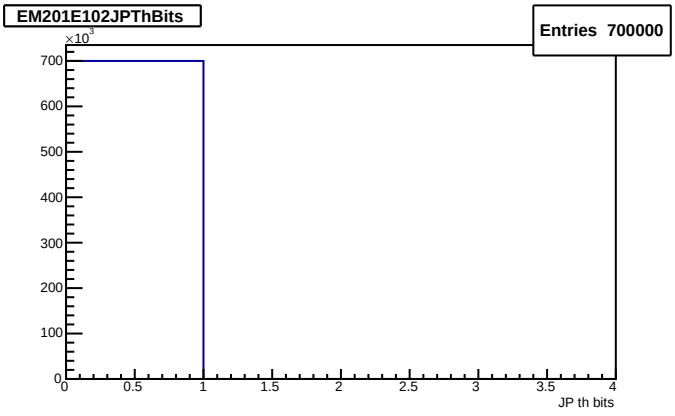

EM201E101JPSum
Entries 0
simu
data

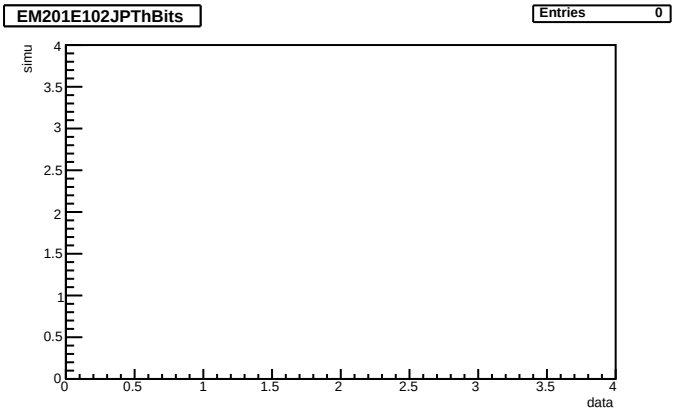

EM201E101JPId
Entries 700000
×10^3
JP partial Id

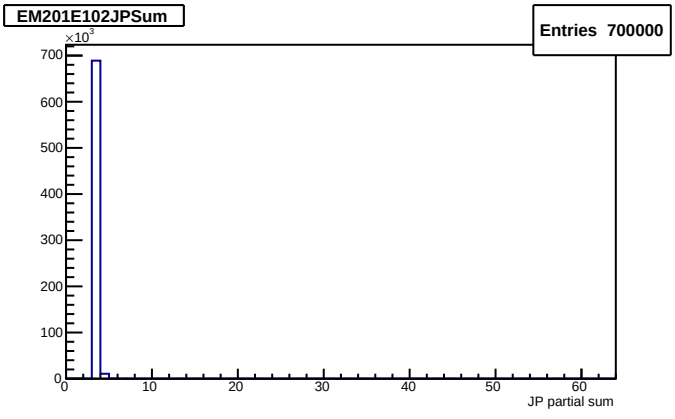

EM201E101JPId
Entries 0
simu
data

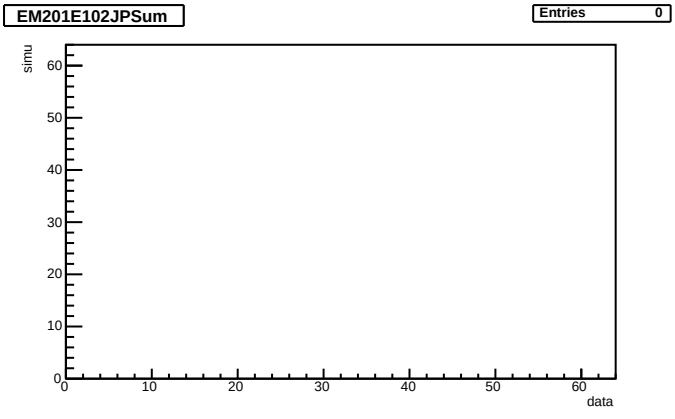

EM201E101Bits
Entries 3500000
700000
700000
700000
700000
700000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

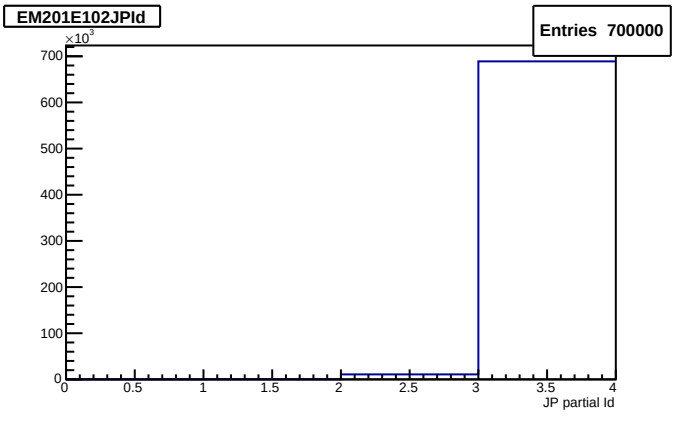

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

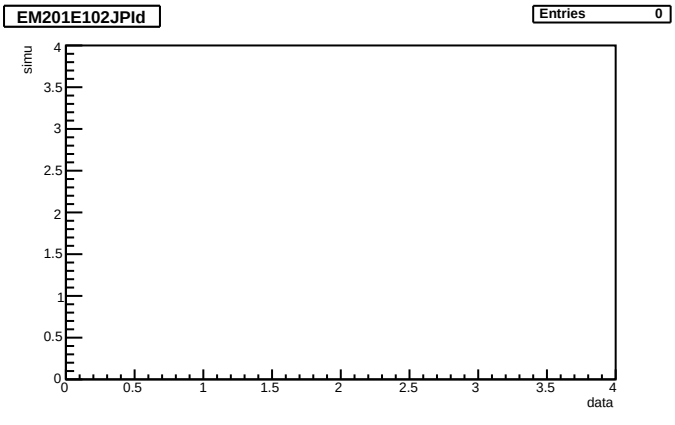

EM201E102JPThBits
Entries 700000
×10^3
JP th bits

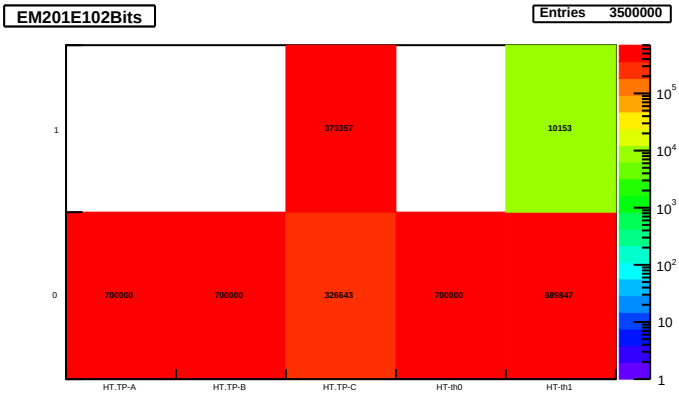

EM201E102JPThBits
Entries 0
simu
data

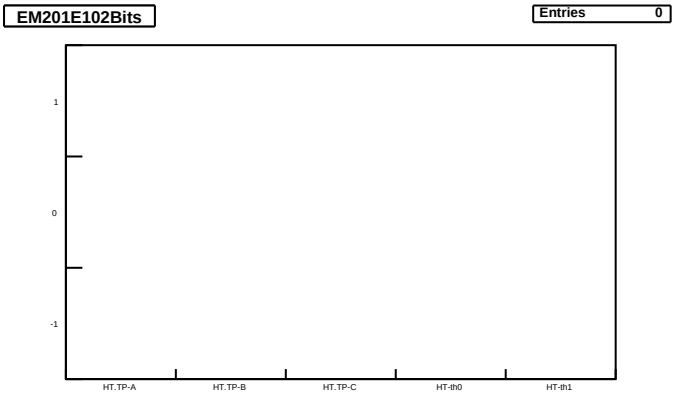

EM201E102JPSum
Entries 700000
×10^3
JP partial sum

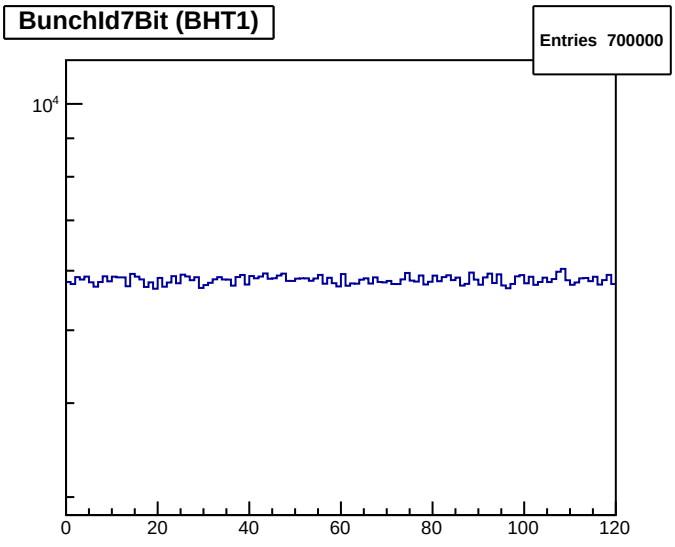

EM201E102JPSum
Entries 0
simu
data

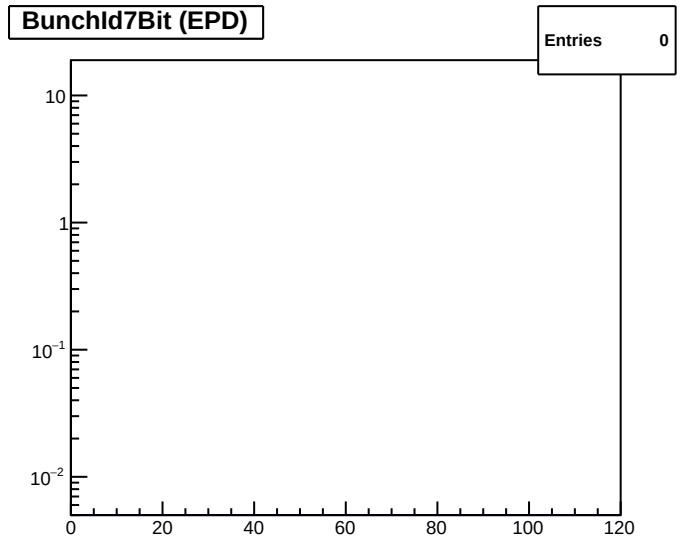

EM201E102JPId
Entries 700000
×10^3
JP partial Id

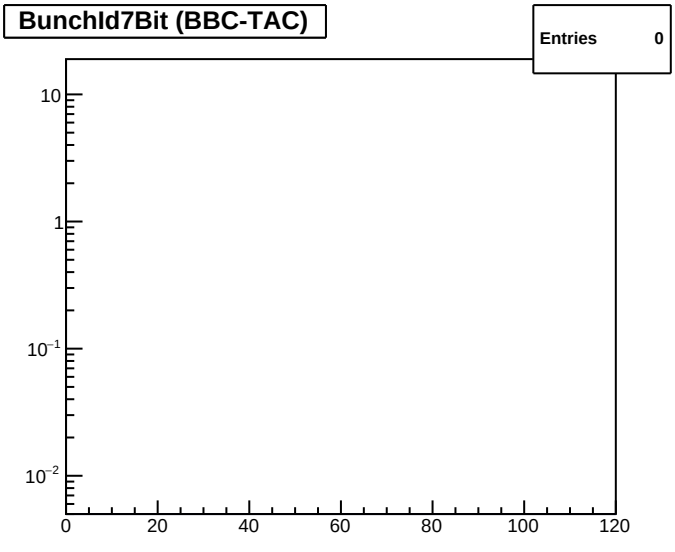

EM201E102JPId
Entries 0
simu
data

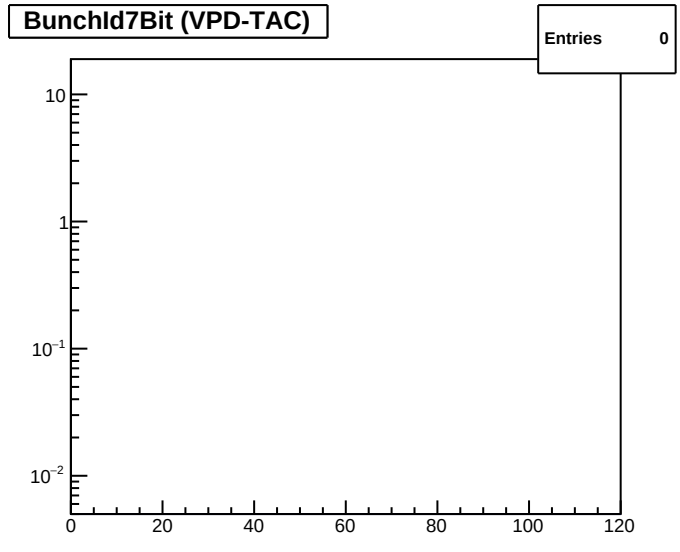

EM201E102Bits
Entries 3500000
373357
10153
700000
700000
326643
700000
689847
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

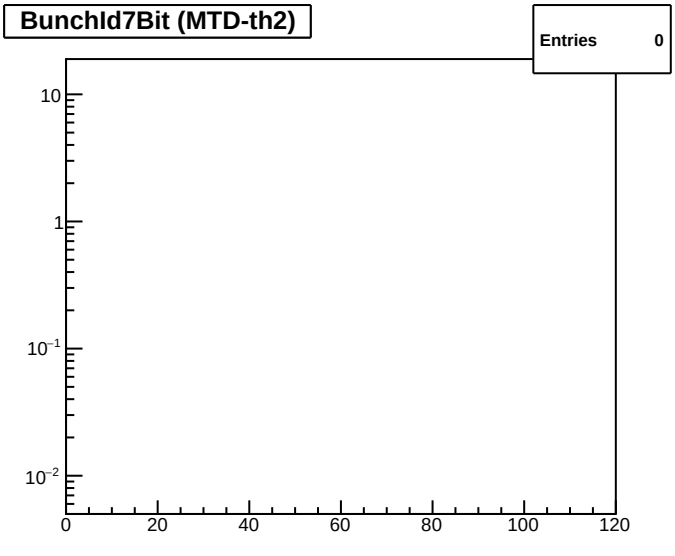

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

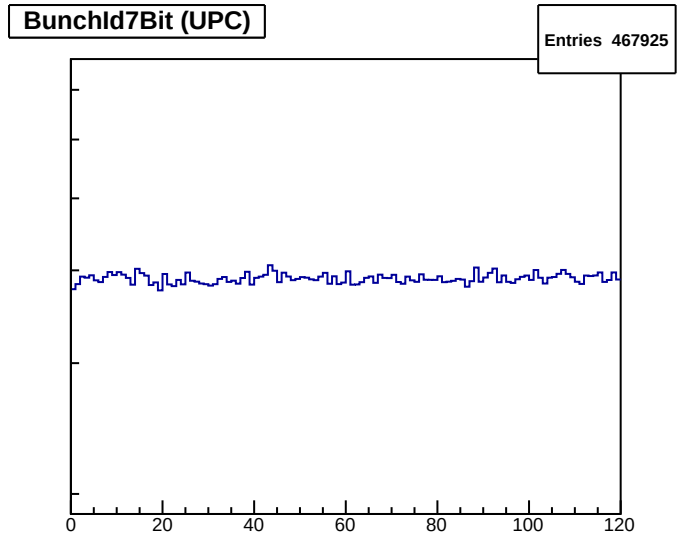

BunchId7Bit (BHT1)
Entries 700000
10^4
0
20
40
60
80
100
120

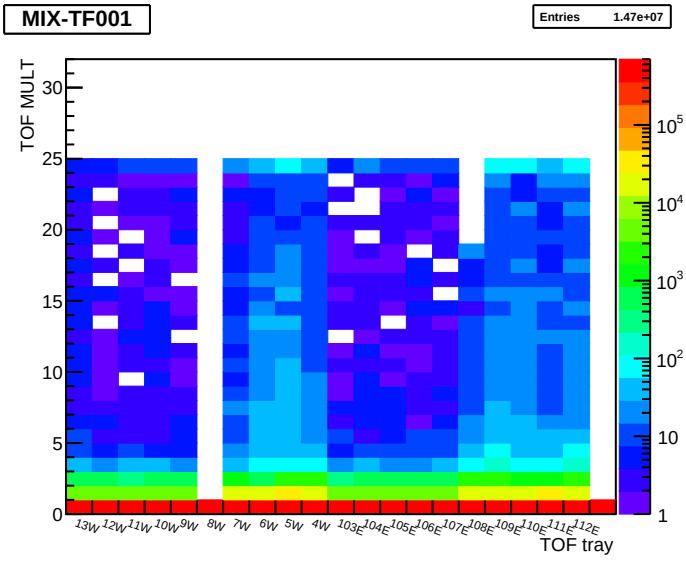

BunchId7Bit (EPD)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

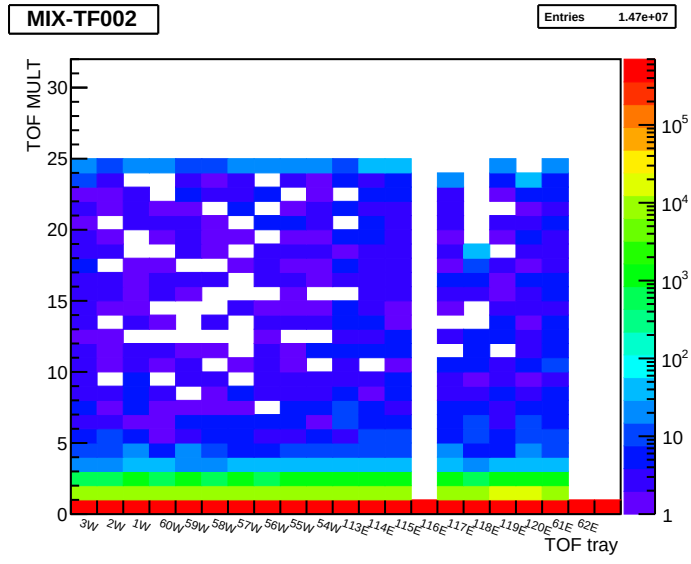

BunchId7Bit (BBC-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

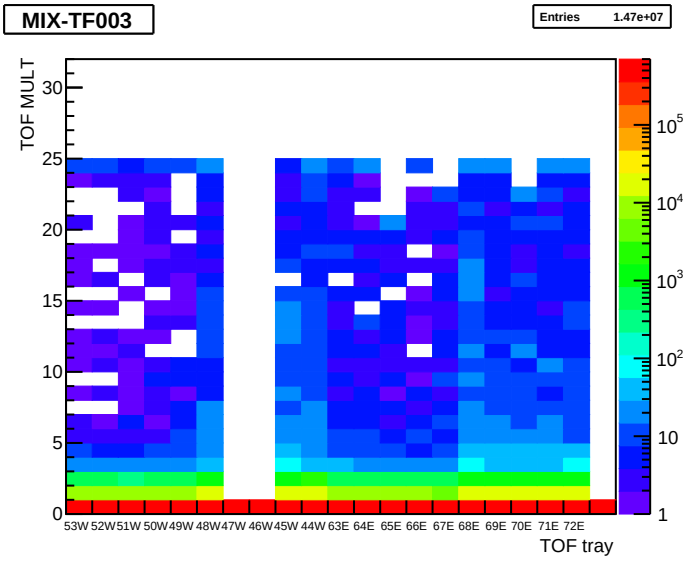

BunchId7Bit (VPD-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

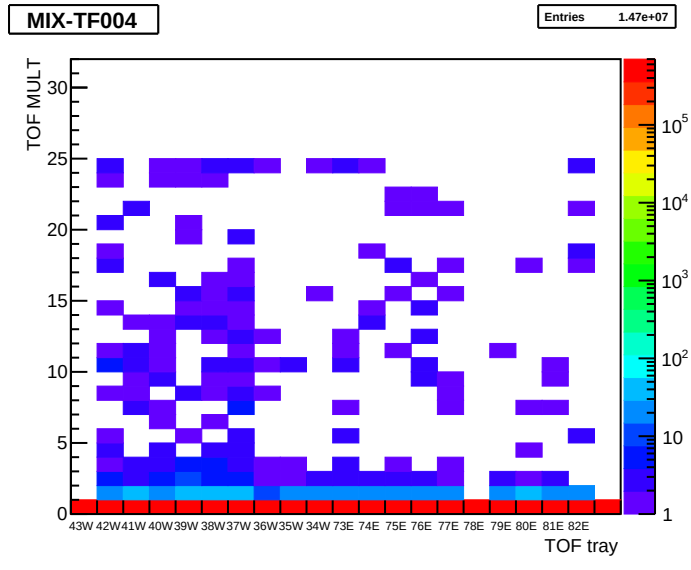

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

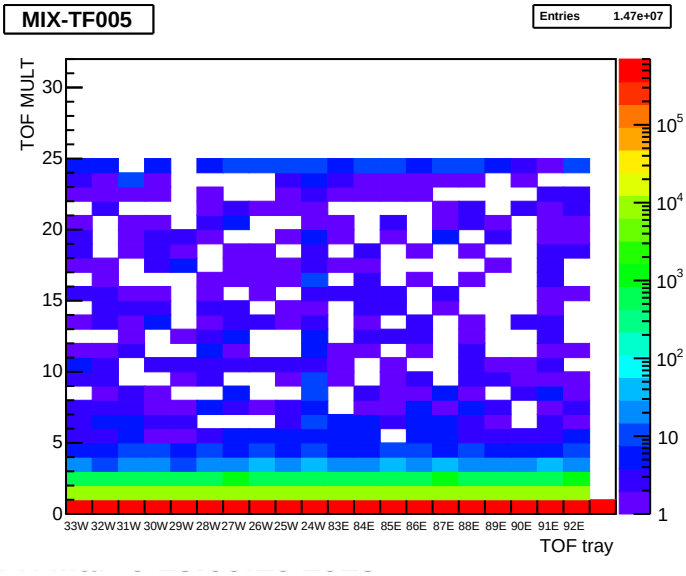

BunchId7Bit (UPC)
Entries 467925
0
20
40
60
80
100
120

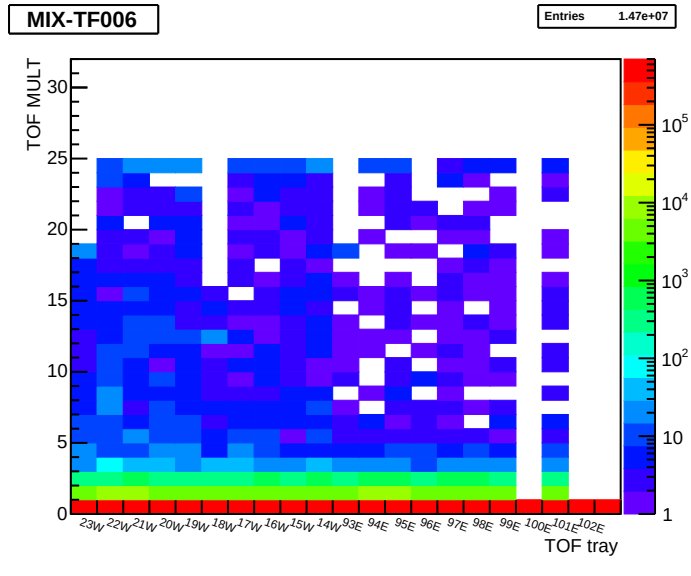

MIX-TF001
Entries 1.47e+07
TOF MULT
TOF tray

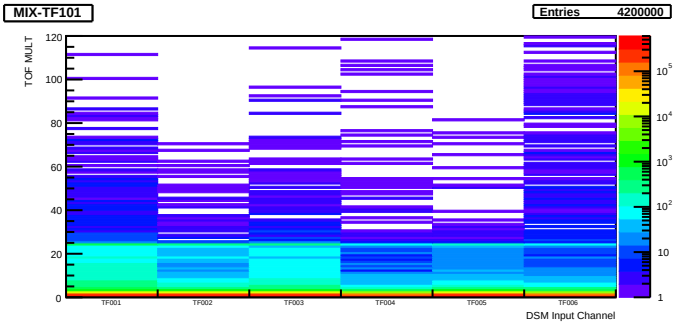

MIX-TF002
Entries 1.47e+07
TOF MULT
TOF tray

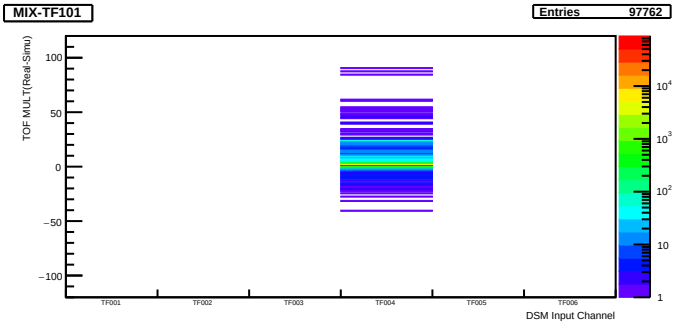

MIX-TF003
Entries 1.47e+07
TOF MULT
TOF tray

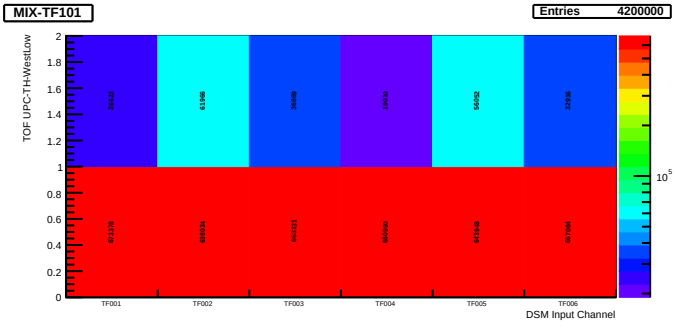

MIX-TF004
Entries 1.47e+07
TOF MULT
TOF tray

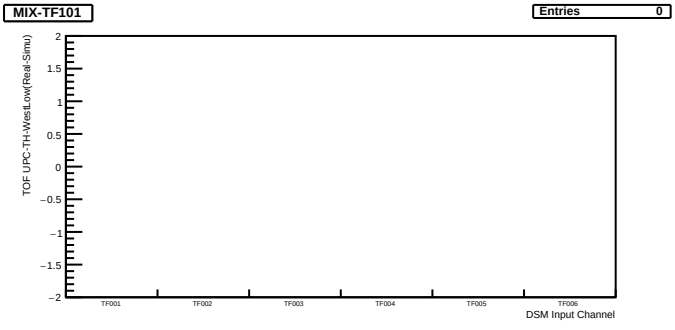

MIX-TF005
Entries 1.47e+07
TOF MULT
TOF tray

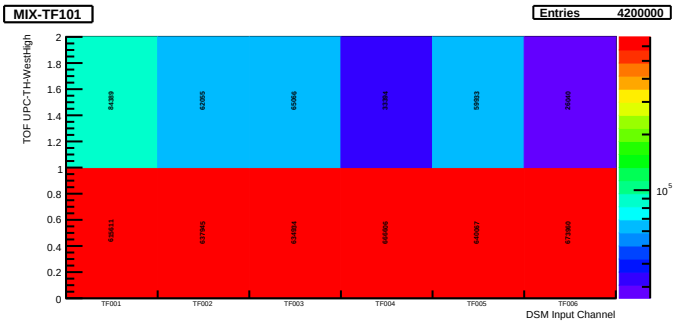

MIX-TF006
Entries 1.47e+07
TOF MULT
TOF tray

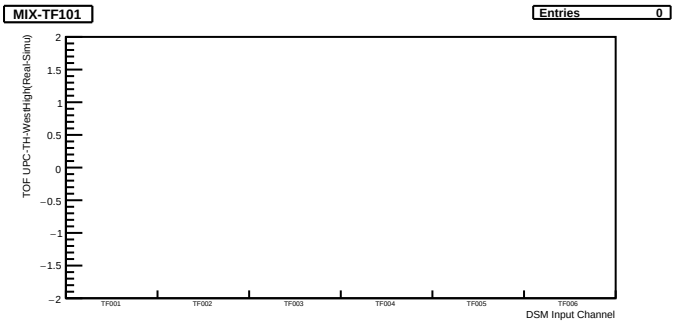

MIX-TF101
Entries 4200000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

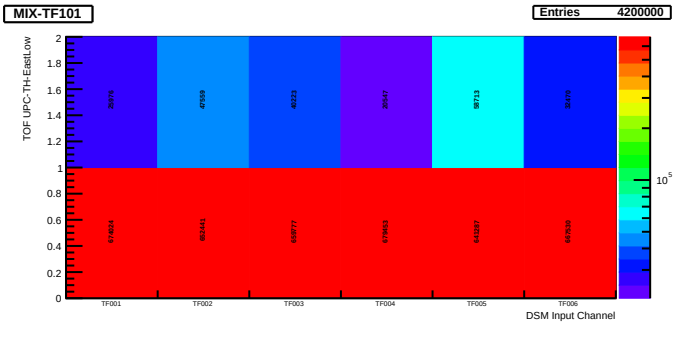

MIX-TF101
Entries 97762
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

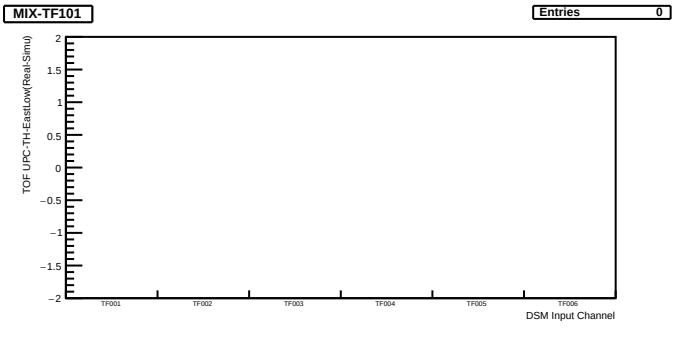

MIX-TF101
Entries 4200000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

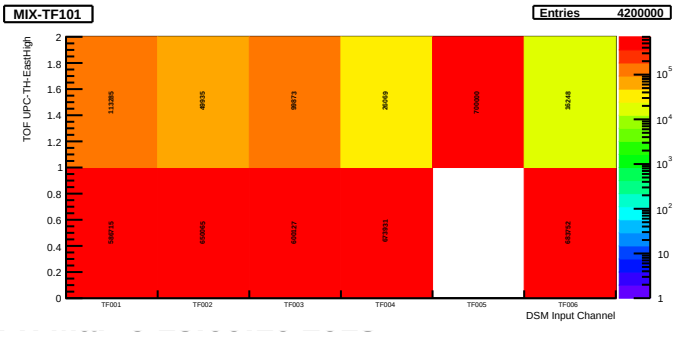

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

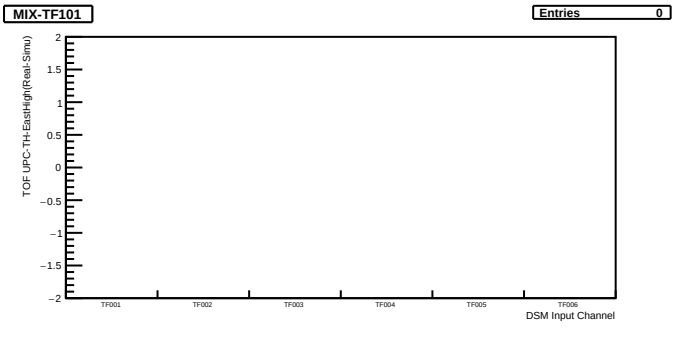

MIX-TF101
Entries 4200000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

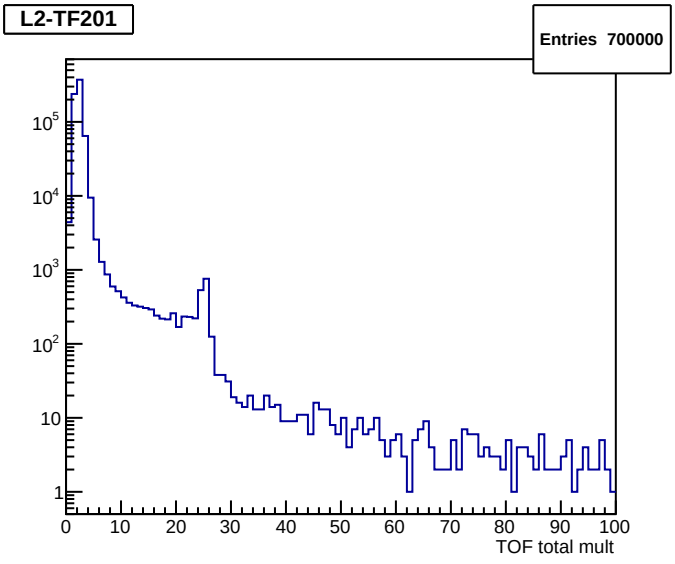

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

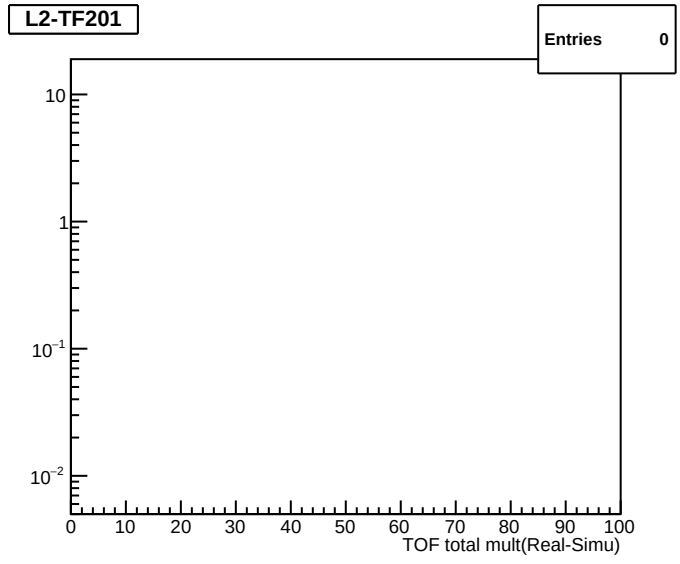

MIX-TF101
Entries 4200000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

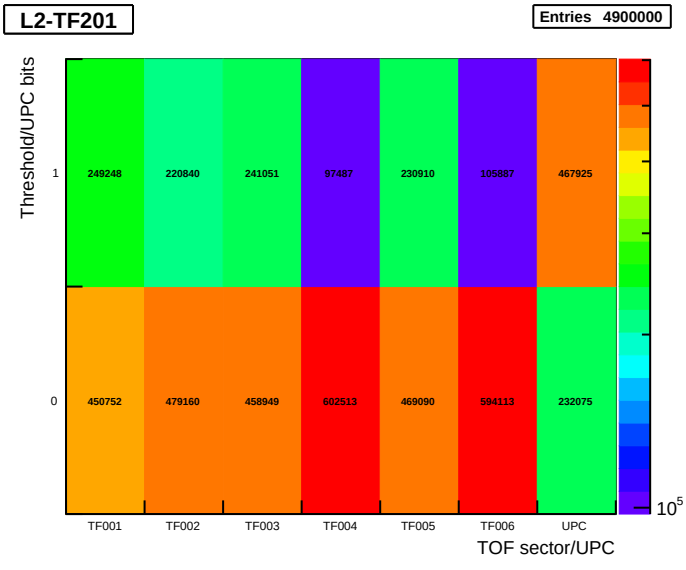

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

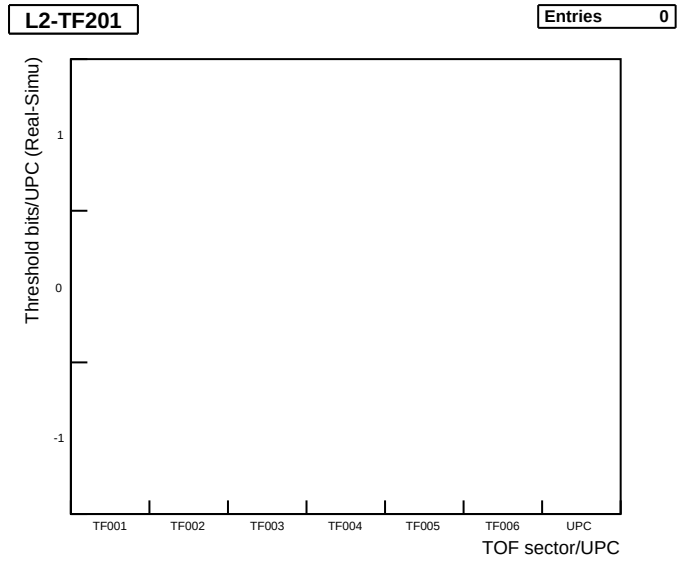

MIX-TF101
Entries 4200000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

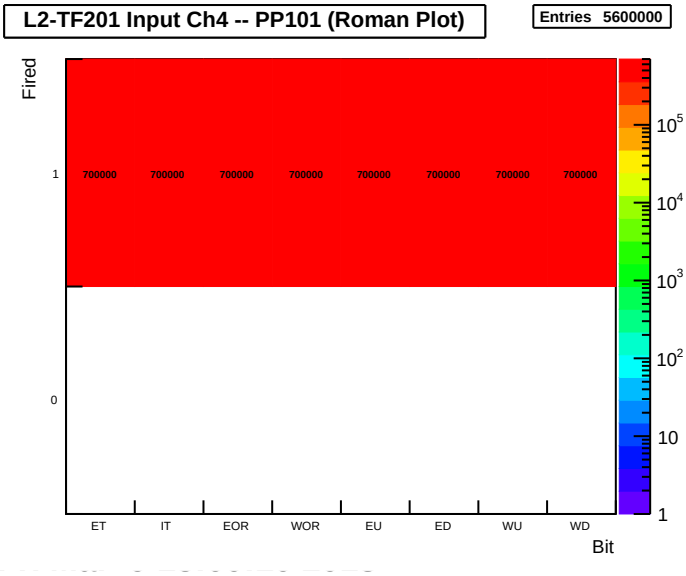

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

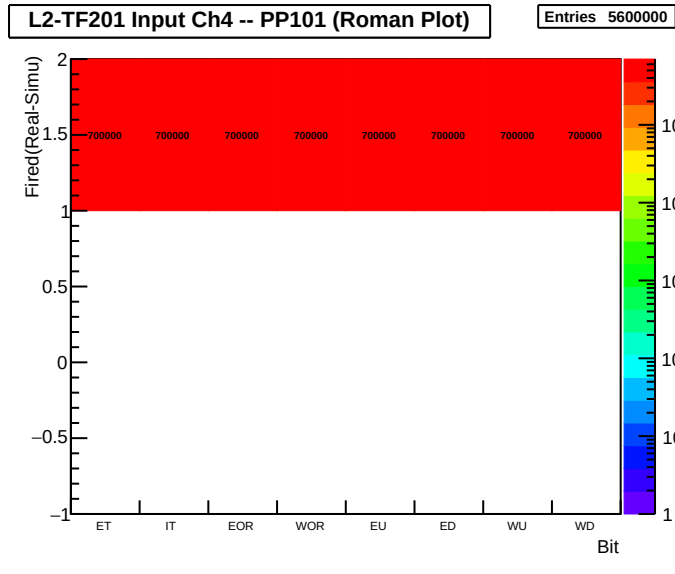

L2-TF201
Entries 700000
TOF total mult

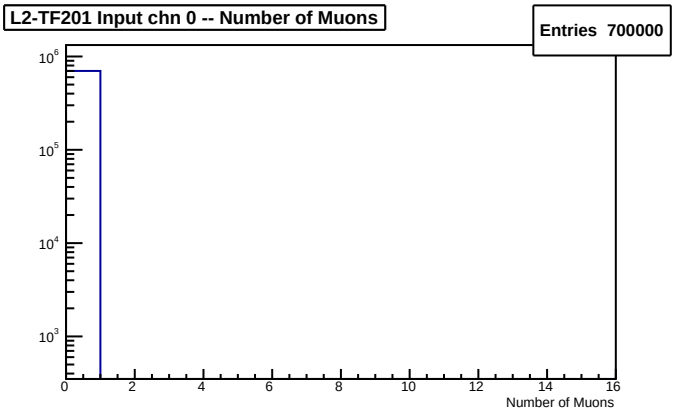

L2-TF201
Entries 0
TOF total mult(Real-Simu)

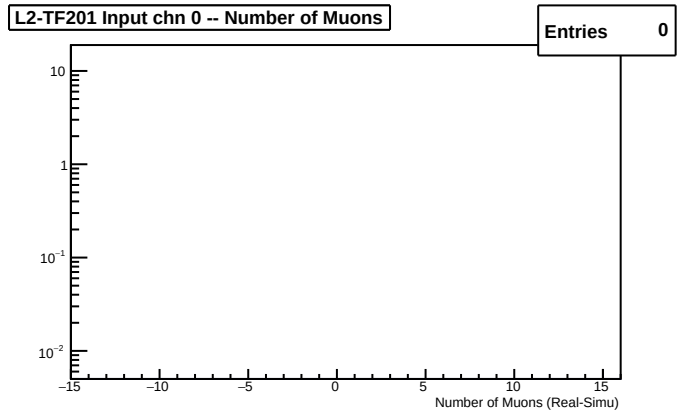

L2-TF201
Entries 4900000
Threshold/UPC bits
249248
220840
241051
97487
230910
105887
467925
450752
479160
458949
602513
469090
594113
232075
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

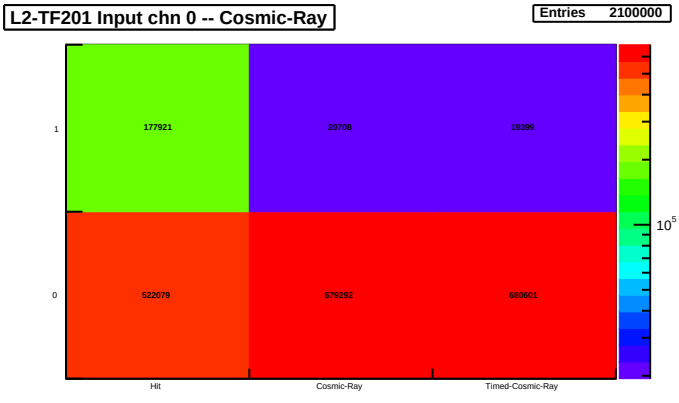

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

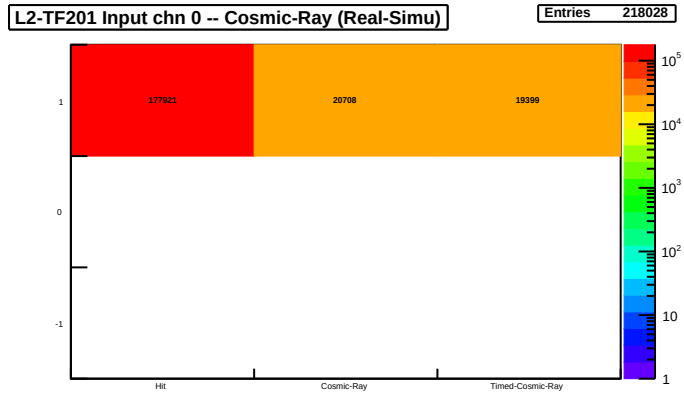

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 5600000
Fired
700000
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

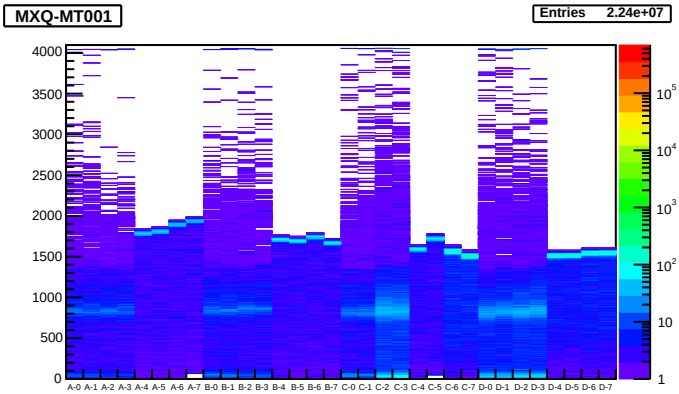

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 5600000
Fired(Real-Simu)
700000
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

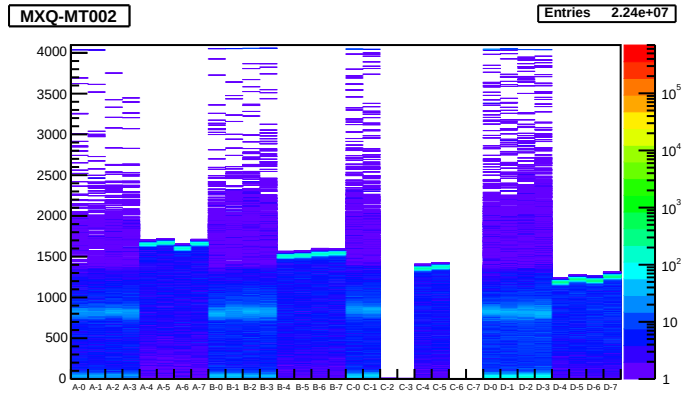

L2-TF201 Input chn 0 -- Number of Muons
Entries 700000
Number of Muons

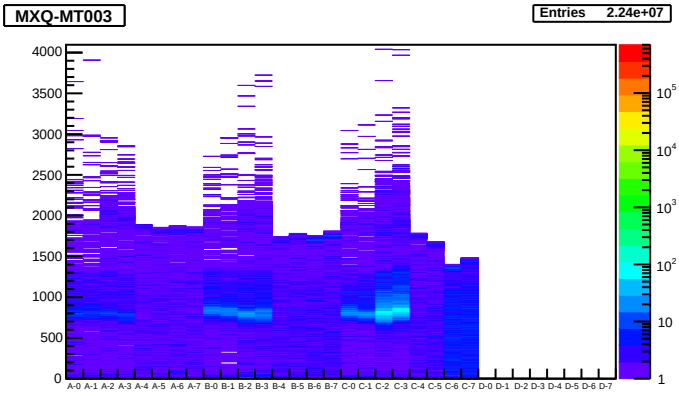

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

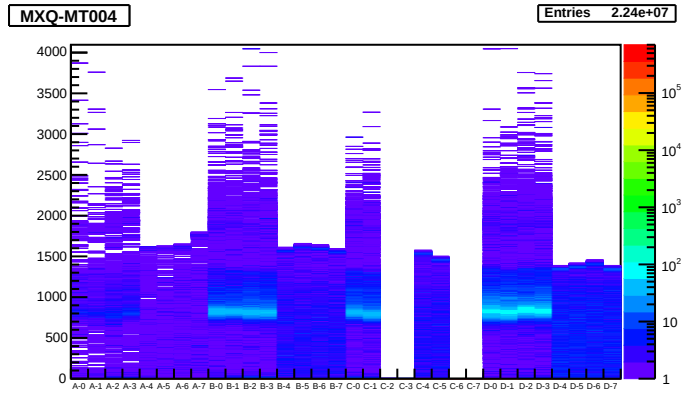

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 2100000
177921
20708
19399
522079
679292
680601
Hit
Cosmic-Ray
Timed-Cosmic-Ray

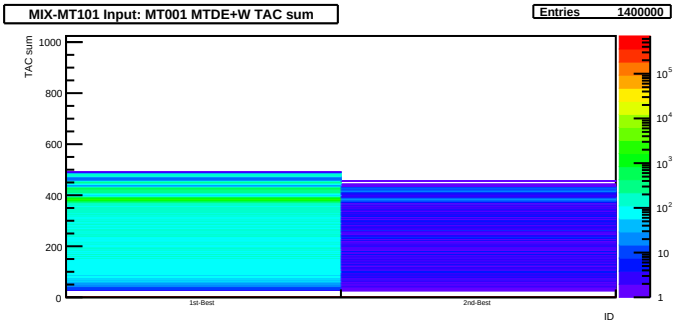

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 218028
177921
20708
19399
Hit
Cosmic-Ray
Timed-Cosmic-Ray

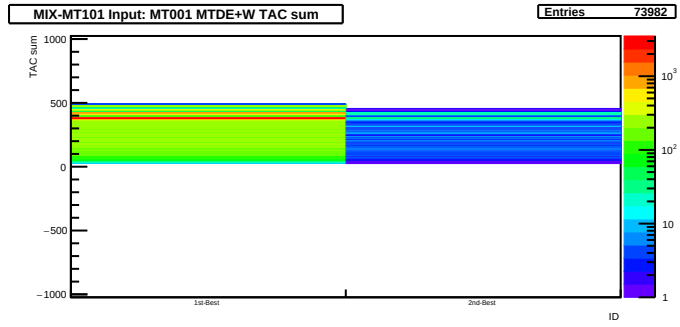

MXQ-MT001
Entries 2.24e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

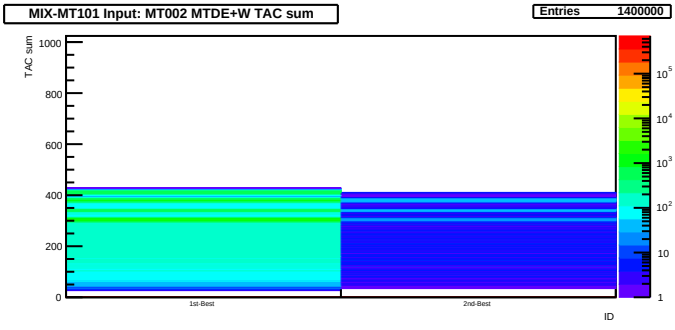

MXQ-MT002
Entries 2.24e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

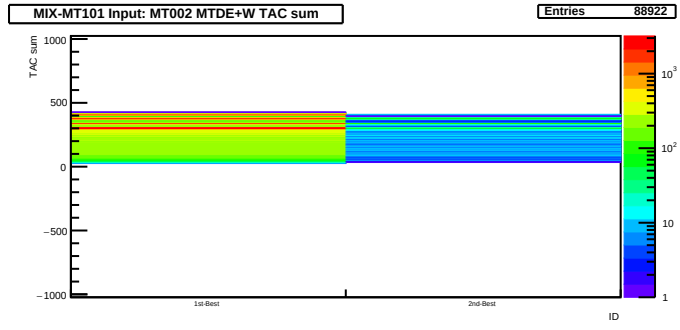

MXQ-MT003
Entries 2.24e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

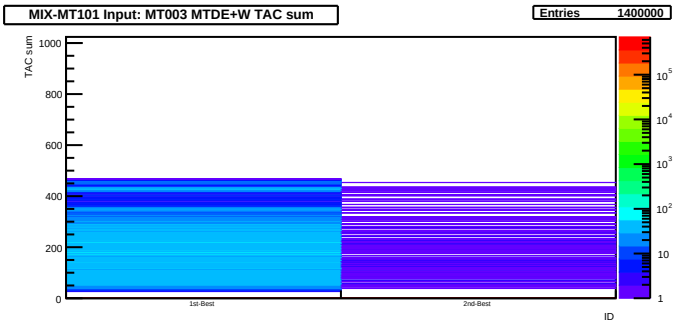

MXQ-MT004
Entries 2.24e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

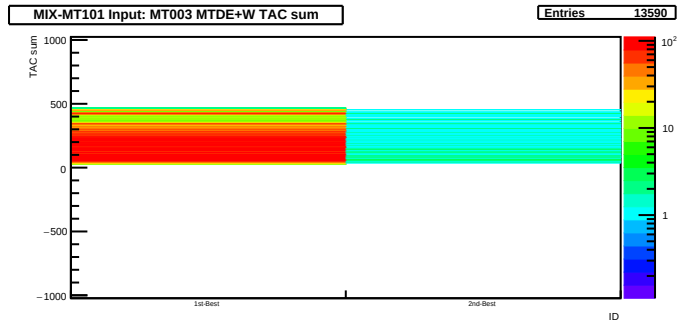

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 1400000
TAC sum
1st-Best
2nd-Best
ID

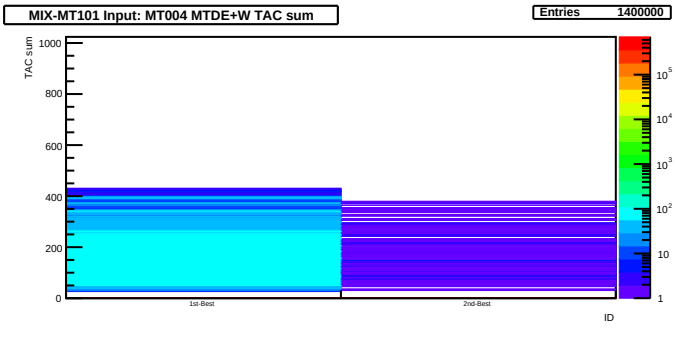

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 73982
TAC sum
1st-Best
2nd-Best
ID

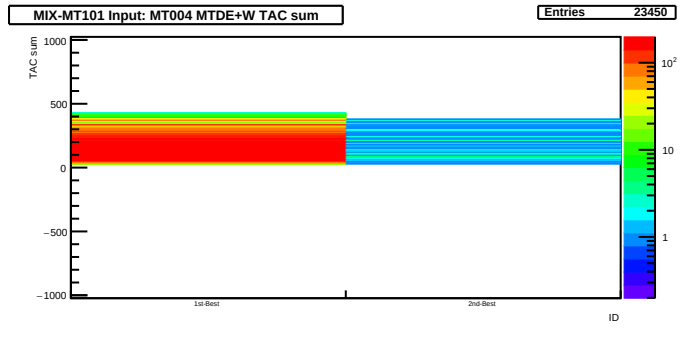

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 1400000
TAC sum
1st-Best
2nd-Best
ID

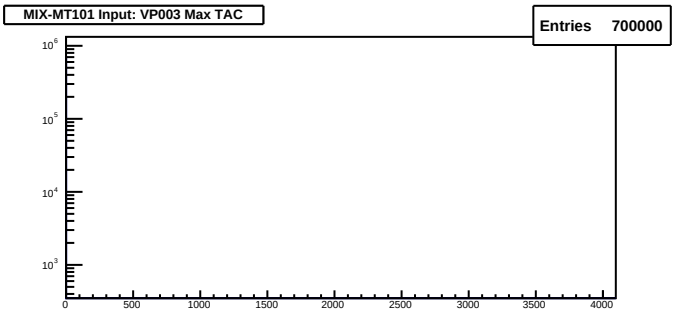

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 88922
TAC sum
1st-Best
2nd-Best
ID

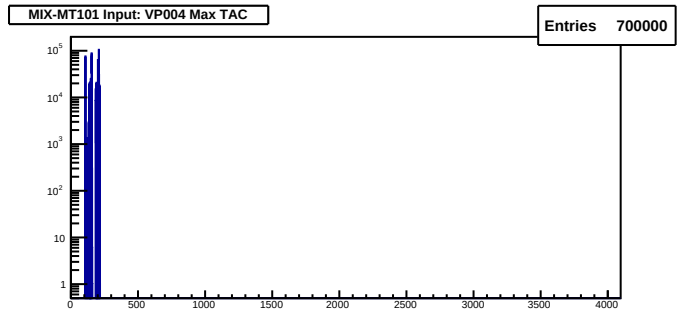

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 1400000
TAC sum
1st-Best
2nd-Best
ID

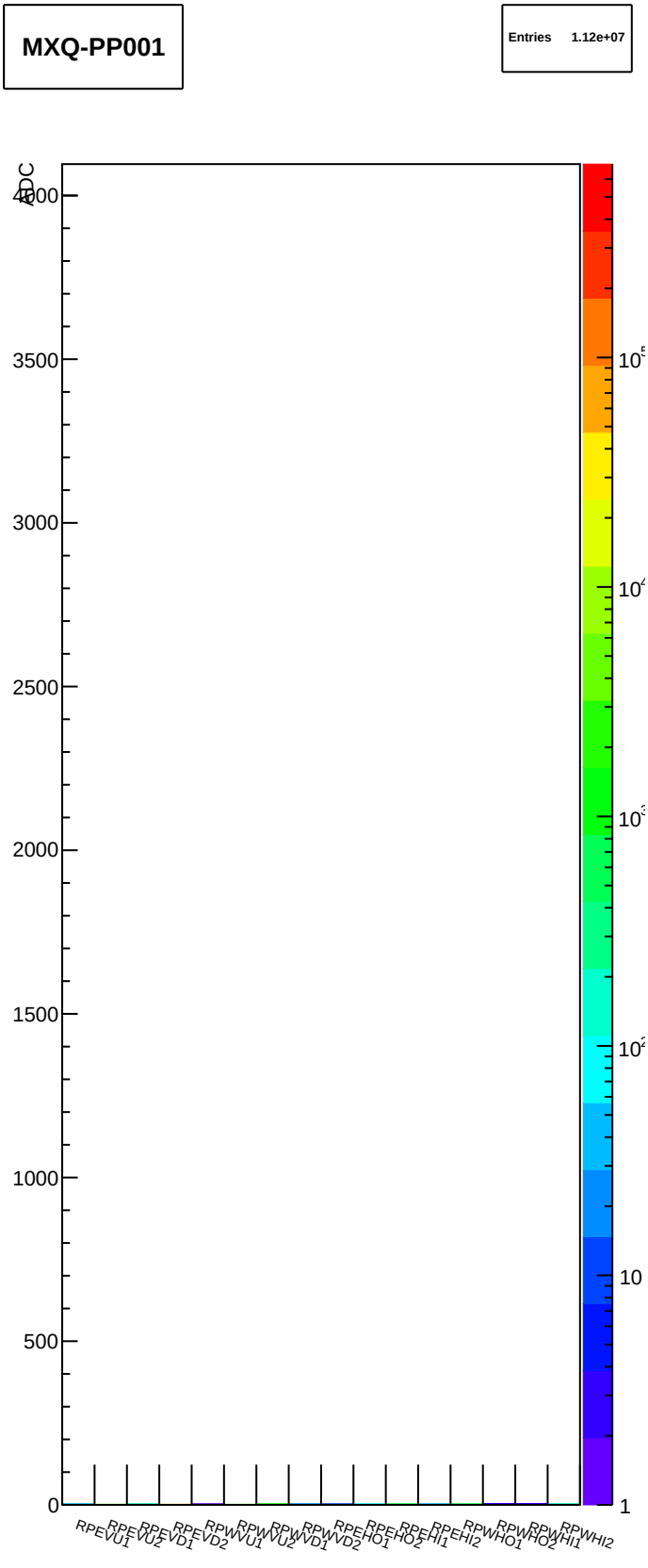

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 13590
TAC sum
1st-Best
2nd-Best
ID

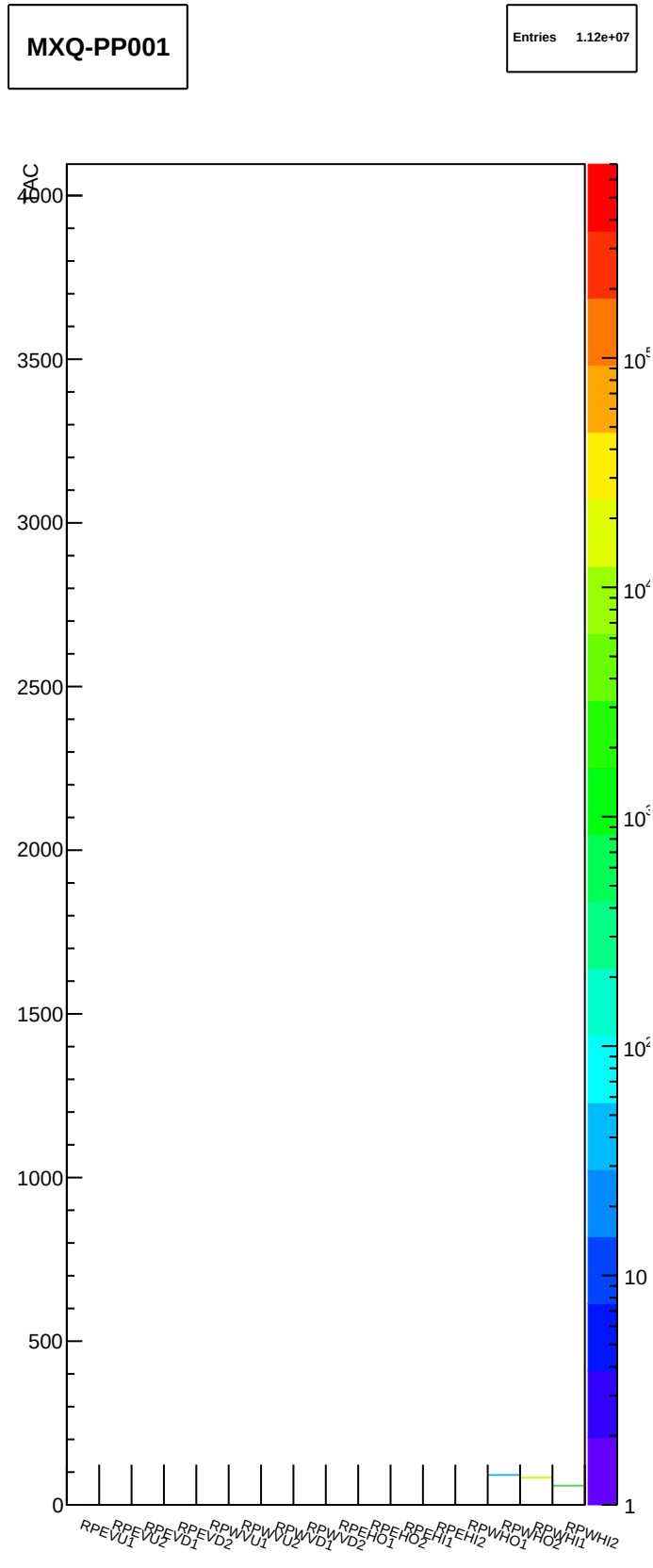

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 1400000
TAC sum
1st-Best
2nd-Best
ID

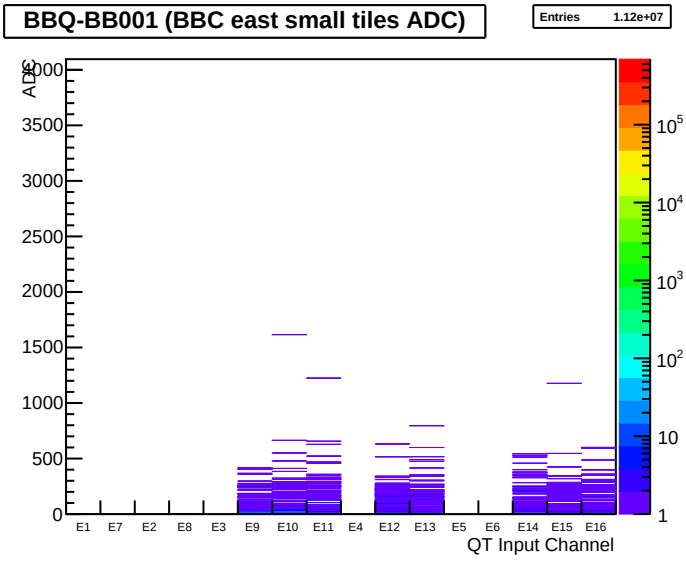

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 23450
TAC sum
1st-Best
2nd-Best
ID

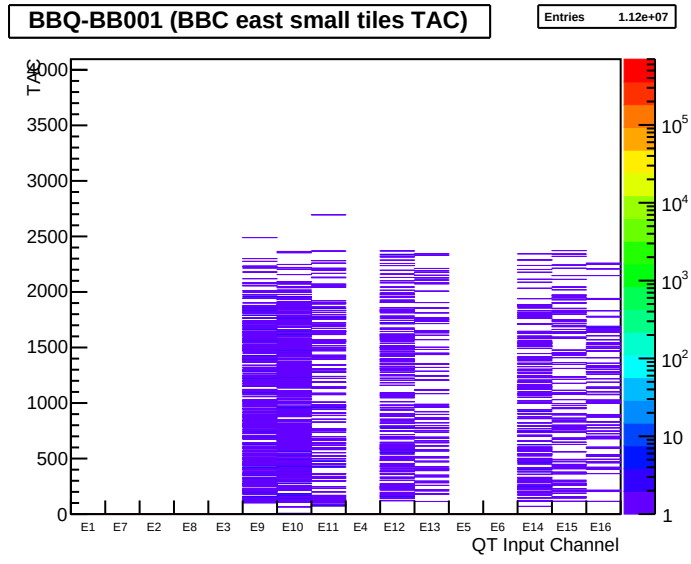

MIX-MT101 Input: VP003 Max TAC
Entries 700000

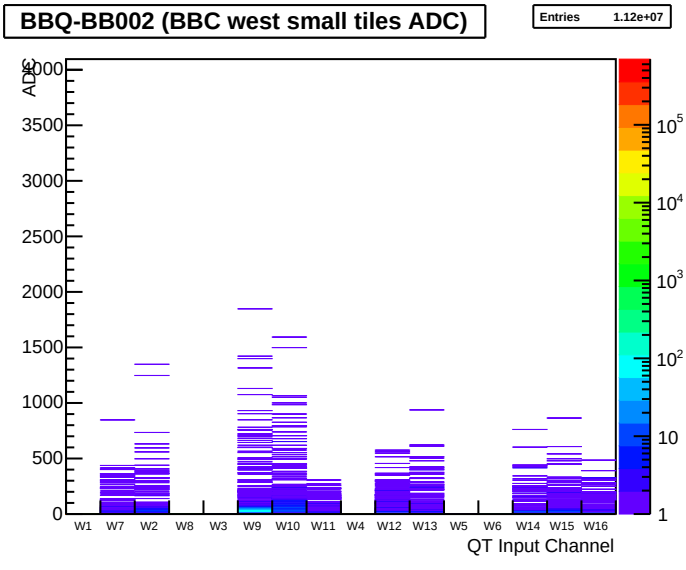

MIX-MT101 Input: VP004 Max TAC
Entries 700000

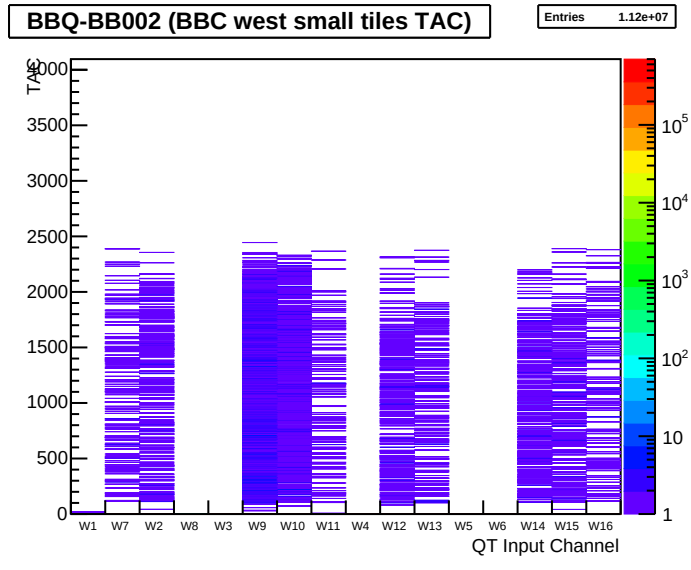

MXQ-PP001
Entries 1.12e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

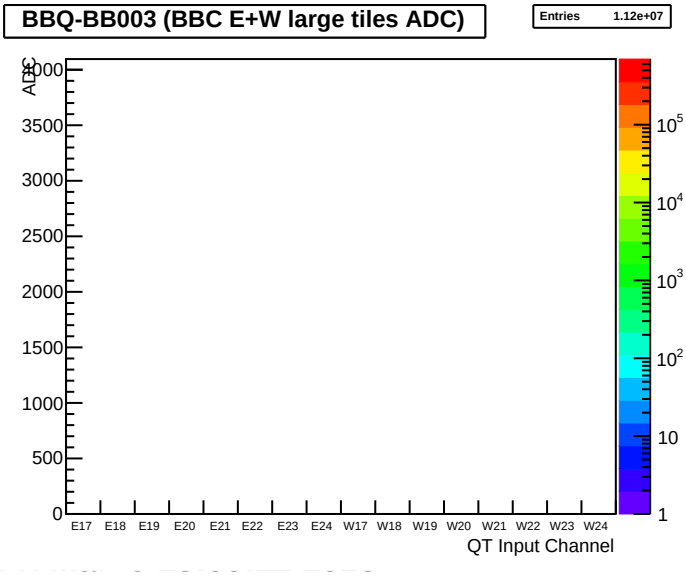

MXQ-PP001
Entries 1.12e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

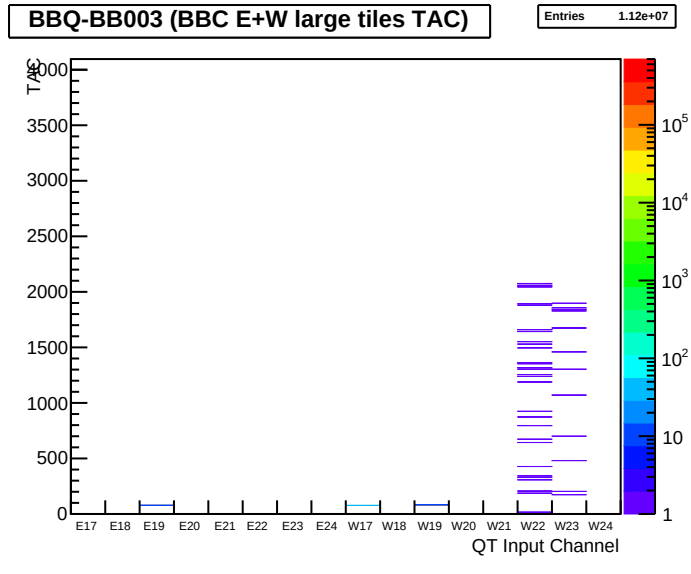

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.12e+07
ADC
QT Input Channel

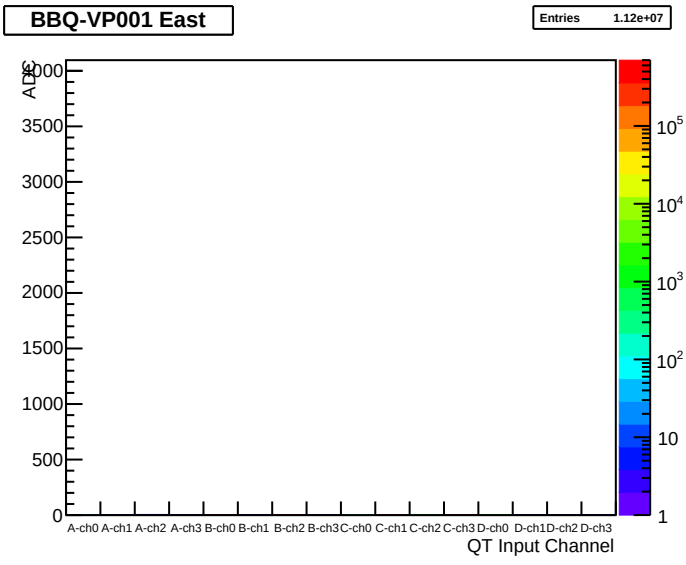

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.12e+07
TAC
QT Input Channel

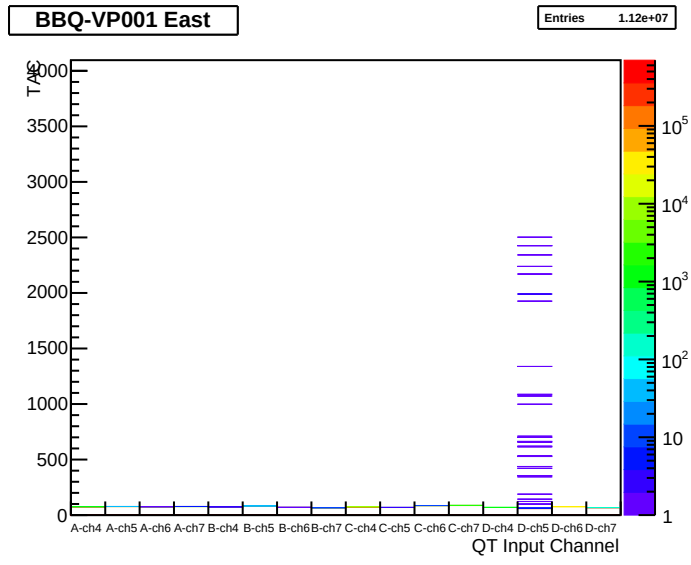

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.12e+07
ADC
QT Input Channel

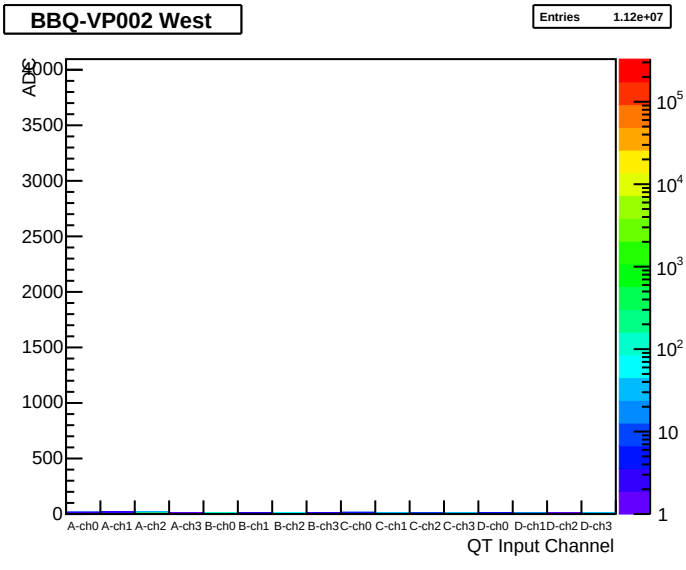

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.12e+07
TAC
QT Input Channel

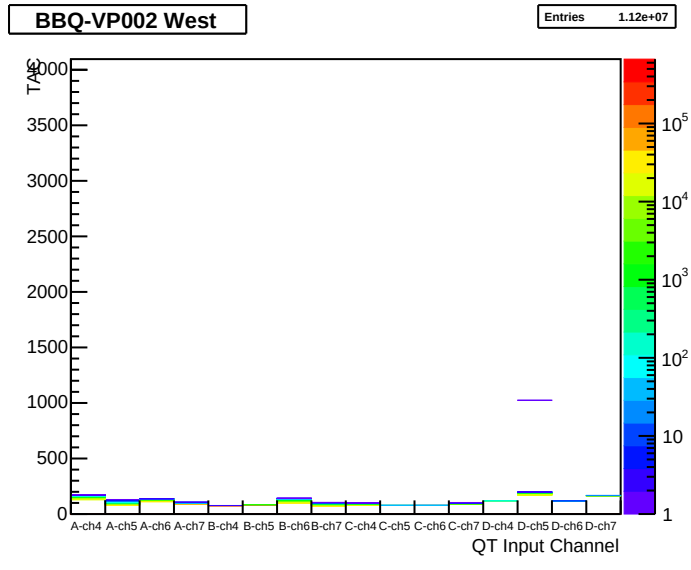

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.12e+07
ADC
QT Input Channel

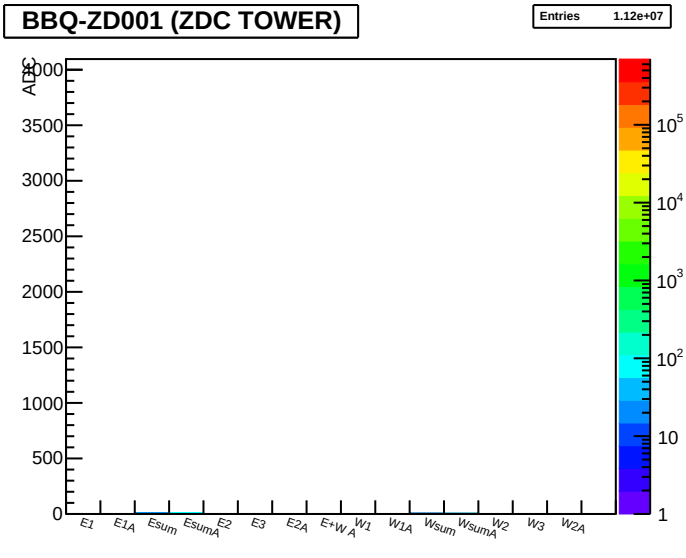

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.12e+07
TAC
QT Input Channel

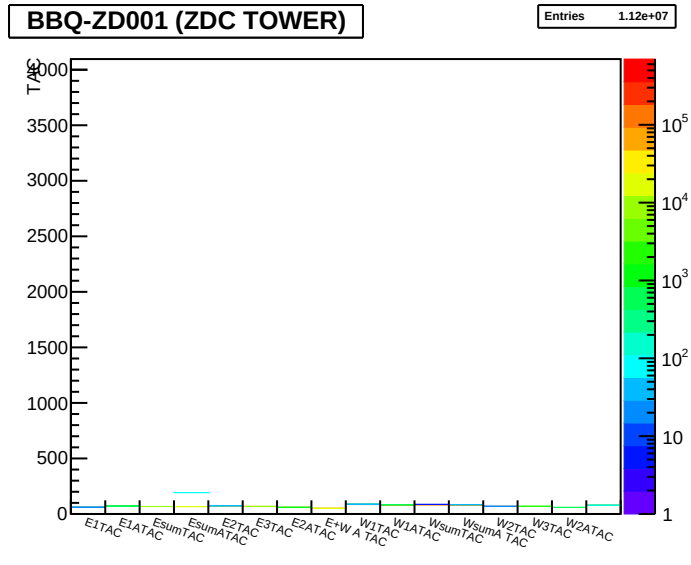

BBQ-VP001 East
Entries 1.12e+07
ADC
QT Input Channel

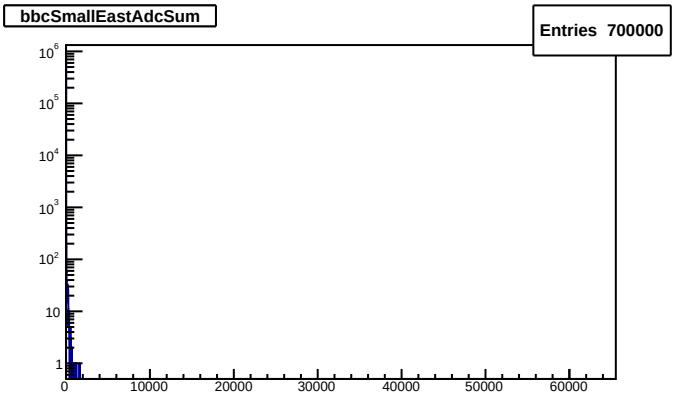

BBQ-VP001 East
Entries 1.12e+07
TAC
QT Input Channel

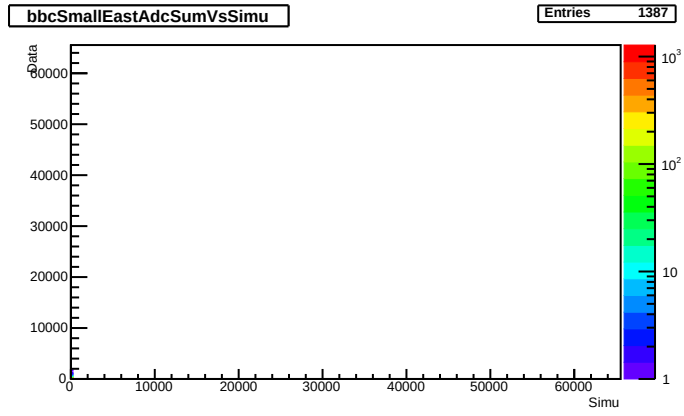

BBQ-VP002 West
Entries 1.12e+07
ADC
QT Input Channel

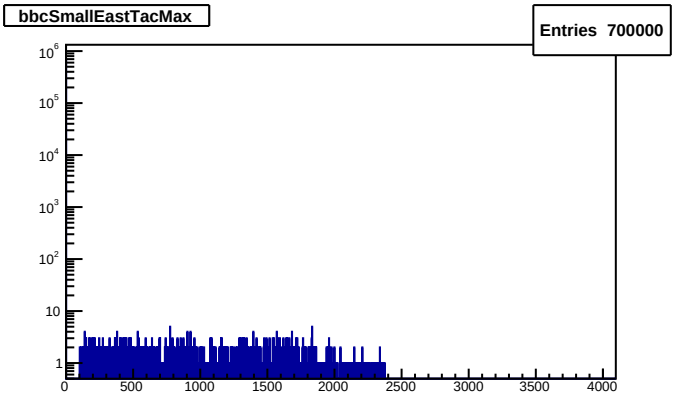

BBQ-VP002 West
Entries 1.12e+07
TAC
QT Input Channel

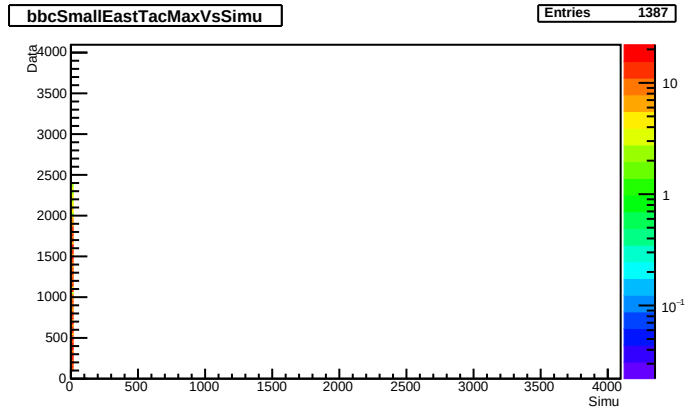

BBQ-ZD001 (ZDC TOWER)
Entries 1.12e+07
ADC

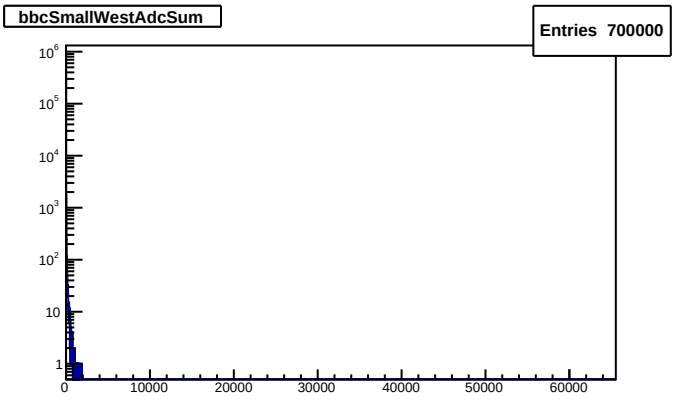

BBQ-ZD001 (ZDC TOWER)
Entries 1.12e+07
TAC

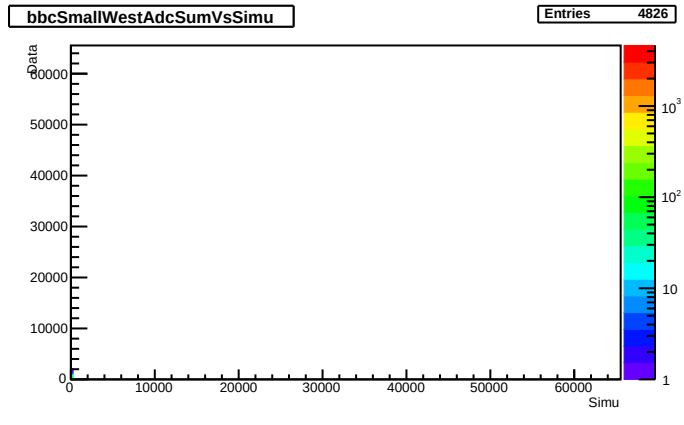

bbcSmallEastAdcSum
Entries 700000

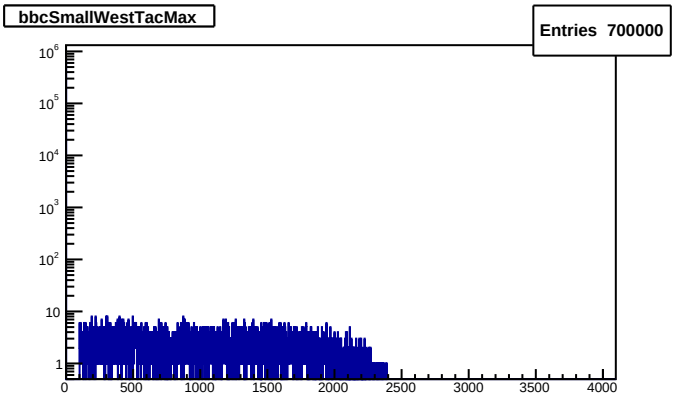

bbcSmallEastAdcSumVsSimu
Entries 1387
Data
Simu

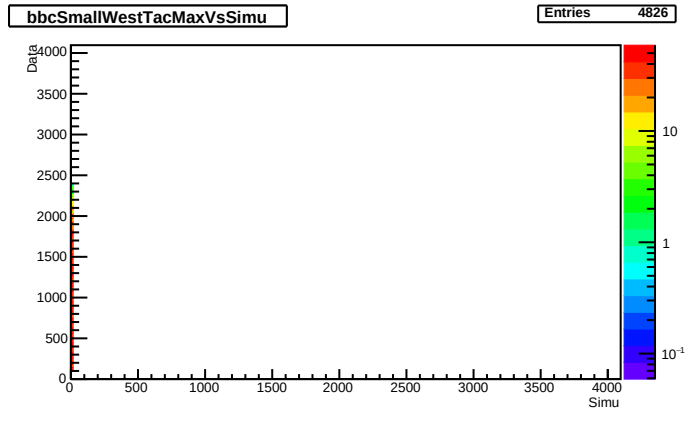

bbcSmallEastTacMax
Entries 700000

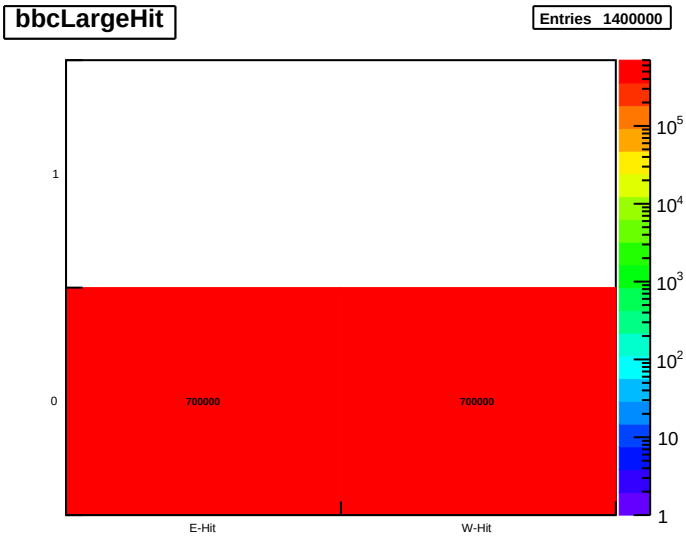

bbcSmallEastTacMaxVsSimu
Entries 1387
Data
Simu

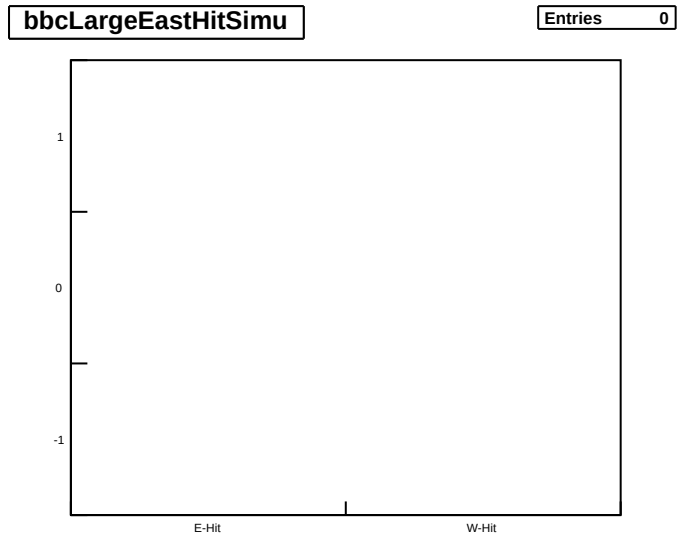

bbcSmallWestAdcSum
Entries 700000

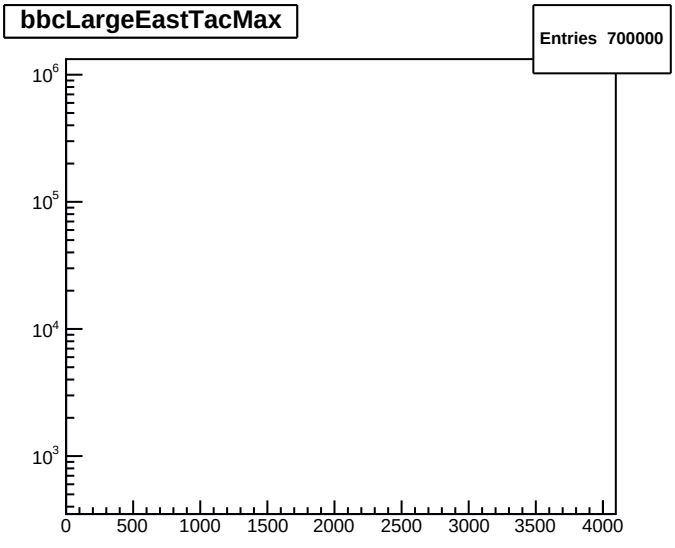

bbcSmallWestAdcSumVsSimu
Entries 4826
Data
Simu

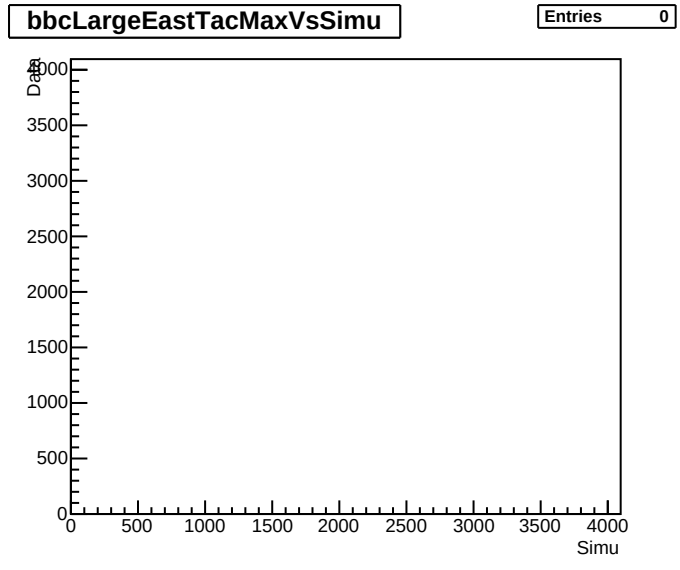

bbcSmallWestTacMax
Entries 700000

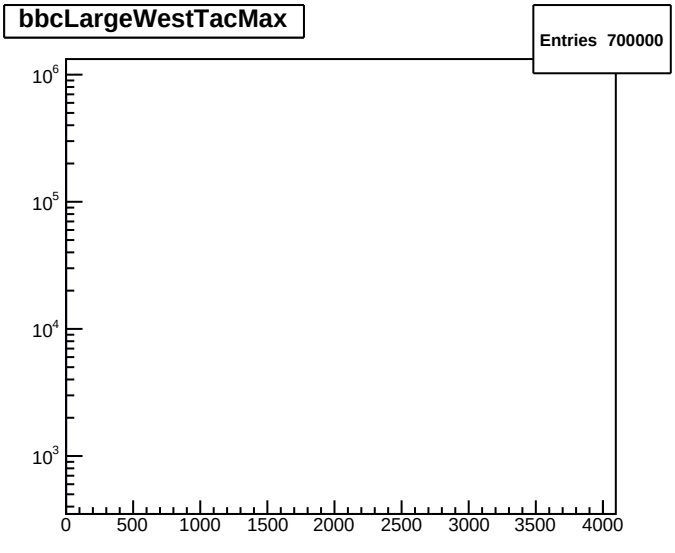

bbcSmallWestTacMaxVsSimu
Entries 4826
Data
Simu

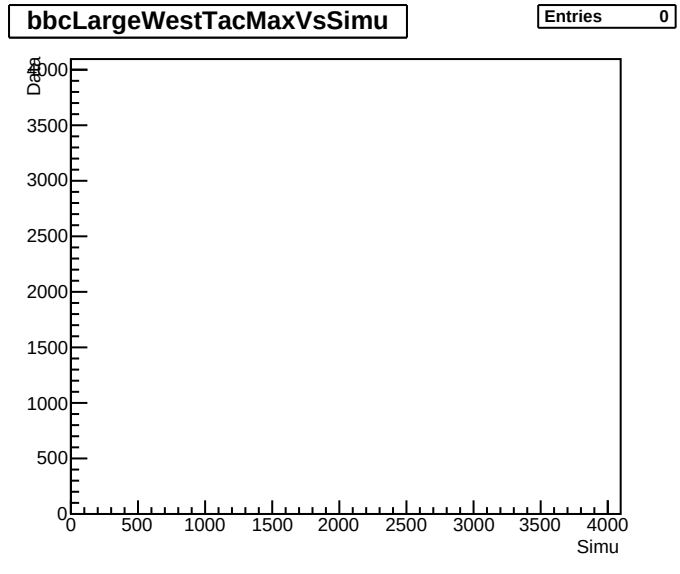

bbcLargeHit
Entries 1400000
700000
700000
E-Hit
W-Hit

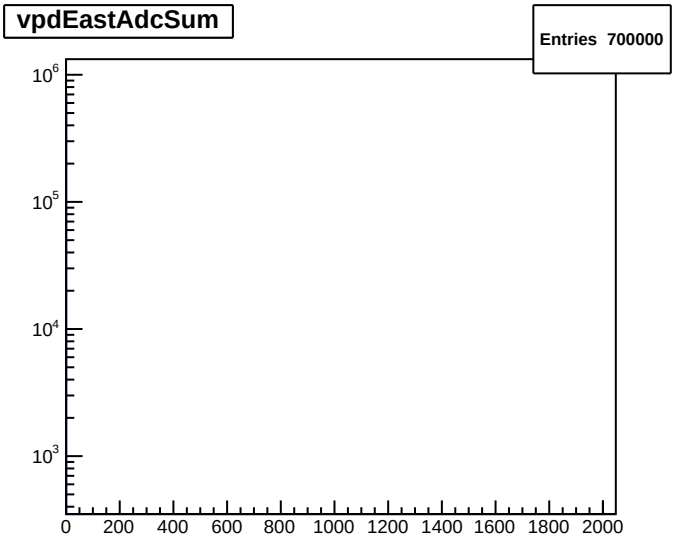

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

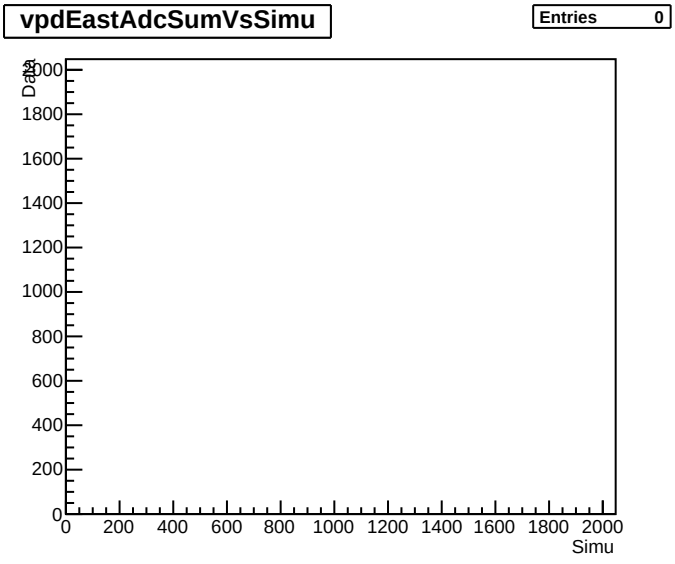

bbcLargeEastTacMax
Entries 700000

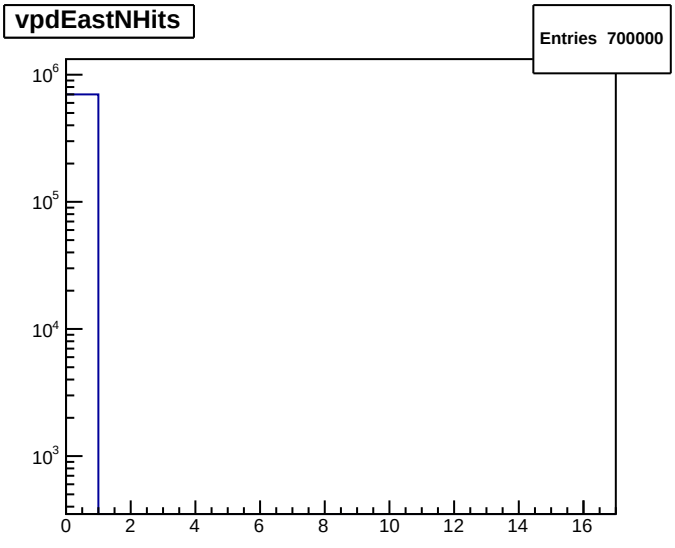

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

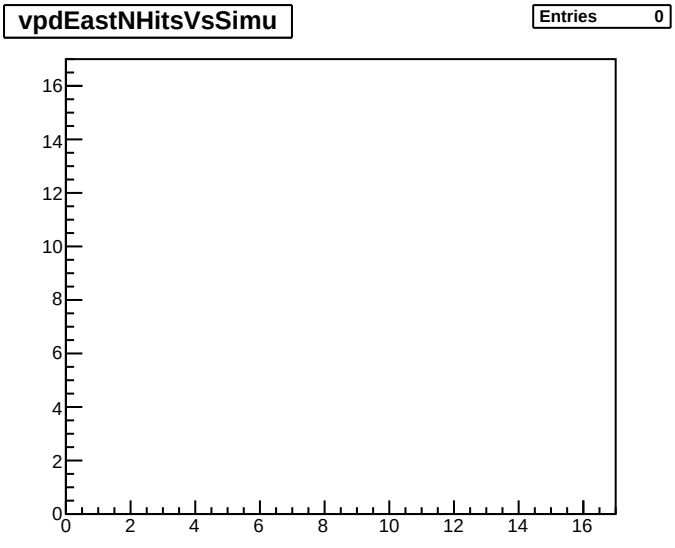

bbcLargeWestTacMax
Entries 700000

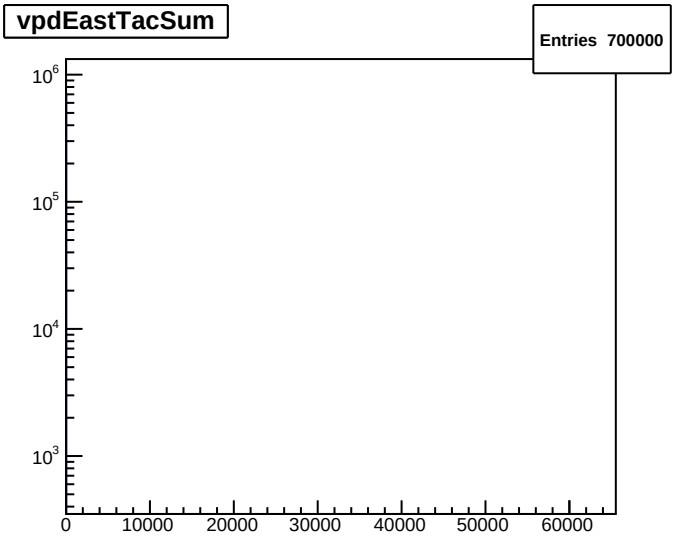

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

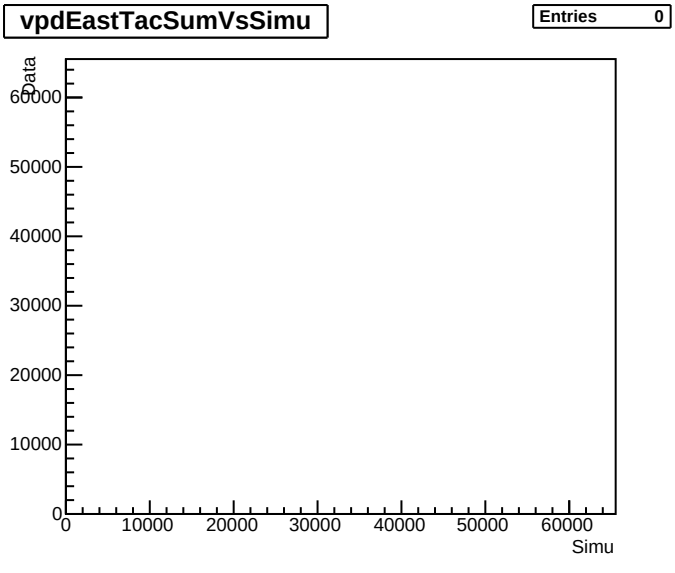

vpdEastAdcSum
Entries 700000

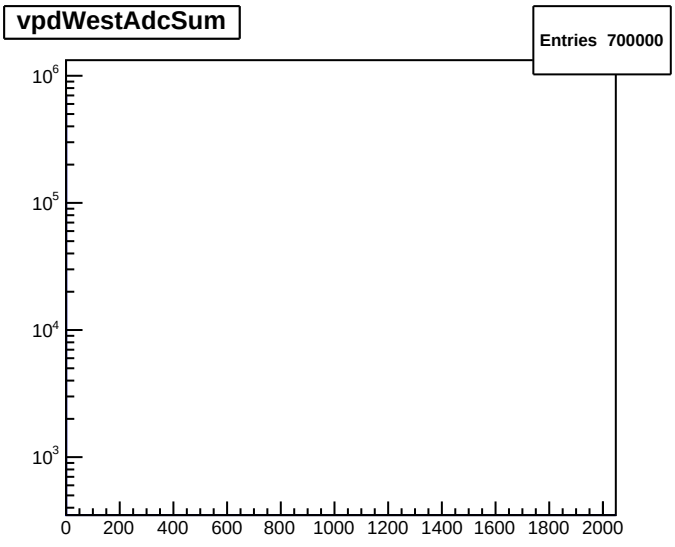

vpdEastAdcSumVsSimu
Entries 0
Data
Simu

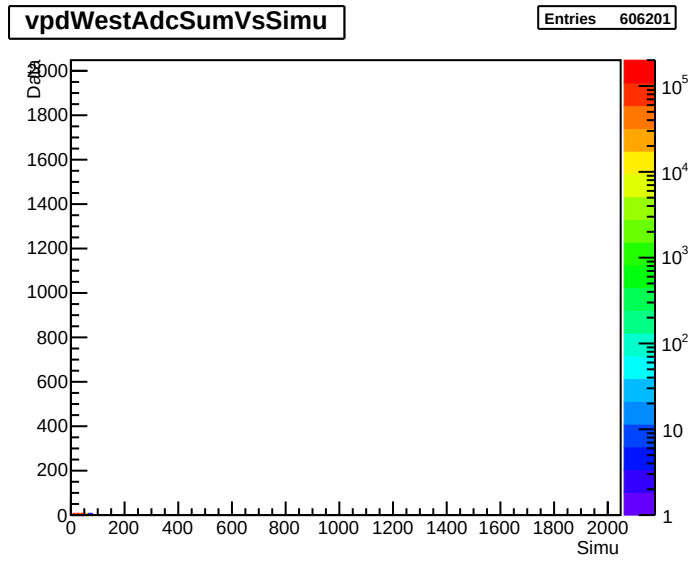

vpdEastNHits
Entries 700000

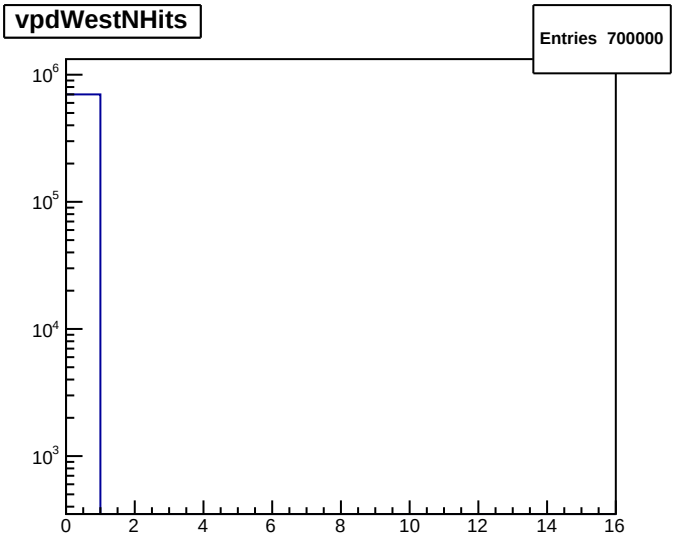

vpdEastNHitsVsSimu
Entries 0

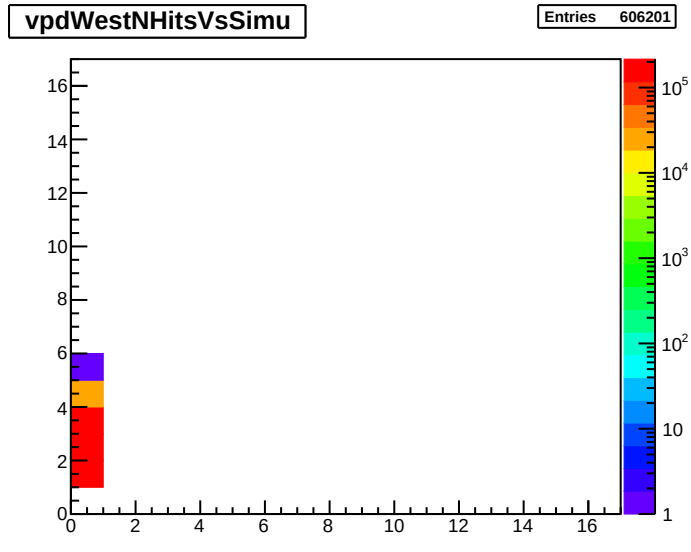

vpdEastTacSum
Entries 700000

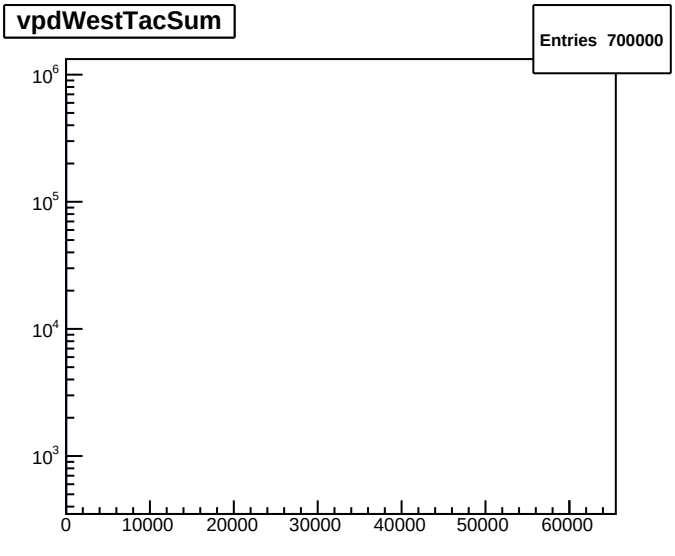

vpdEastTacSumVsSimu
Entries 0
Data
Simu

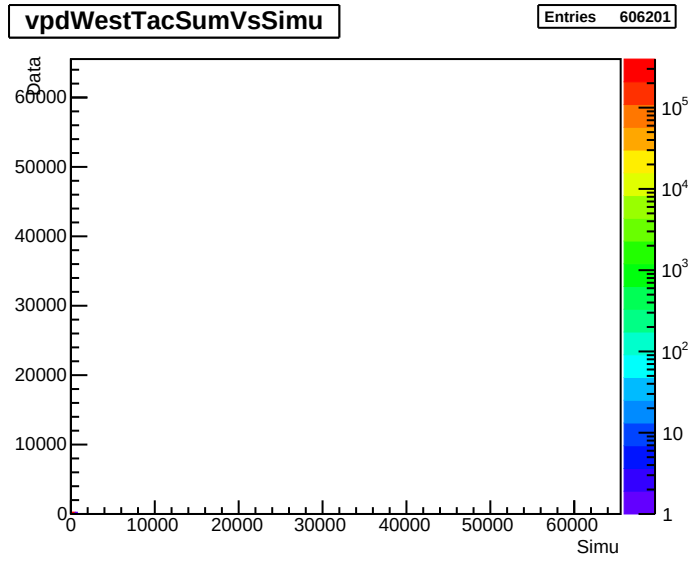

vpdWestAdcSum
Entries 700000

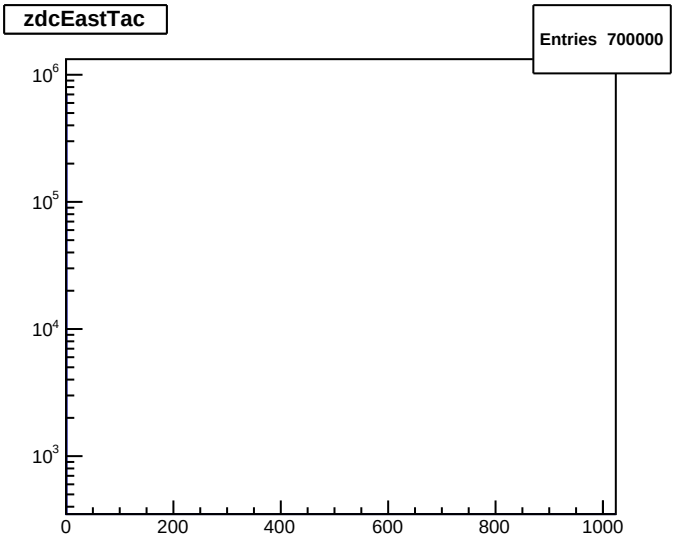

vpdWestAdcSumVsSimu
Entries 606201
Data
Simu

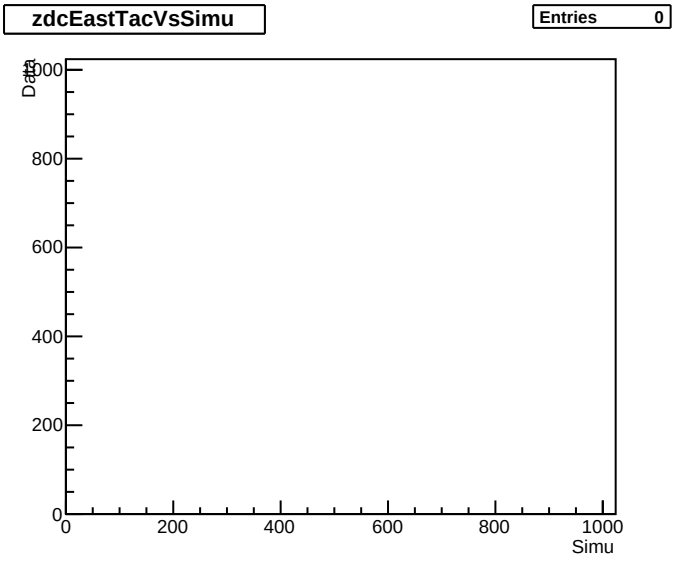

vpdWestNHits
Entries 700000

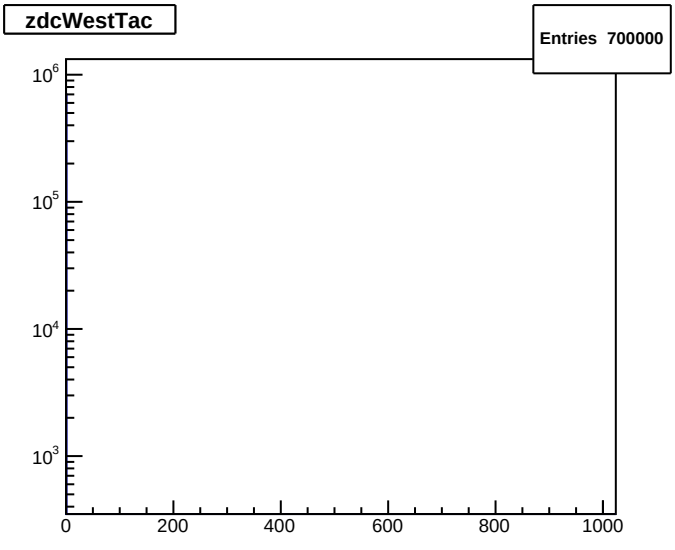

vpdWestNHitsVsSimu
Entries 606201

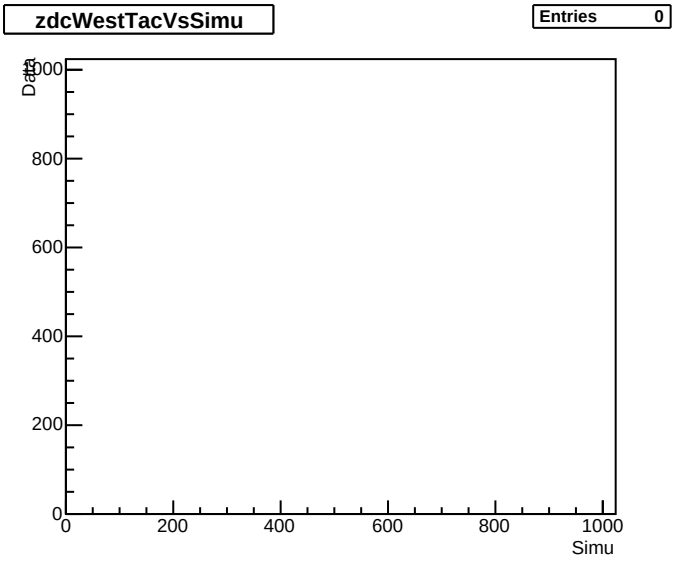

vpdWestTacSum
Entries 700000

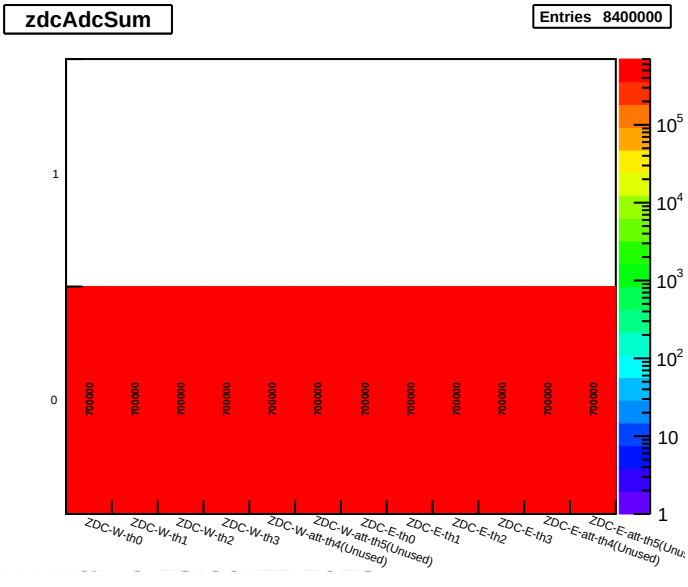

vpdWestTacSumVsSimu
Entries 606201
Data
Simu

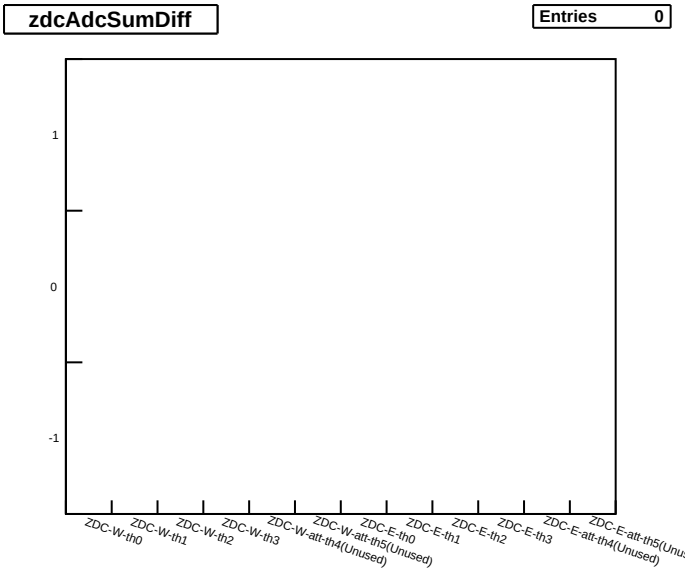

zdcEastTac
Entries 700000
10^6
10^5
10^4
10^3
0
200
400
600
800
1000

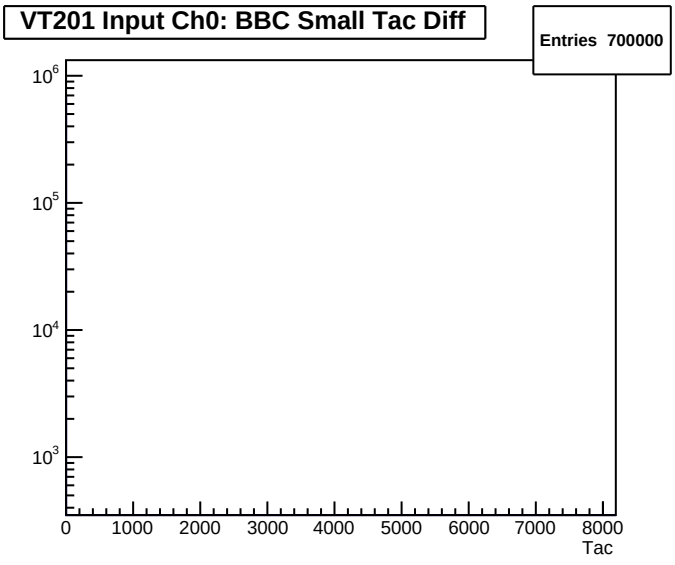

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

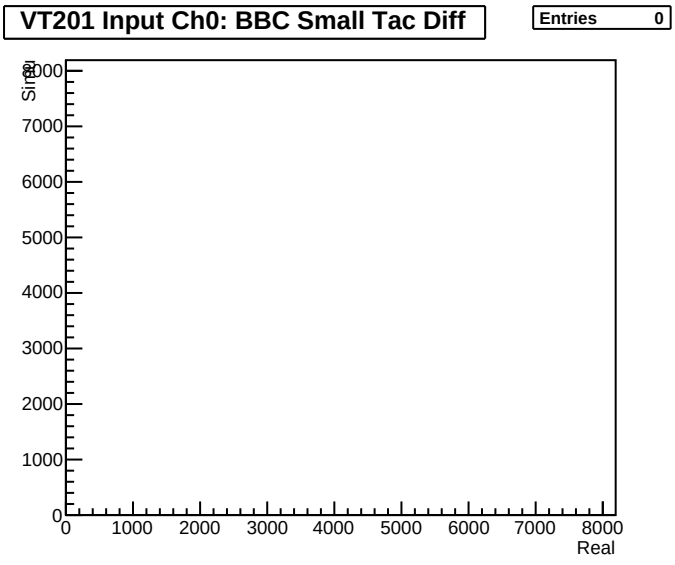

zdcWestTac
Entries 700000
10^6
10^5
10^4
10^3
0
200
400
600
800
1000

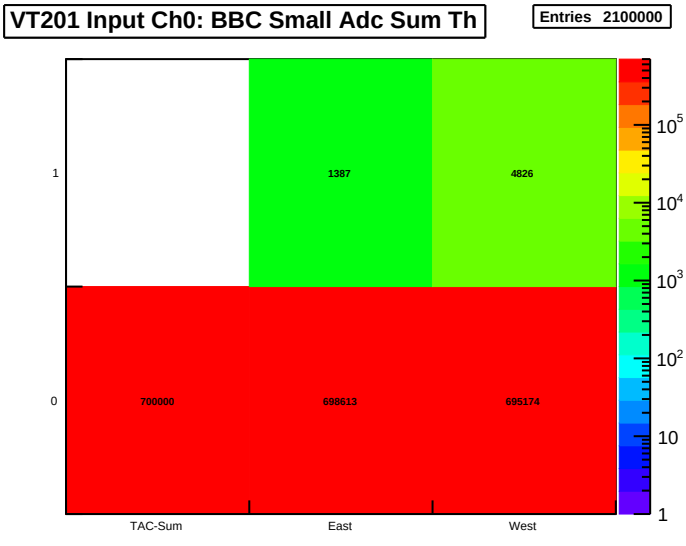

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

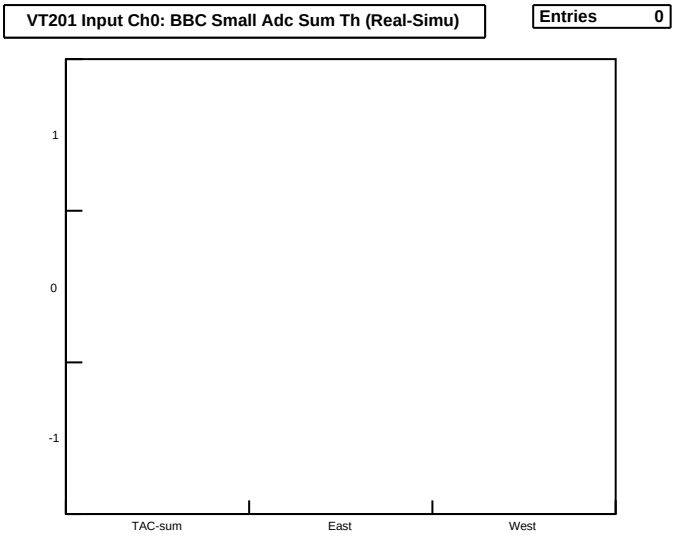

zdcAdcSum
Entries 8400000
1
0
700000
10^5
10^4
10^3
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

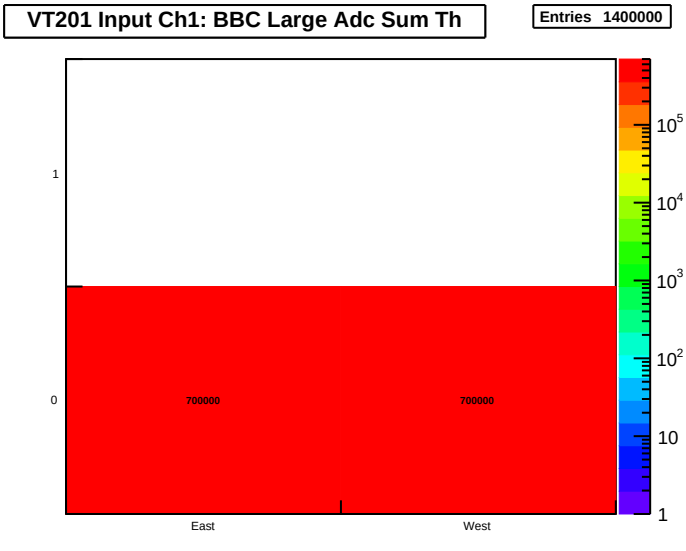

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

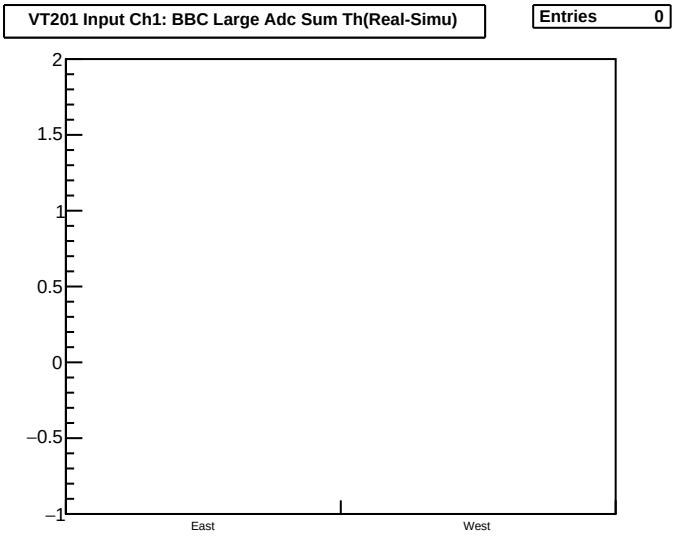

VT201 Input Ch0: BBC Small Tac Diff
Entries 700000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

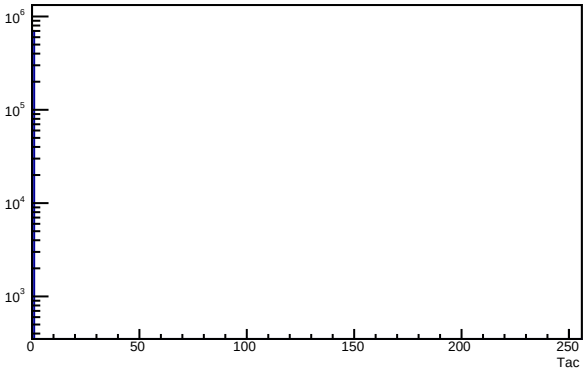

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 2100000
1387
4826
700000
698613
695174
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

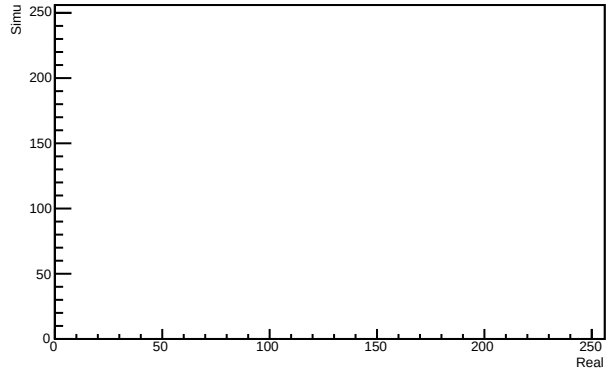

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 1400000
700000
700000
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

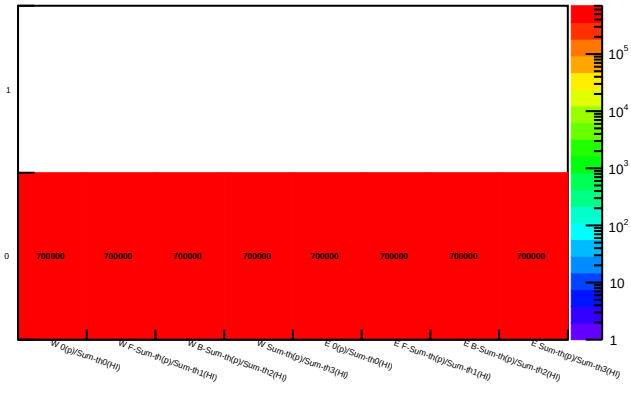
VT201 Input Ch2: ZDC Tac Diff



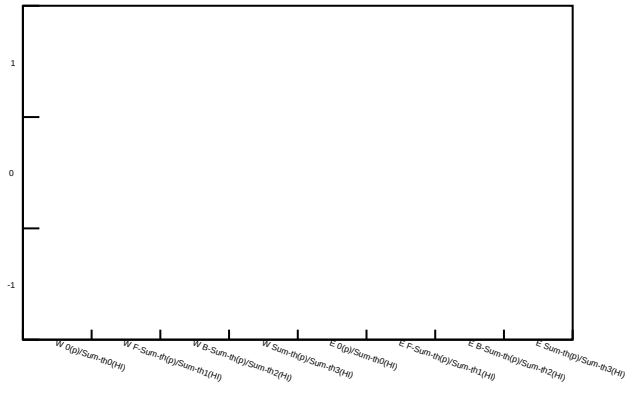
VT201 Input Ch2: ZDC Tac Diff



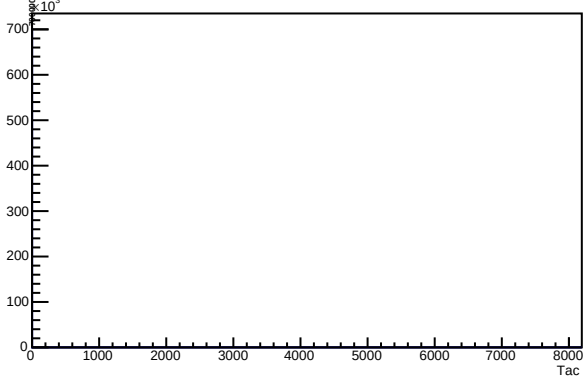
VT201 Input Ch2: ZDC Adc Sum Th



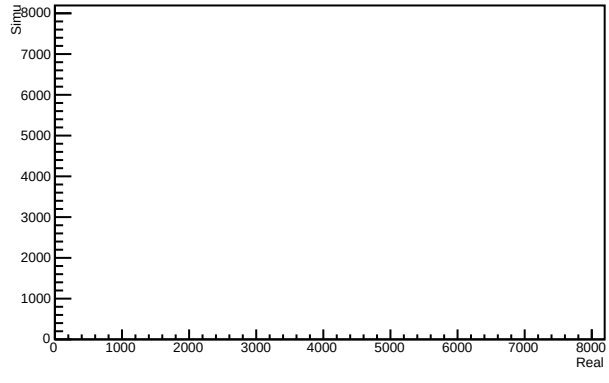
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



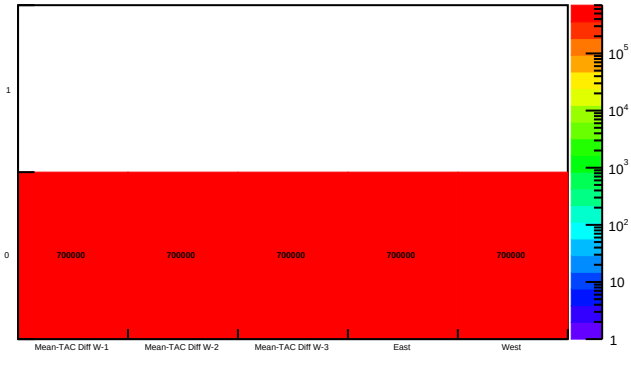
VT201 Input Ch4: VPD Tac Diff



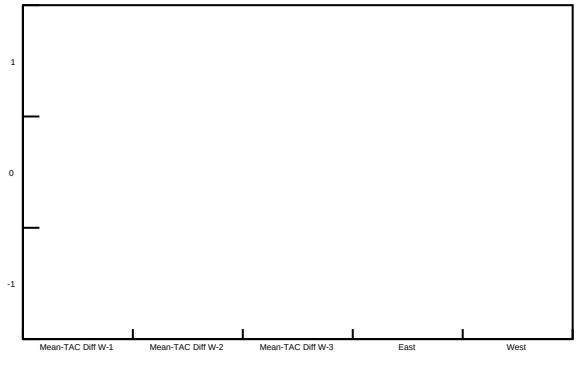
VT201 Input Ch4: VPD Tac Diff

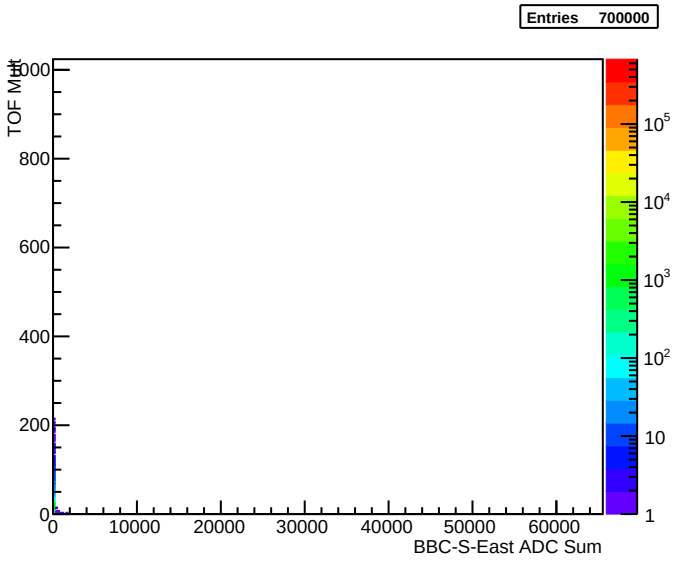


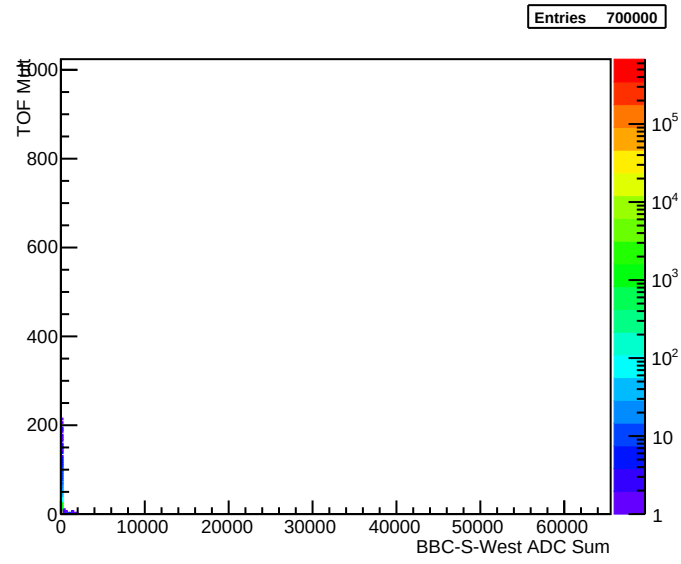
VT201 Input Ch4: VPD Adc Sum Th

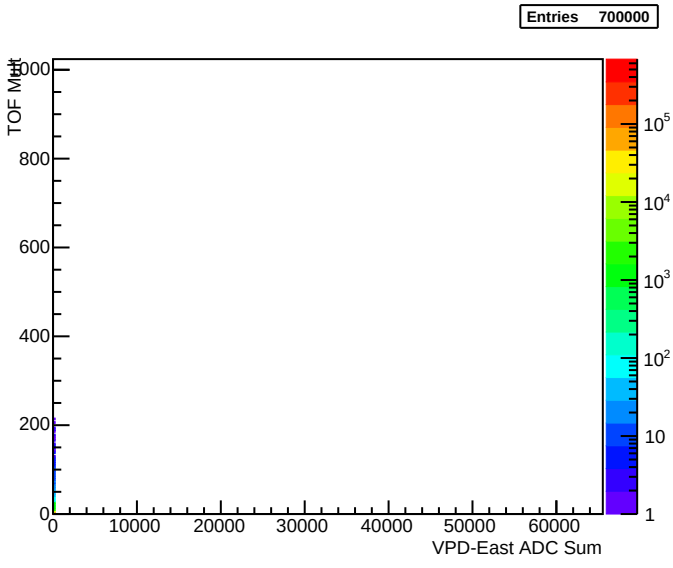


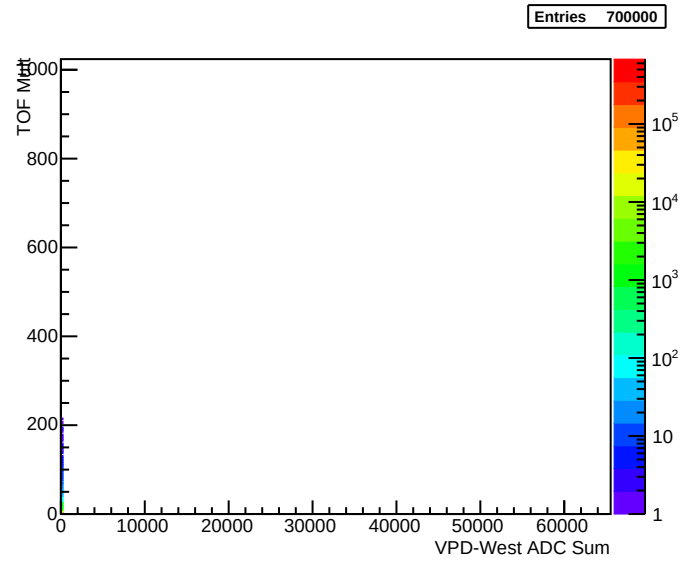
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

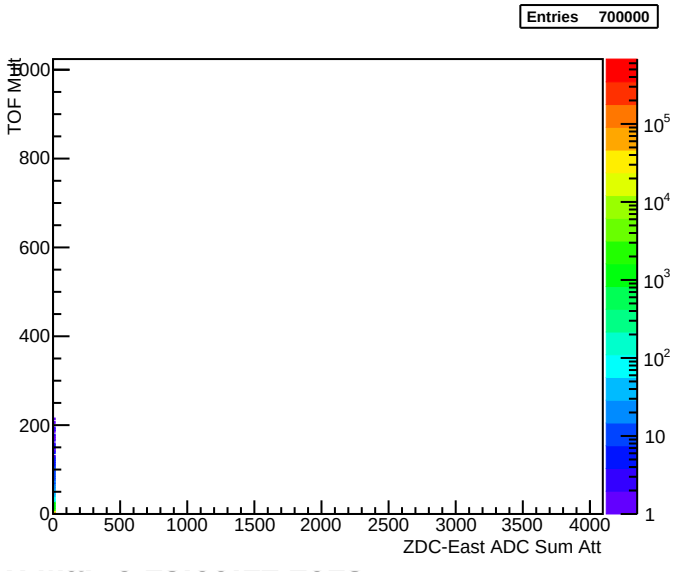


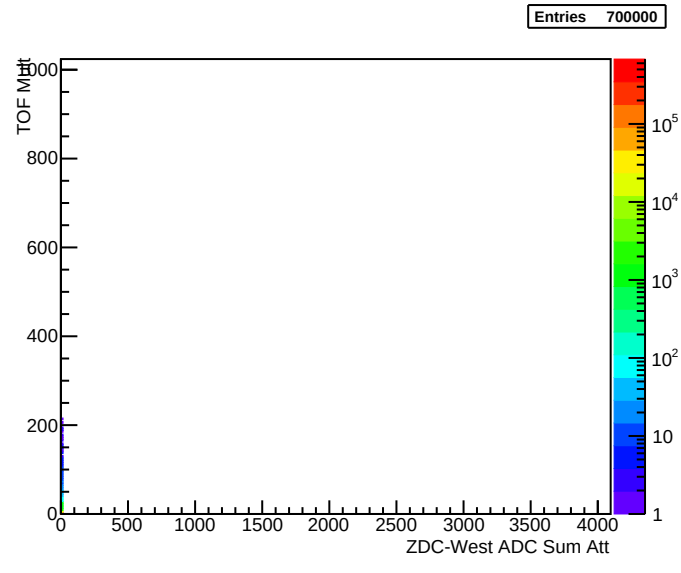

Entries 700000
TOF Mult
BBC-S-East ADC Sum

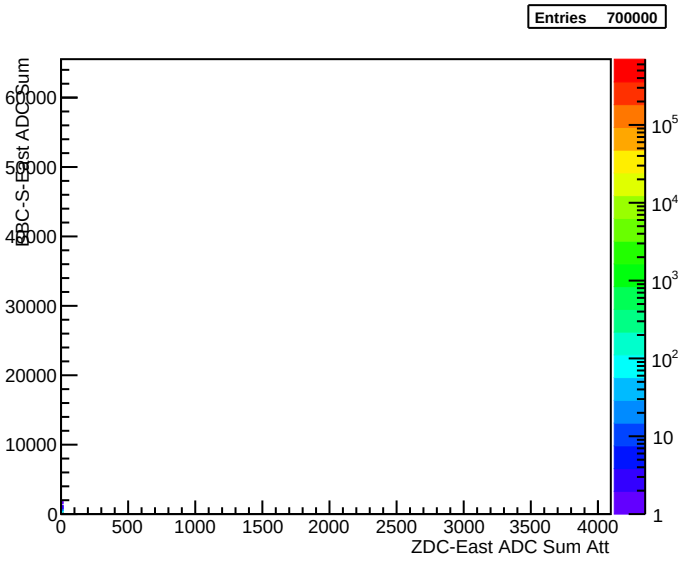

Entries 700000
TOF Mult
BBC-S-West ADC Sum

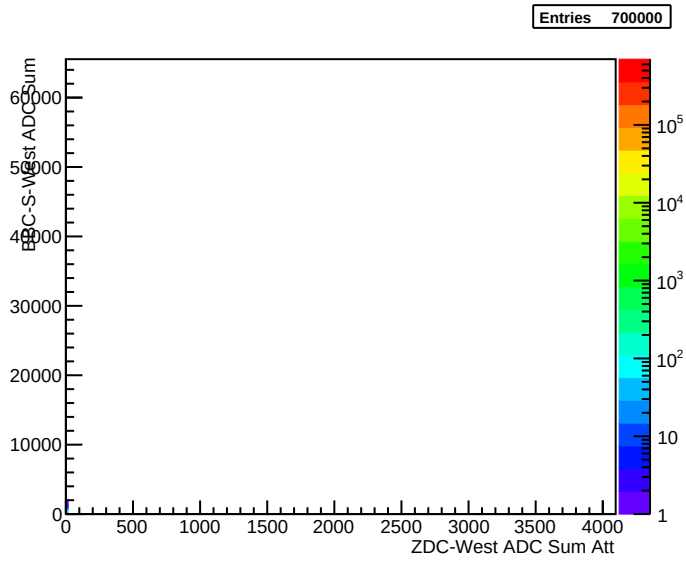

Entries 700000
TOF Mult
VPD-East ADC Sum

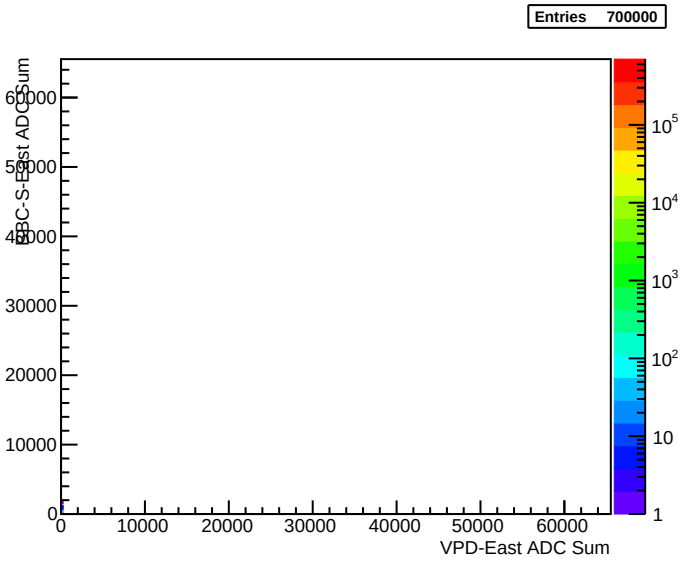

Entries 700000
TOF Mult
VPD-West ADC Sum

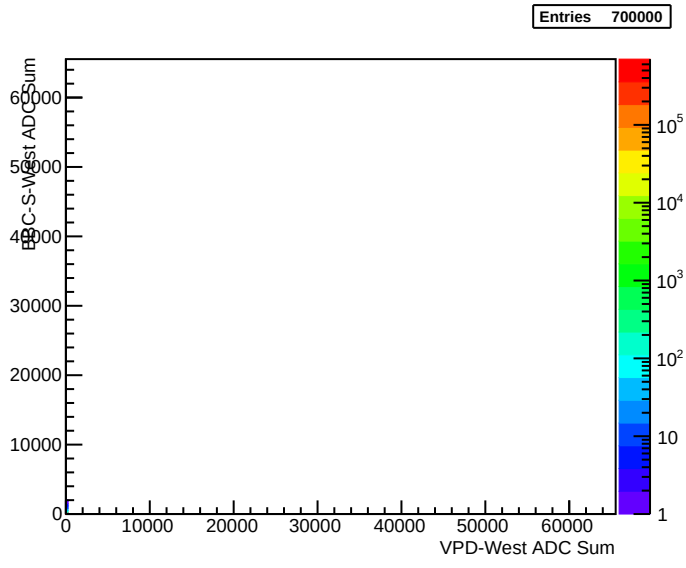

Entries 700000
TOF Mult
ZDC-East ADC Sum Att

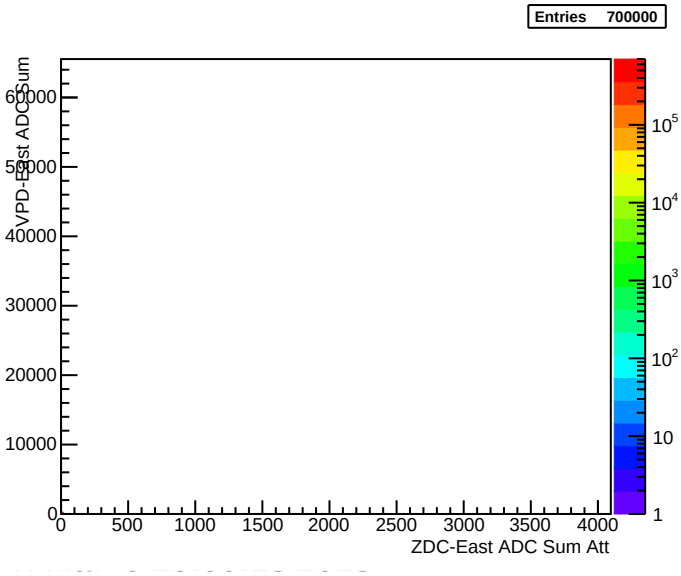

Entries 700000
TOF Mult
ZDC-West ADC Sum Att

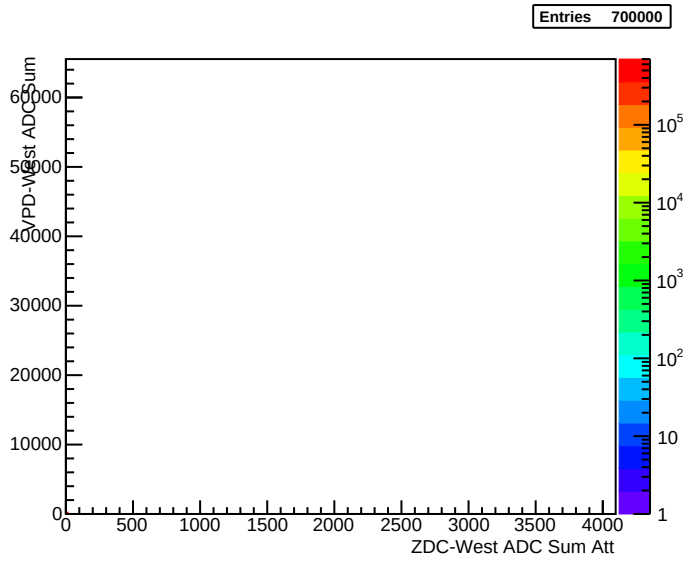

Entries 700000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

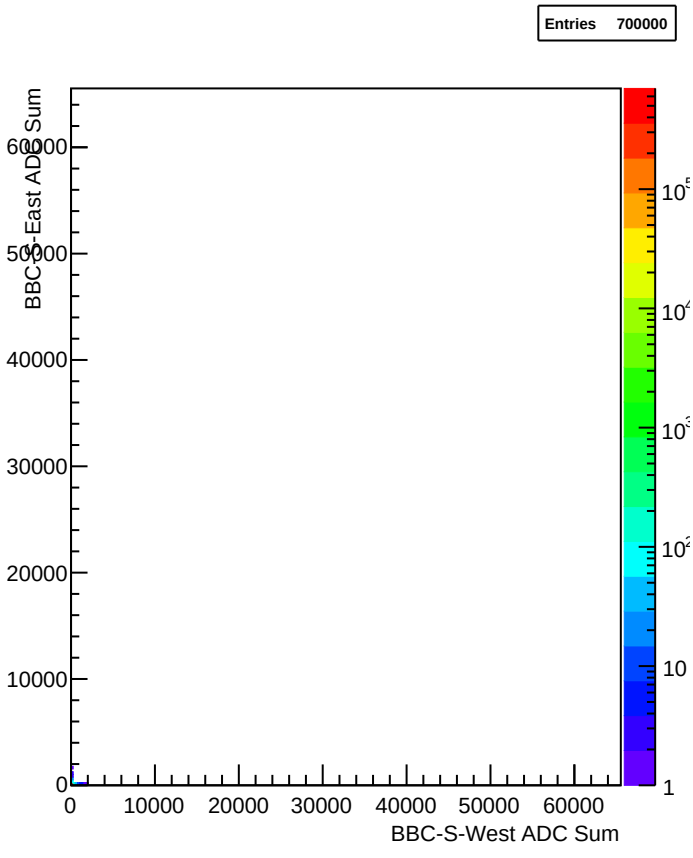

Entries 700000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

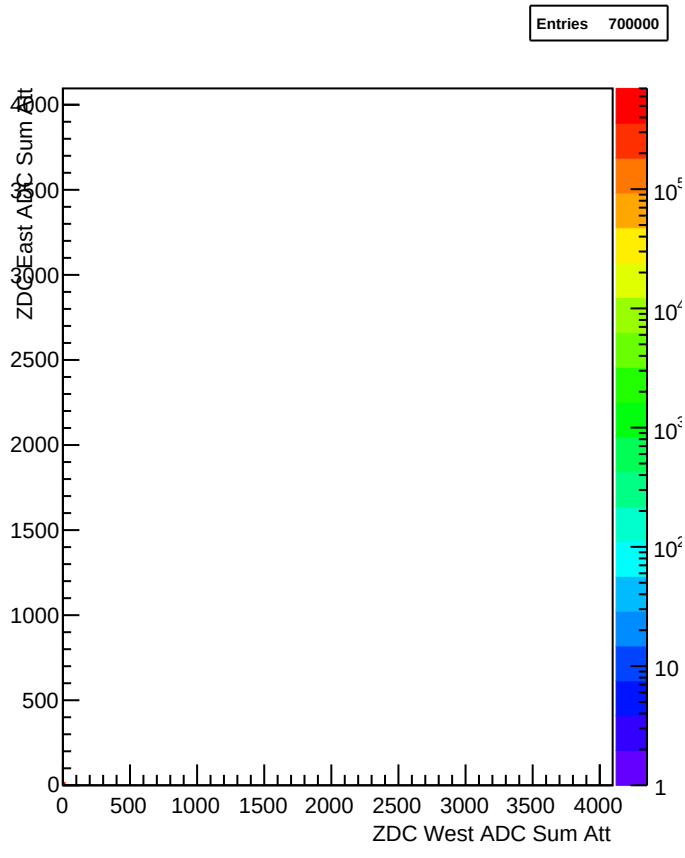

Entries 700000
BBC-S-East ADC Sum
VPD-East ADC Sum

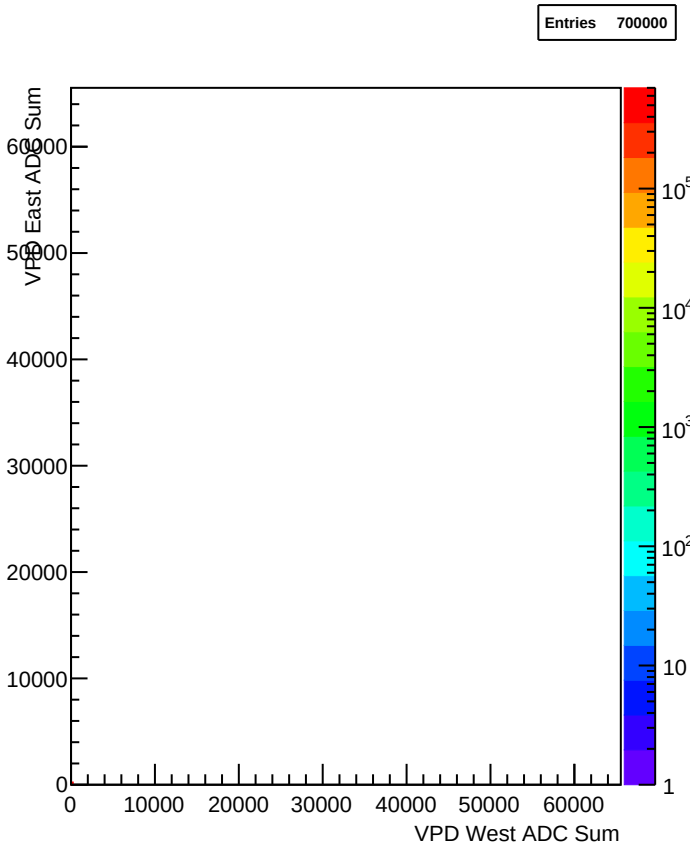

Entries 700000
BBC-S-West ADC Sum
VPD-West ADC Sum

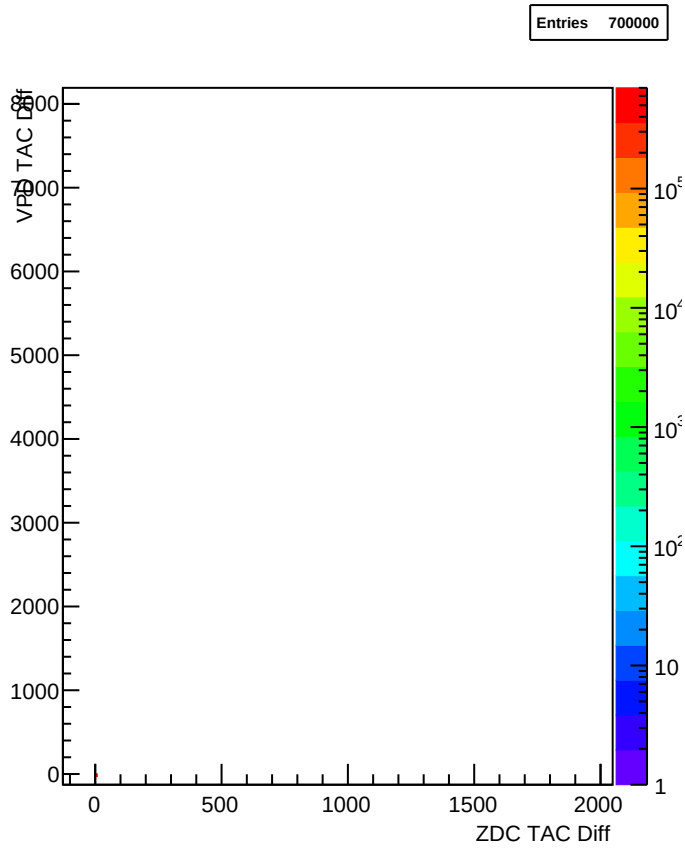

Entries 700000
VPD-East ADC Sum
ZDC-East ADC Sum Att

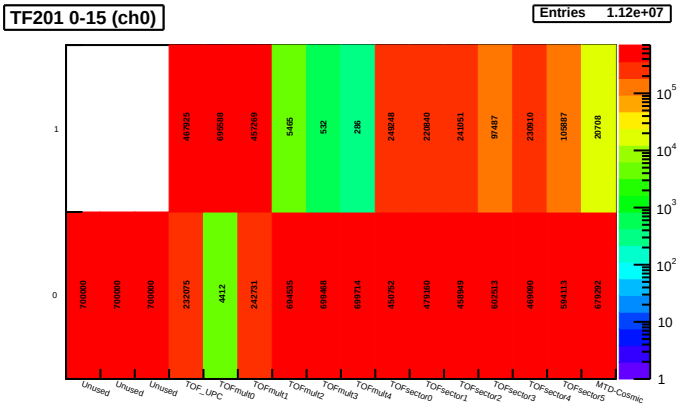

Entries 700000
VPD-West ADC Sum
ZDC-West ADC Sum Att

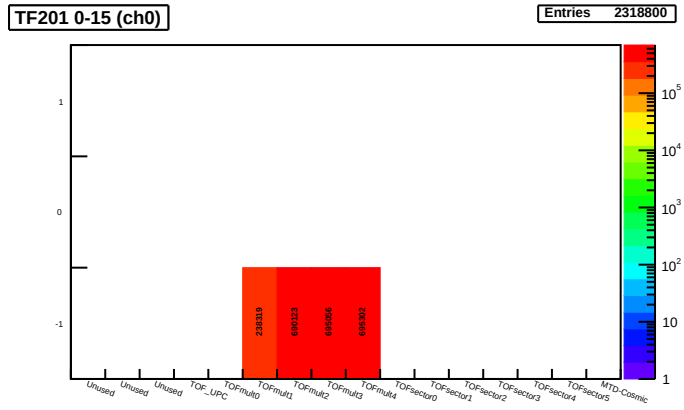

Entries 700000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

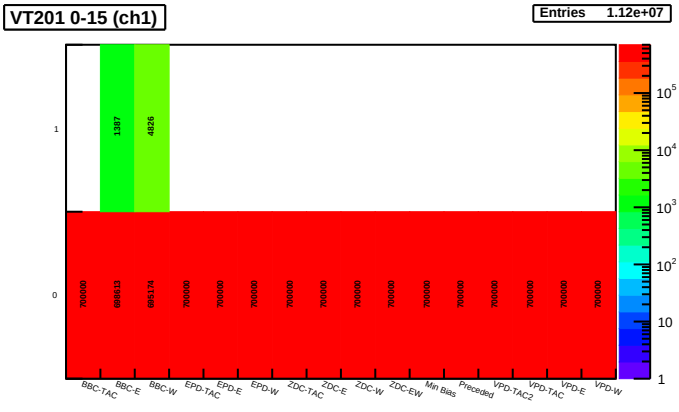

Entries 700000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

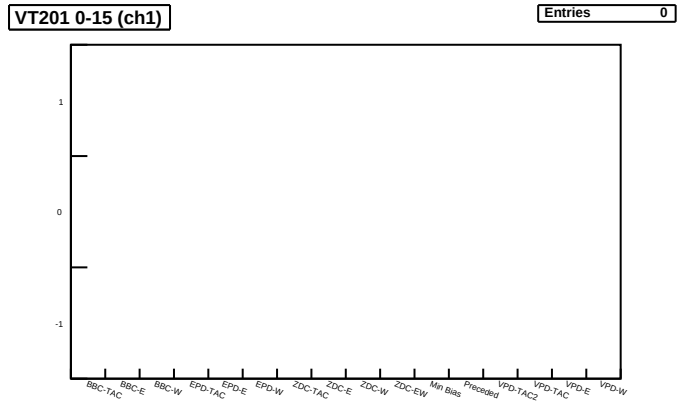

Entries 700000
VPD East ADC Sum
VPD West ADC Sum

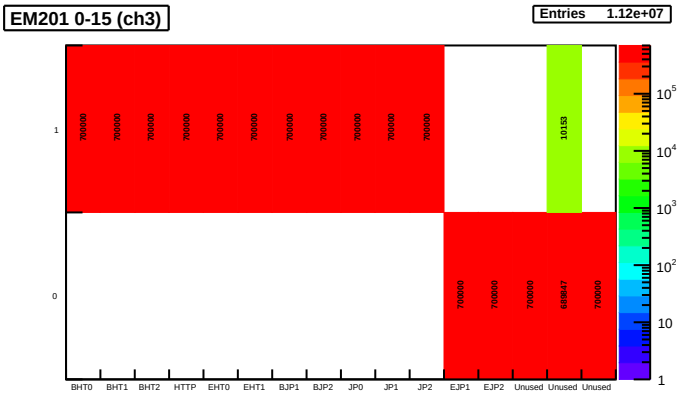

Entries 700000
VPD TAC Diff
ZDC TAC Diff

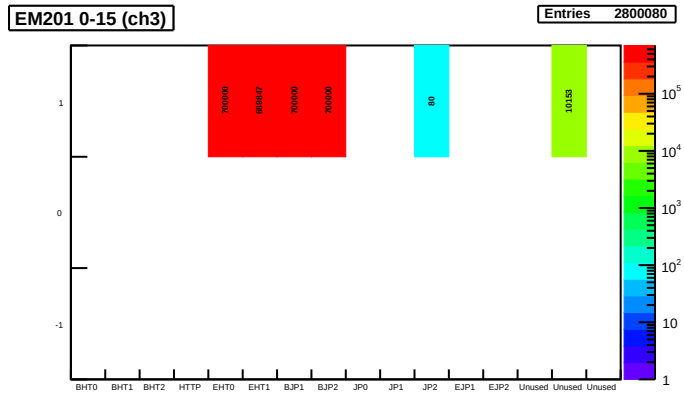

TF201 0-15 (ch0)
Entries 1.12e+07
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

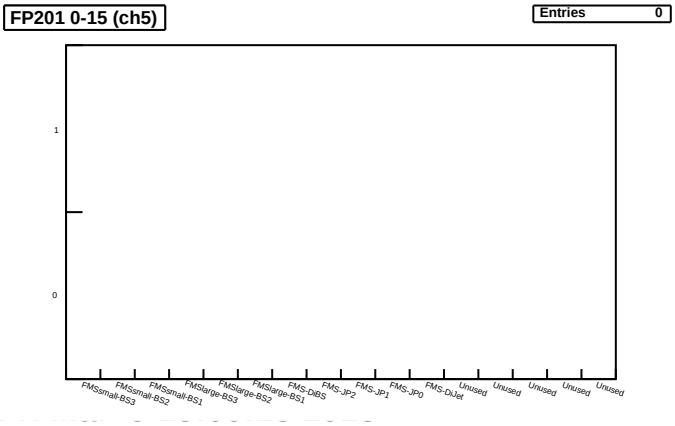

TF201 0-15 (ch0)
Entries 2318800
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

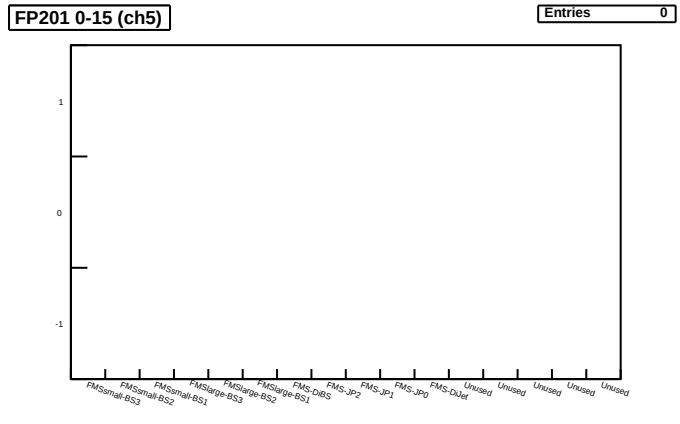

VT201 0-15 (ch1)
Entries 1.12e+07
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 1.12e+07
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 2800080
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused