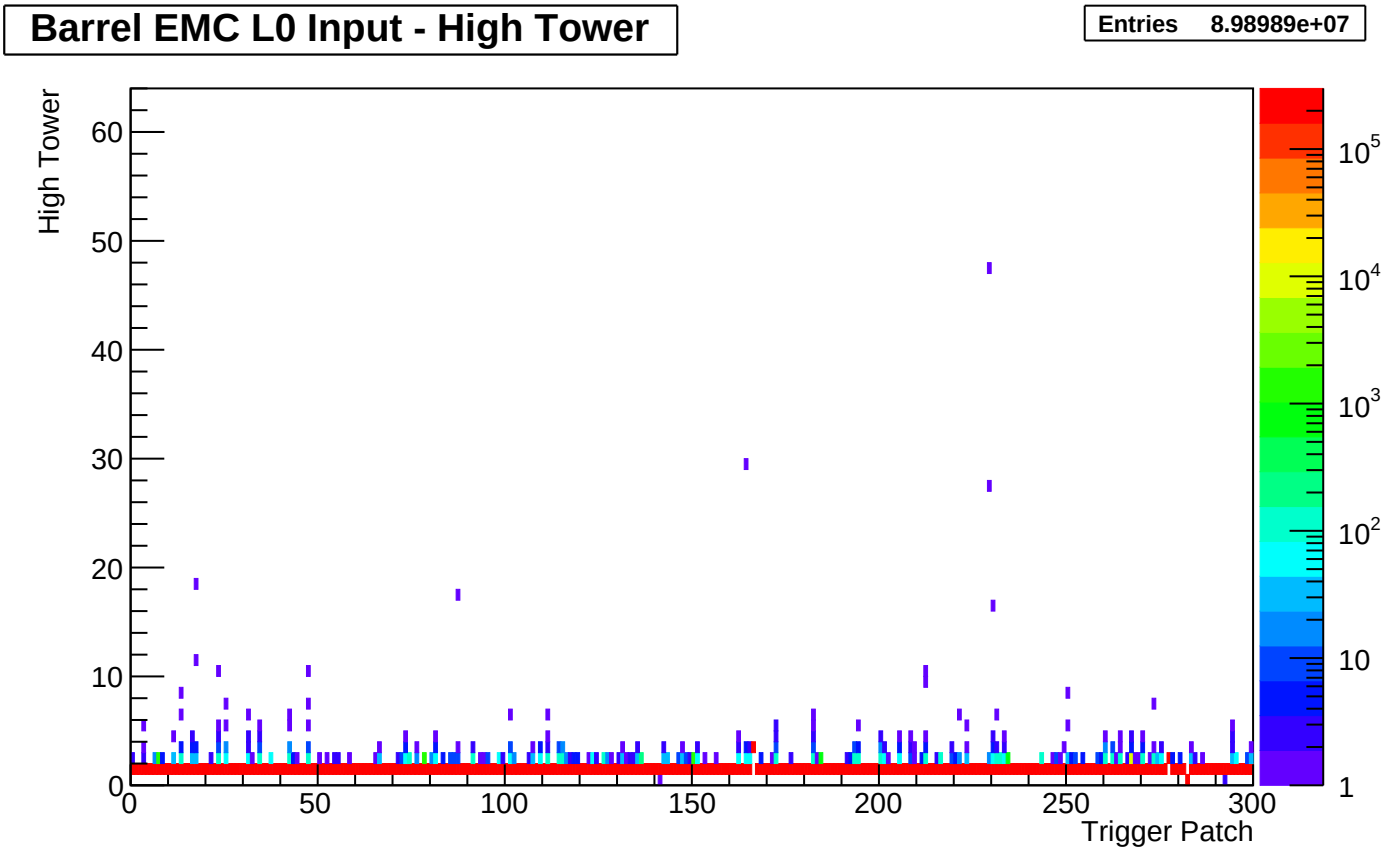
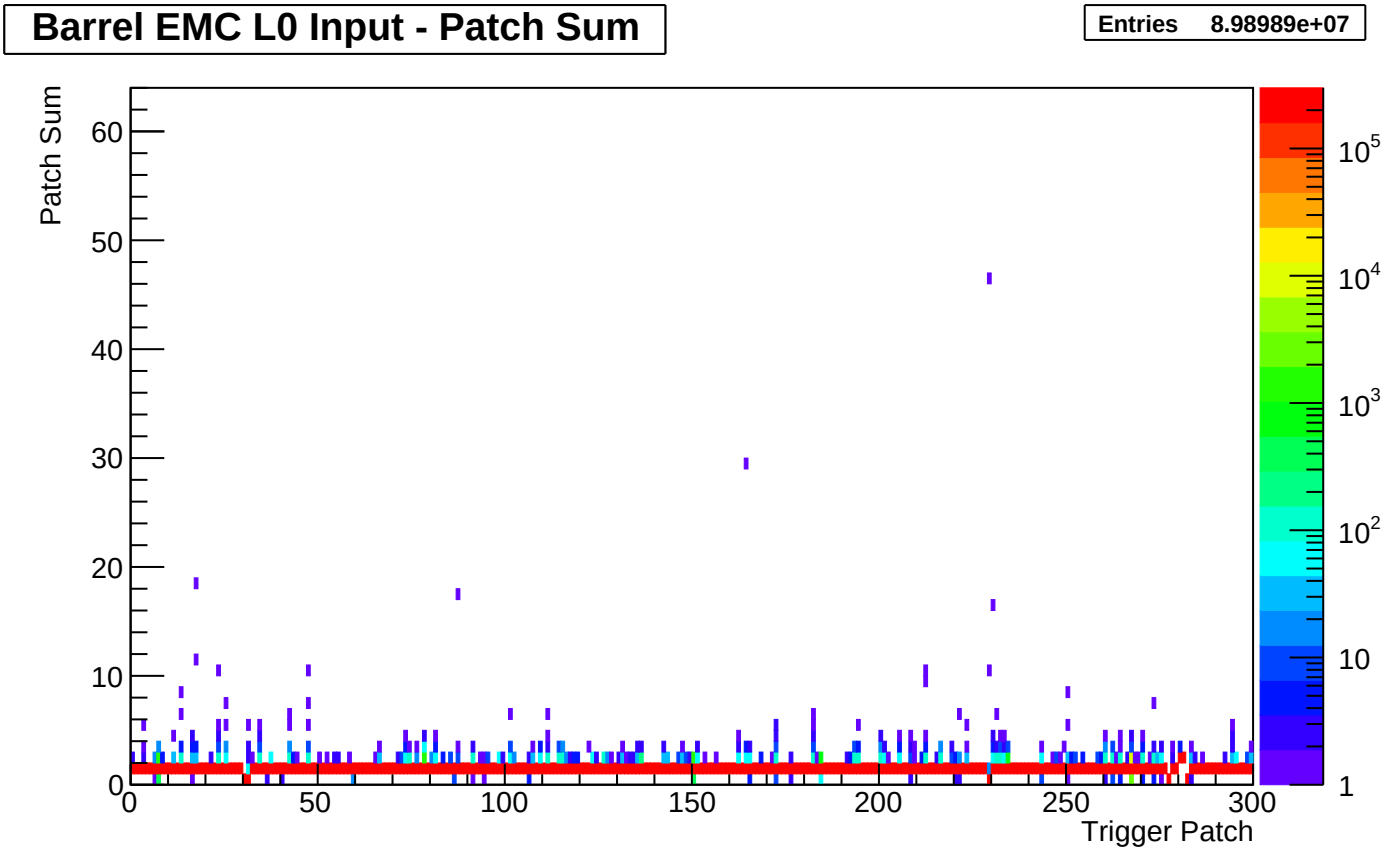
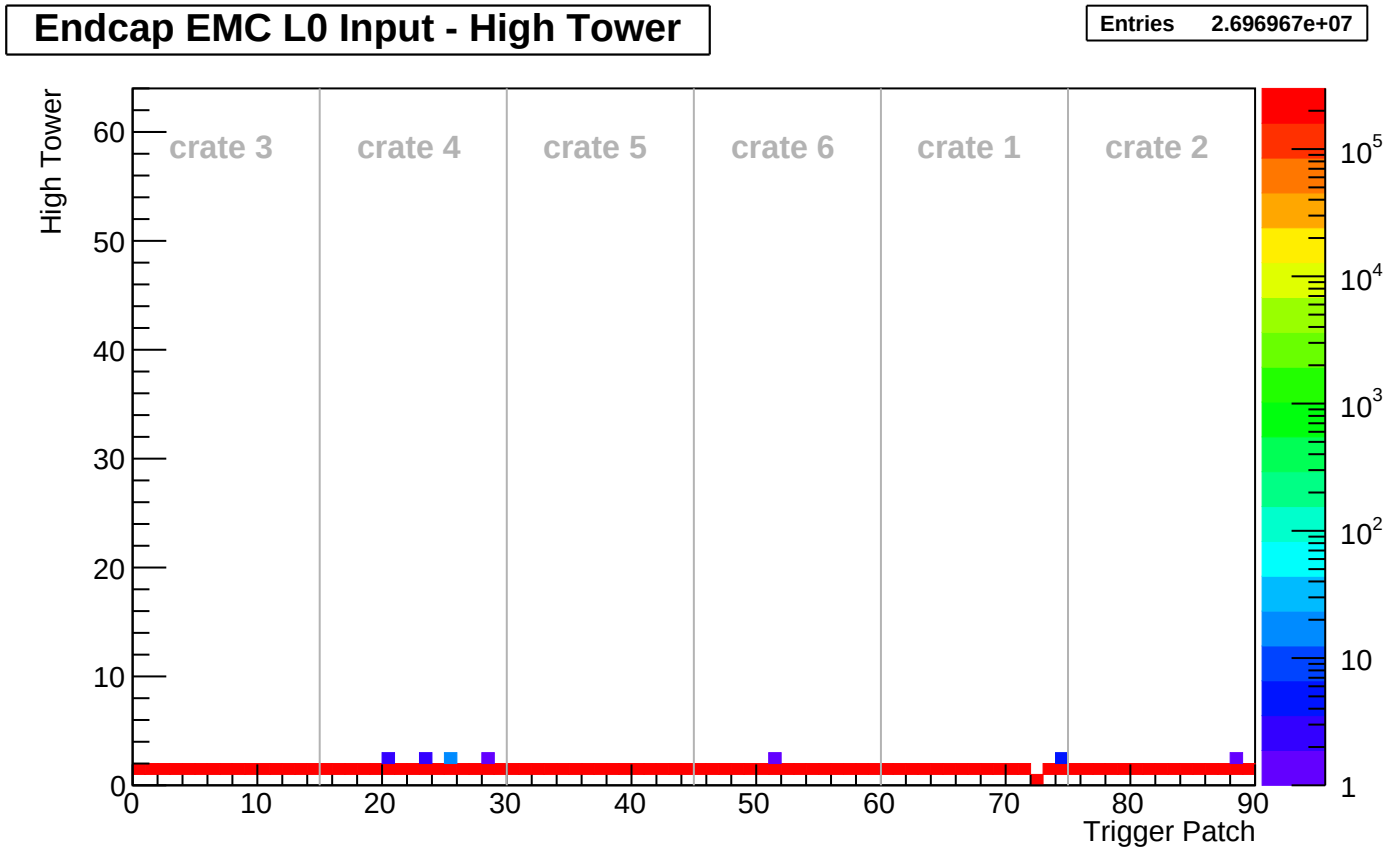
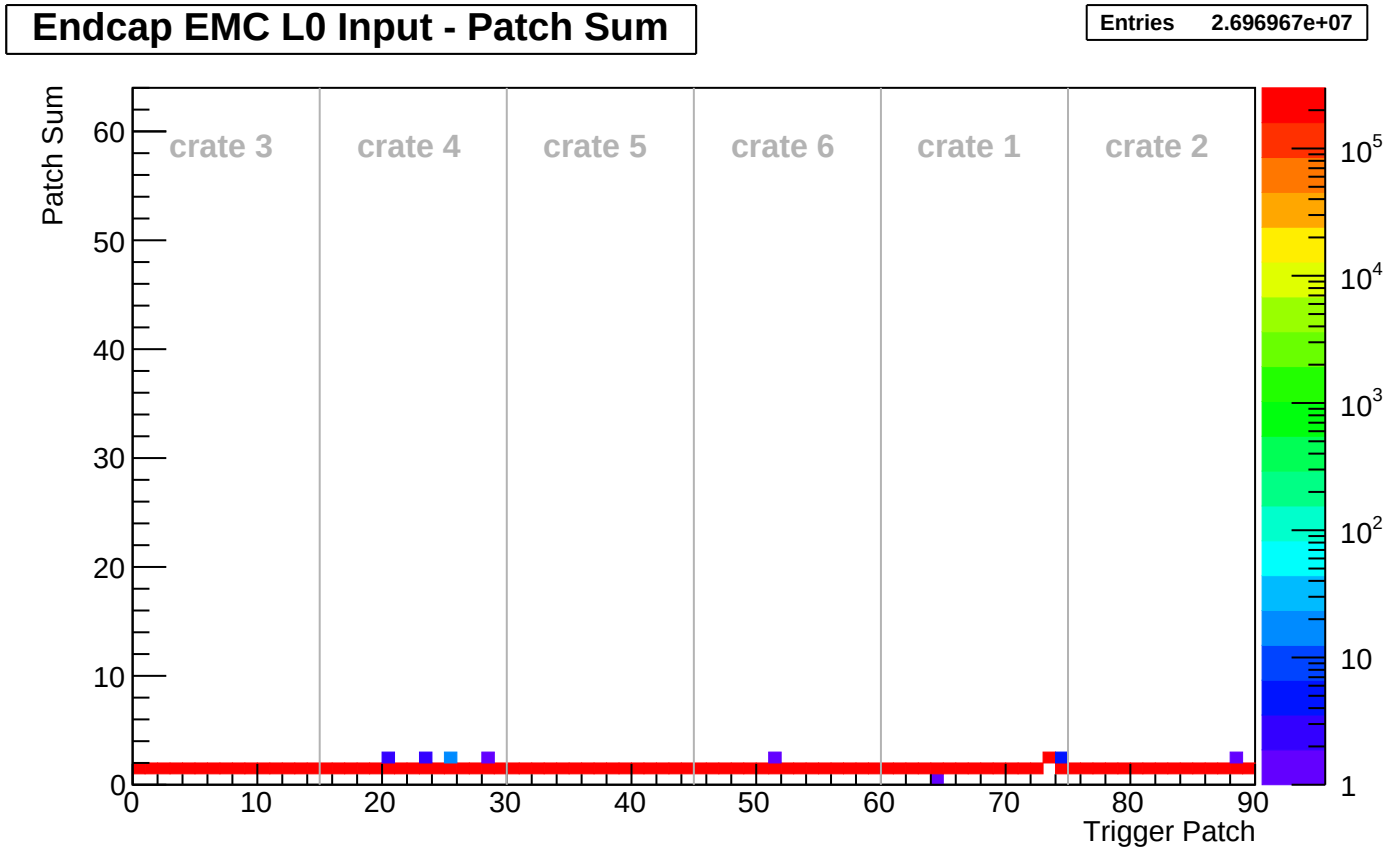
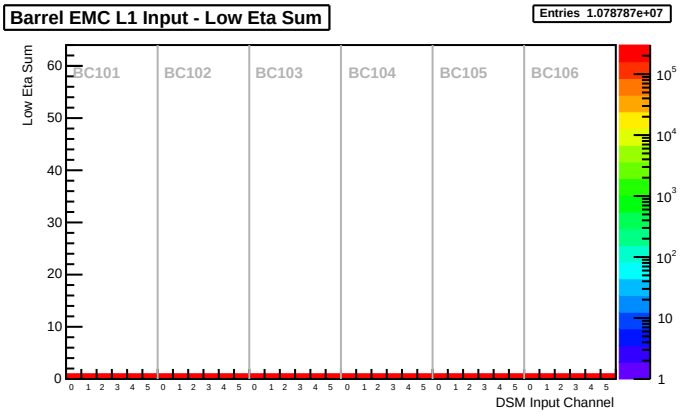
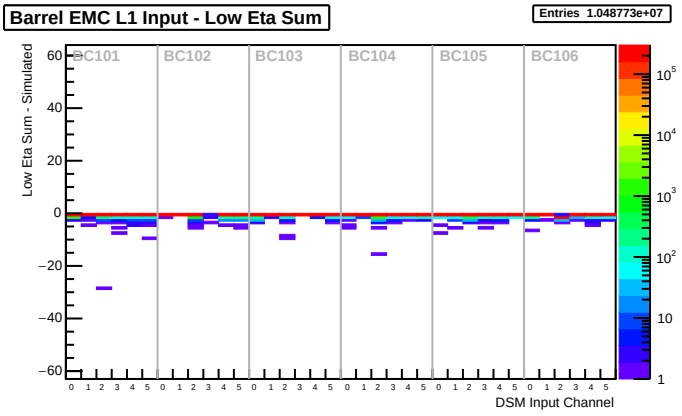

Barrel EMC L0 Input - High Tower
Entries 8.98989e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

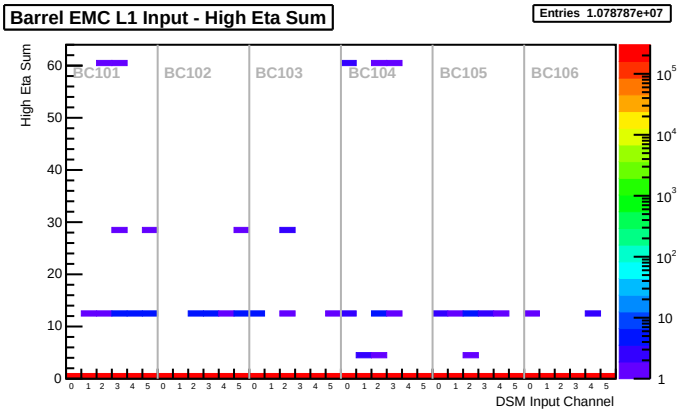

Barrel EMC L0 Input - Patch Sum
Entries 8.98989e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

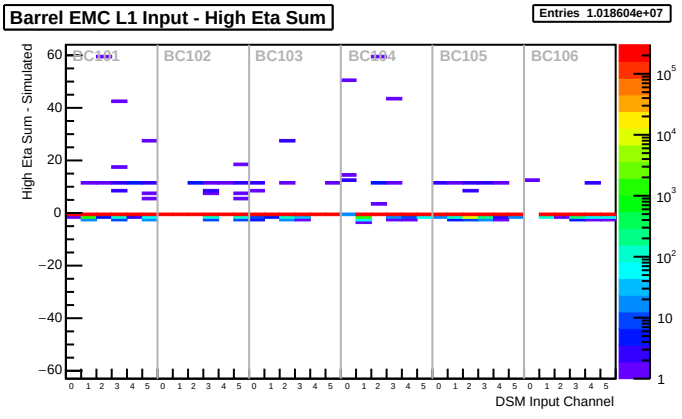

Endcap EMC L0 Input - High Tower
Entries 2.696967e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

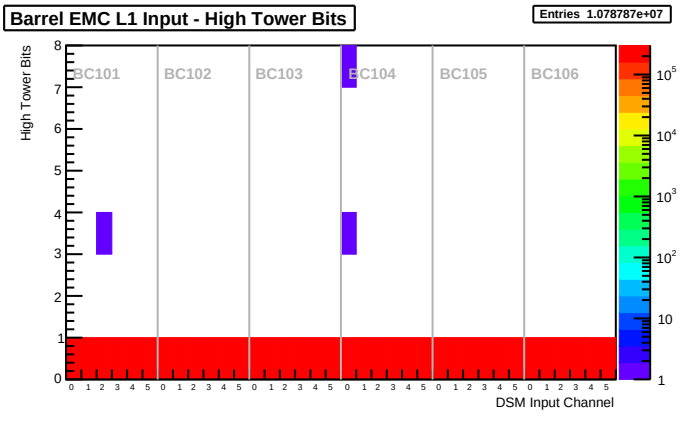

Endcap EMC L0 Input - Patch Sum
Entries 2.696967e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

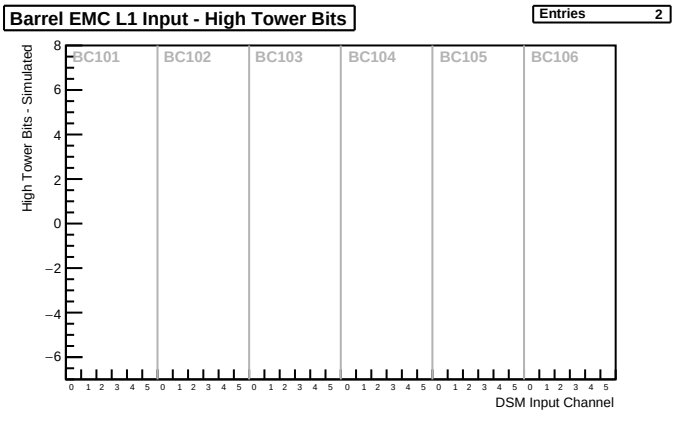

Barrel EMC L1 Input - Low Eta Sum
Entries 1.078787e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

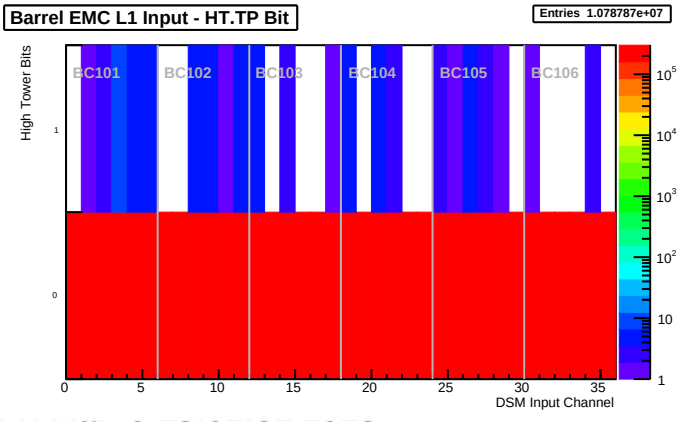

Barrel EMC L1 Input - Low Eta Sum
Entries 1.048773e+07
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

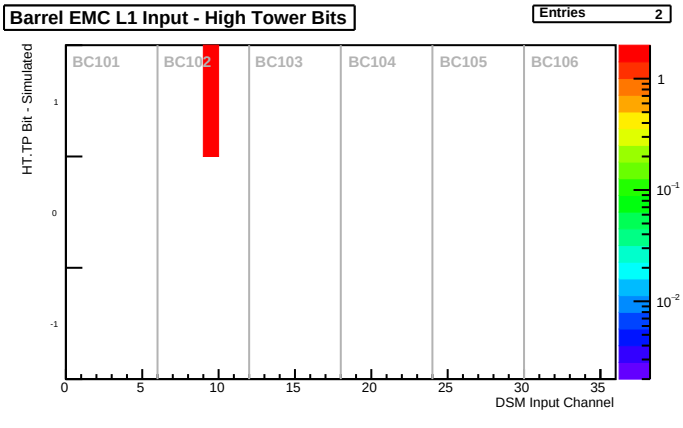

Barrel EMC L1 Input - High Eta Sum
Entries 1.078787e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

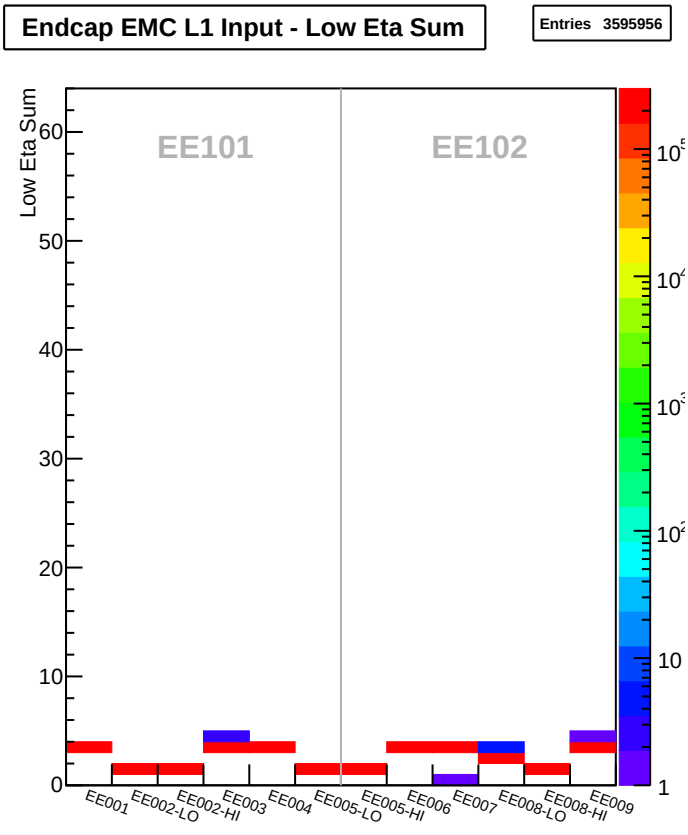

Barrel EMC L1 Input - High Eta Sum
Entries 1.018604e+07
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

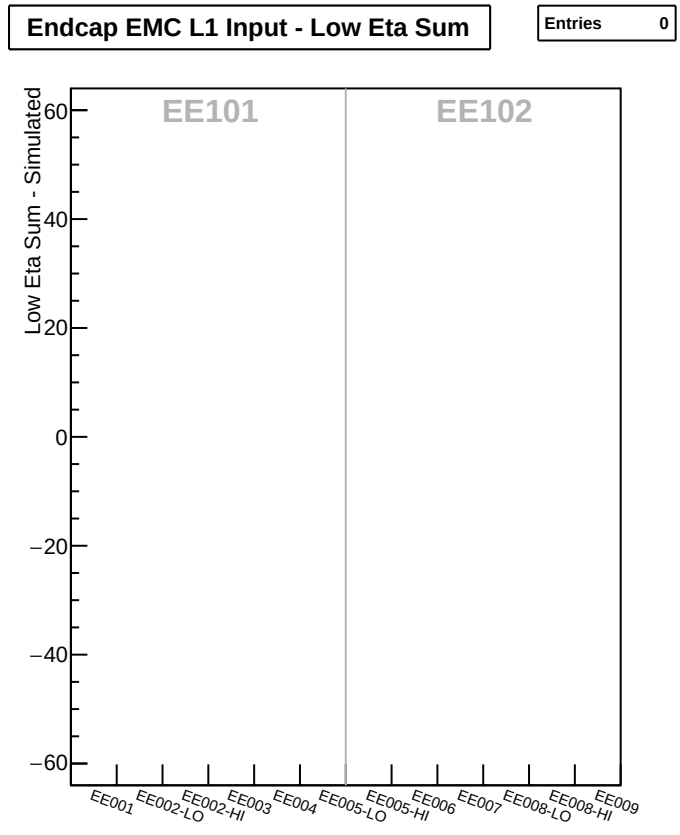

Barrel EMC L1 Input - High Tower Bits
Entries 1.078787e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

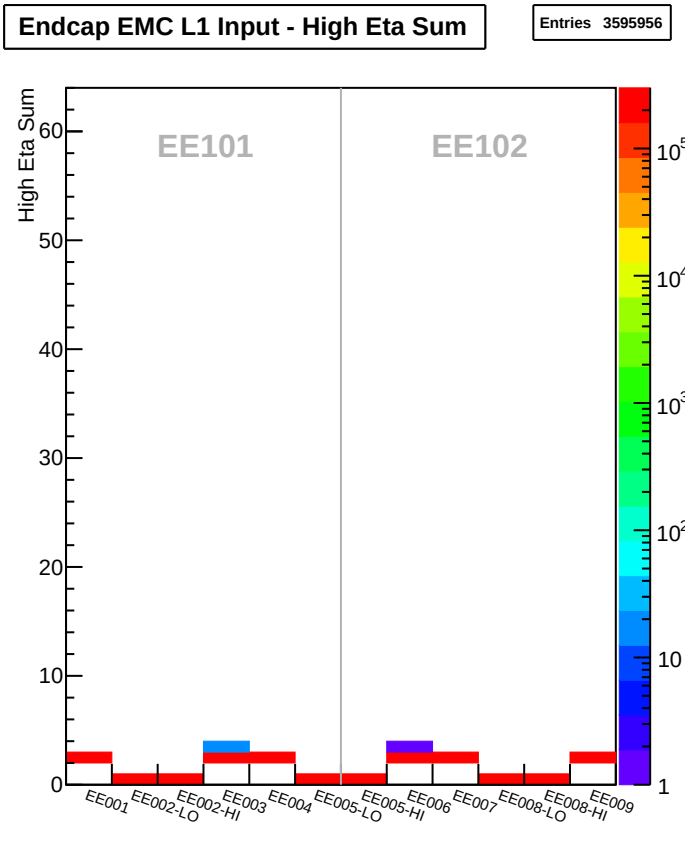

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

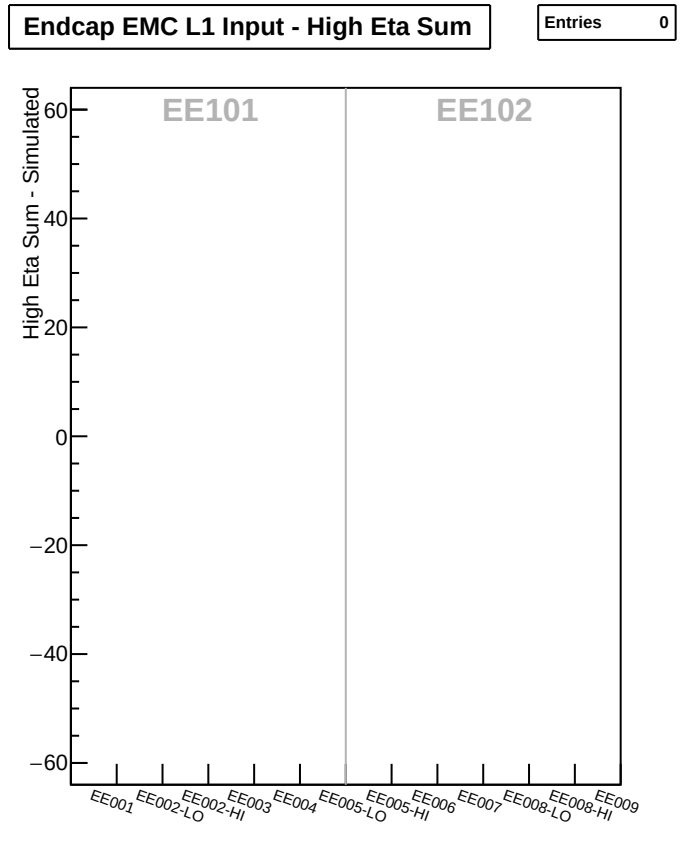

Barrel EMC L1 Input - HT.TP Bit
Entries 1.078787e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

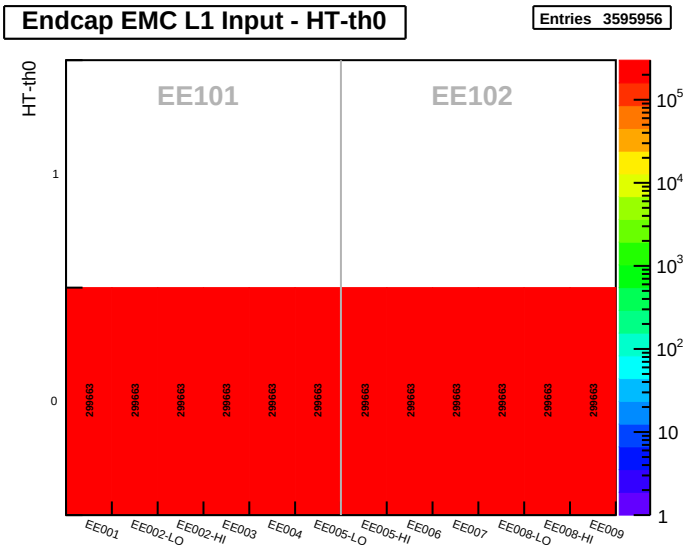

Barrel EMC L1 Input - High Tower Bits
Entries 2
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

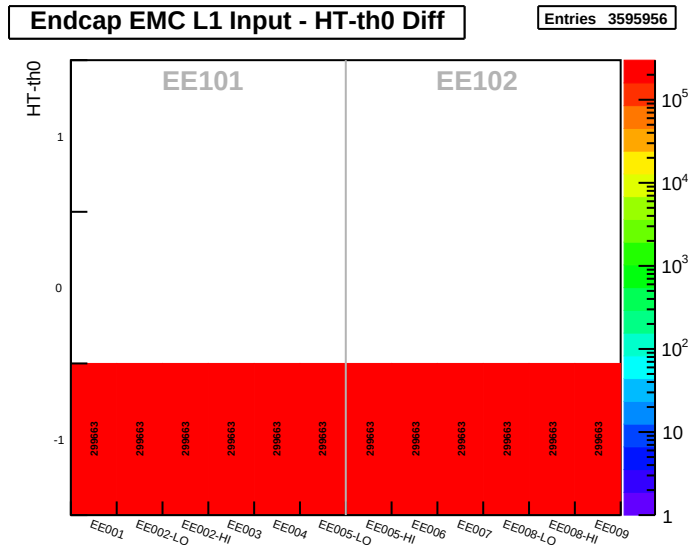

Endcap EMC L1 Input - Low Eta Sum
Entries 3595956
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

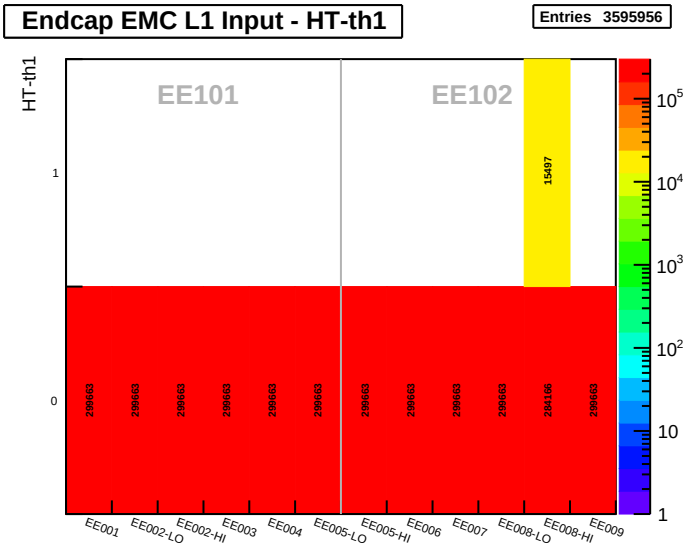

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

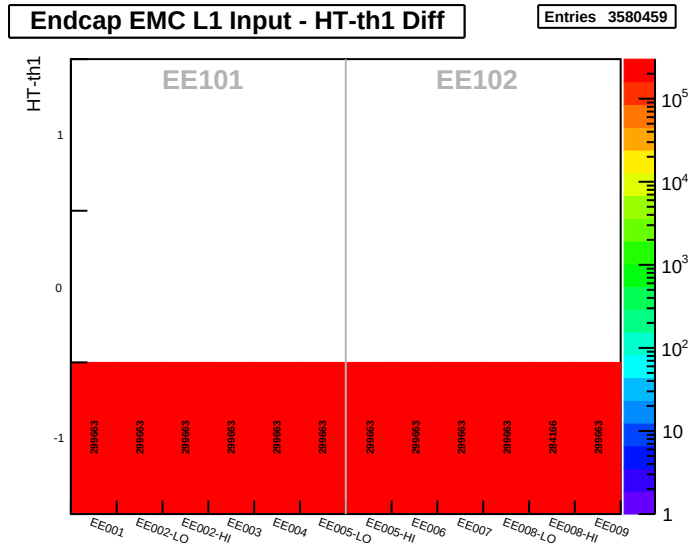

Endcap EMC L1 Input - High Eta Sum
Entries 3595956
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

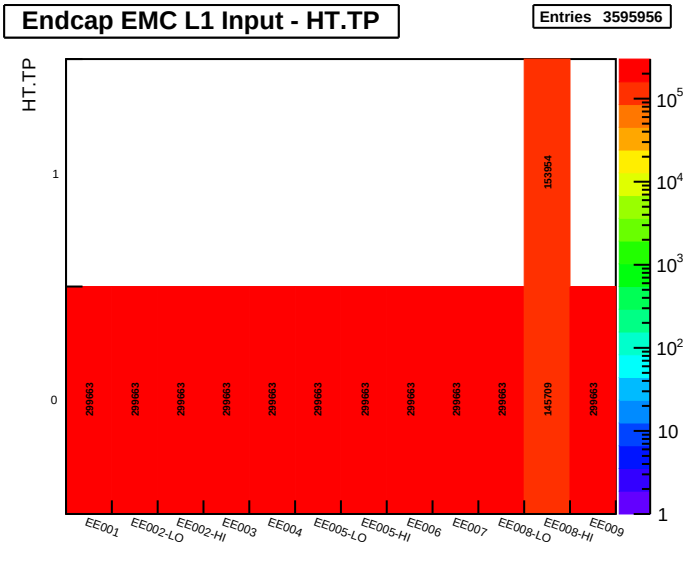

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

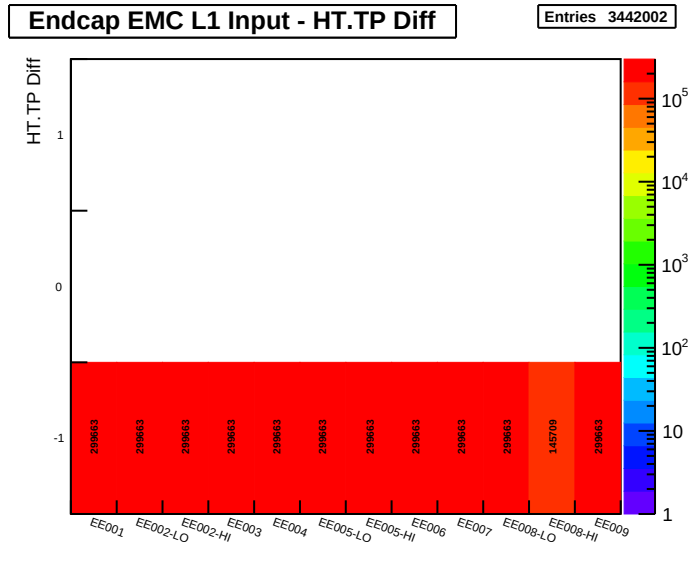

Endcap EMC L1 Input - HT-th0
Entries 3595956
HT-th0
EE101
EE102
299663
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

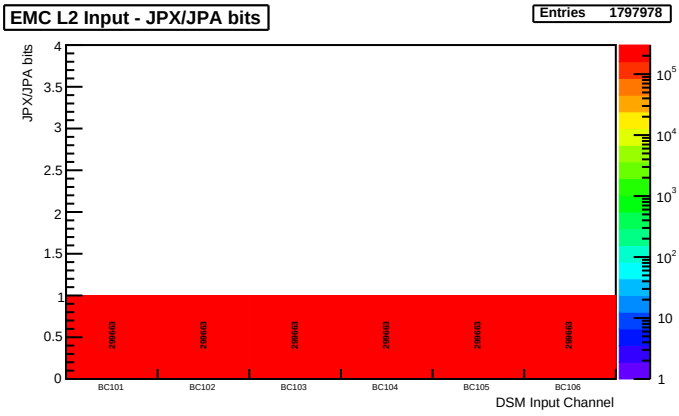

Endcap EMC L1 Input - HT-th0 Diff
Entries 3595956
HT-th0
EE101
EE102
299663
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

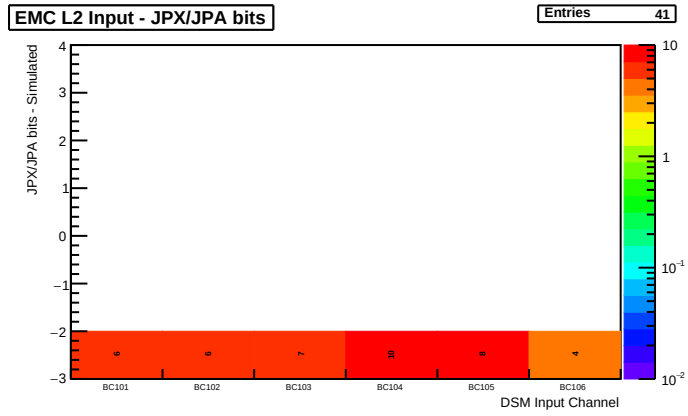

Endcap EMC L1 Input - HT-th1
Entries 3595956
HT-th1
EE101
EE102
15497
299663
284166
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

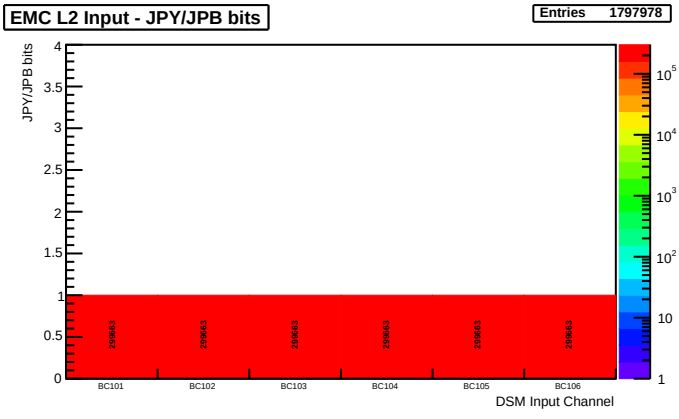

Endcap EMC L1 Input - HT-th1 Diff
Entries 3580459
HT-th1
EE101
EE102
299663
284166
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

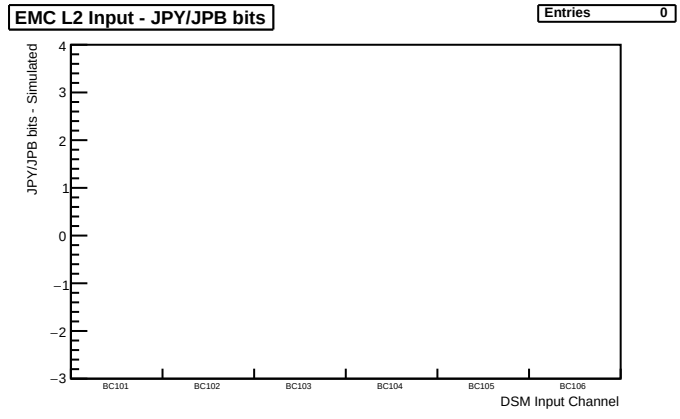

Endcap EMC L1 Input - HT.TP
Entries 3595956
HT.TP
153954
145709
299663
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

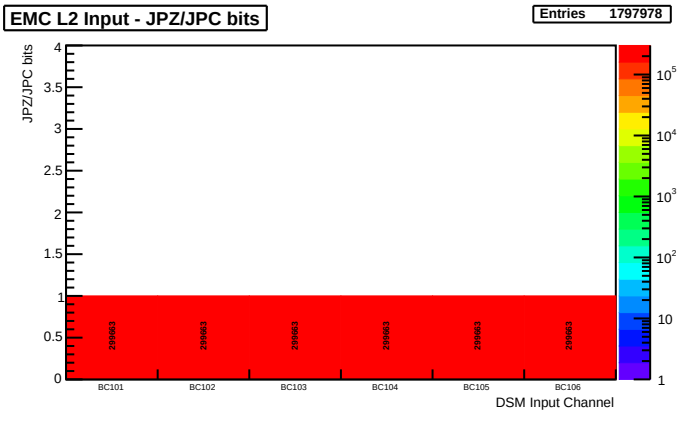

Endcap EMC L1 Input - HT.TP Diff
Entries 3442002
HT.TP Diff
145709
299663
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

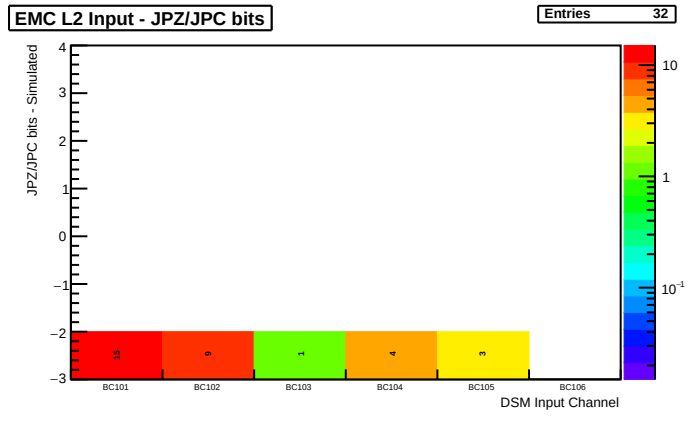

EMC L2 Input - JPX/JPA bits
Entries 1797978
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
299663

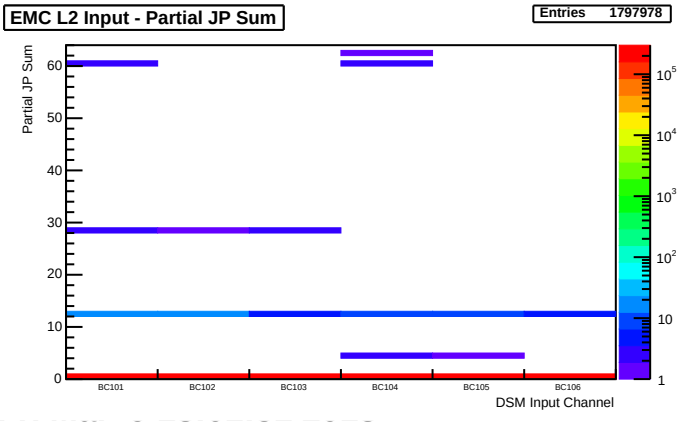

EMC L2 Input - JPX/JPA bits
Entries 41
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
6
6
7
10
8
4

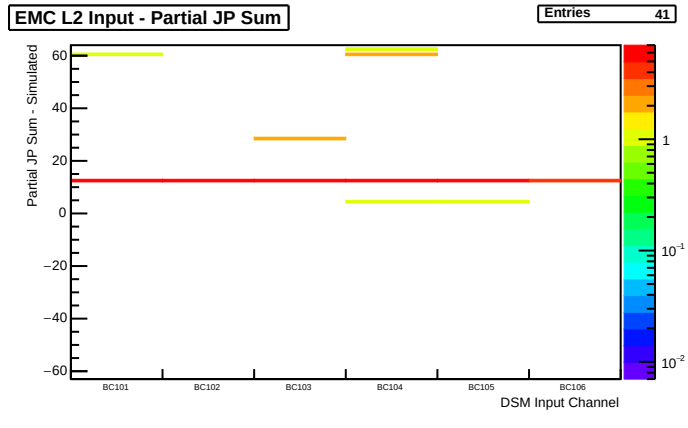

EMC L2 Input - JPY/JPB bits
Entries 1797978
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
299663

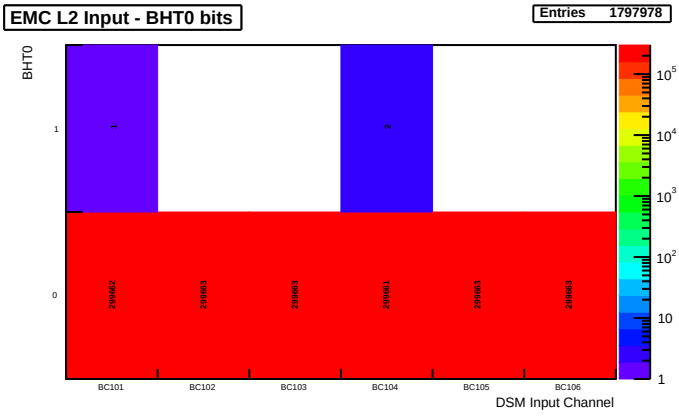

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

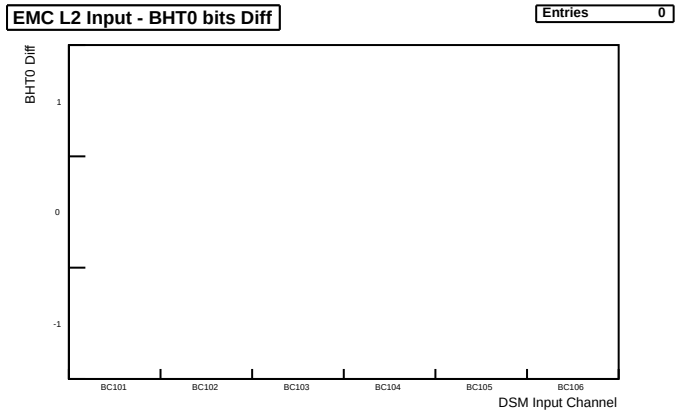

EMC L2 Input - JPZ/JPC bits
Entries 1797978
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
299663

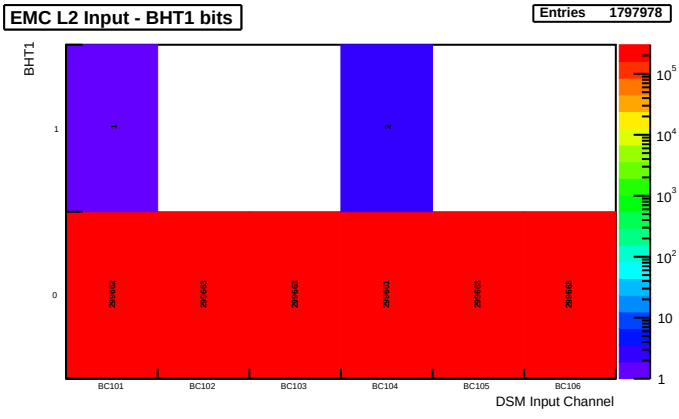

EMC L2 Input - JPZ/JPC bits
Entries 32
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
15
9
1
4
3

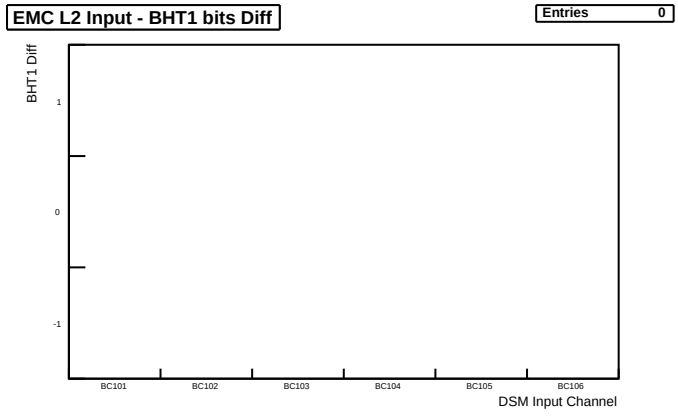

EMC L2 Input - Partial JP Sum
Entries 1797978
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

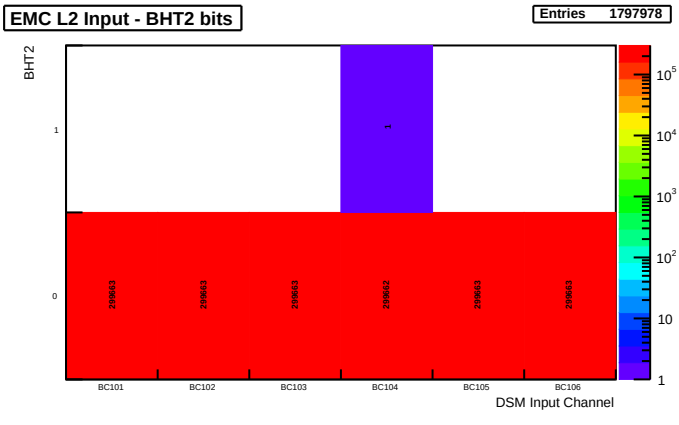

EMC L2 Input - Partial JP Sum
Entries 41
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

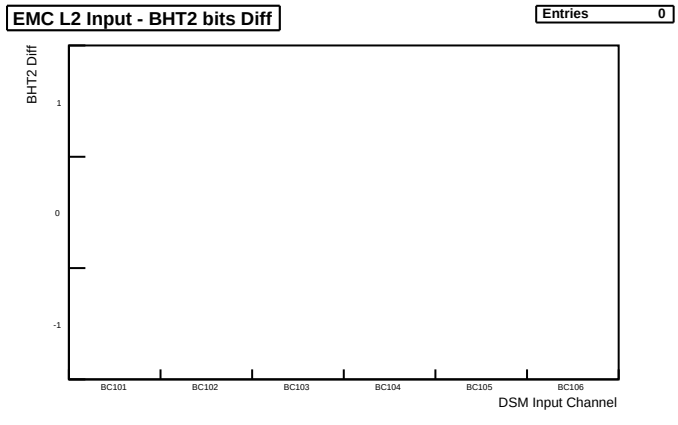

EMC L2 Input - BHT0 bits
Entries 1797978
BHT0
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

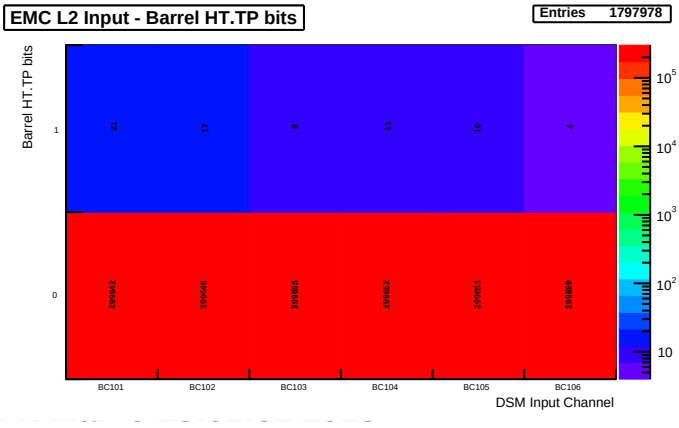

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

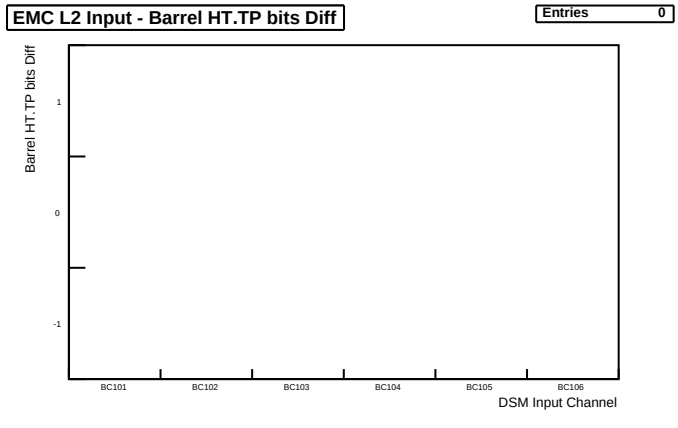

EMC L2 Input - BHT1 bits
Entries 1797978
BHT1
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

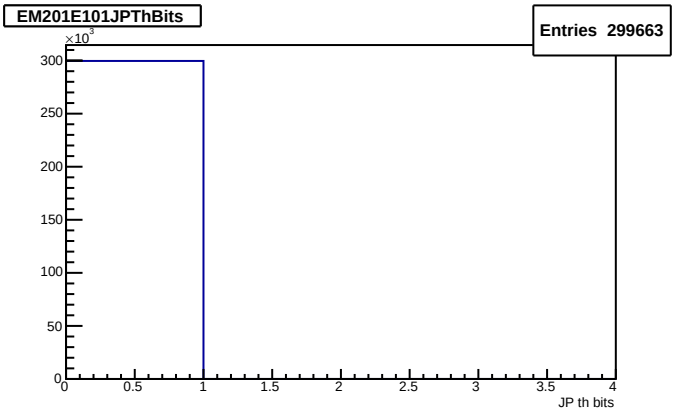

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

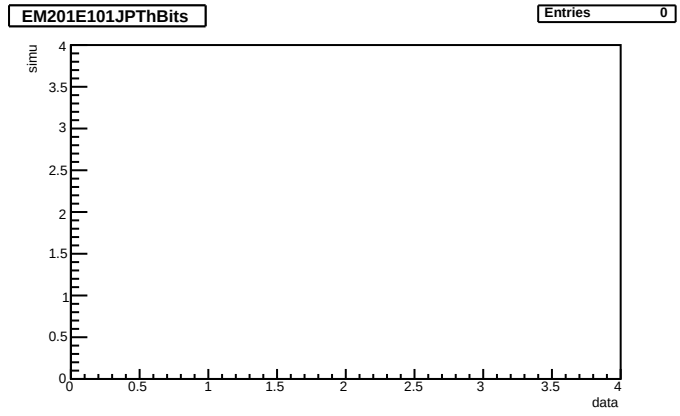

EMC L2 Input - BHT2 bits
Entries 1797978
BHT2
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

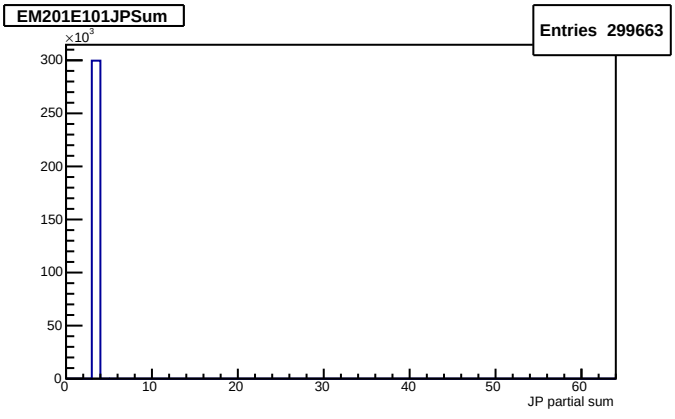

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

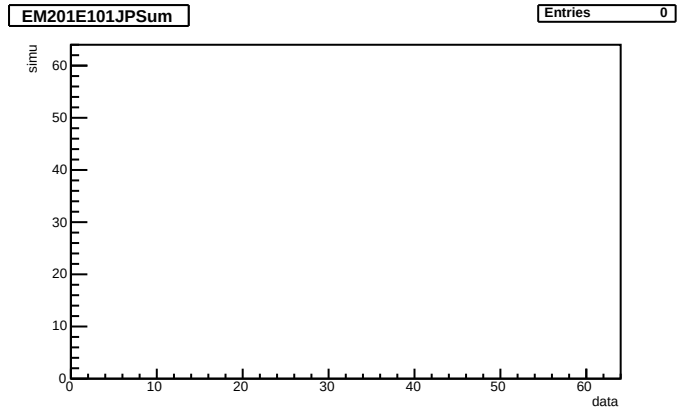

EMC L2 Input - Barrel HT.TP bits
Entries 1797978
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

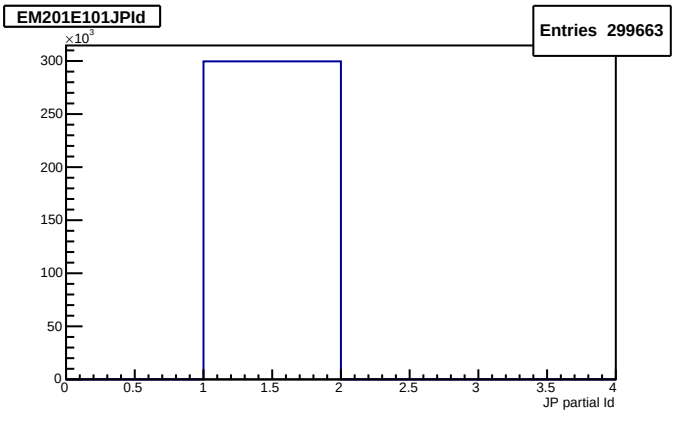

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

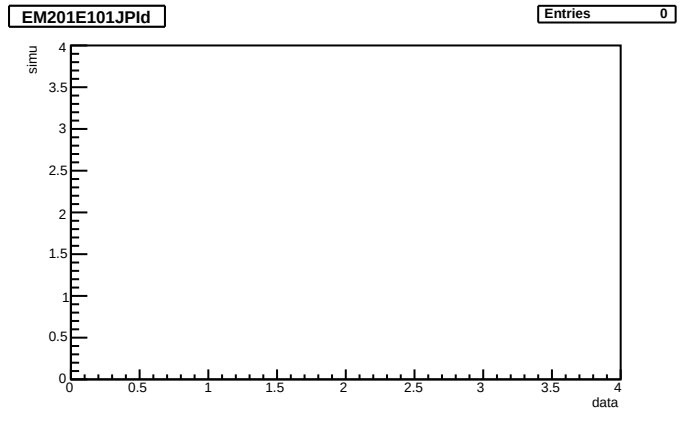

EM201E101JPThBits
Entries 299663
JP th bits

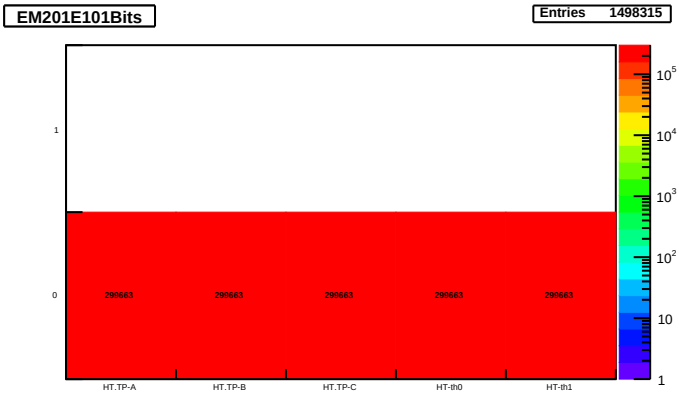

EM201E101JPThBits
Entries 0
simu
data

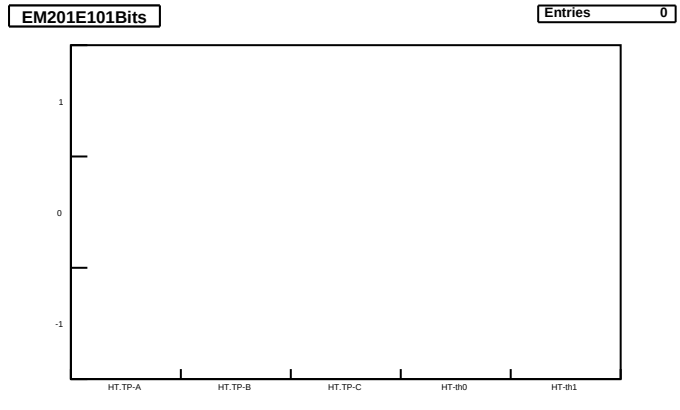

EM201E101JPSum
Entries 299663
JP partial sum

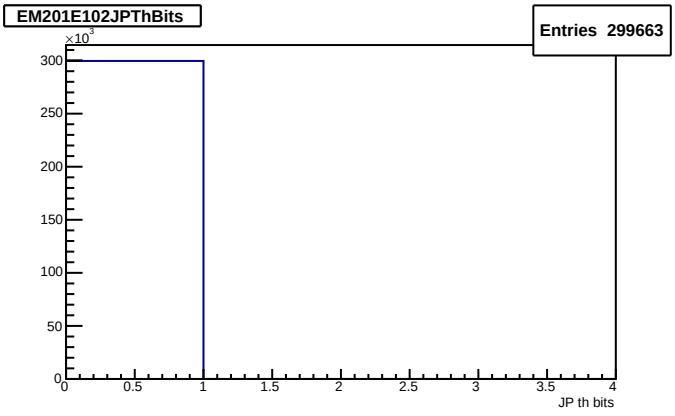

EM201E101JPSum
Entries 0
simu
data

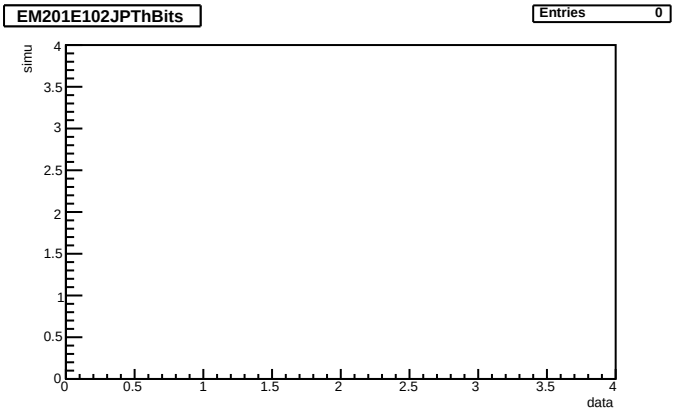

EM201E101JPId
Entries 299663
JP partial Id

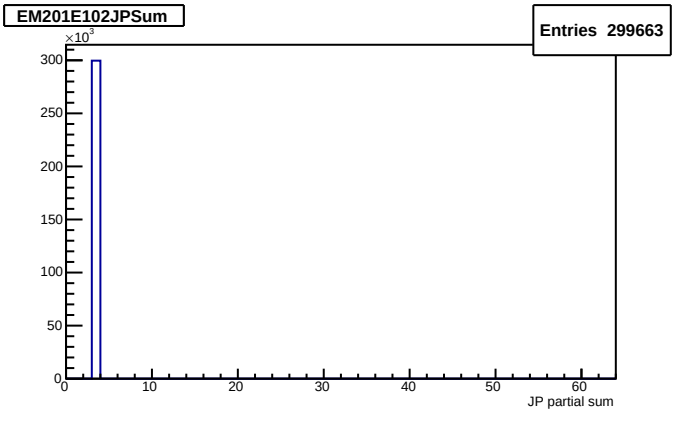

EM201E101JPId
Entries 0
simu
data

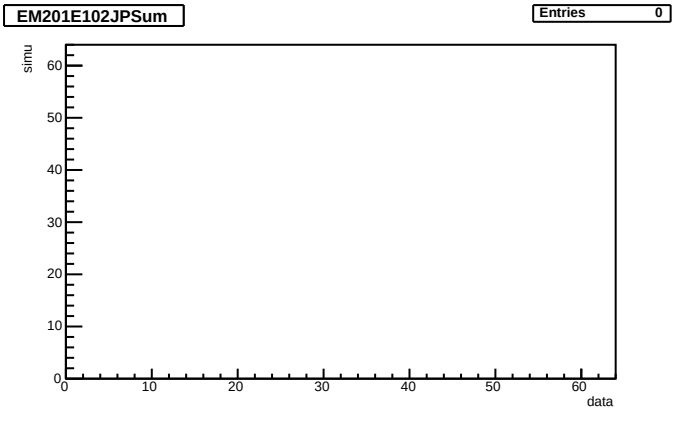

EM201E101Bits
Entries 1498315
299663
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

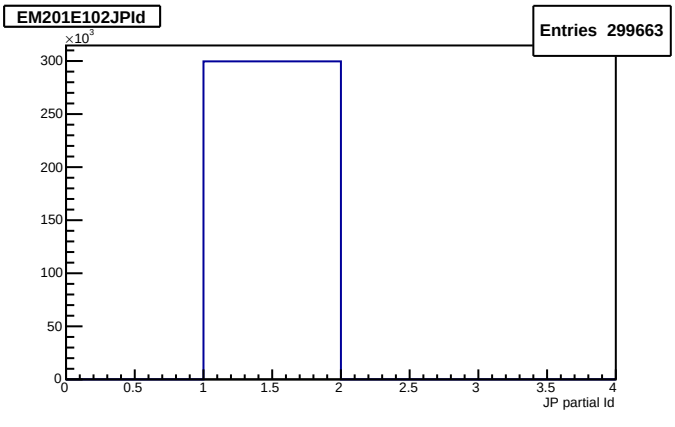

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

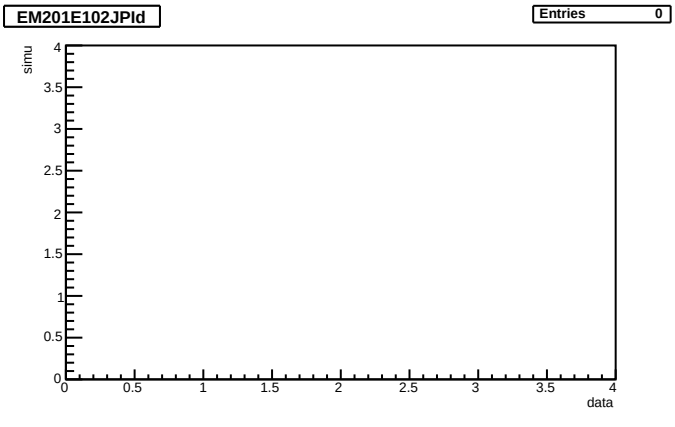

EM201E102JPThBits
Entries 299663
JP th bits

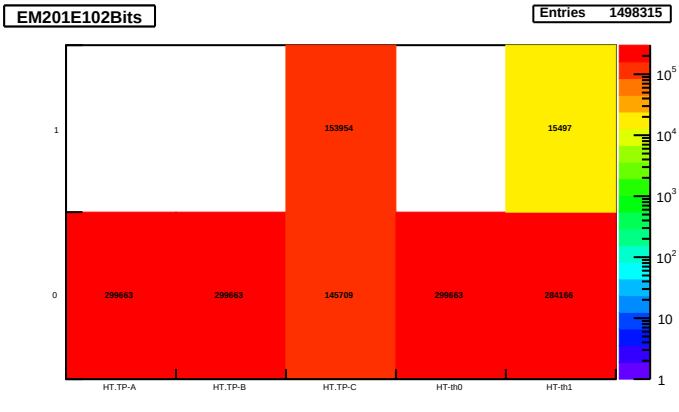

EM201E102JPThBits
Entries 0
simu
data

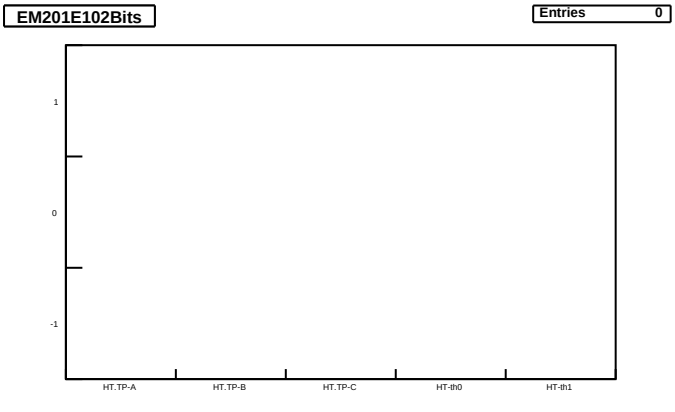

EM201E102JPSum
Entries 299663
JP partial sum

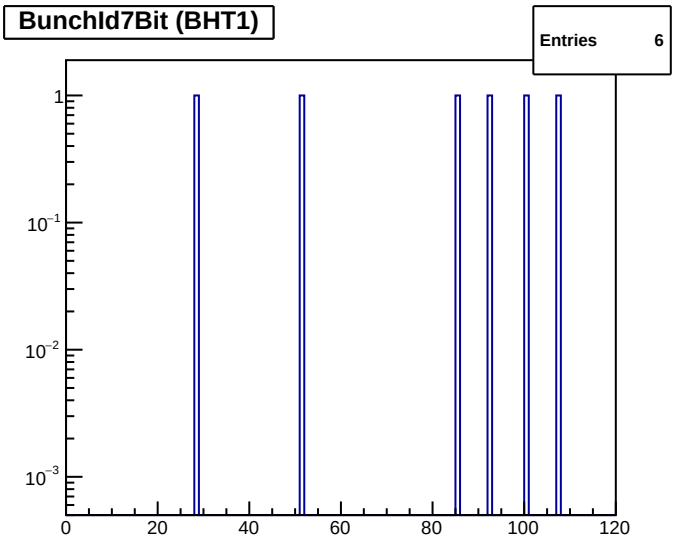

EM201E102JPSum
Entries 0
simu
data

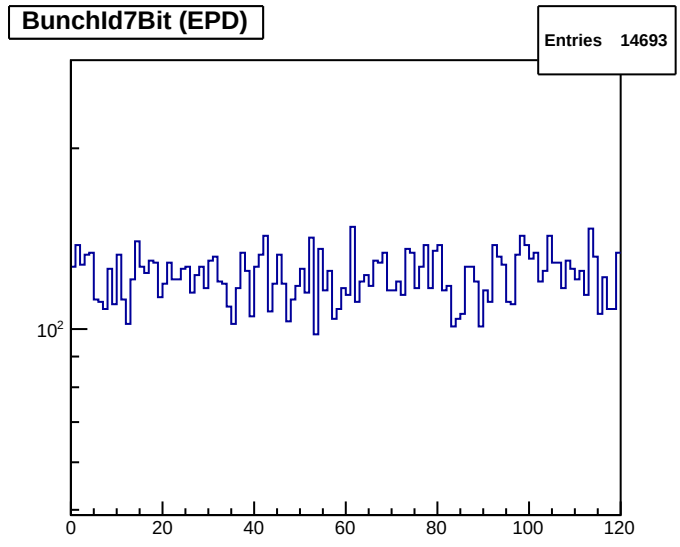

EM201E102JPId
Entries 299663
JP partial Id

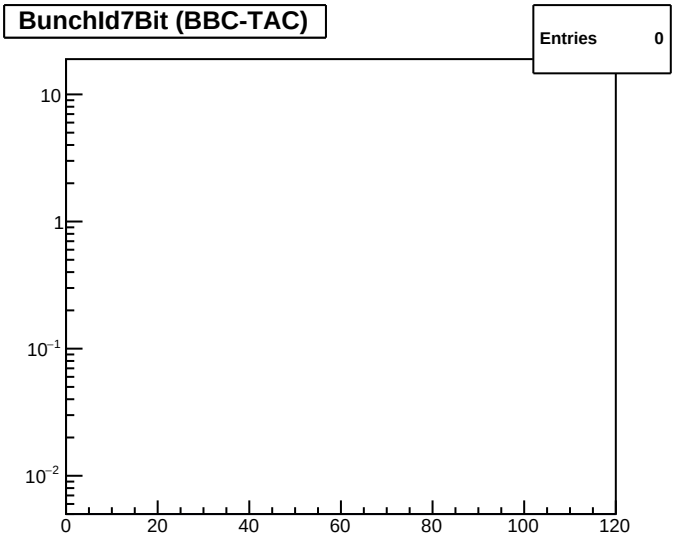

EM201E102JPId
Entries 0
simu
data

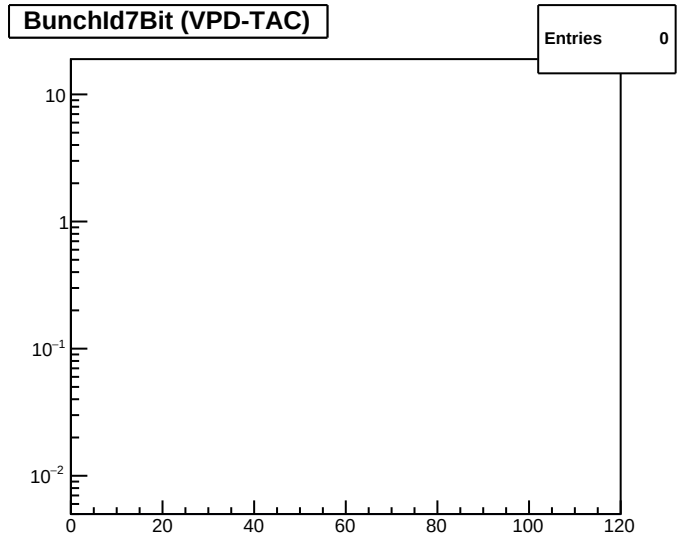

EM201E102Bits
Entries 1498315
153954
15497
299663
299663
145709
299663
284166
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

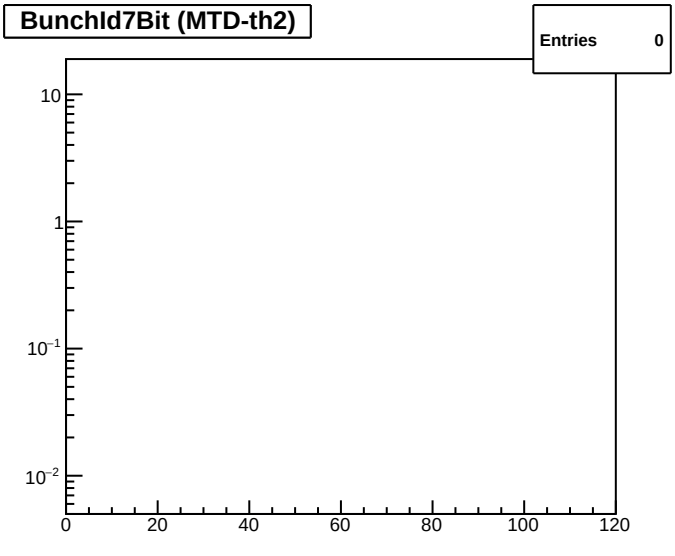

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

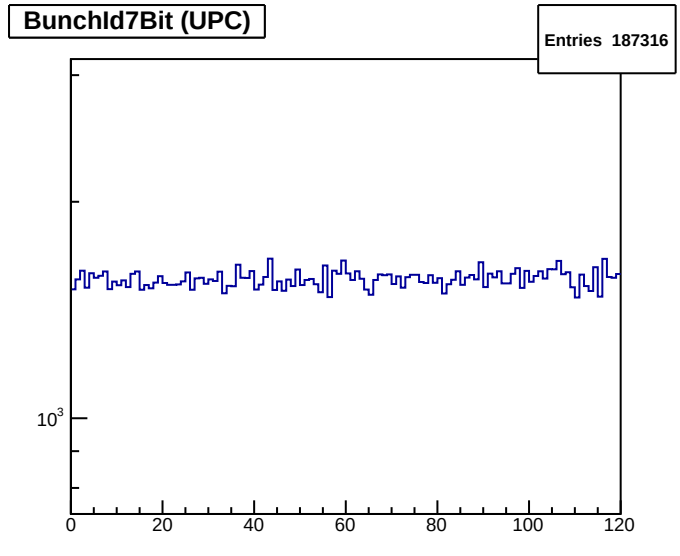

BunchId7Bit (BHT1)
Entries 6
1
10^{-1}
10^{-2}
10^{-3}
0 20 40 60 80 100 120

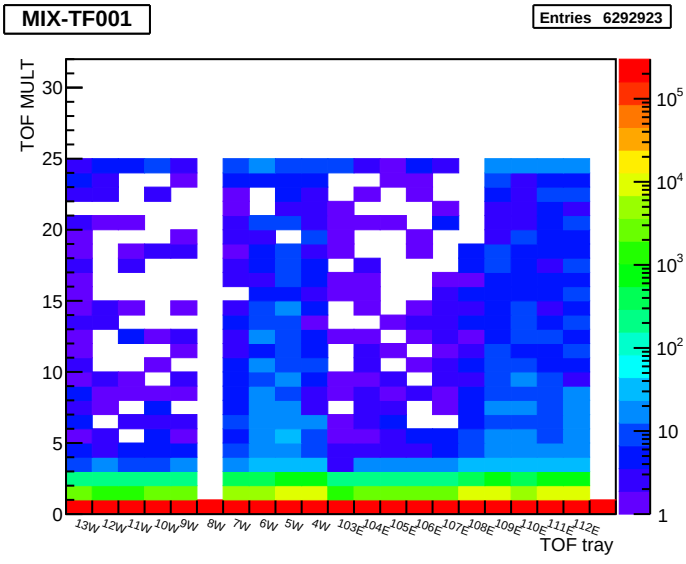

BunchId7Bit (EPD)
Entries 14693
10^{2}
0 20 40 60 80 100 120

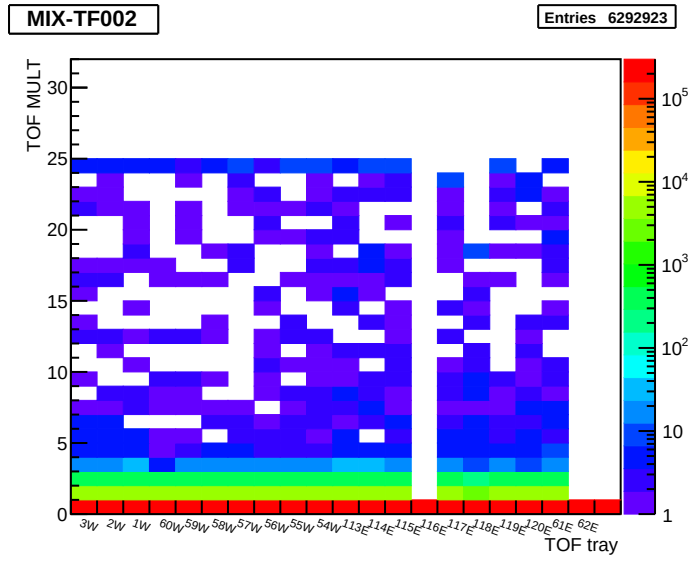

BunchId7Bit (BBC-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

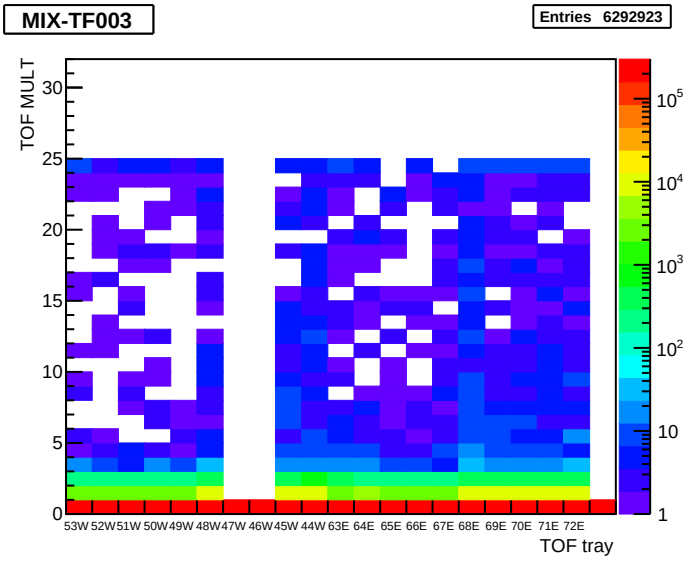

BunchId7Bit (VPD-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

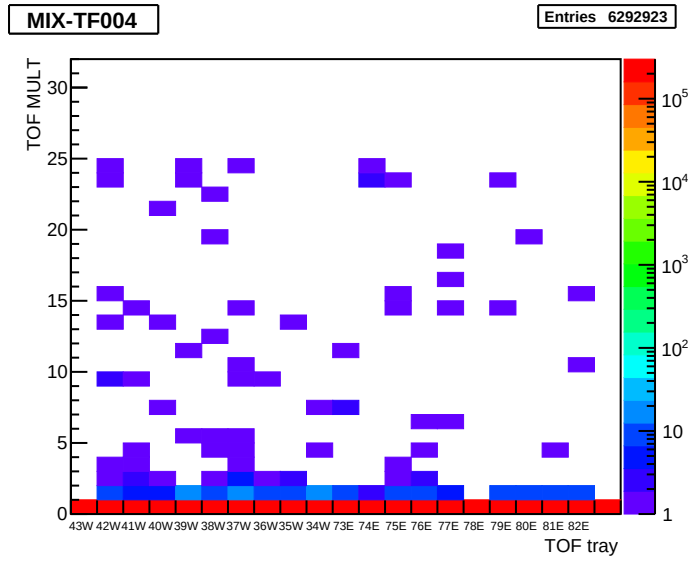

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

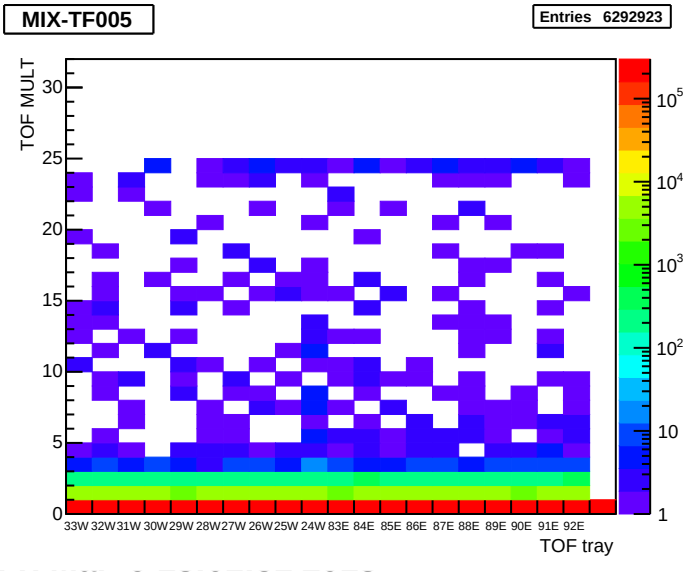

BunchId7Bit (UPC)
Entries 187316
10^{3}
0 20 40 60 80 100 120

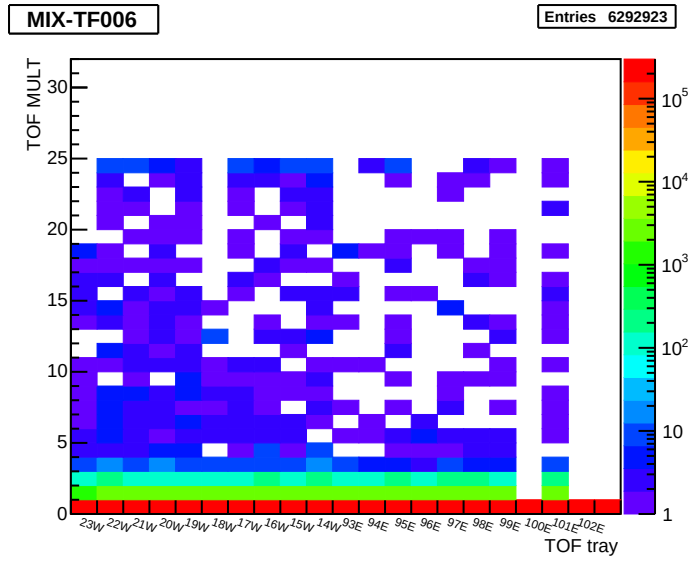

MIX-TF001
Entries 6292923
TOF MULT
TOF tray

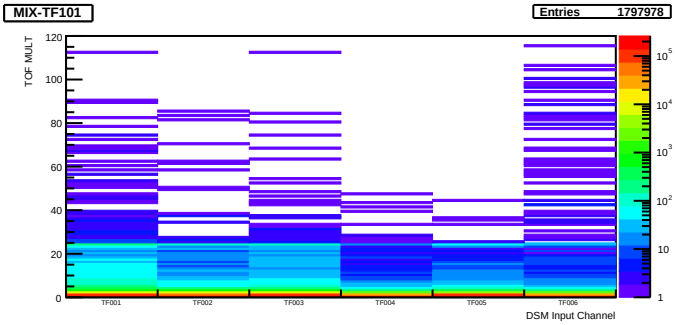

MIX-TF002
Entries 6292923
TOF MULT
TOF tray

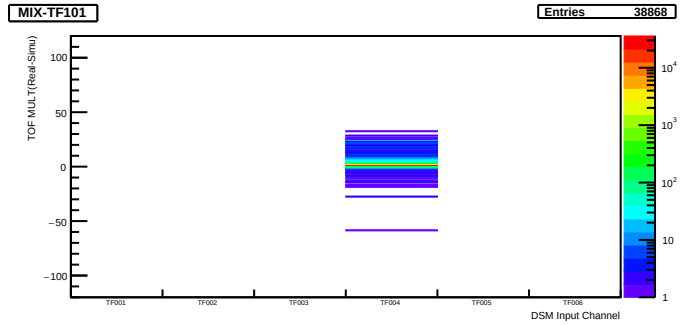

MIX-TF003
Entries 6292923
TOF MULT
TOF tray

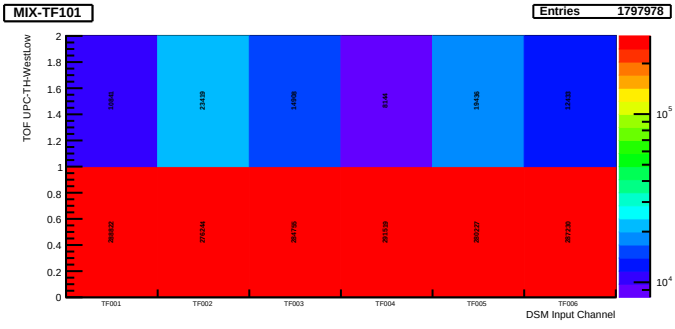

MIX-TF004
Entries 6292923
TOF MULT
TOF tray

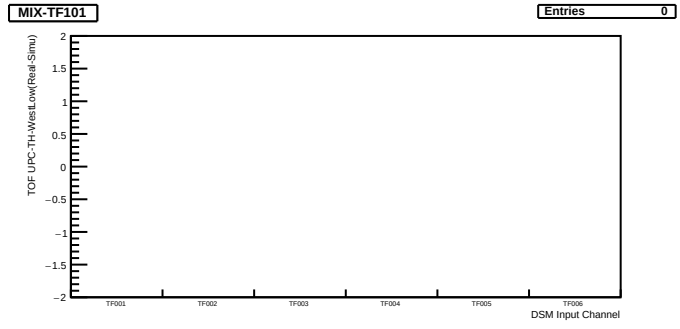

MIX-TF005
Entries 6292923
TOF MULT
TOF tray

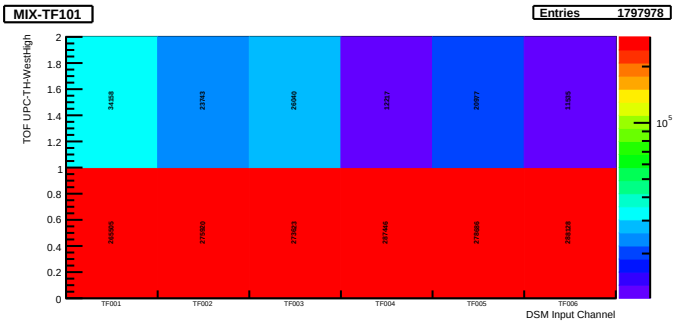

MIX-TF006
Entries 6292923
TOF MULT
TOF tray

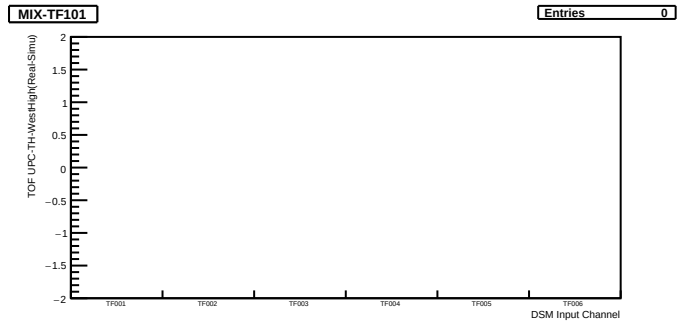

MIX-TF101
Entries 1797978
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

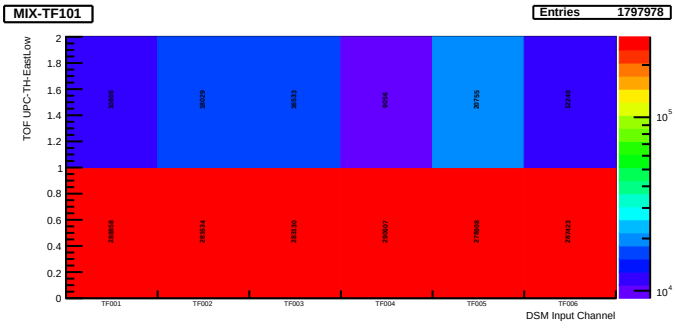

MIX-TF101
Entries 38868
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

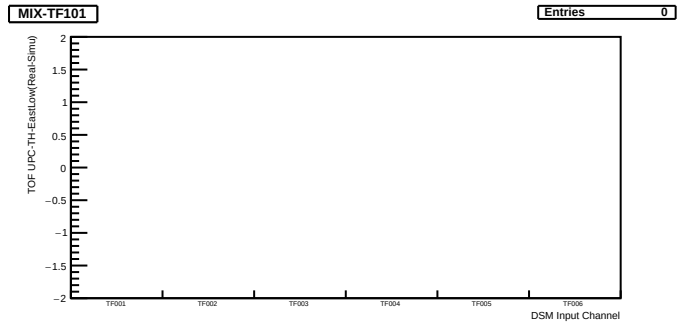

MIX-TF101
Entries 1797978
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

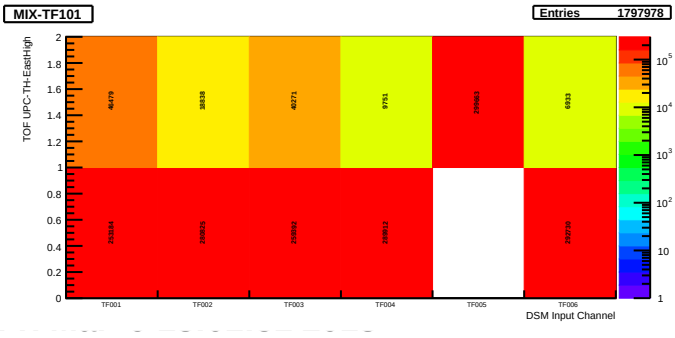

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

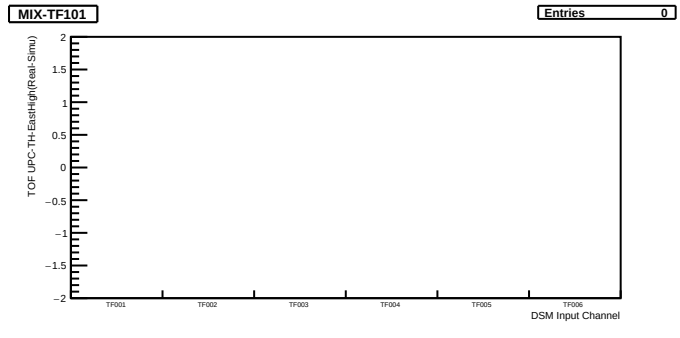

MIX-TF101
Entries 1797978
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

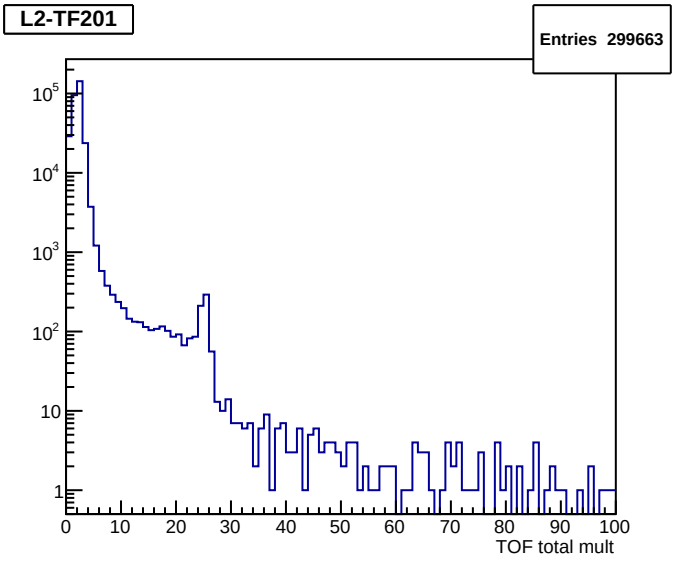

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

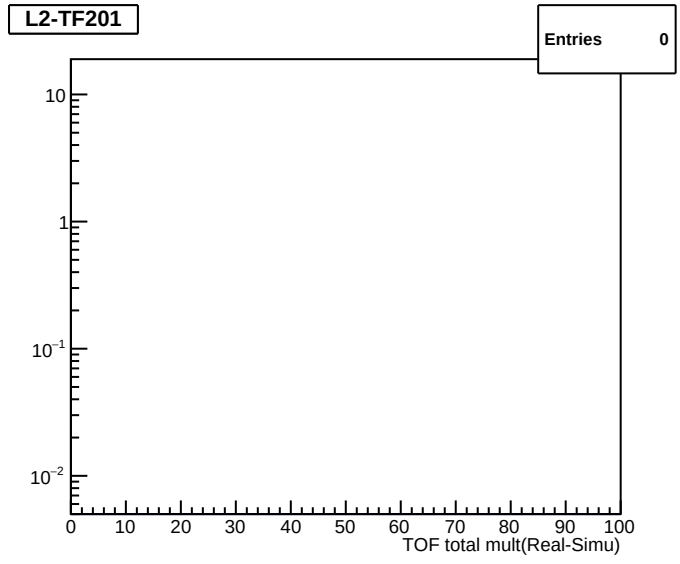

MIX-TF101
Entries 1797978
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

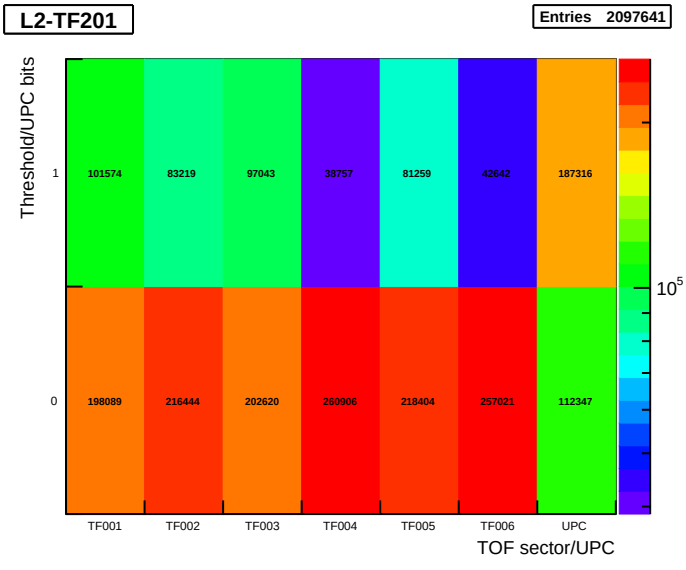

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

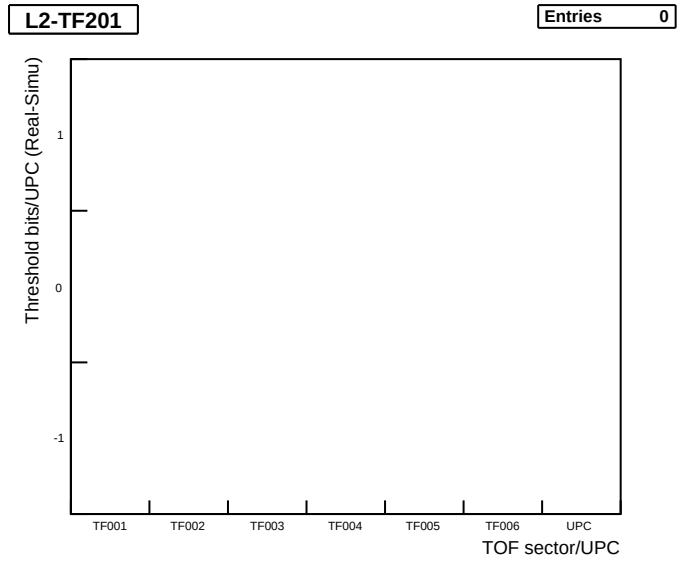

MIX-TF101
Entries 1797978
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

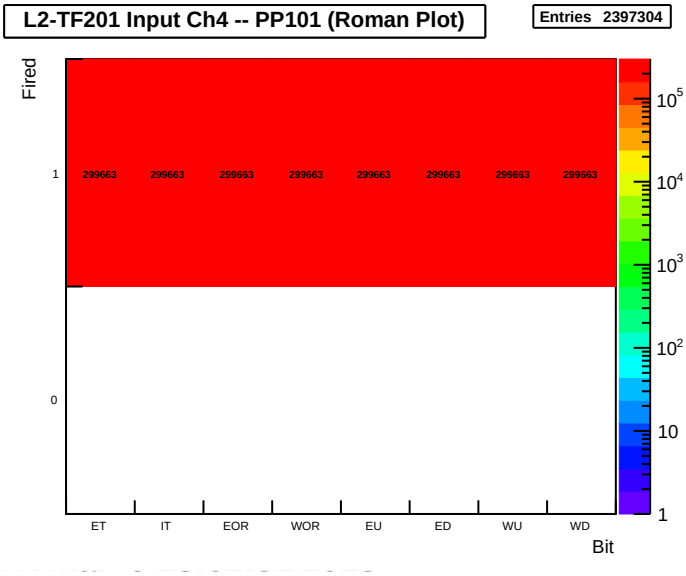

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

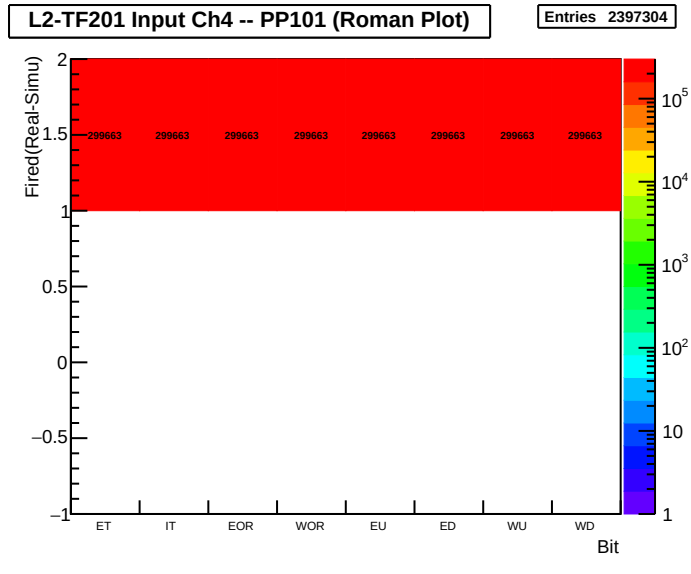

L2-TF201
Entries 299663
TOF total mult

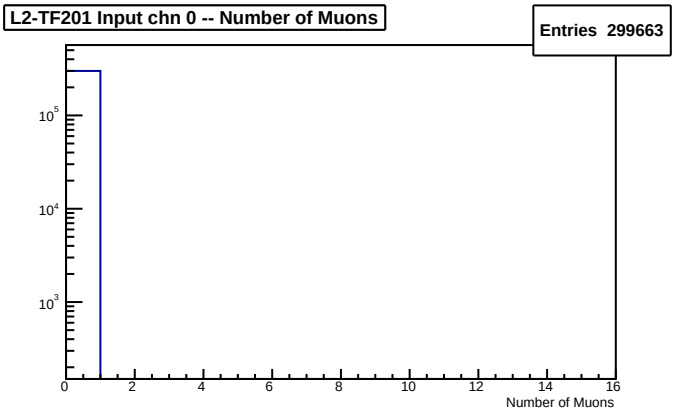

L2-TF201
Entries 0
TOF total mult(Real-Simu)

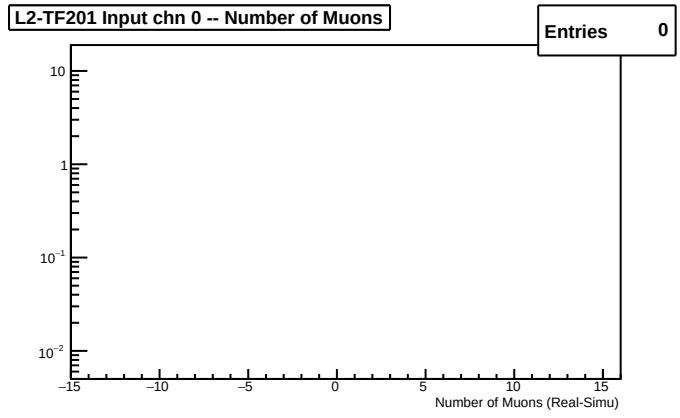

L2-TF201
Entries 2097641
Threshold/UPC bits
101574
83219
97043
38757
81259
42642
187316
198089
216444
202620
260906
218404
257021
112347
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

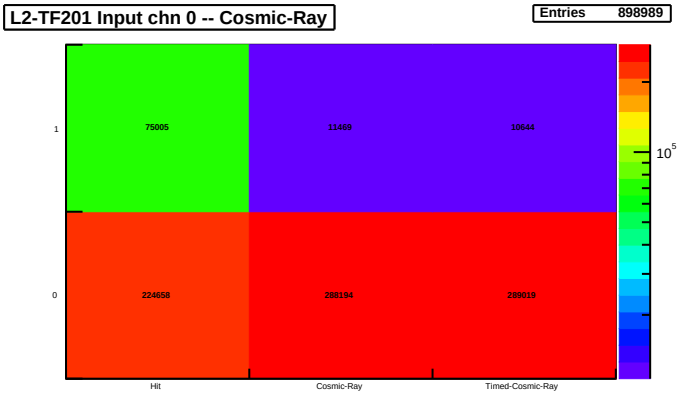

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

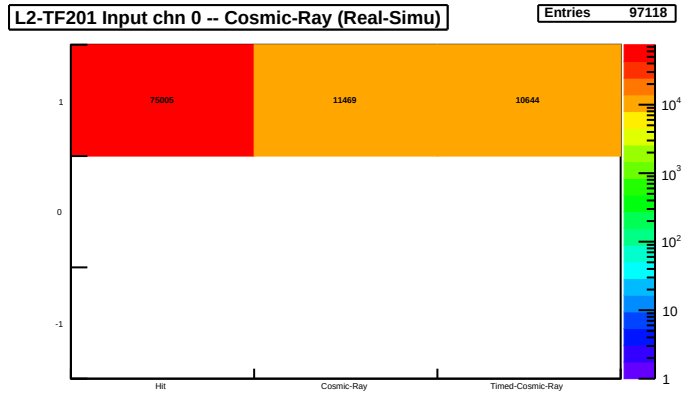

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 2397304
Fired
299663
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

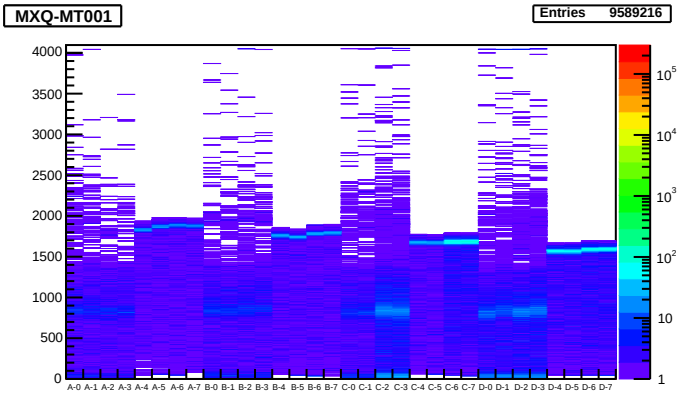

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 2397304
Fired(Real-Simu)
299663
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

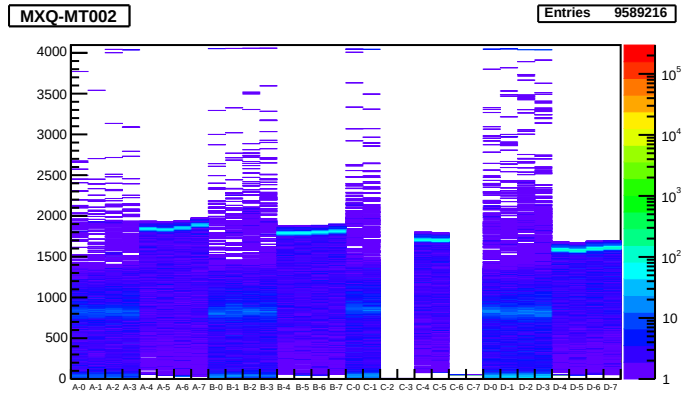

L2-TF201 Input chn 0 -- Number of Muons
Entries 299663
Number of Muons

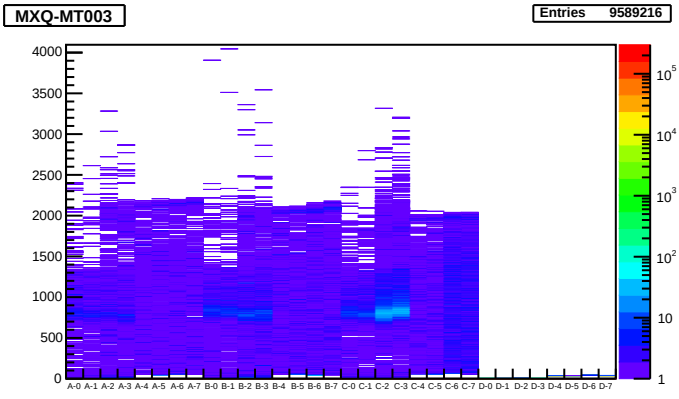

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

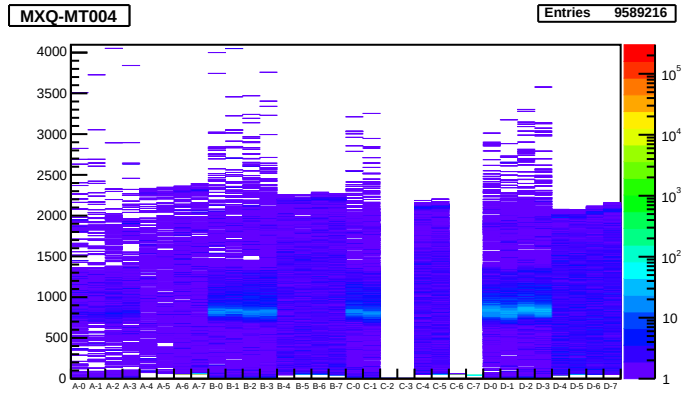

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 898989
75005
11469
10644
224658
288194
289019
Hit
Cosmic-Ray
Timed-Cosmic-Ray

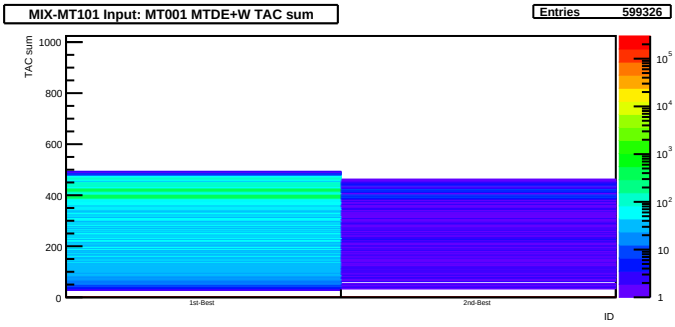

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 97118
75005
11469
10644
Hit
Cosmic-Ray
Timed-Cosmic-Ray

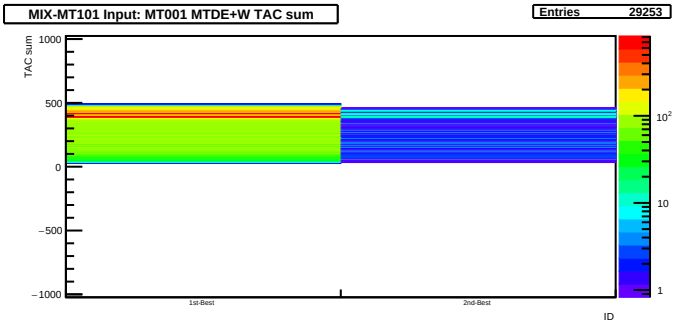

MXQ-MT001
Entries 9589216
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

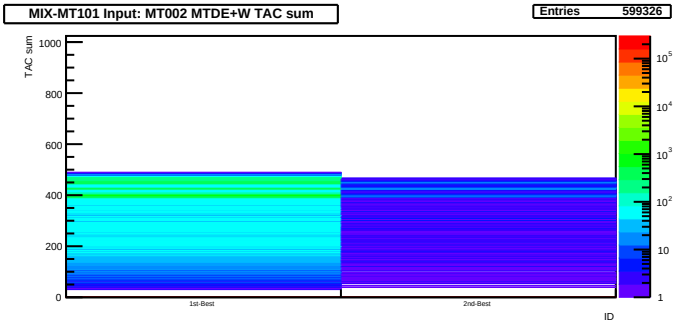

MXQ-MT002
Entries 9589216
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

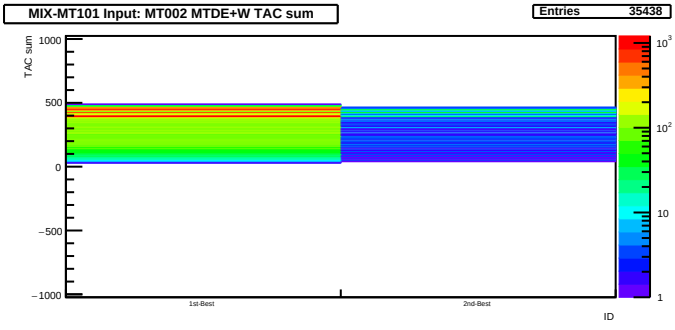

MXQ-MT003
Entries 9589216
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

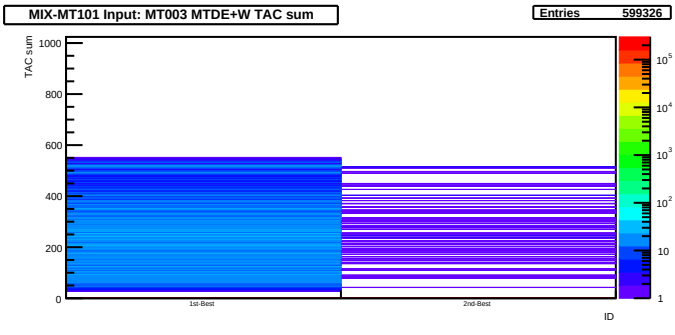

MXQ-MT004
Entries 9589216
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

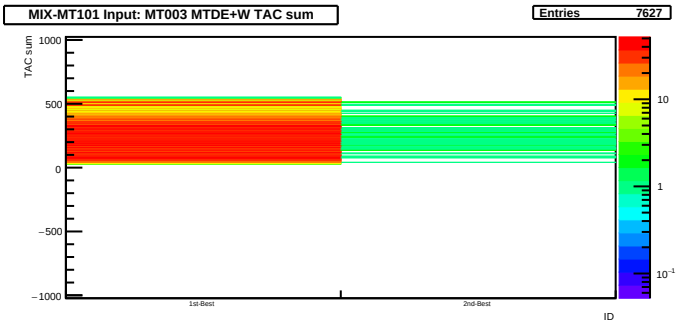

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 599326
TAC sum
1st-Best
2nd-Best
ID

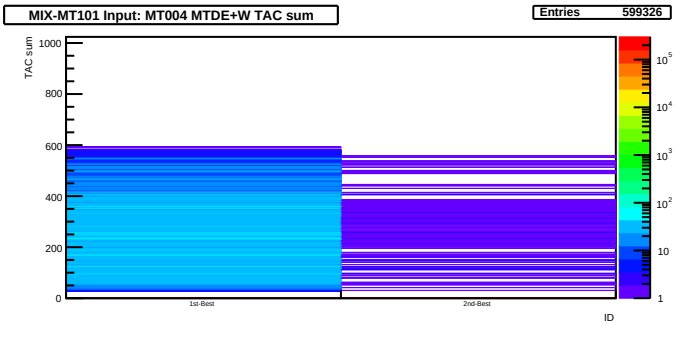

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 29253
TAC sum
1st-Best
2nd-Best
ID

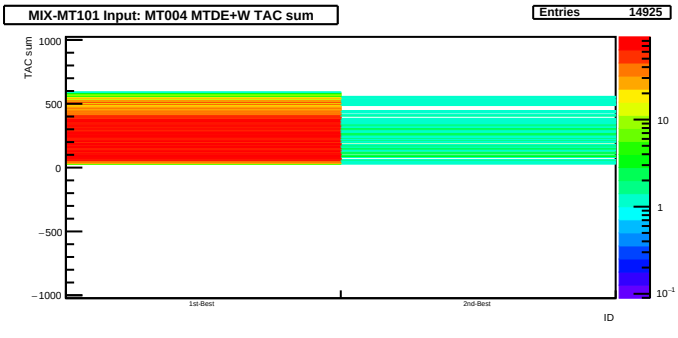

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 599326
TAC sum
1st-Best
2nd-Best
ID

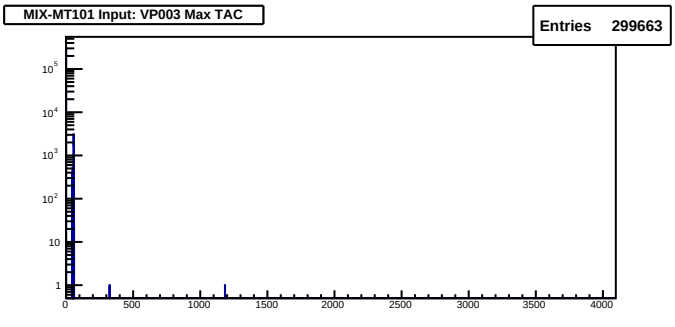

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 35438
TAC sum
1st-Best
2nd-Best
ID

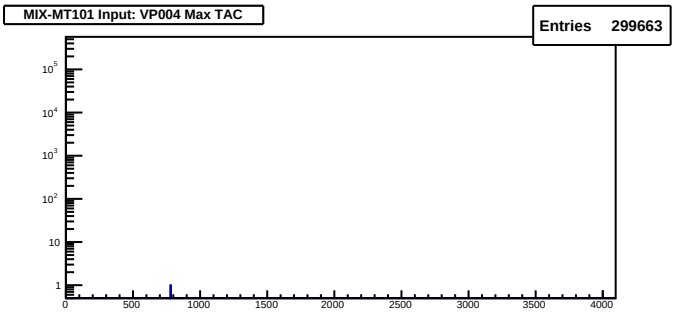

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 599326
TAC sum
1st-Best
2nd-Best
ID

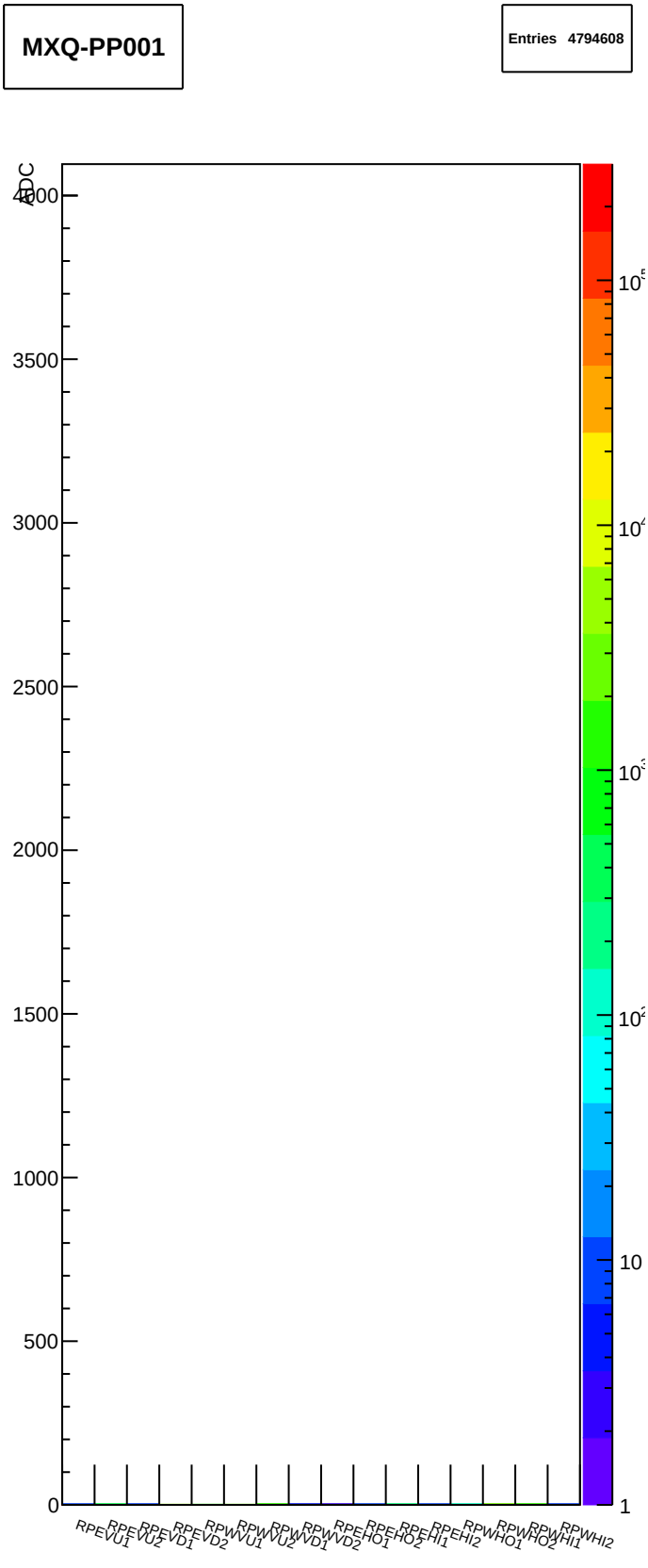

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 7627
TAC sum
1st-Best
2nd-Best
ID


MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 599326
TAC sum
1st-Best
2nd-Best
ID


MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 14925
TAC sum
1st-Best
2nd-Best
ID


MIX-MT101 Input: VP003 Max TAC
Entries 299663


MIX-MT101 Input: VP004 Max TAC
Entries 299663


MXQ-PP001
Entries 4794608
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
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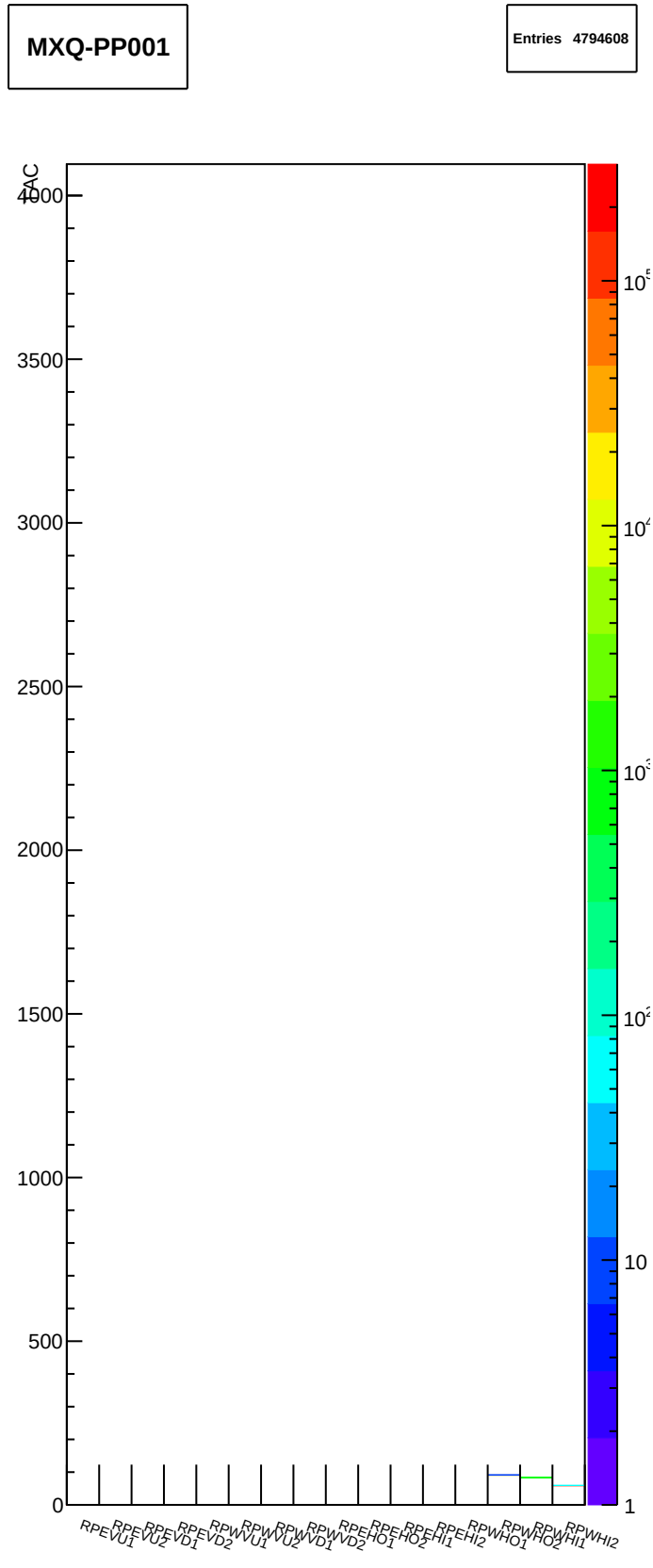
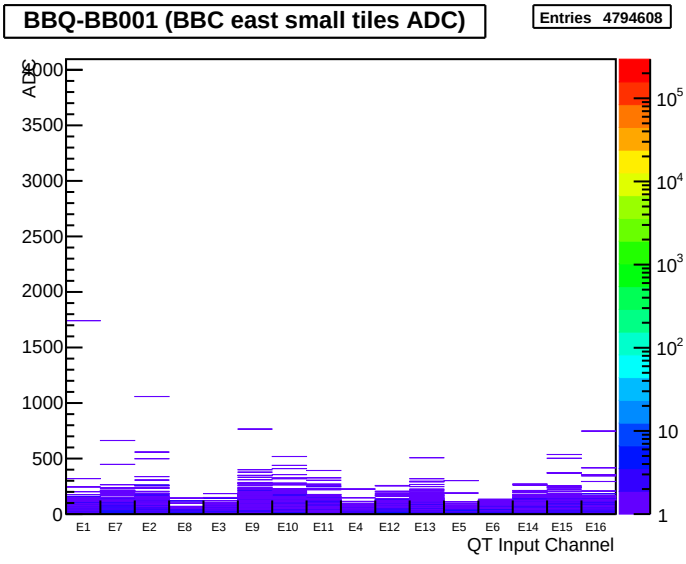
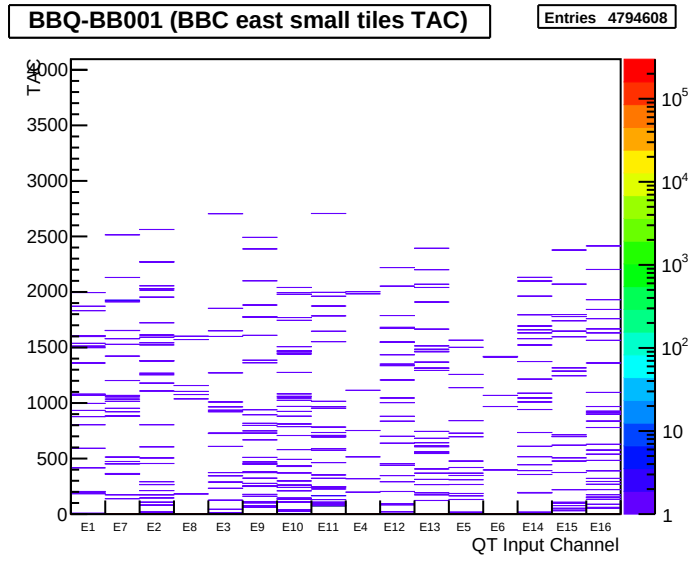
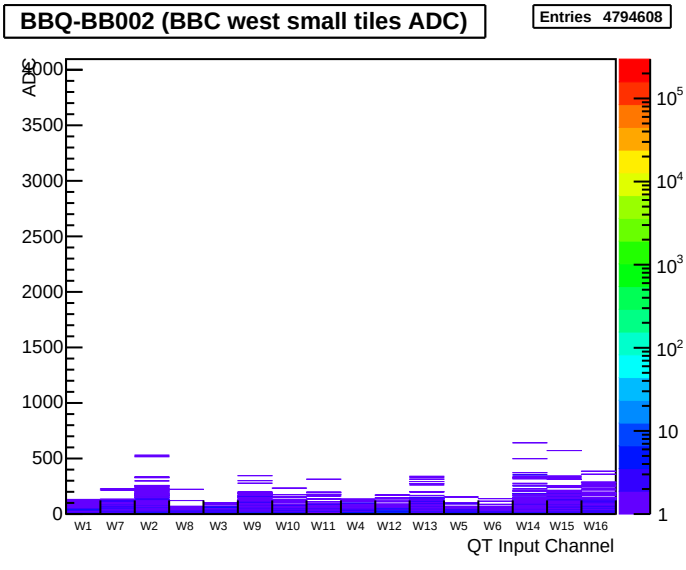
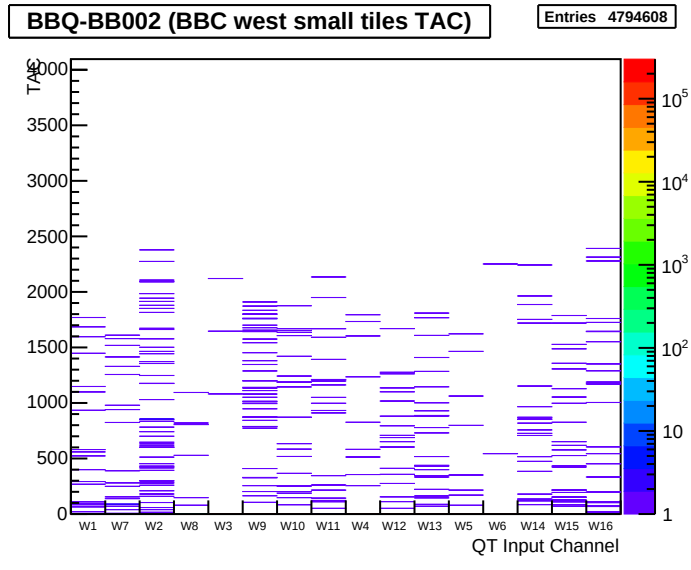
RPWHO2
RPWHI1
RPWHI2
10
1

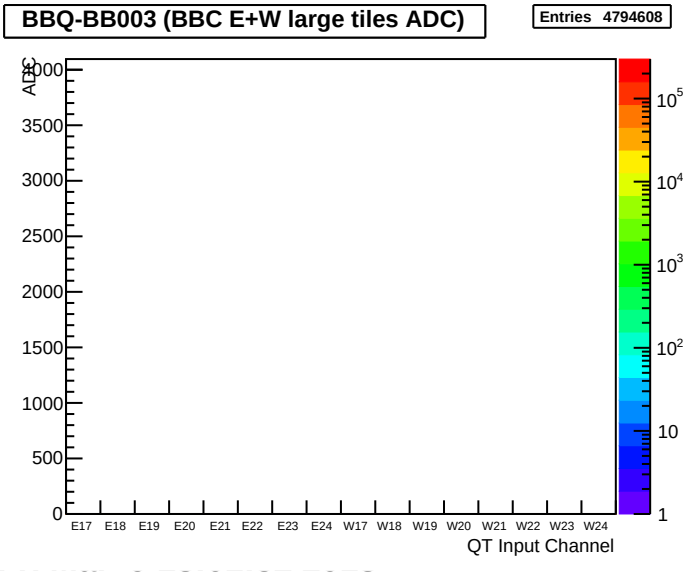

MXQ-PP001
Entries 4794608
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

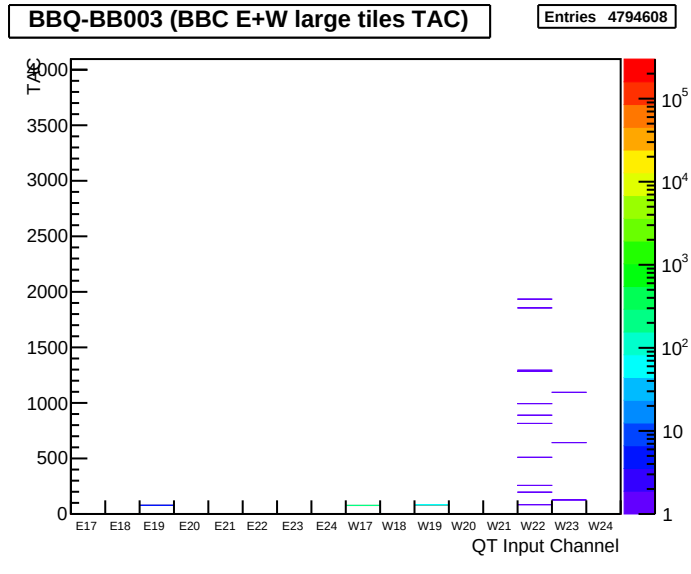

BBQ-BB001 (BBC east small tiles ADC)
Entries 4794608
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

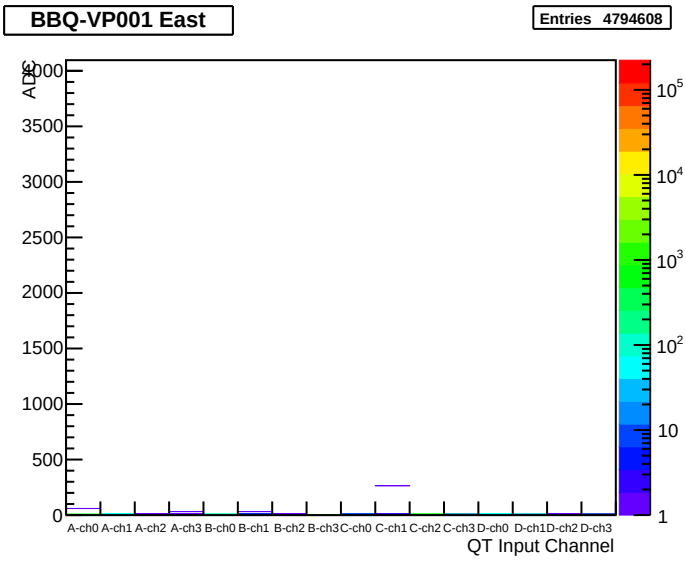

BBQ-BB001 (BBC east small tiles TAC)
Entries 4794608
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

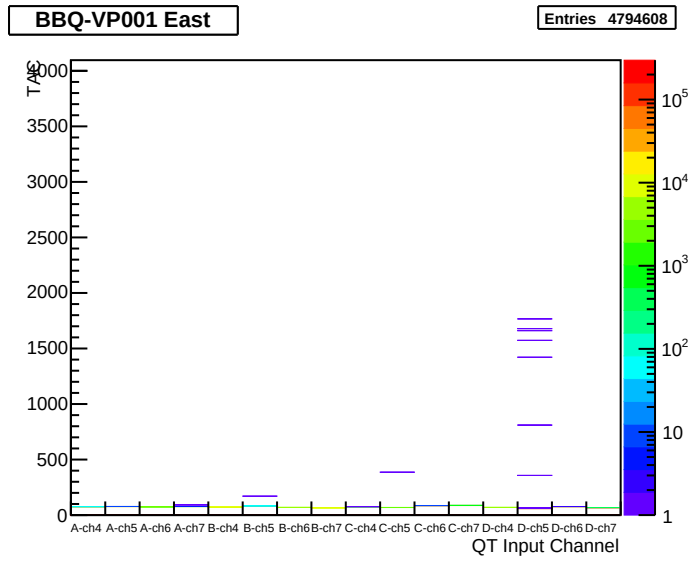

BBQ-BB002 (BBC west small tiles ADC)
Entries 4794608
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

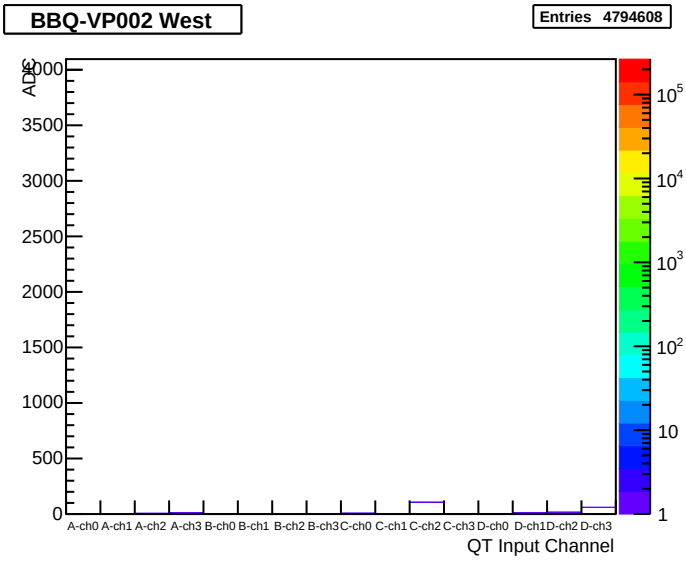

BBQ-BB002 (BBC west small tiles TAC)
Entries 4794608
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

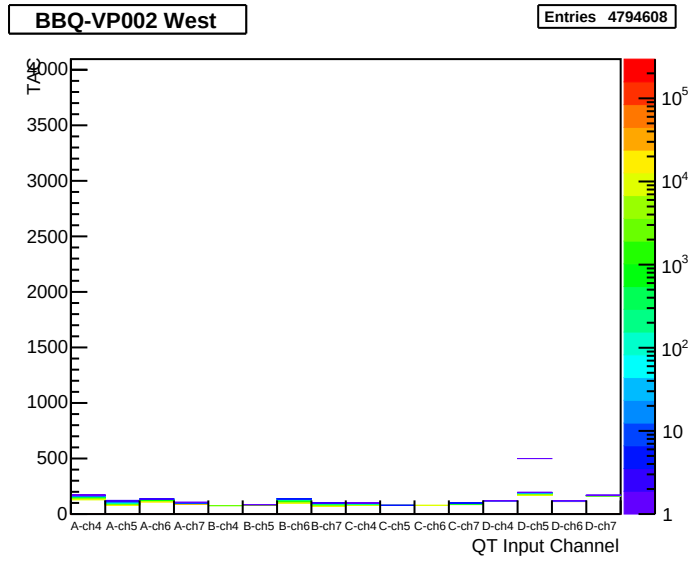

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4794608
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

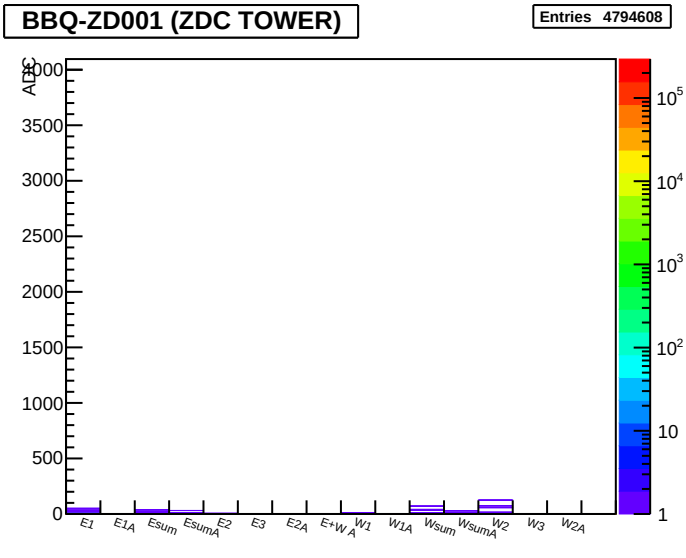

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4794608
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

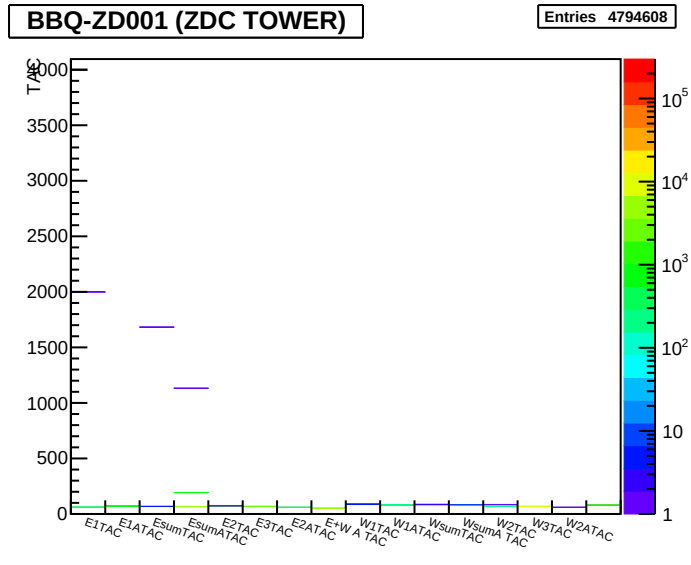

BBQ-VP001 East
Entries 4794608
ADC
QT Input Channel

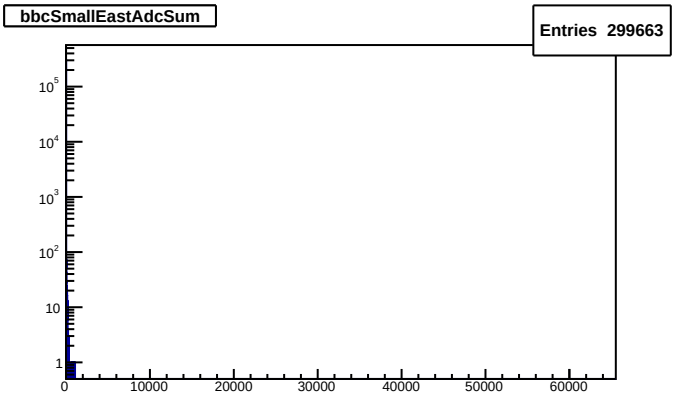

BBQ-VP001 East
Entries 4794608
TAC
QT Input Channel

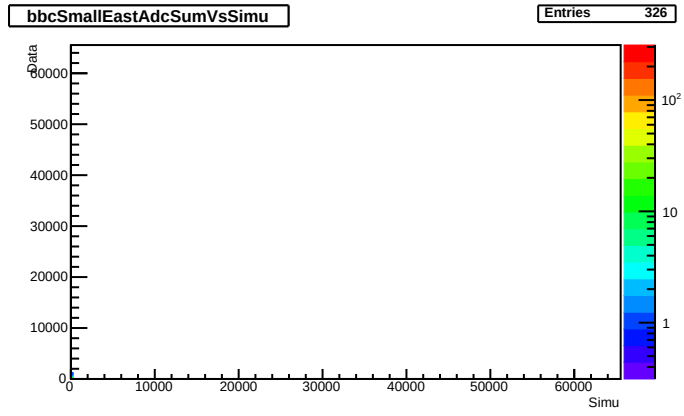

BBQ-VP002 West
Entries 4794608
ADC
QT Input Channel

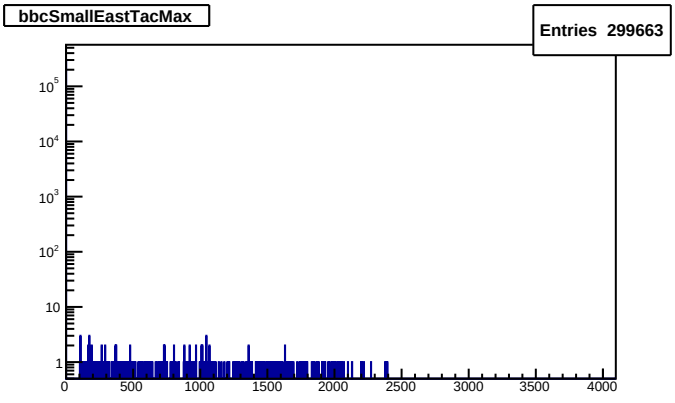

BBQ-VP002 West
Entries 4794608
TAC
QT Input Channel

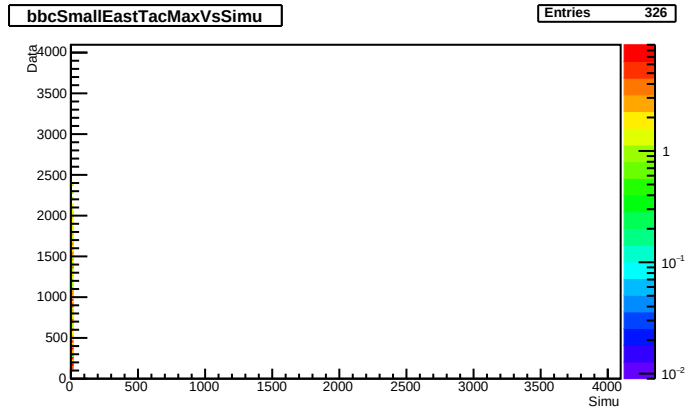

BBQ-ZD001 (ZDC TOWER)
Entries 4794608
ADC

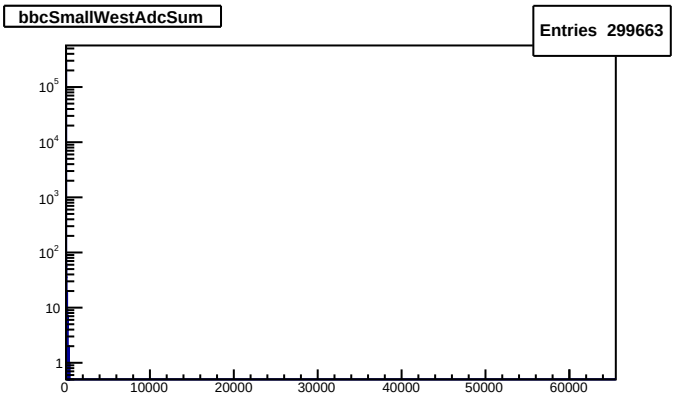

BBQ-ZD001 (ZDC TOWER)
Entries 4794608
TAC

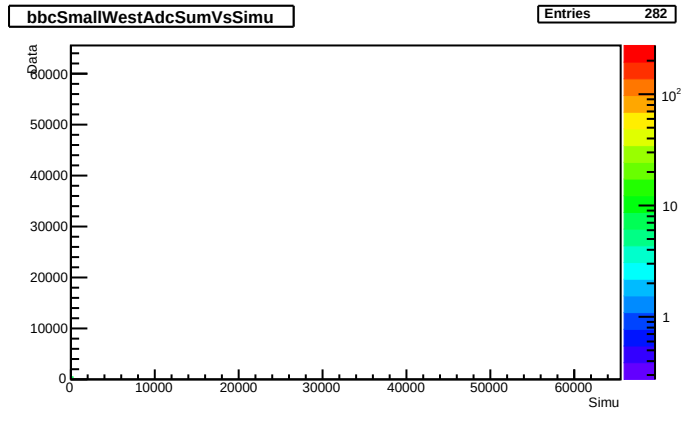

bbcSmallEastAdcSum
Entries 299663

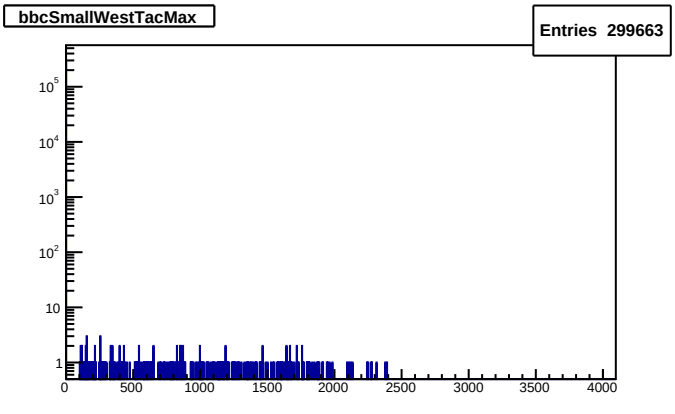

bbcSmallEastAdcSumVsSimu
Entries 326
Data
Simu

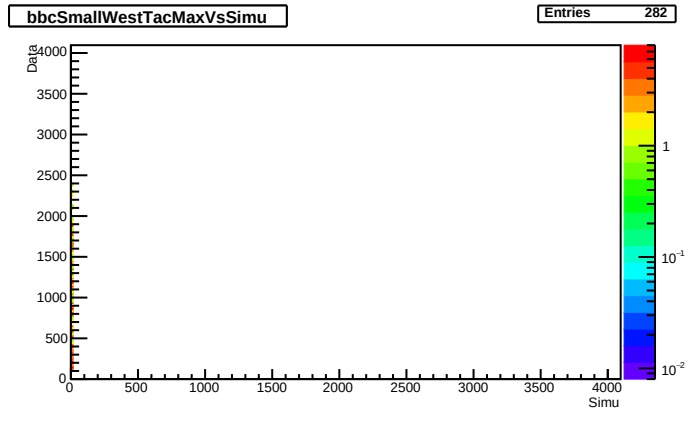

bbcSmallEastTacMax
Entries 299663

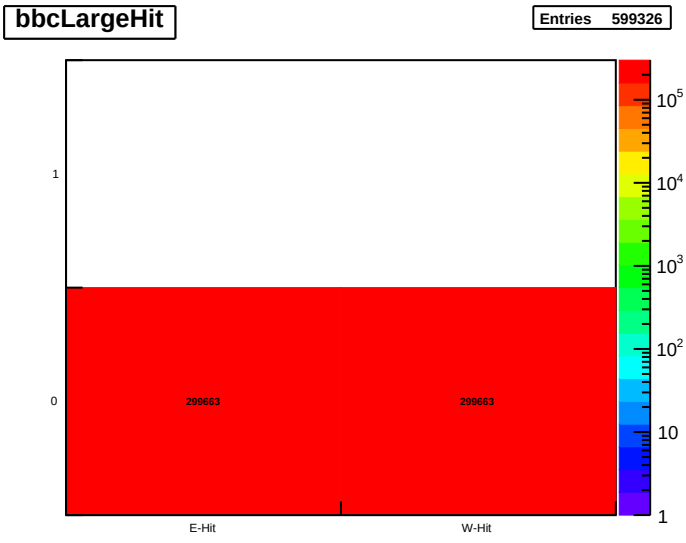

bbcSmallEastTacMaxVsSimu
Entries 326
Data
Simu

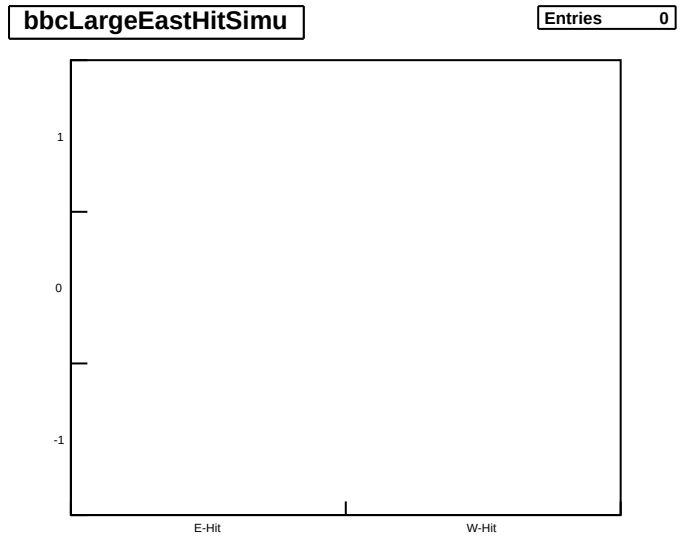

bbcSmallWestAdcSum
Entries 299663

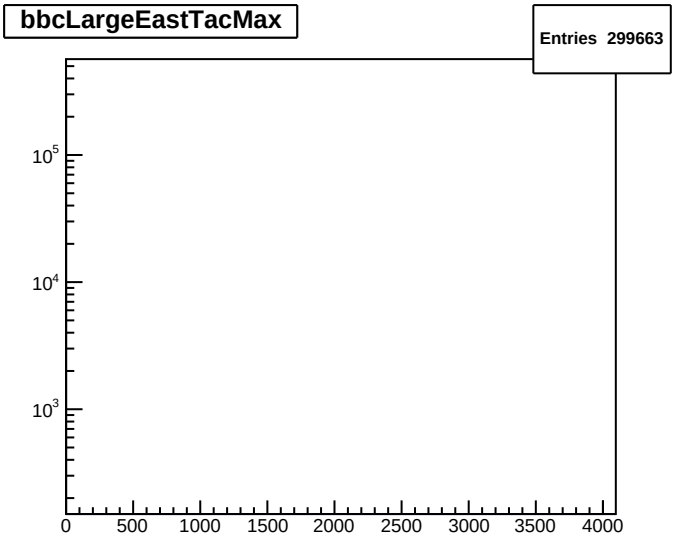

bbcSmallWestAdcSumVsSimu
Entries 282
Data
Simu

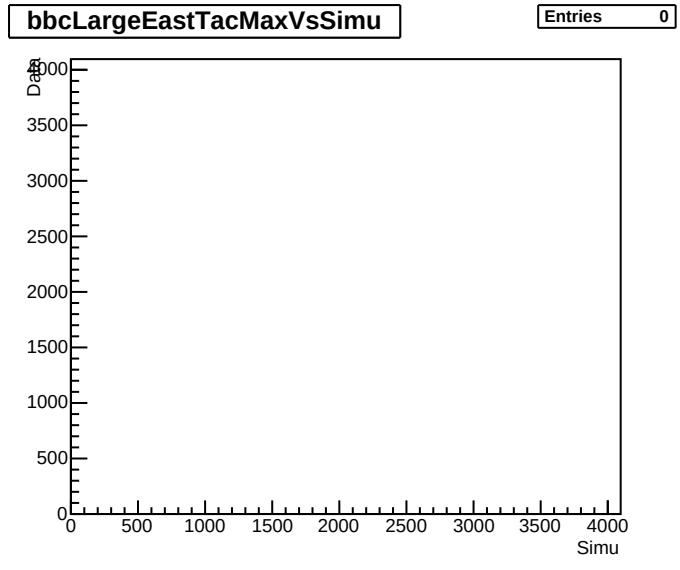

bbcSmallWestTacMax
Entries 299663

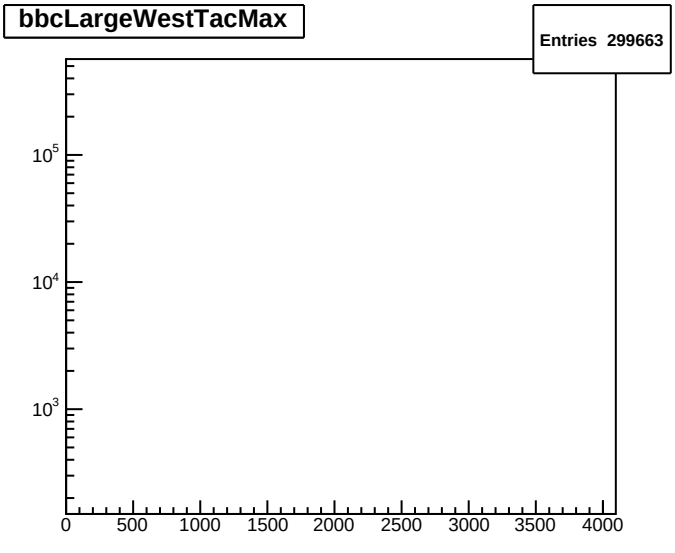

bbcSmallWestTacMaxVsSimu
Entries 282
Data
Simu

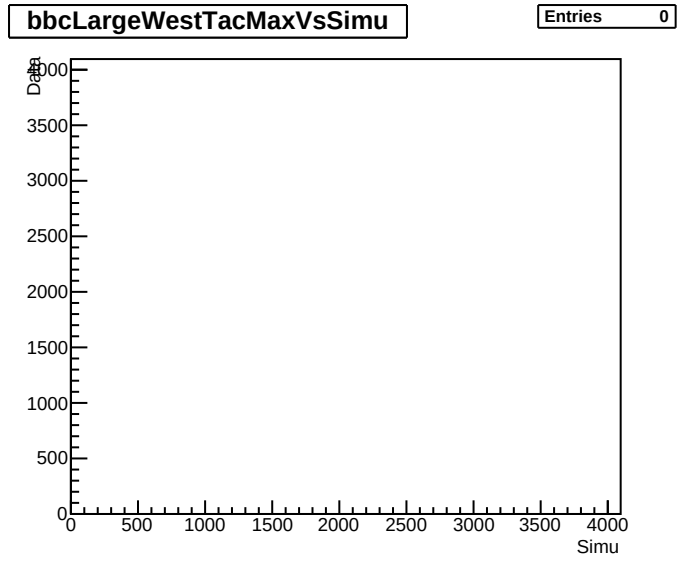

bbcLargeHit
Entries 599326
299663
299663
E-Hit
W-Hit

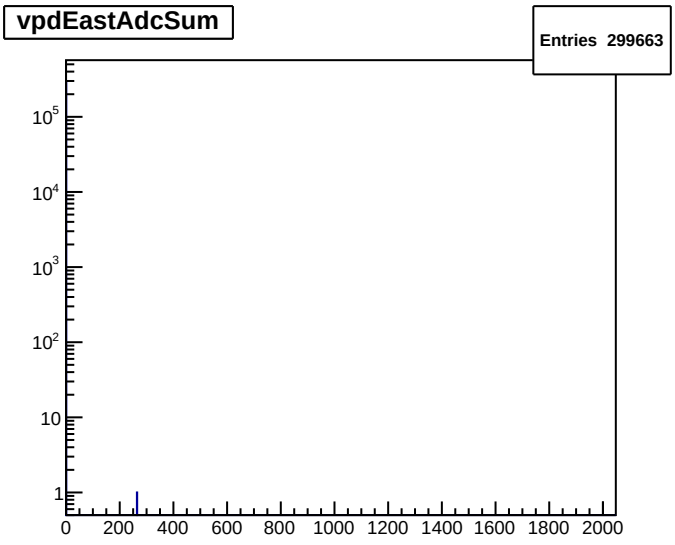

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

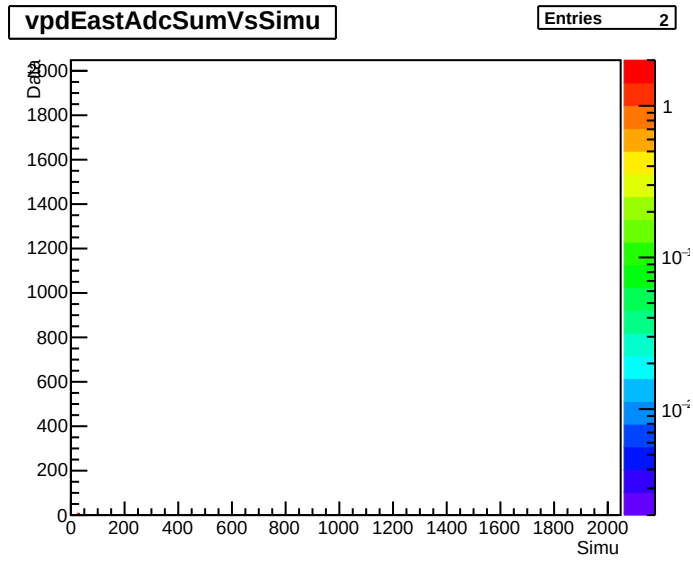

bbcLargeEastTacMax
Entries 299663

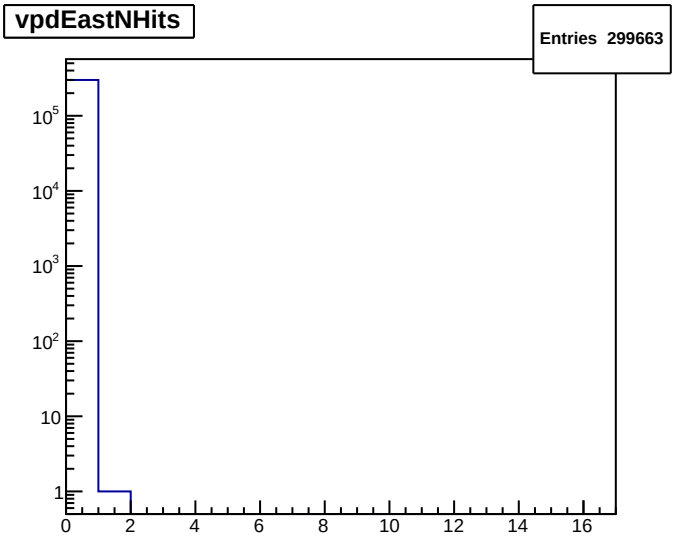

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

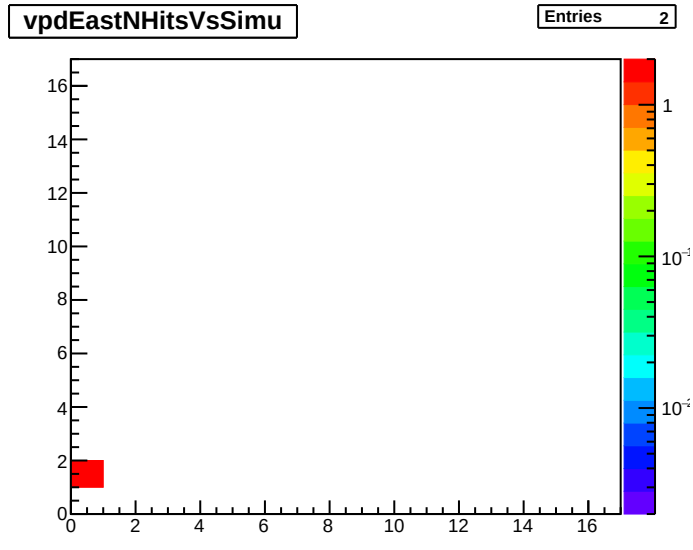

bbcLargeWestTacMax
Entries 299663

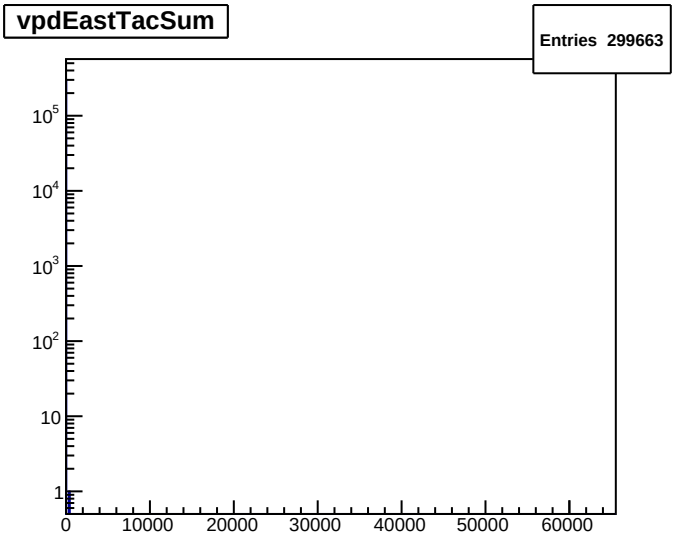

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

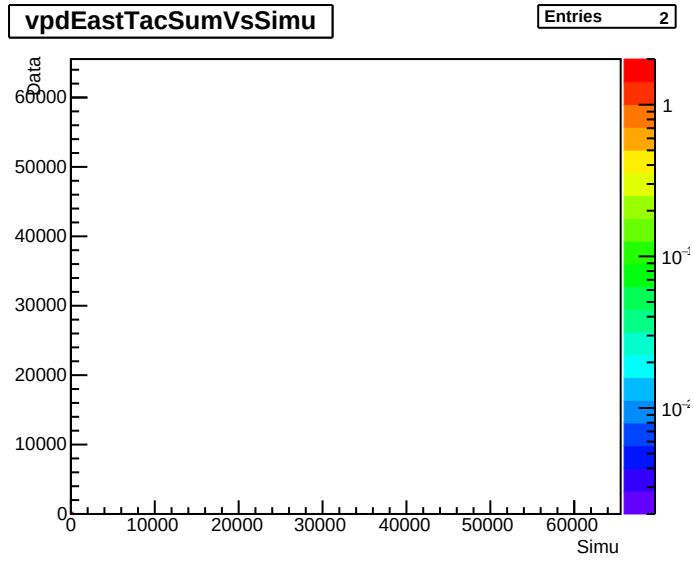

vpdEastAdcSum
Entries 299663

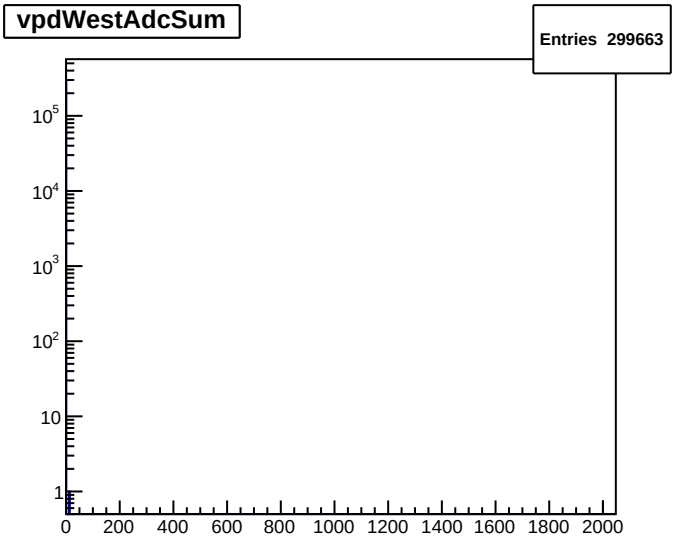

vpdEastAdcSumVsSimu
Entries 2
Data
Simu

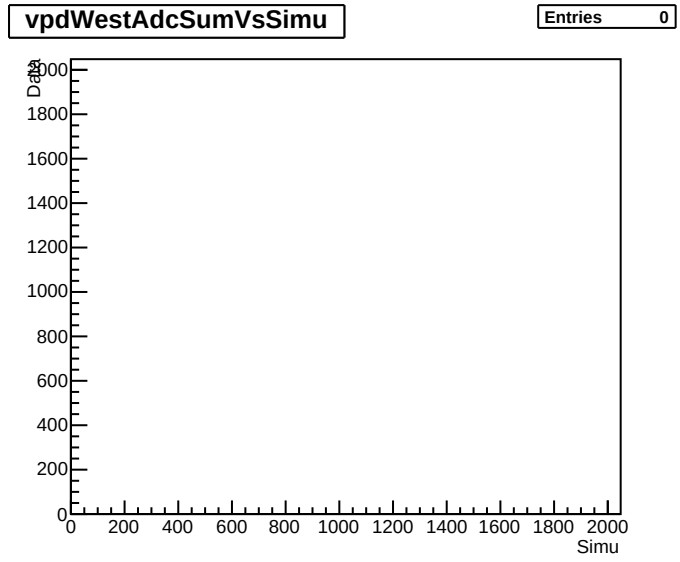

vpdEastNHits
Entries 299663

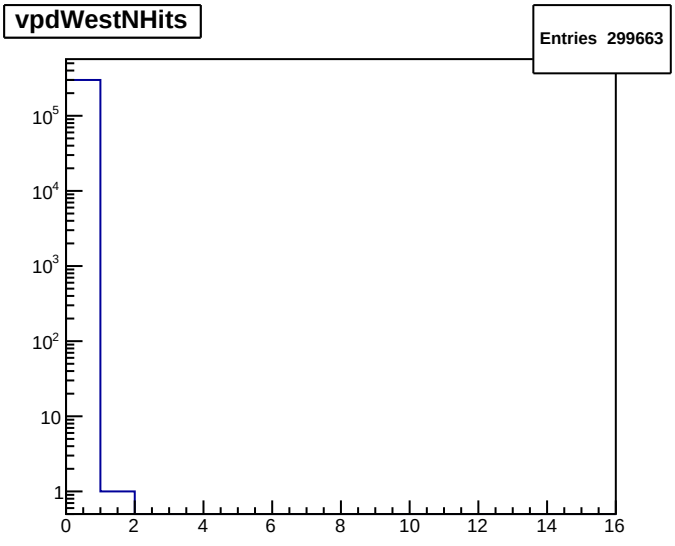

vpdEastNHitsVsSimu
Entries 2

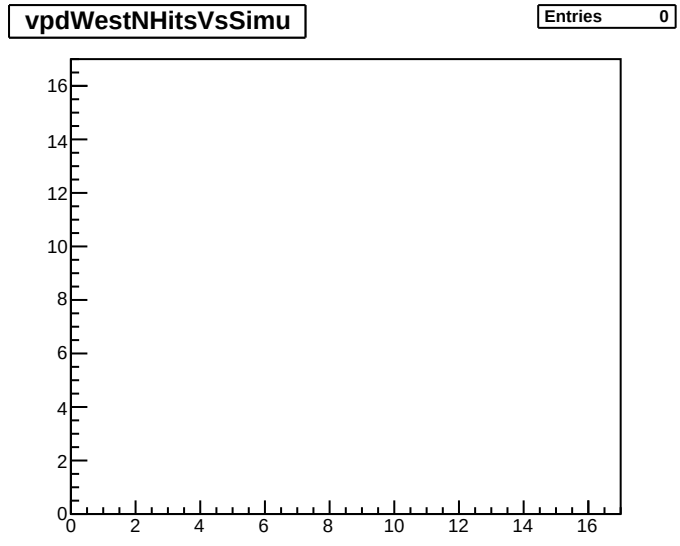

vpdEastTacSum
Entries 299663

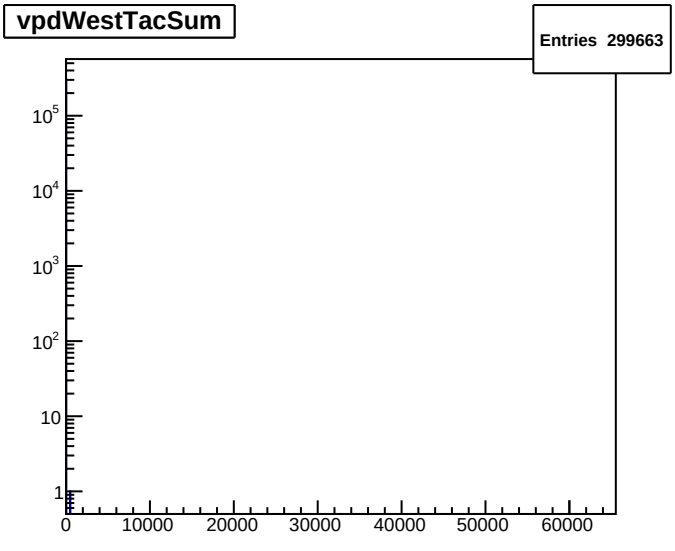

vpdEastTacSumVsSimu
Entries 2
Data
Simu

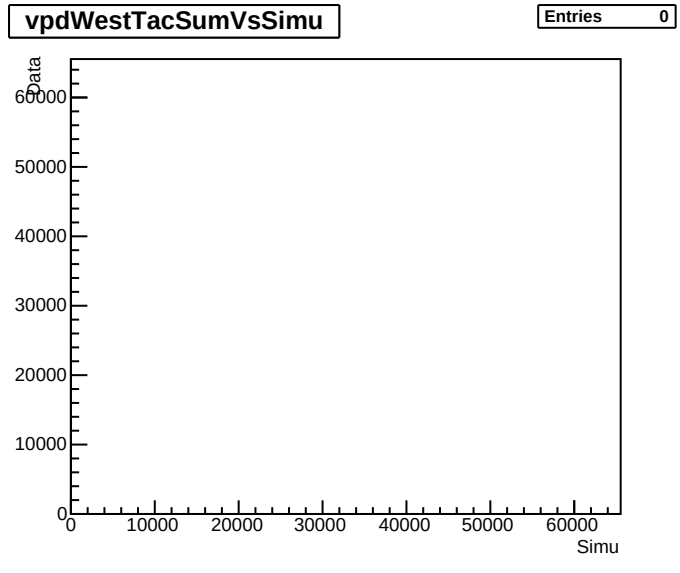

vpdWestAdcSum
Entries 299663

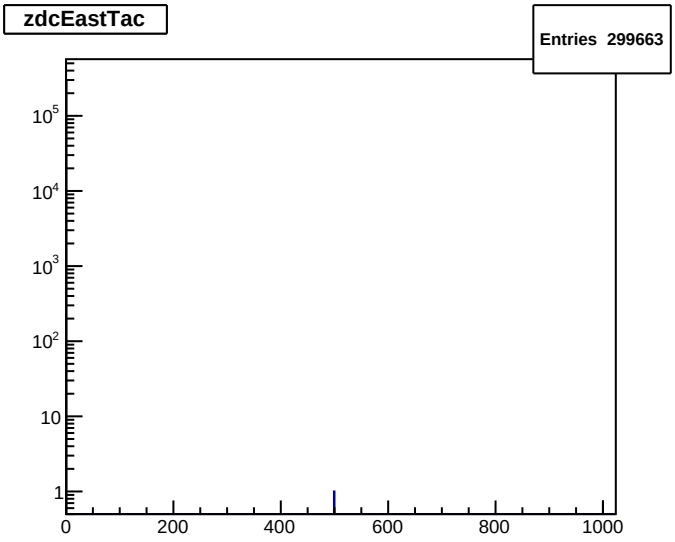

vpdWestAdcSumVsSimu
Entries 0
Data
Simu

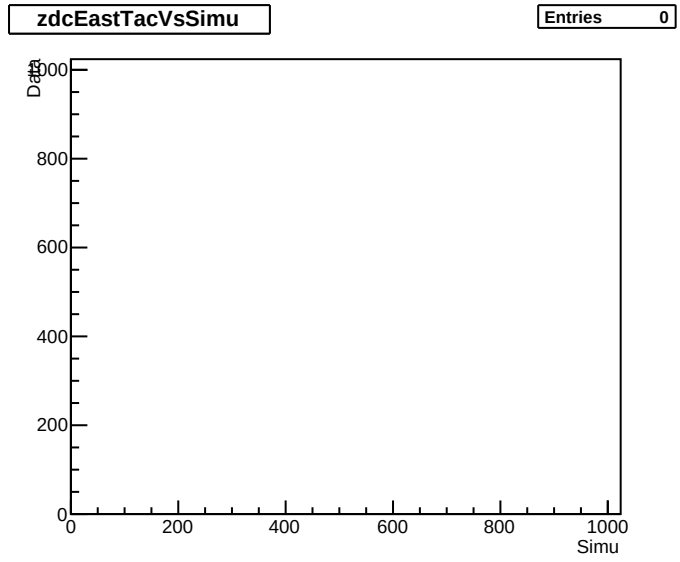

vpdWestNHits
Entries 299663

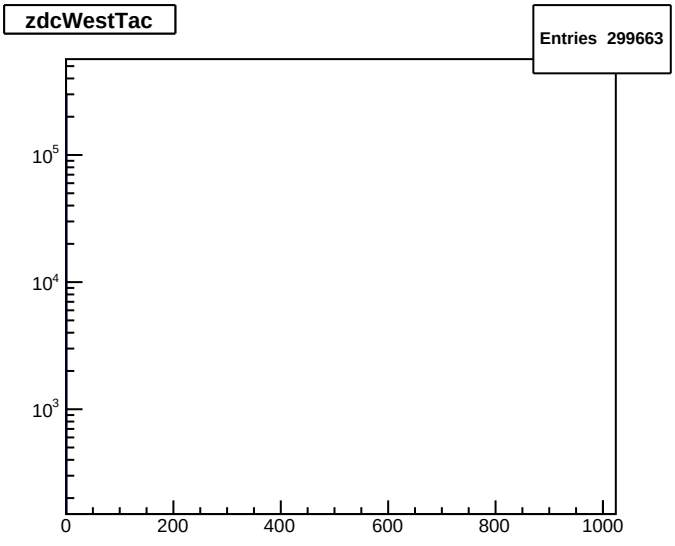

vpdWestNHitsVsSimu
Entries 0

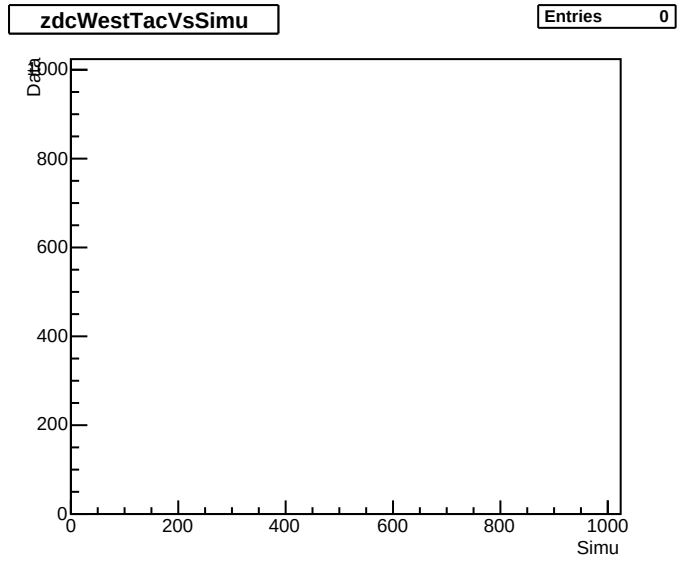

vpdWestTacSum
Entries 299663

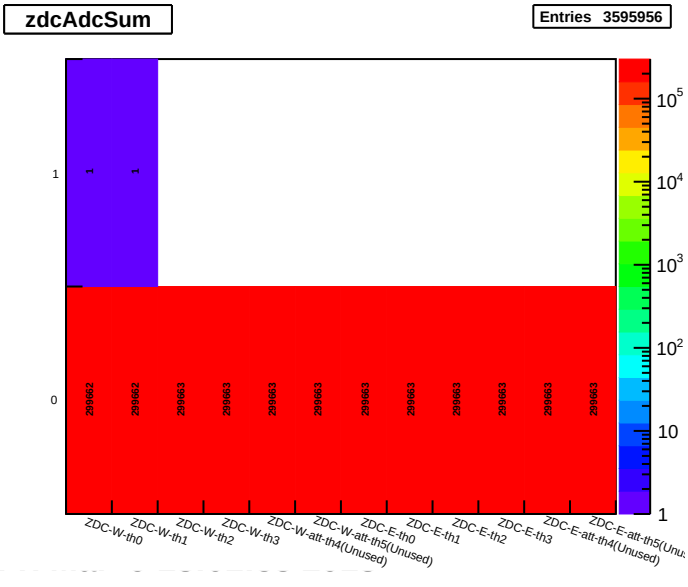

vpdWestTacSumVsSimu
Entries 0
Data
Simu

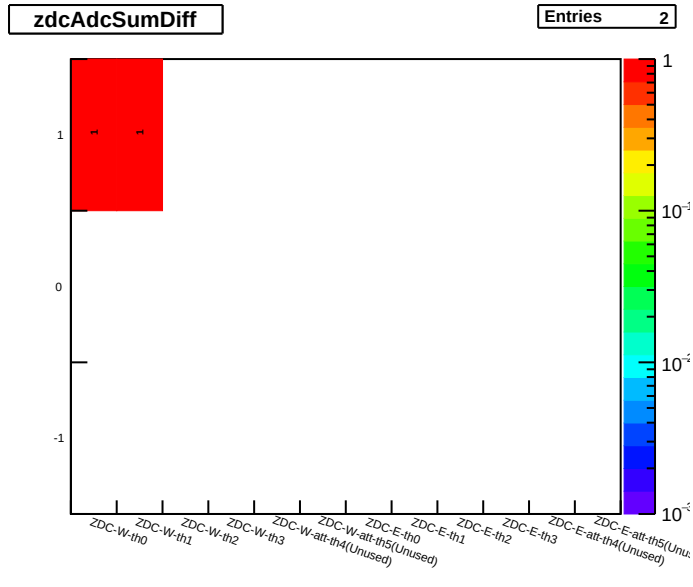

zdcEastTac
Entries 299663
10^5
10^4
10^3
10^2
10
1
0
200
400
600
800
1000

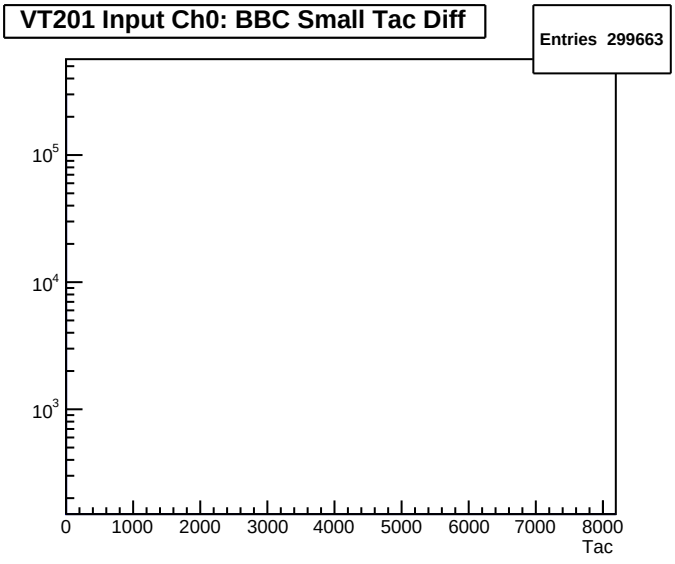

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

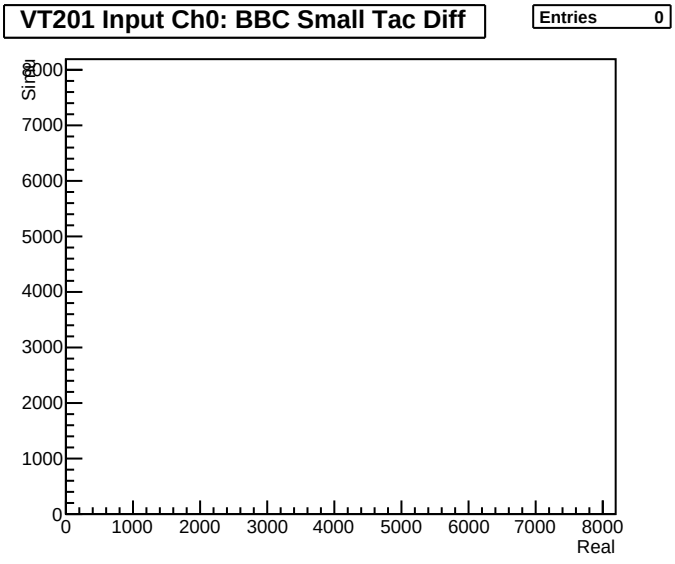

zdcWestTac
Entries 299663
10^5
10^4
10^3
0
200
400
600
800
1000

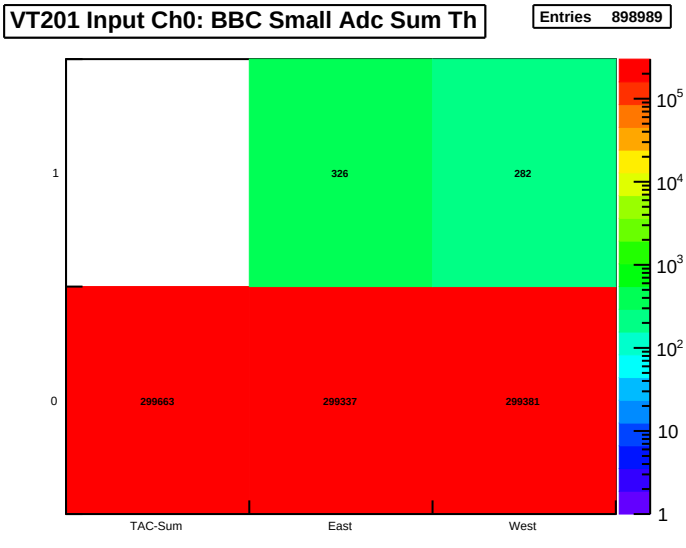

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

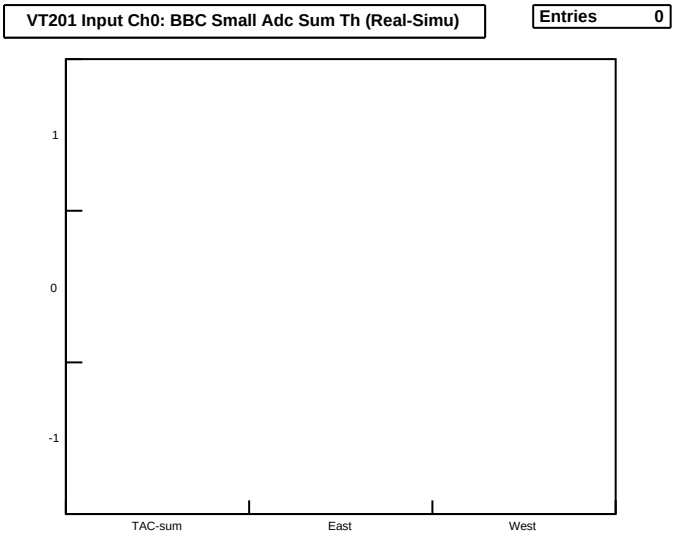

zdcAdcSum
Entries 3595956
1
0
1
1
299662
299662
299663
299663
299663
299663
299663
299663
299663
299663
299663
299663
10^5
10^4
10^3
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

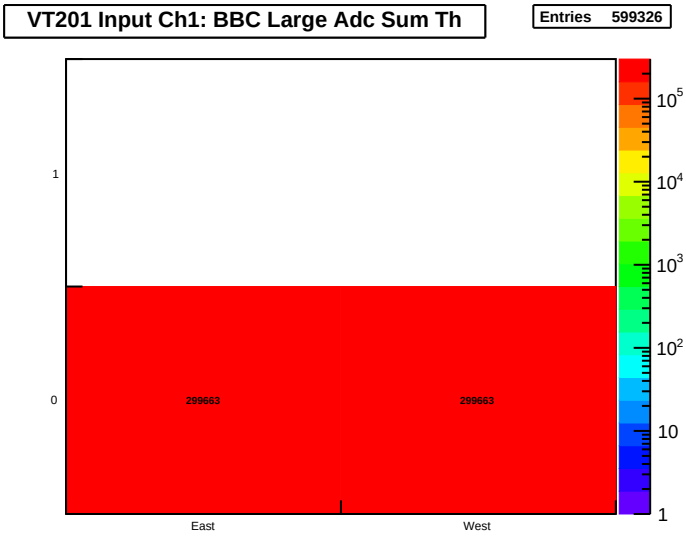

zdcAdcSumDiff
Entries 2
1
0
-1
1
1
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

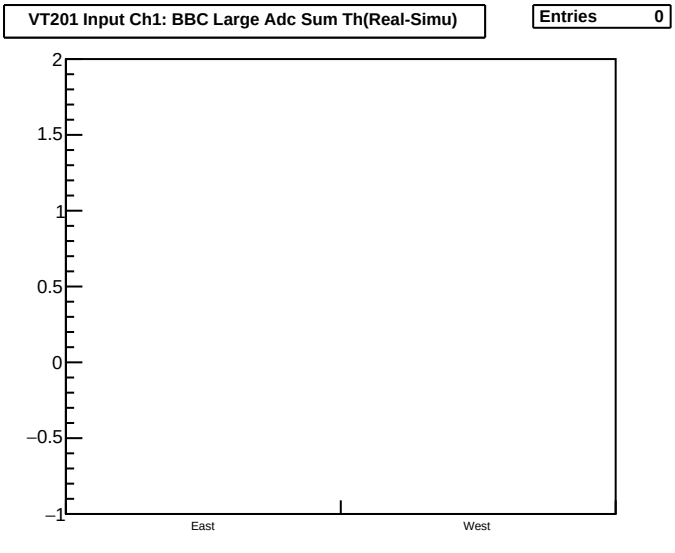

VT201 Input Ch0: BBC Small Tac Diff
Entries 299663
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

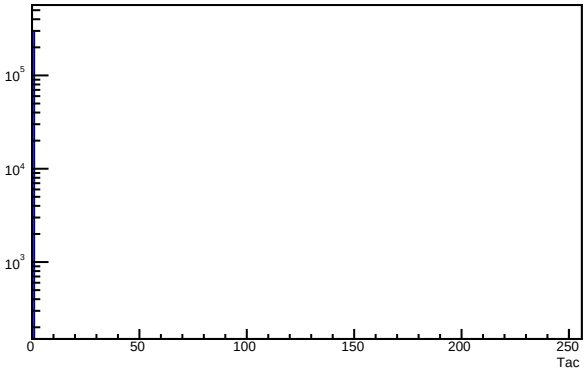

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 898989
326
282
299663
299337
299381
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

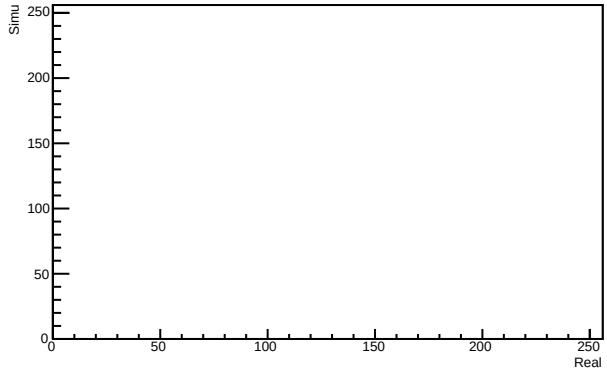

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 599326
299663
299663
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

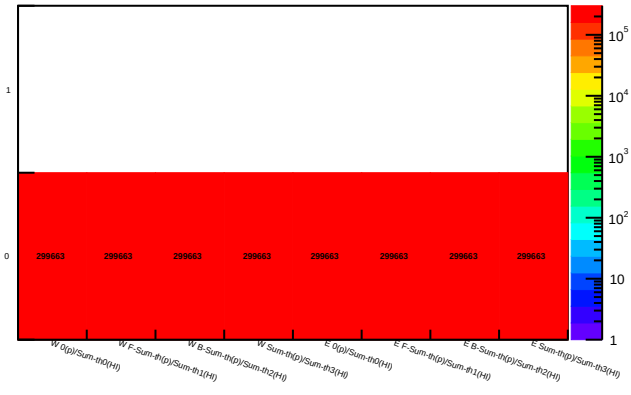
VT201 Input Ch2: ZDC Tac Diff



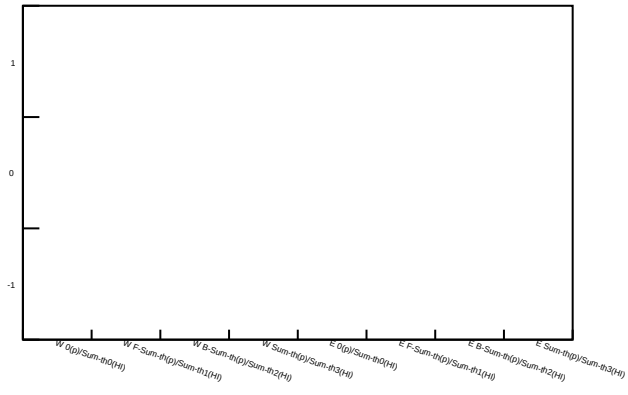
VT201 Input Ch2: ZDC Tac Diff



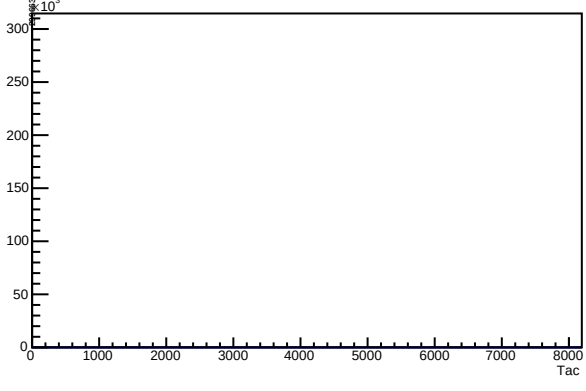
VT201 Input Ch2: ZDC Adc Sum Th



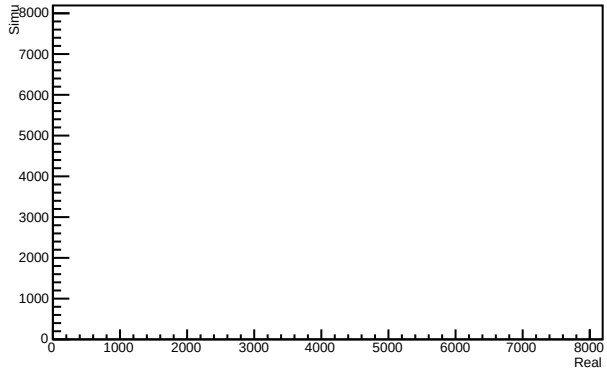
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



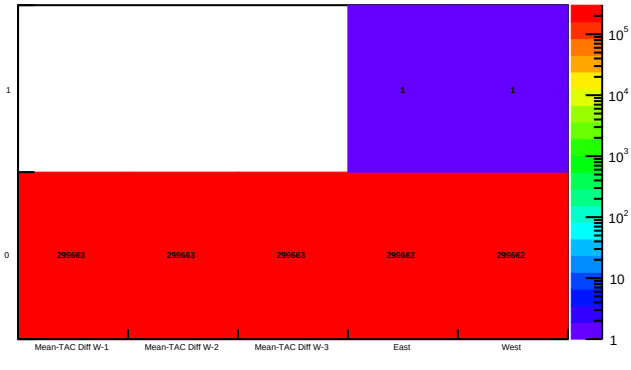
VT201 Input Ch4: VPD Tac Diff



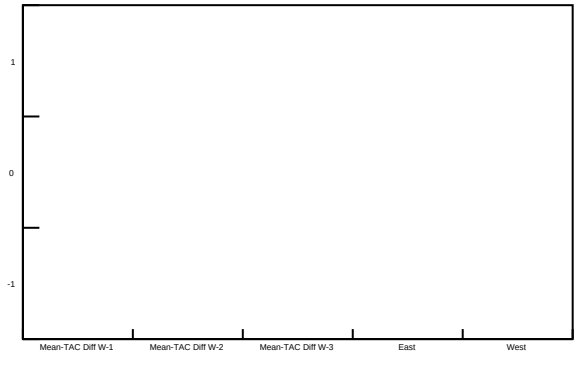
VT201 Input Ch4: VPD Tac Diff

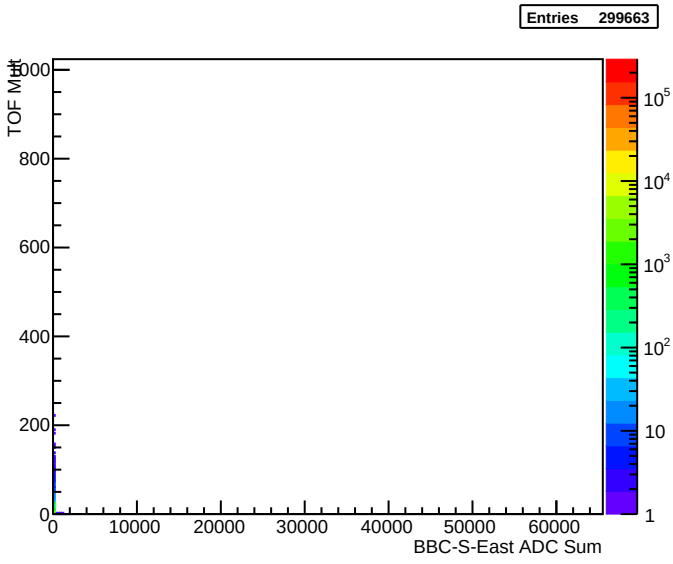


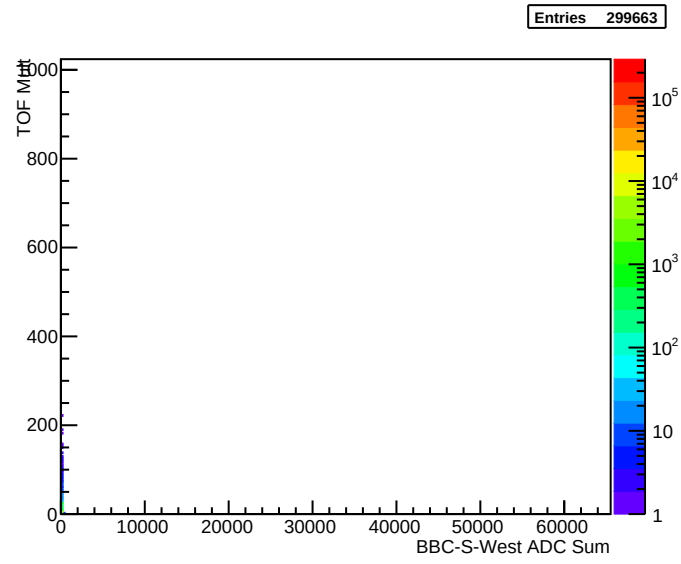
VT201 Input Ch4: VPD Adc Sum Th

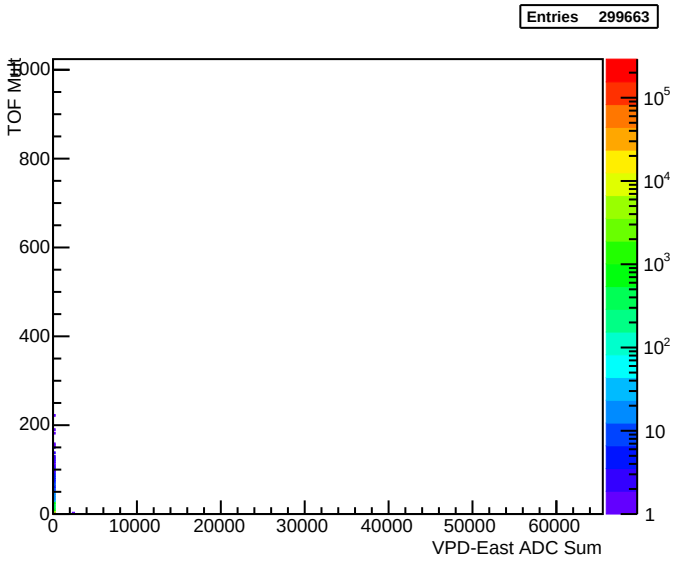


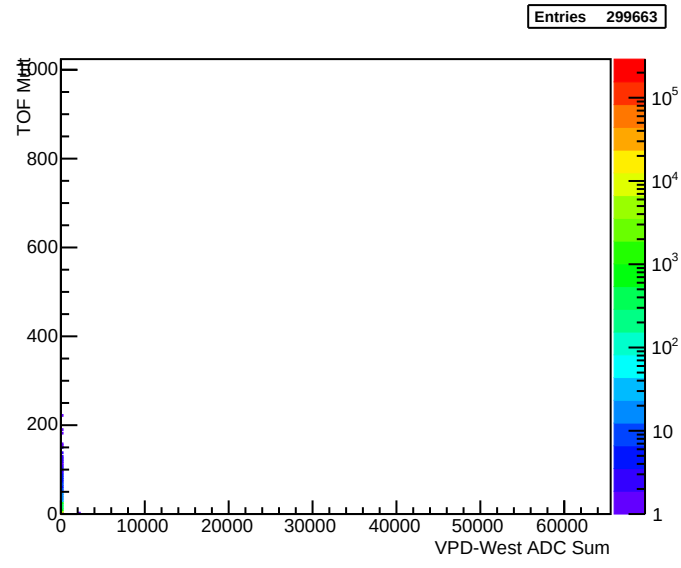
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

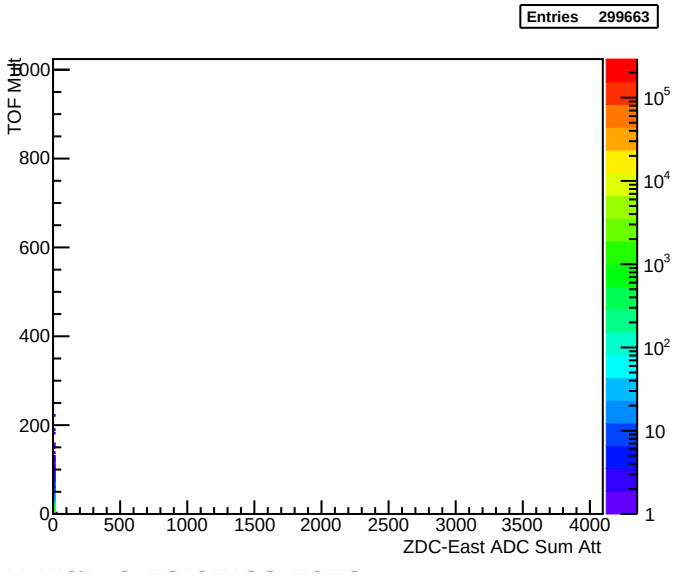


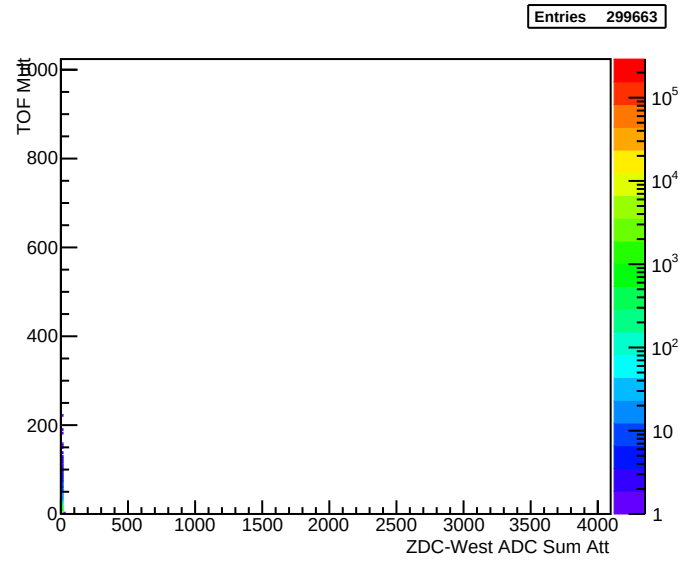

Entries 299663
TOF Mult
BBC-S-East ADC Sum

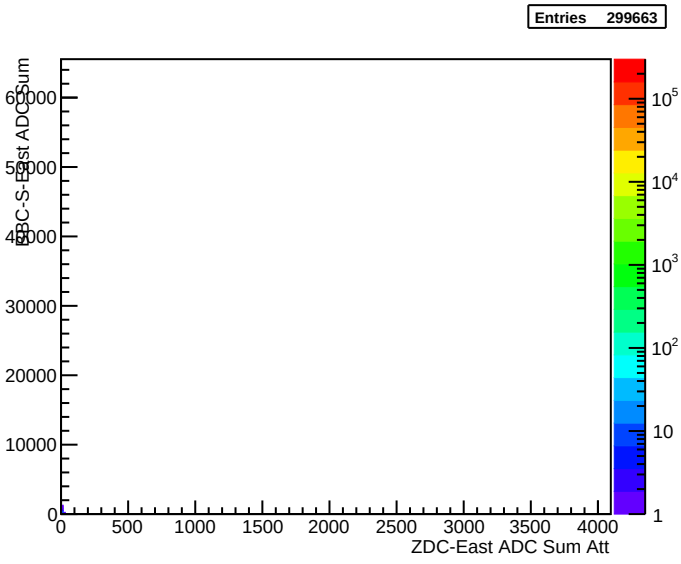

Entries 299663
TOF Mult
BBC-S-West ADC Sum

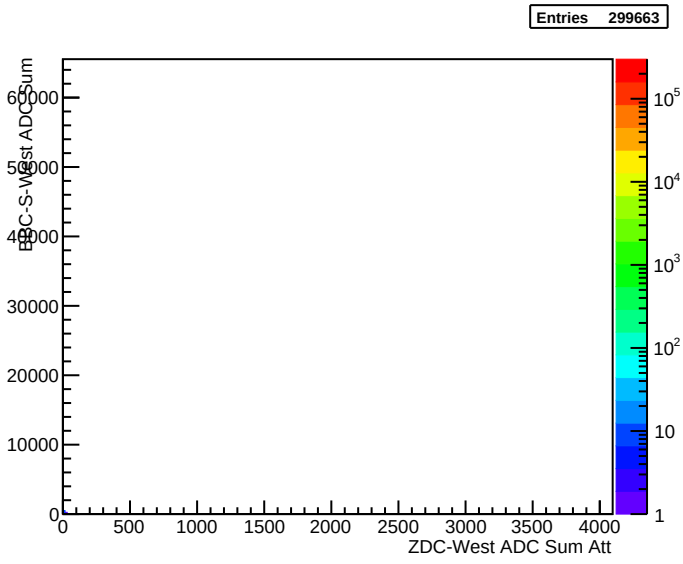

Entries 299663
TOF Mult
VPD-East ADC Sum

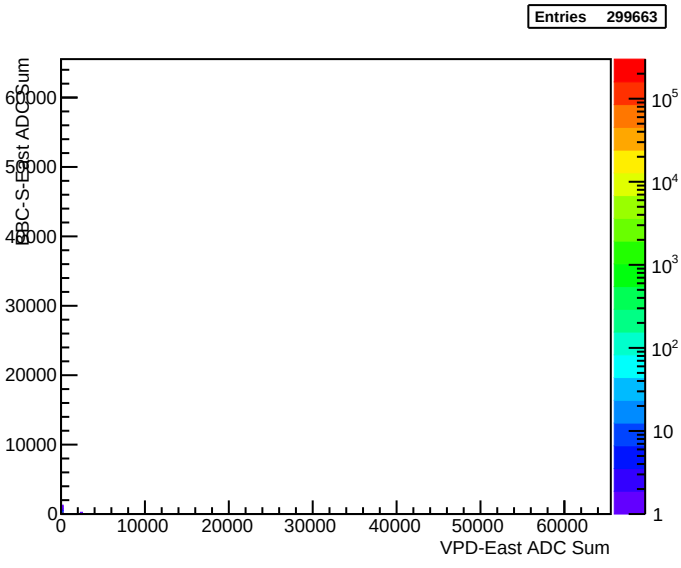

Entries 299663
TOF Mult
VPD-West ADC Sum

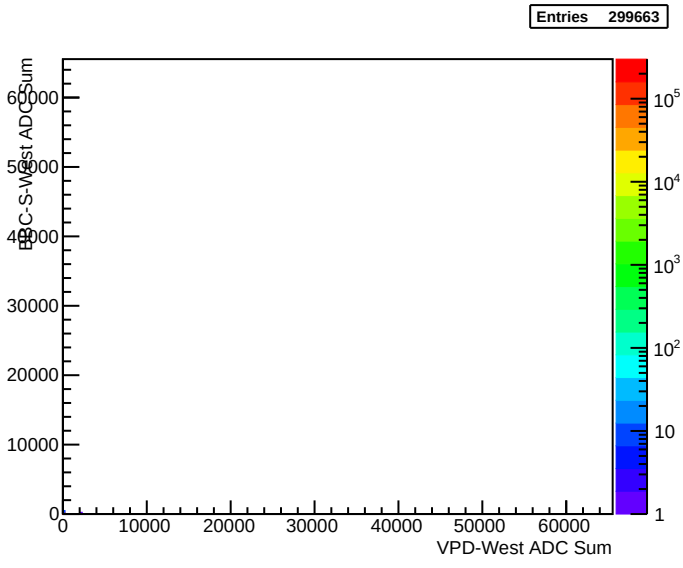

Entries 299663
TOF Mult
ZDC-East ADC Sum Att

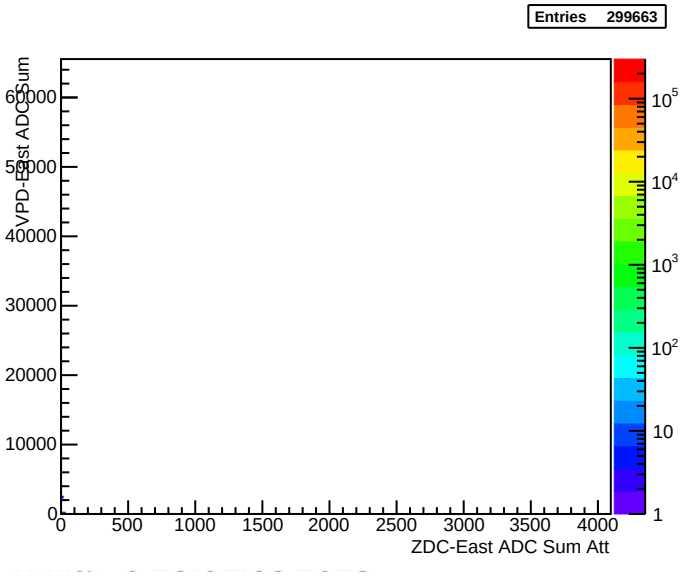

Entries 299663
TOF Mult
ZDC-West ADC Sum Att

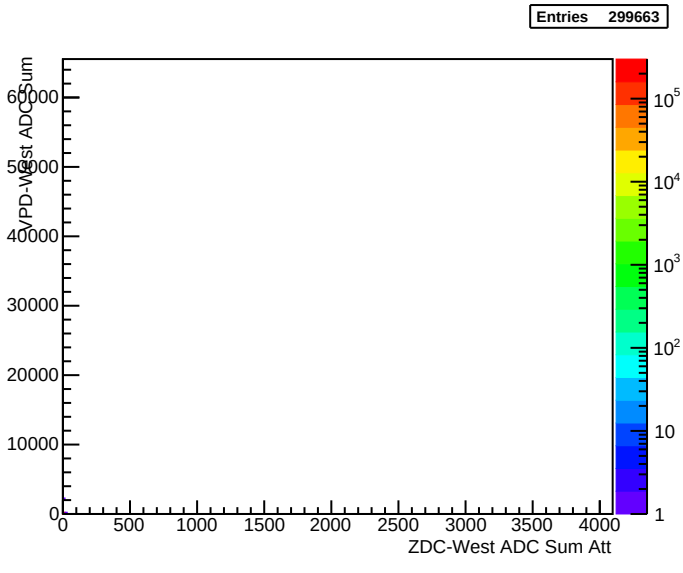

Entries 299663
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

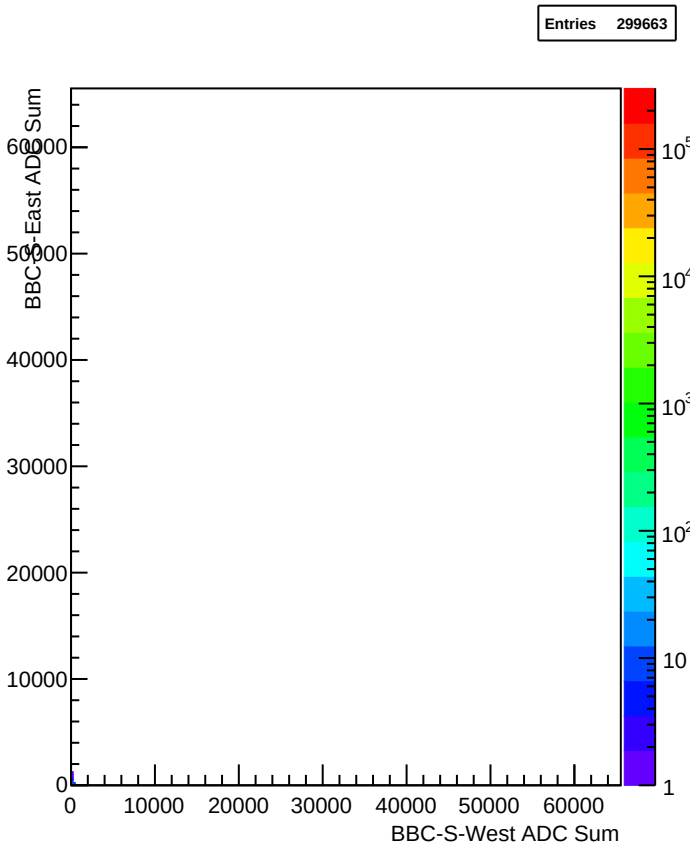

Entries 299663
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

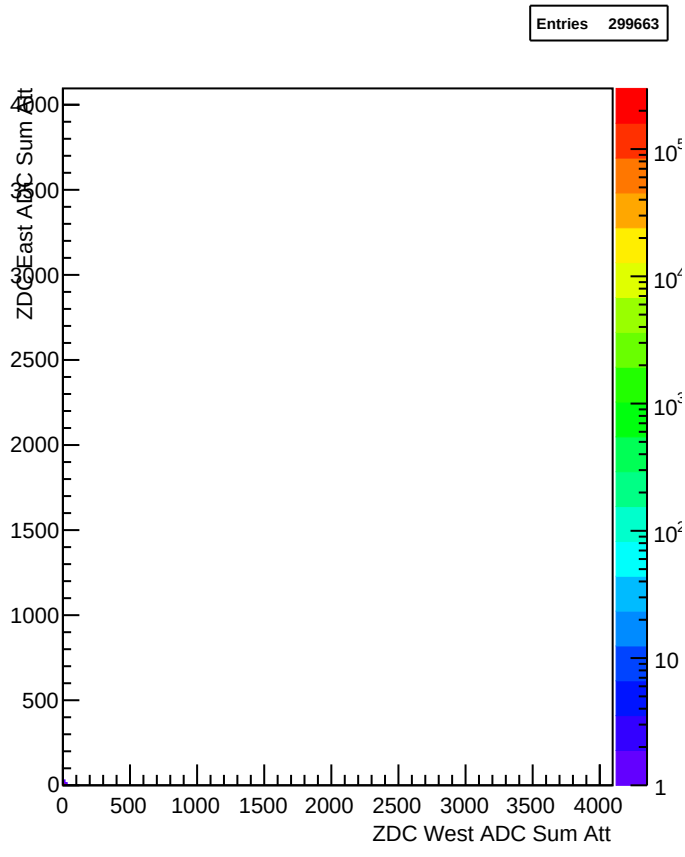

Entries 299663
BBC-S-East ADC Sum
VPD-East ADC Sum

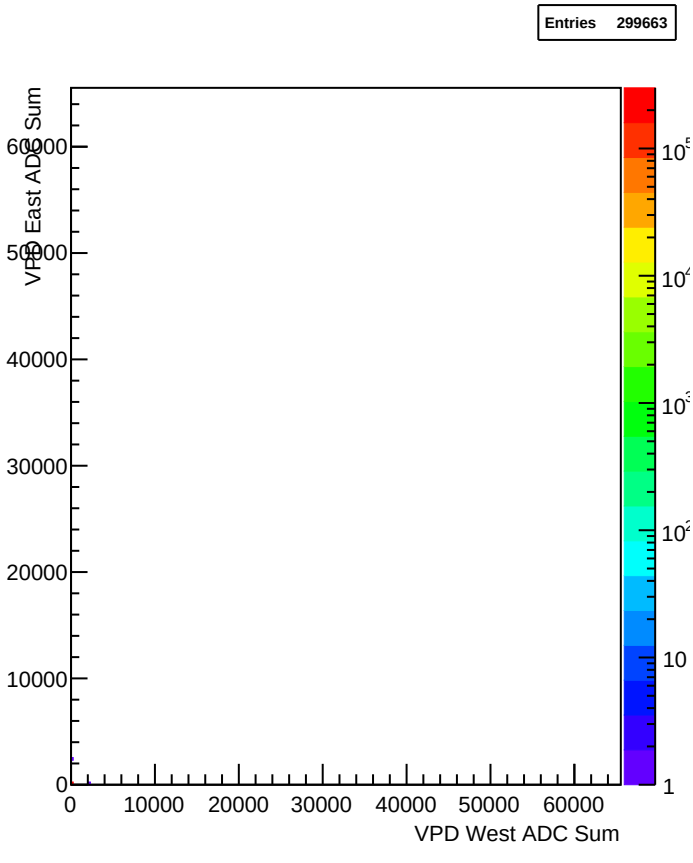

Entries 299663
BBC-S-West ADC Sum
VPD-West ADC Sum

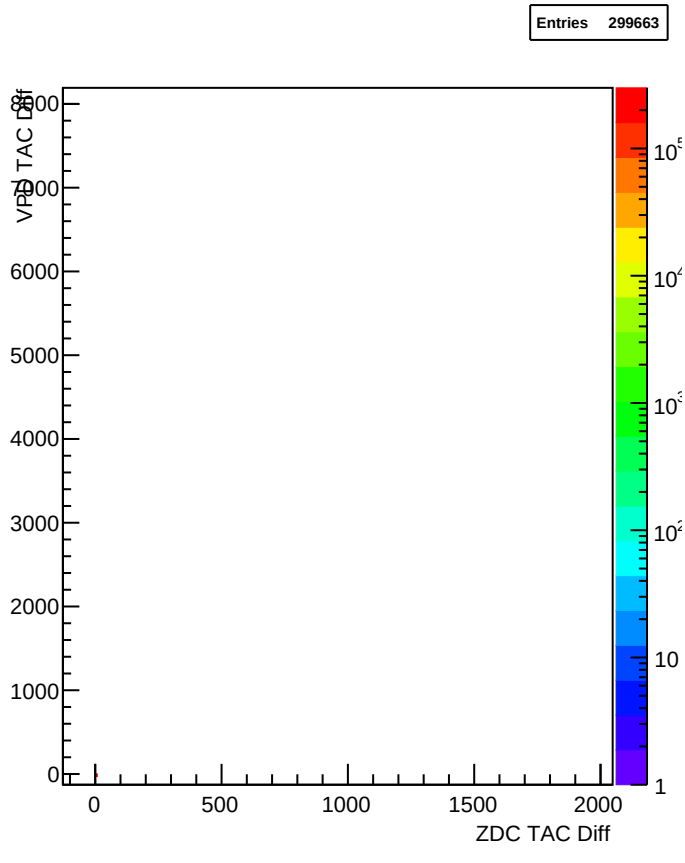

Entries 299663
VPD-East ADC Sum
ZDC-East ADC Sum Att

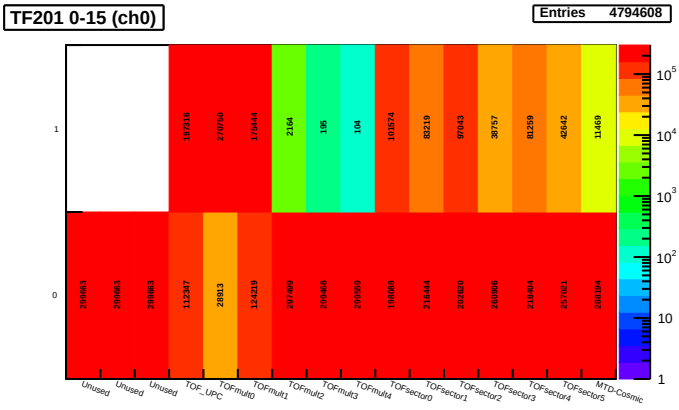

Entries 299663
VPD-West ADC Sum
ZDC-West ADC Sum Att

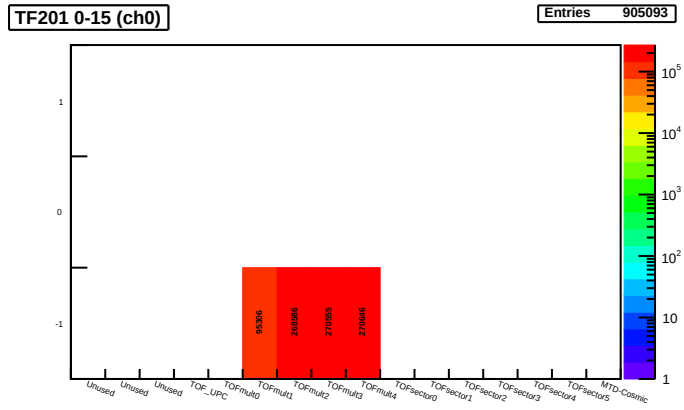

Entries 299663
BBC-S-East ADC Sum
BBC-S-West ADC Sum

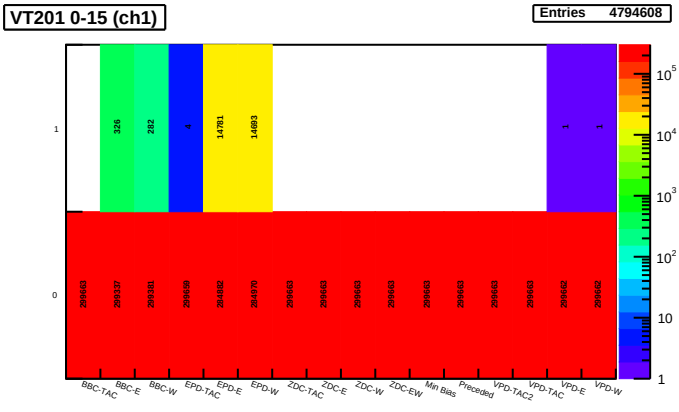

Entries 299663
ZDC East ADC Sum Att
ZDC West ADC Sum Att

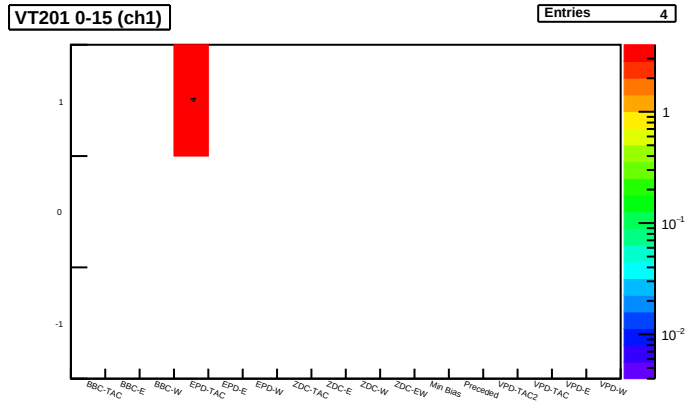

Entries 299663
VPD East ADC Sum
VPD West ADC Sum

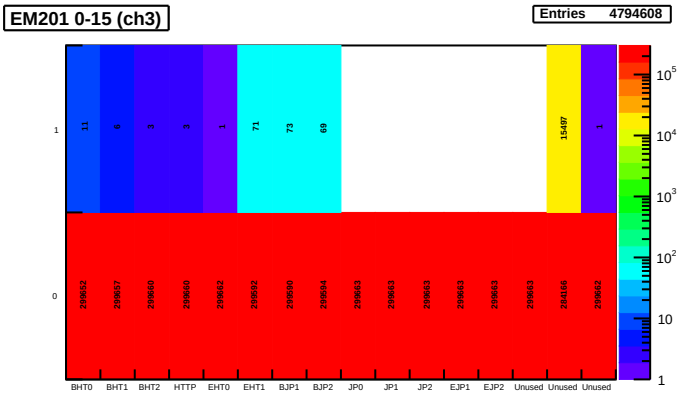

Entries 299663
VPD TAC Diff
ZDC TAC Diff

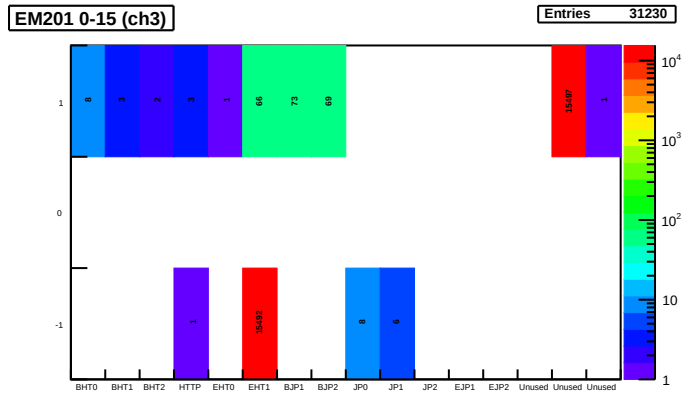

TF201 0-15 (ch0)
Entries 4794608
1 187316 270750 175444 2164 195 104 103574 83219 87043 38757 81259 42642 11469
0 299663 299663 299663 112347 28913 124219 237499 299468 299559 198089 216444 202820 260906 218404 257021 238194
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic
1 10 10^2 10^3 10^4 10^5

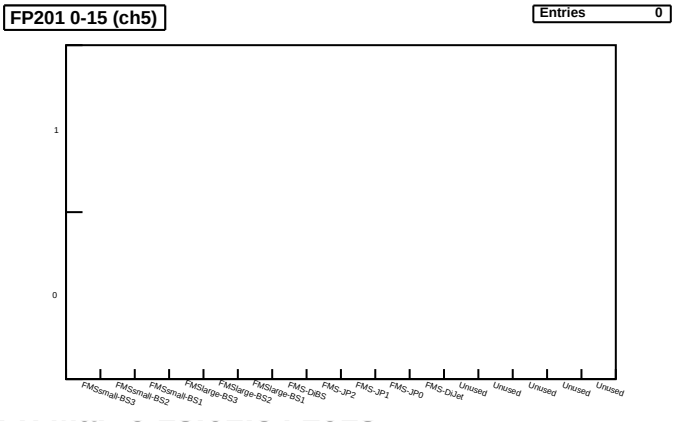

TF201 0-15 (ch0)
Entries 905093
1
0
-1 95306 268386 270655 270646
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic
1 10 10^2 10^3 10^4 10^5

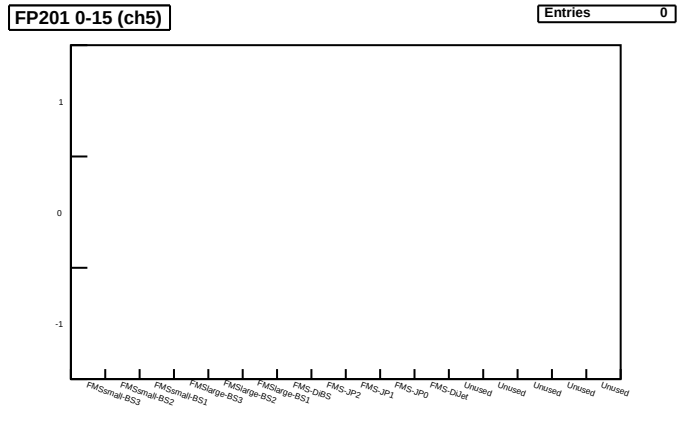

VT201 0-15 (ch1)
Entries 4794608
1 326 282 4 14781 14493 1 1
0 299663 299337 299381 299659 284832 284970 299663 299663 299663 299663 299663 299663 299663 299663 299662 299662
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W
1 10 10^2 10^3 10^4 10^5


VT201 0-15 (ch1)
Entries 4
1 4
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W
1 10^-1 10^-2


EM201 0-15 (ch3)
Entries 4794608
1 11 6 3 3 1 71 73 69 15497 1
0 299652 299657 299660 299660 299662 299592 299590 299594 299663 299663 299663 299663 299663 284166 299662
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused
1 10 10^2 10^3 10^4 10^5


EM201 0-15 (ch3)
Entries 31230
1 8 3 2 3 1 66 73 69 15497 1
0
-1 1 15492 8 6
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused
1 10 10^2 10^3 10^4


FP201 0-15 (ch5)
Entries 0
1
0
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused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FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused