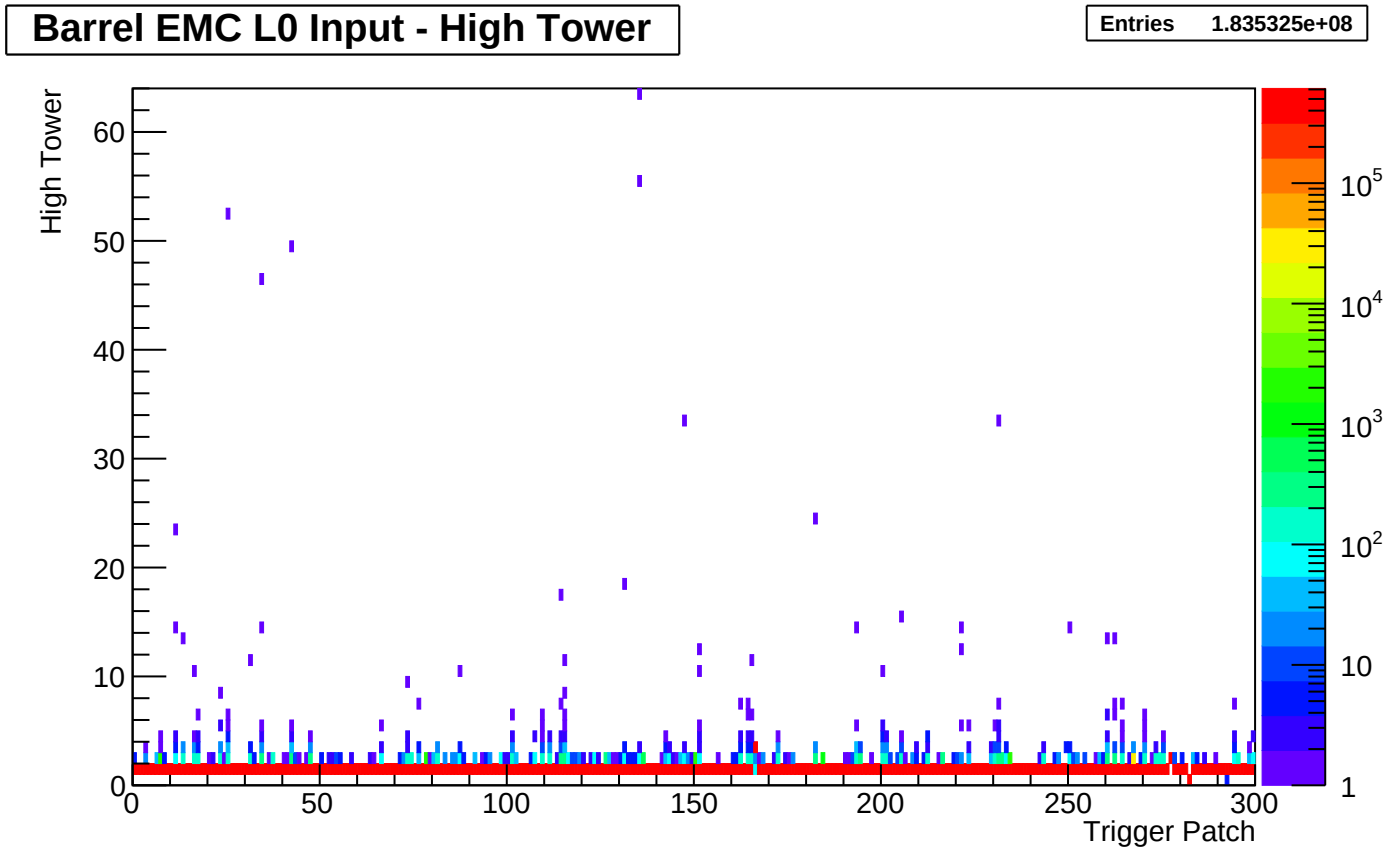
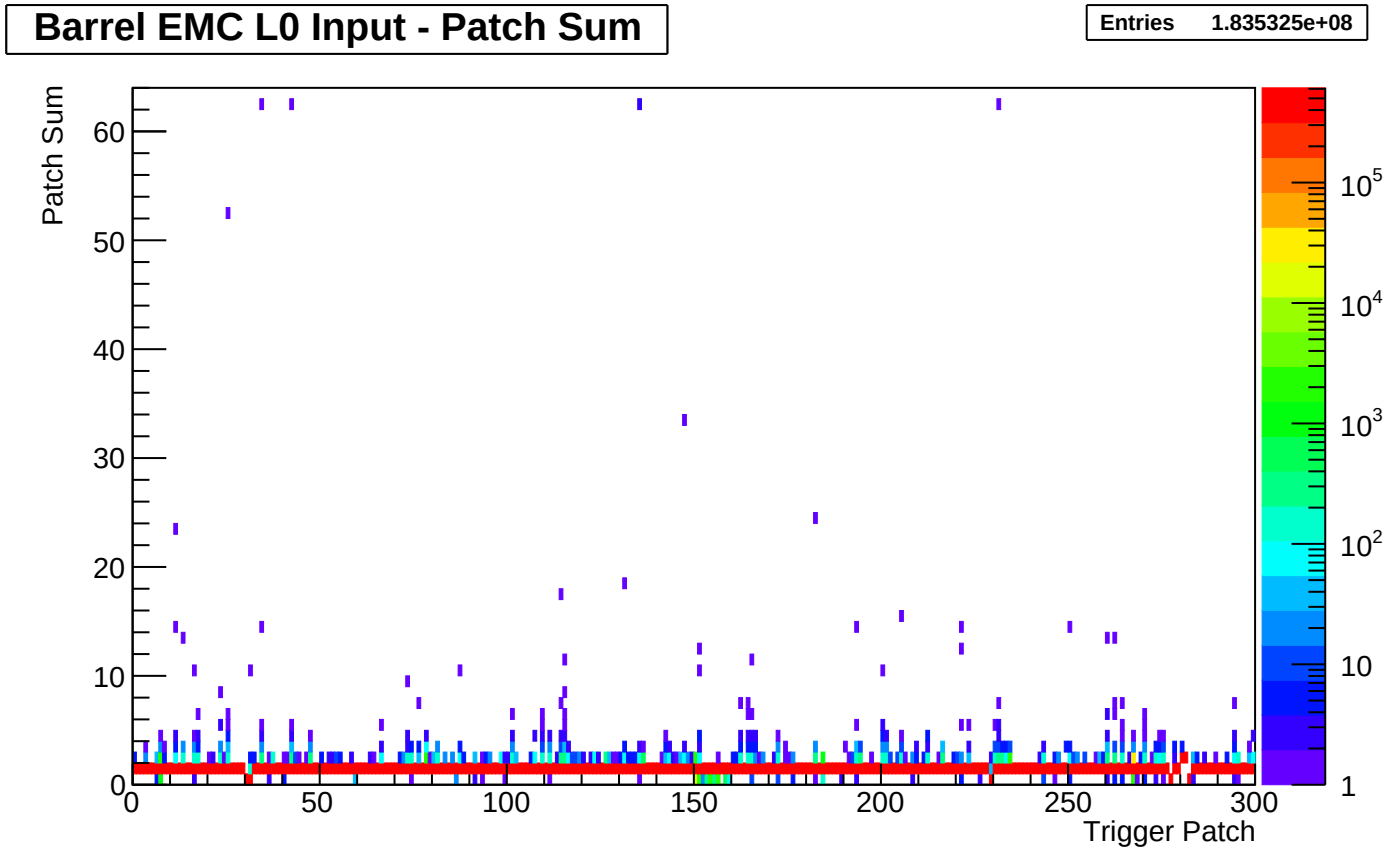
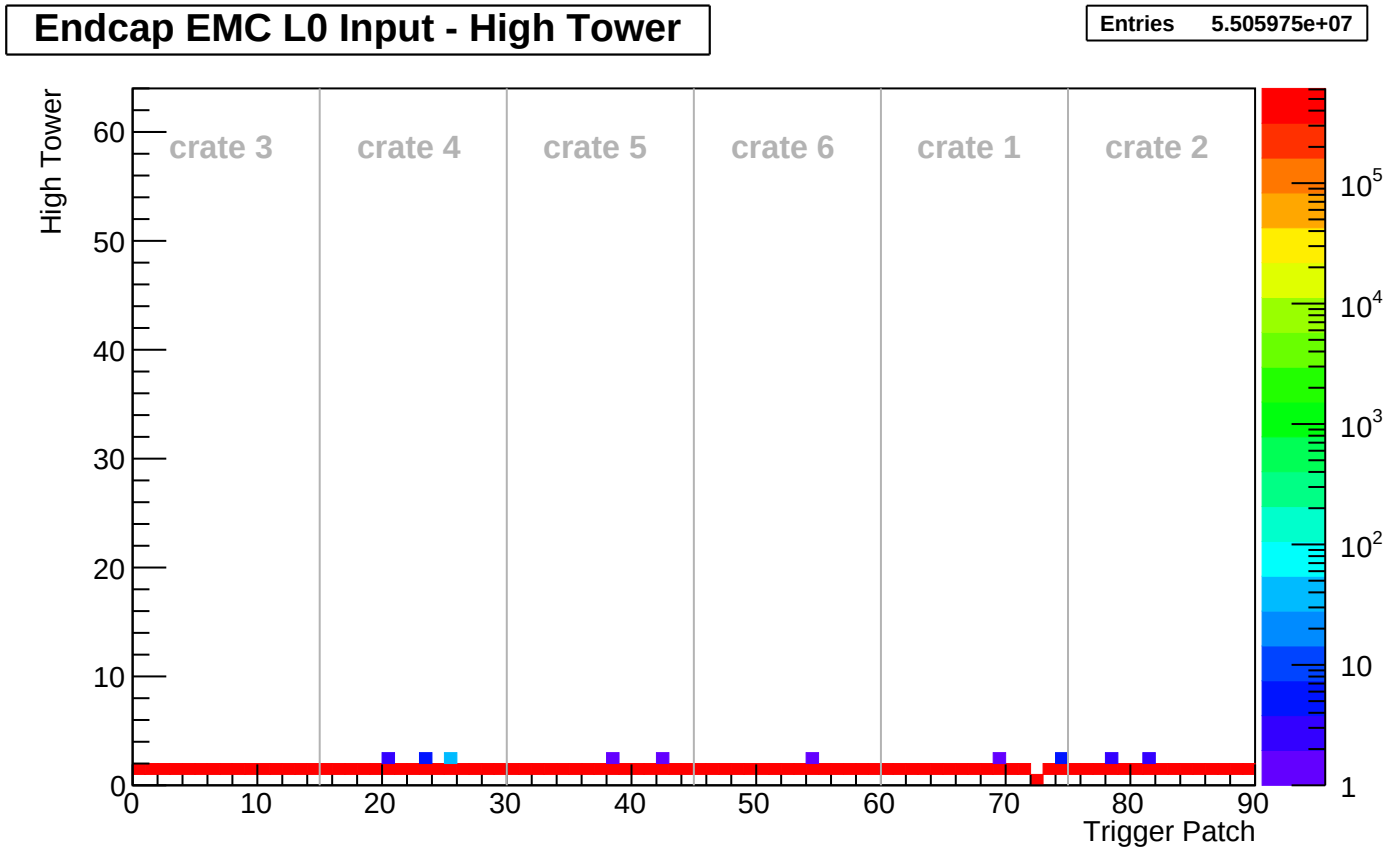
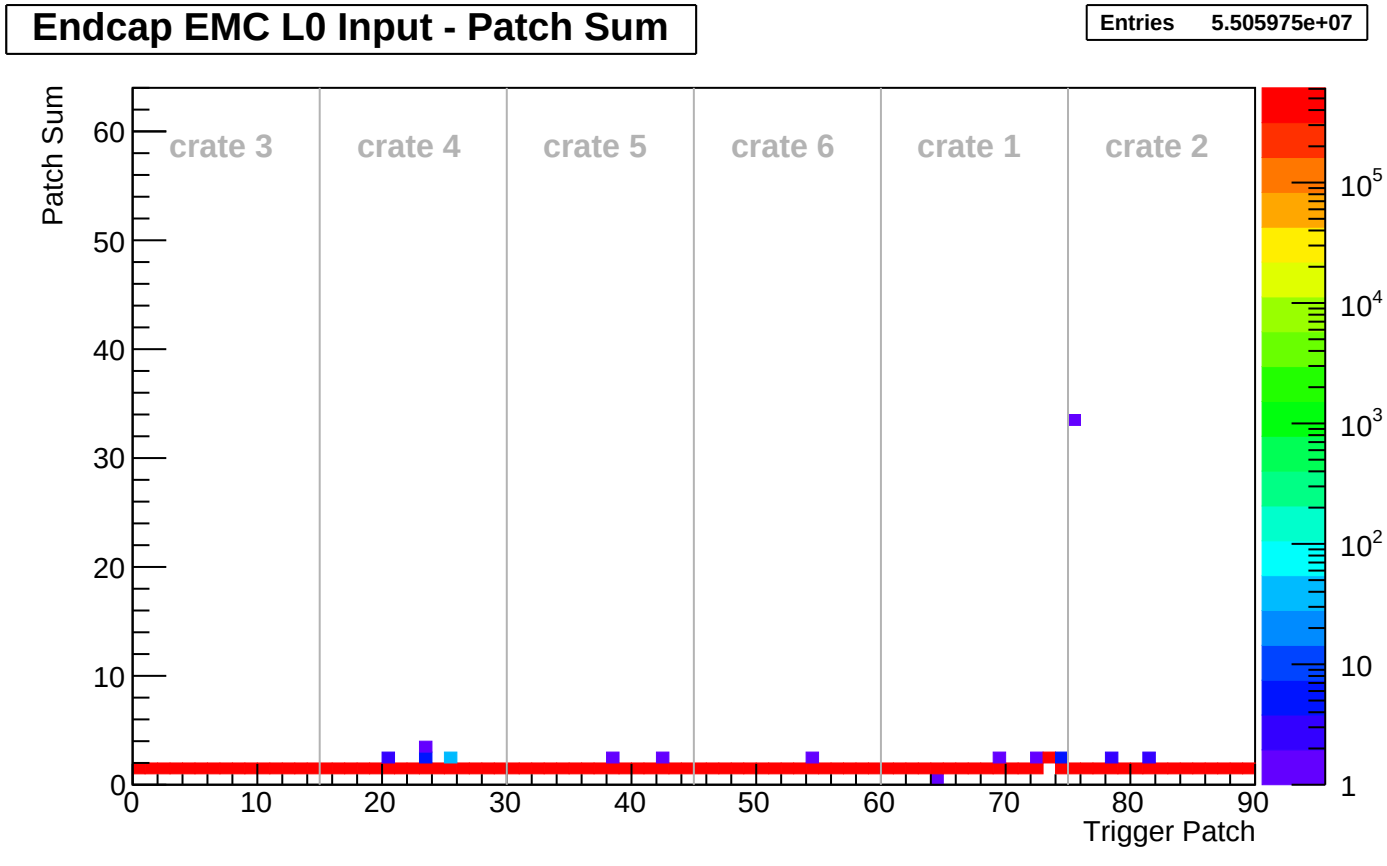
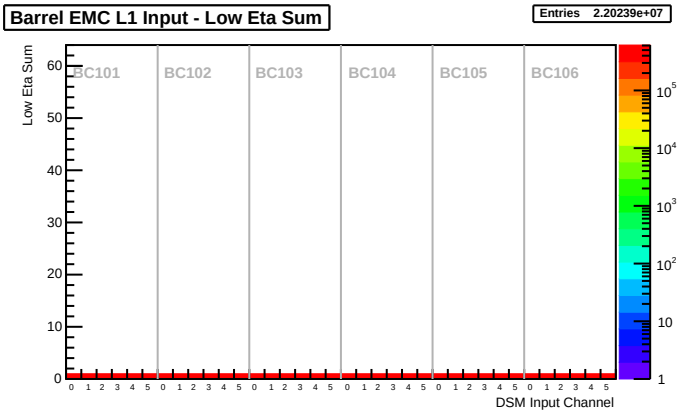
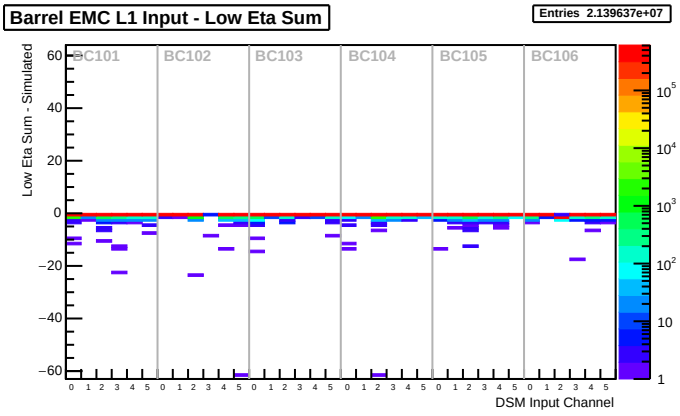

Barrel EMC L0 Input - High Tower
Entries 1.835325e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

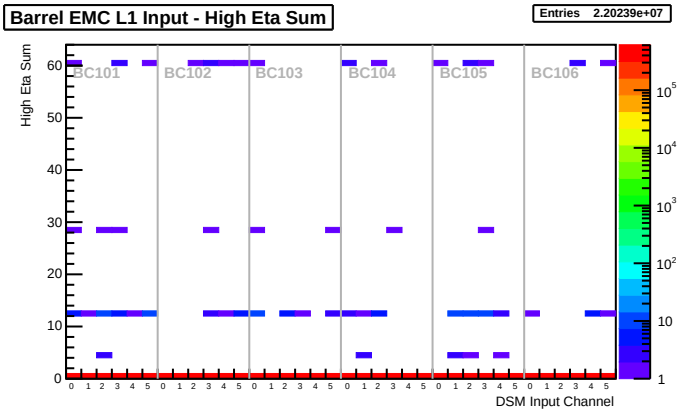

Barrel EMC L0 Input - Patch Sum
Entries 1.835325e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

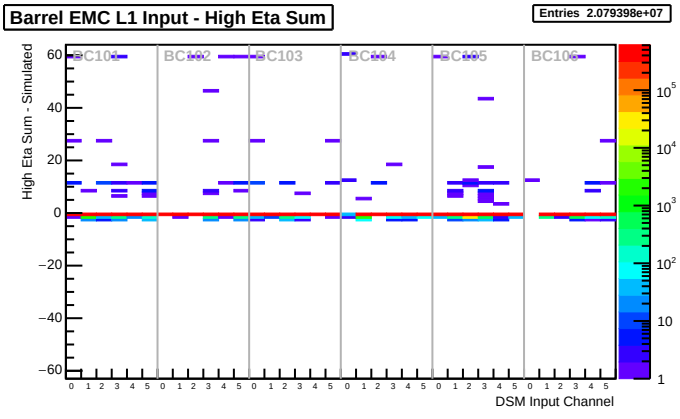

Endcap EMC L0 Input - High Tower
Entries 5.505975e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

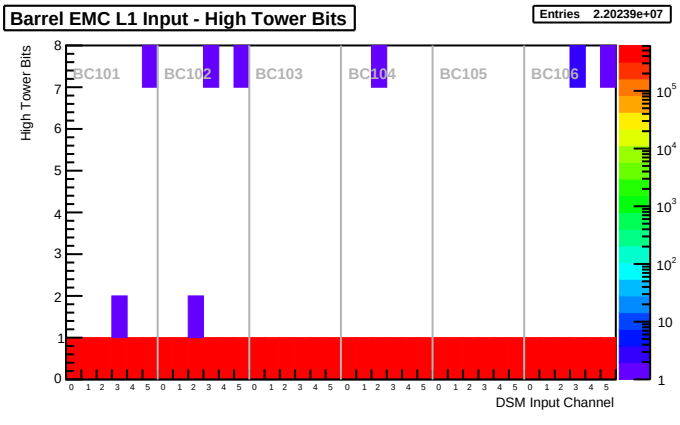

Endcap EMC L0 Input - Patch Sum
Entries 5.505975e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

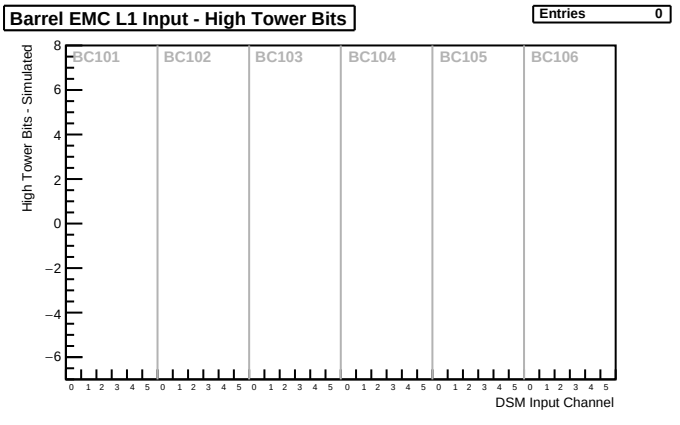

Barrel EMC L1 Input - Low Eta Sum
Entries 2.20239e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

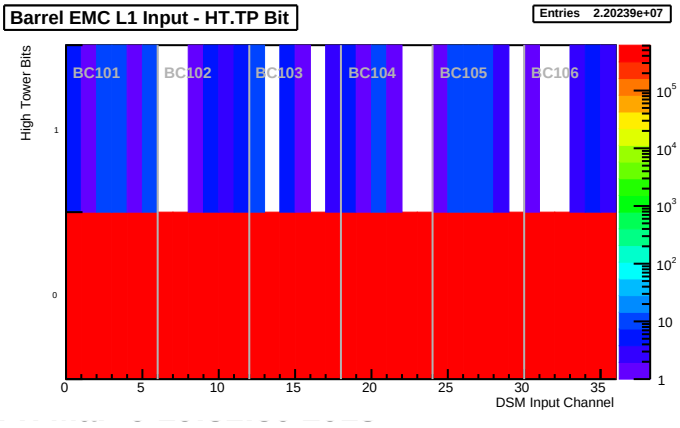

Barrel EMC L1 Input - Low Eta Sum
Entries 2.139637e+07
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

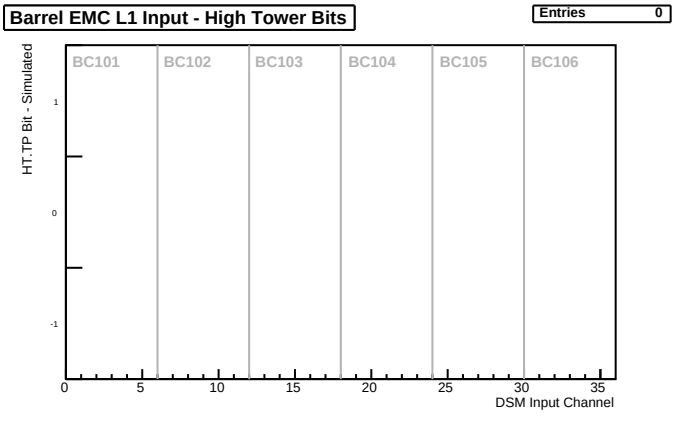

Barrel EMC L1 Input - High Eta Sum
Entries 2.20239e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

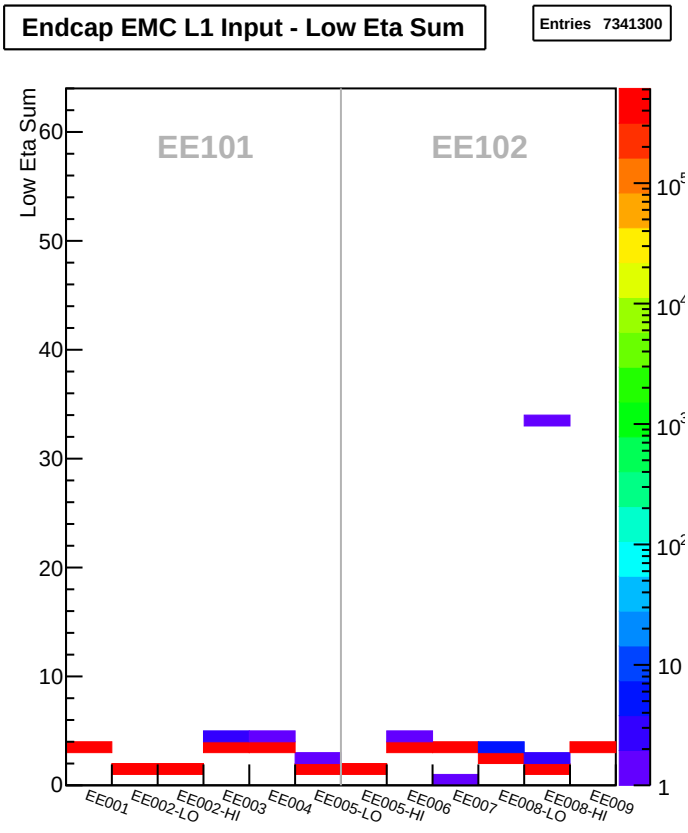

Barrel EMC L1 Input - High Eta Sum
Entries 2.079398e+07
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

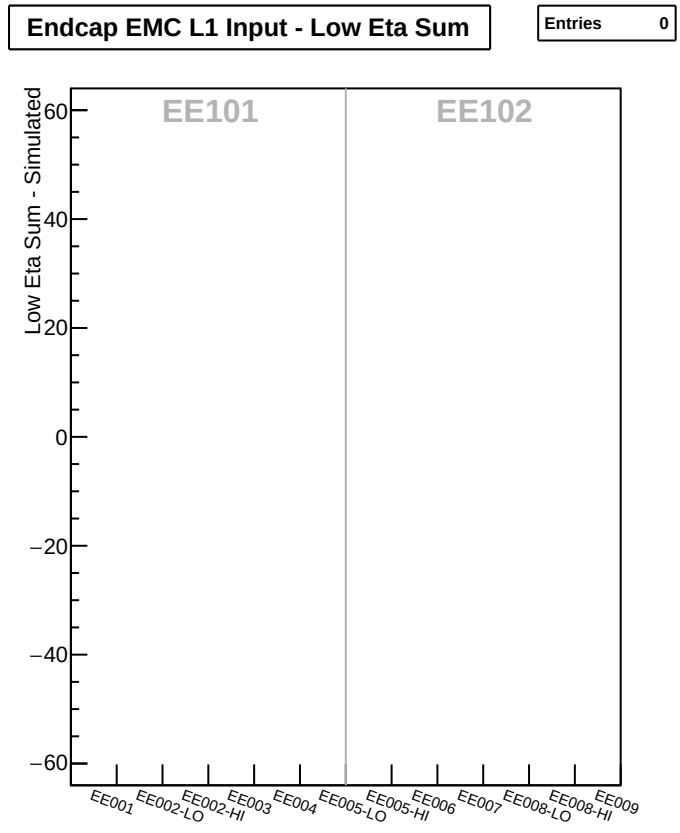

Barrel EMC L1 Input - High Tower Bits
Entries 2.20239e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

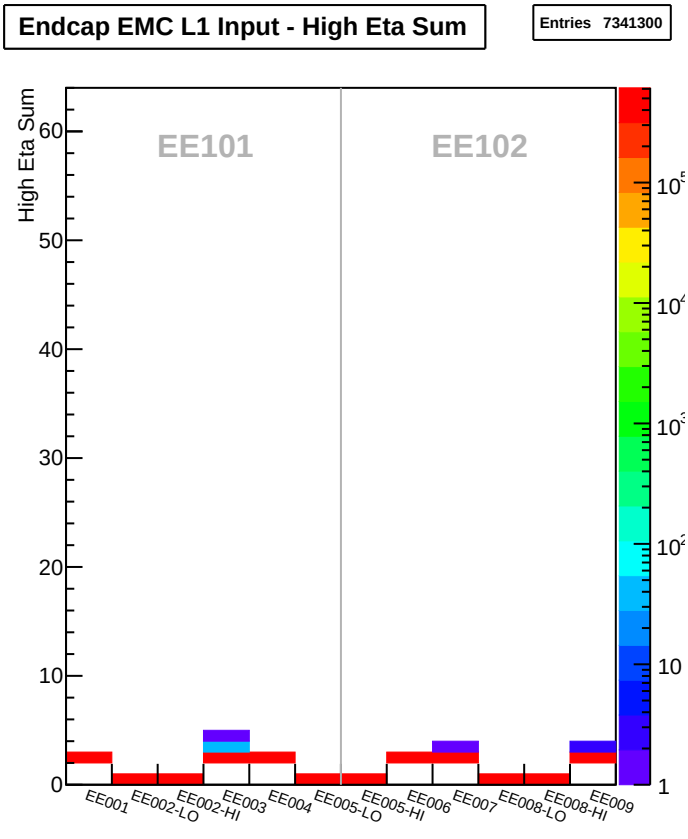

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

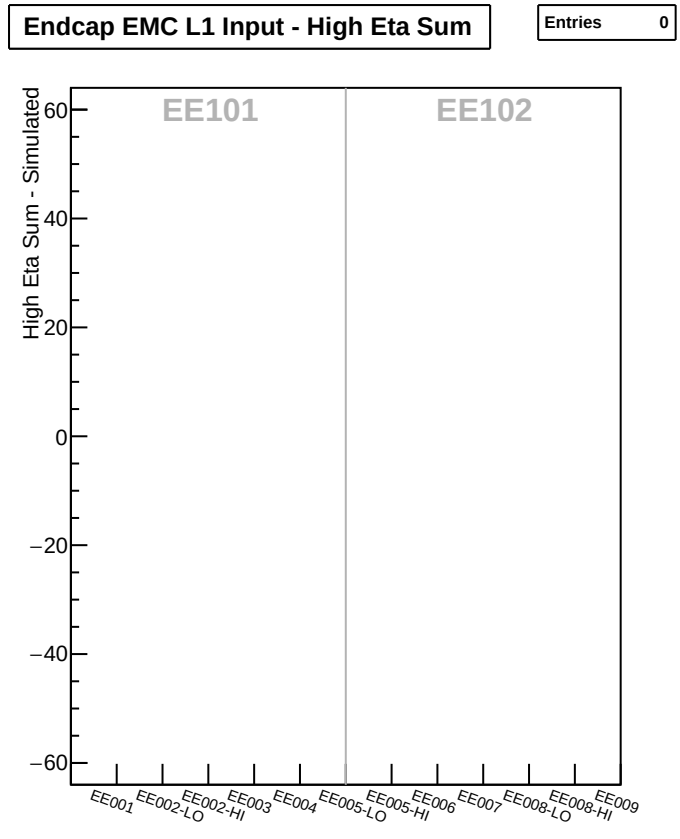

Barrel EMC L1 Input - HT.TP Bit
Entries 2.20239e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

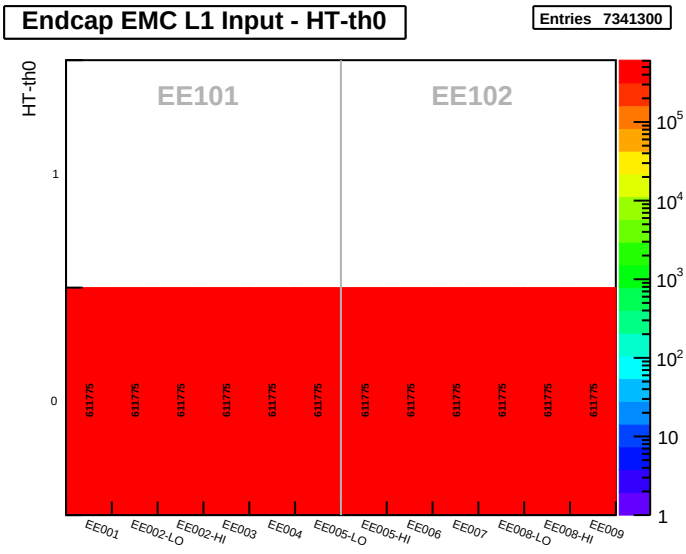

Barrel EMC L1 Input - High Tower Bits
Entries 0
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

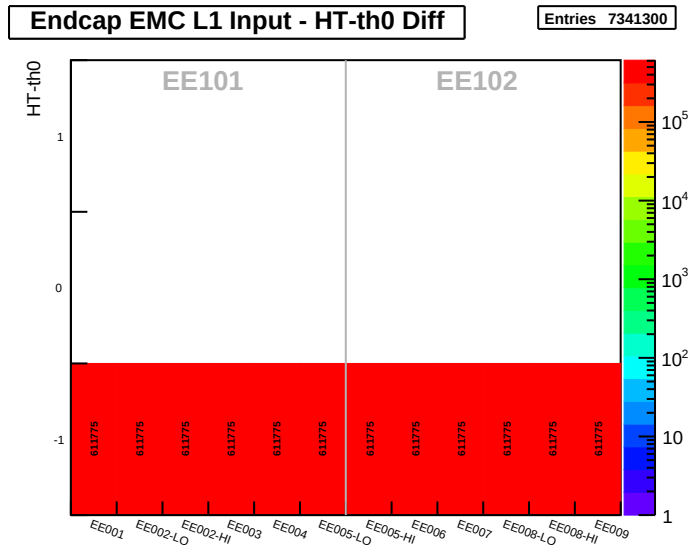

Endcap EMC L1 Input - Low Eta Sum
Entries 7341300
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

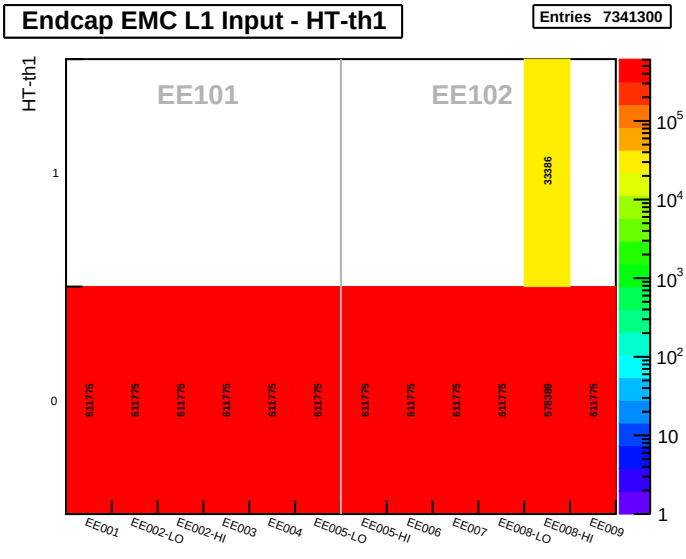

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

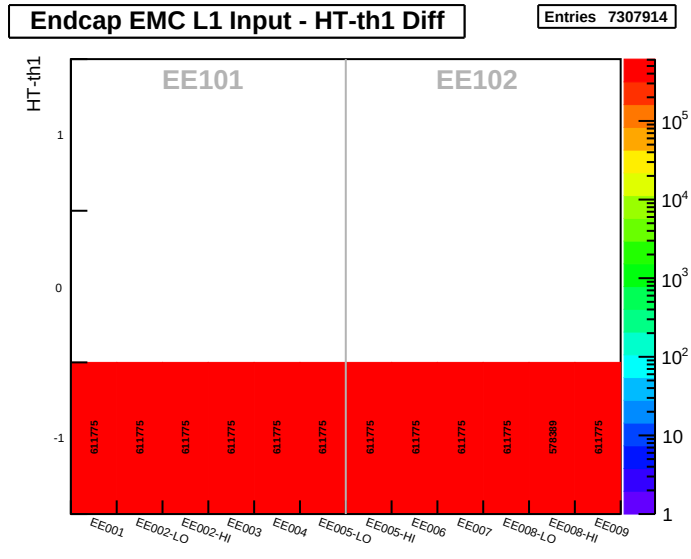

Endcap EMC L1 Input - High Eta Sum
Entries 7341300
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

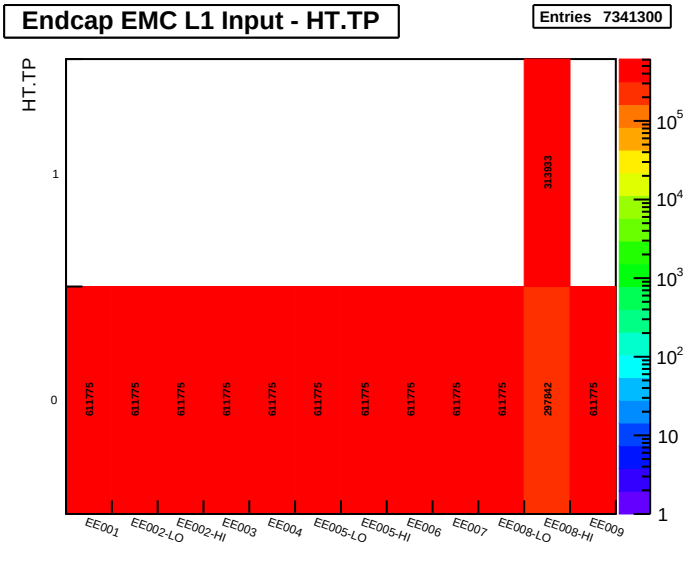

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

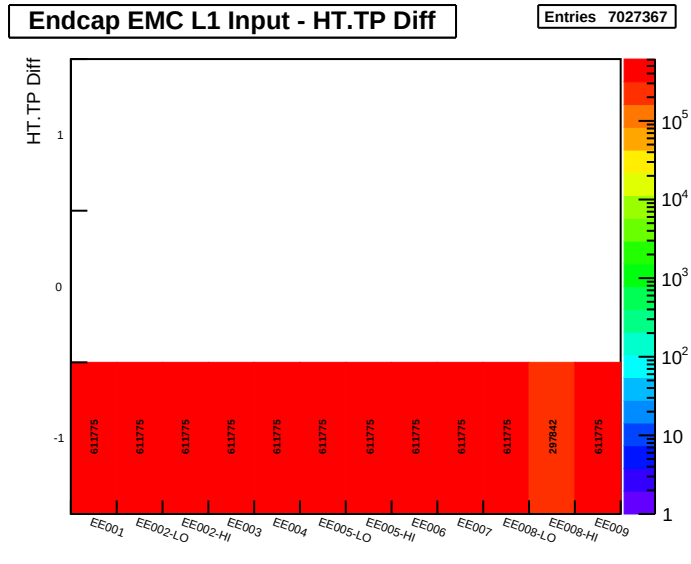

Endcap EMC L1 Input - HT-th0
Entries 7341300
HT-th0
EE101
EE102
611775
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

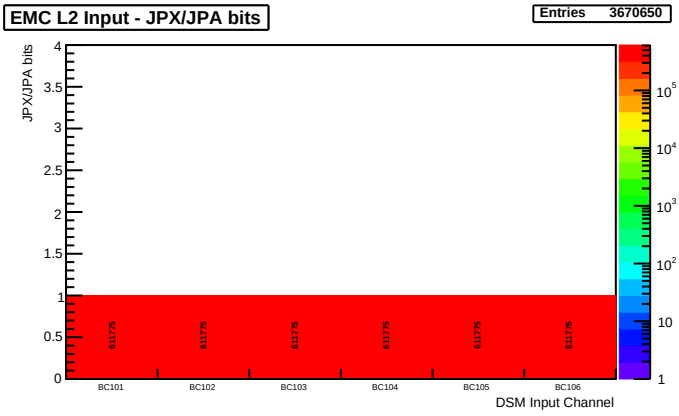

Endcap EMC L1 Input - HT-th0 Diff
Entries 7341300
HT-th0
EE101
EE102
611775
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

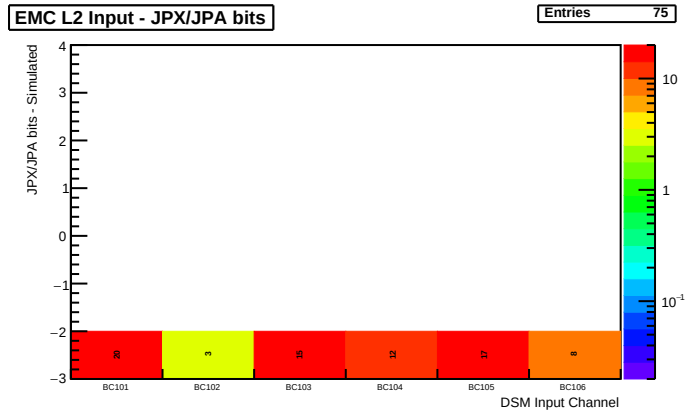

Endcap EMC L1 Input - HT-th1
Entries 7341300
HT-th1
EE101
EE102
611775
33386
578389
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

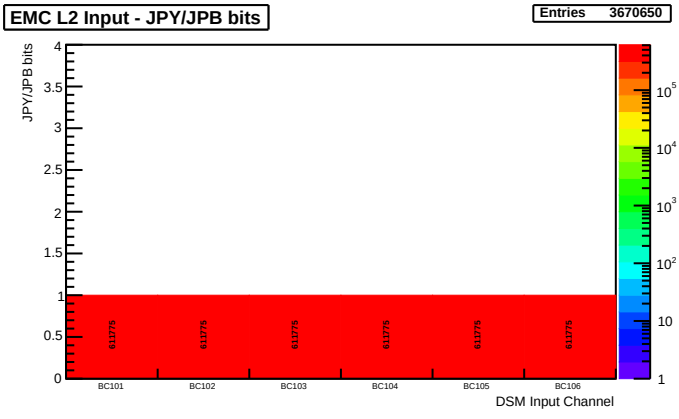

Endcap EMC L1 Input - HT-th1 Diff
Entries 7307914
HT-th1
EE101
EE102
611775
578389
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

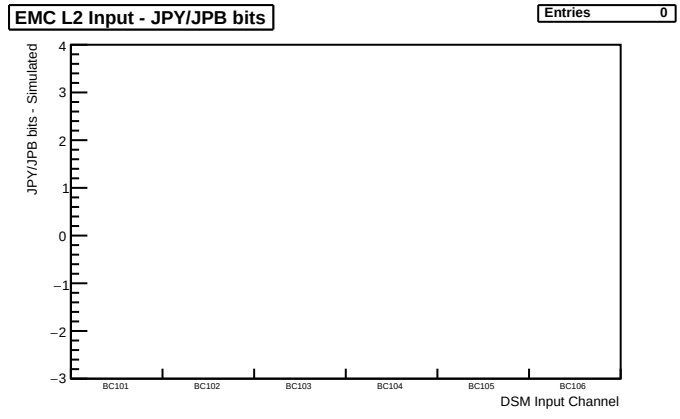

Endcap EMC L1 Input - HT.TP
Entries 7341300
HT.TP
611775
313933
297842
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

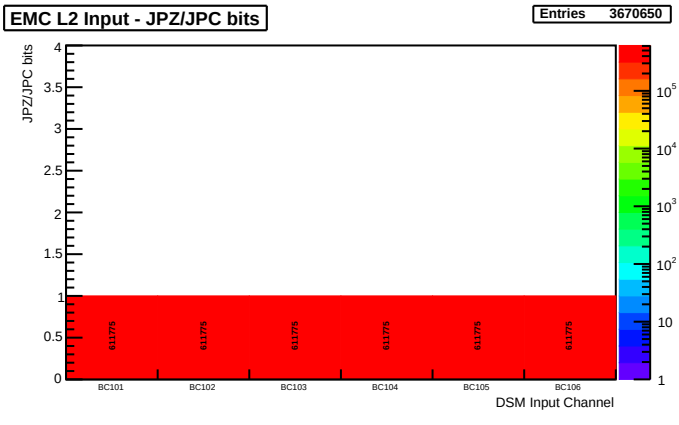

Endcap EMC L1 Input - HT.TP Diff
Entries 7027367
HT.TP Diff
611775
297842
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

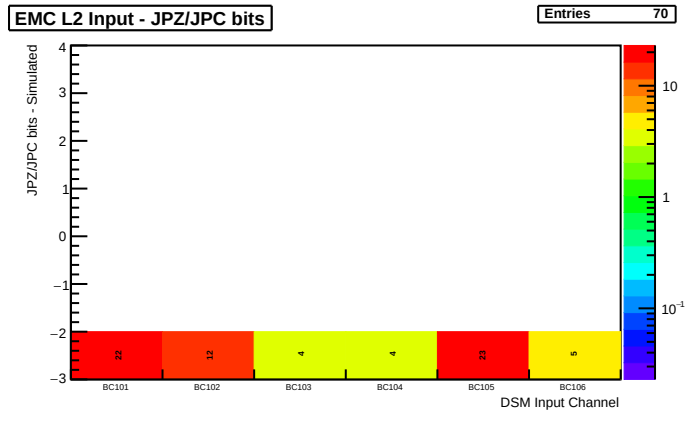

EMC L2 Input - JPX/JPA bits
Entries 3670650
JPX/JPA bits
611775
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

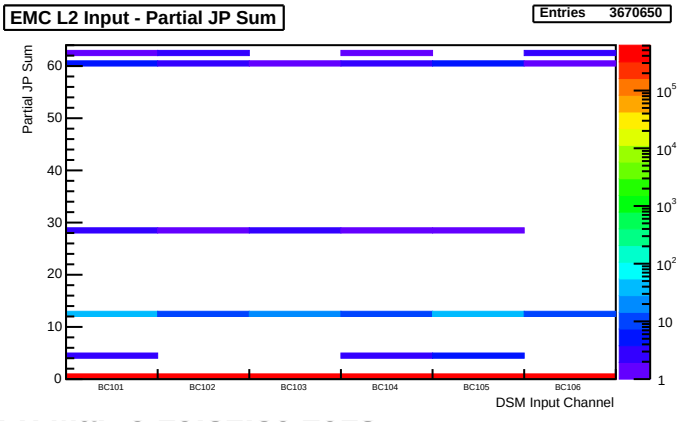

EMC L2 Input - JPX/JPA bits
Entries 75
JPX/JPA bits - Simulated
20
3
15
12
17
8
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

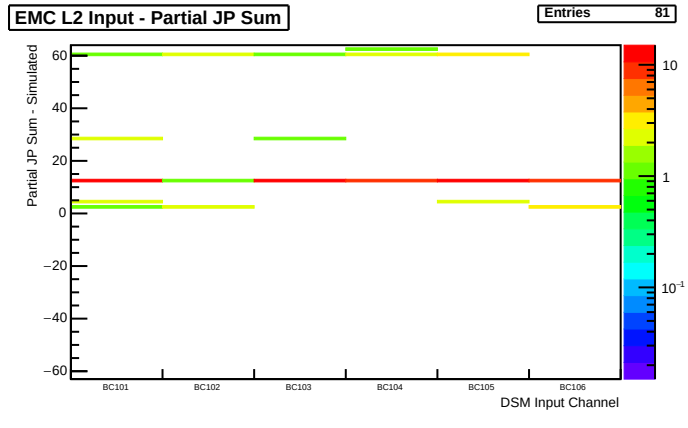

EMC L2 Input - JPY/JPB bits
Entries 3670650
JPY/JPB bits
611775
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

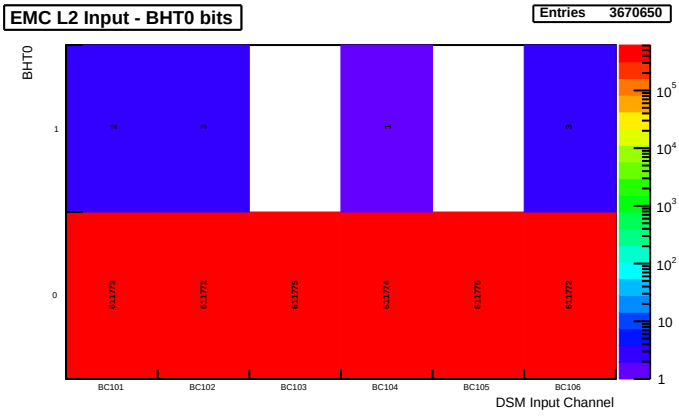

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

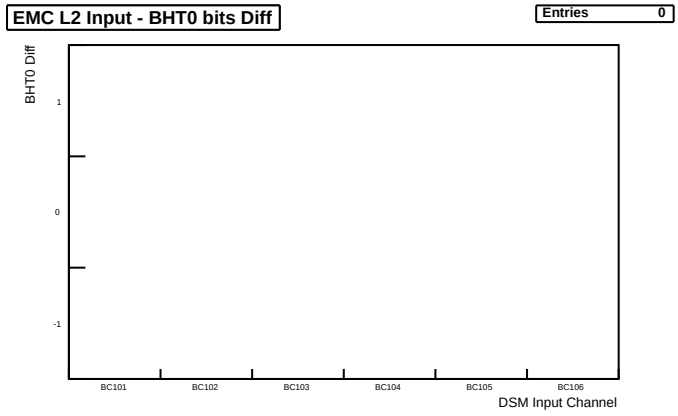

EMC L2 Input - JPZ/JPC bits
Entries 3670650
JPZ/JPC bits
611775
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

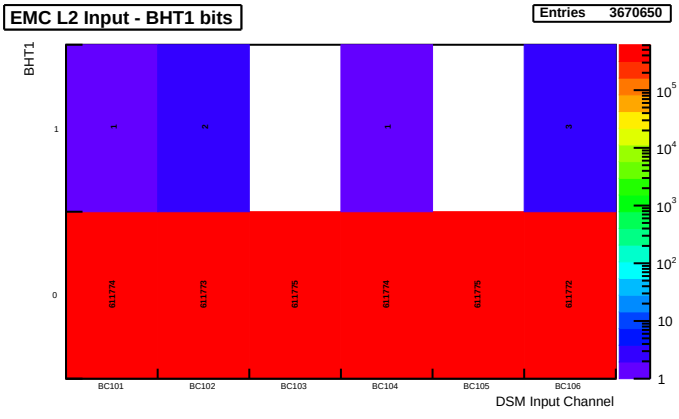

EMC L2 Input - JPZ/JPC bits
Entries 70
JPZ/JPC bits - Simulated
22
12
4
4
23
5
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

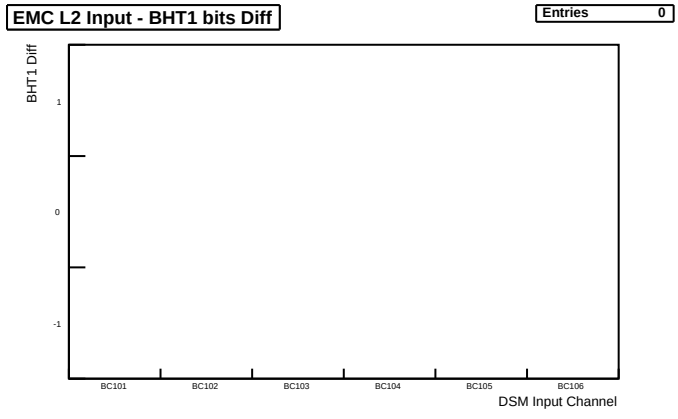

EMC L2 Input - Partial JP Sum
Entries 3670650
Partial JP Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

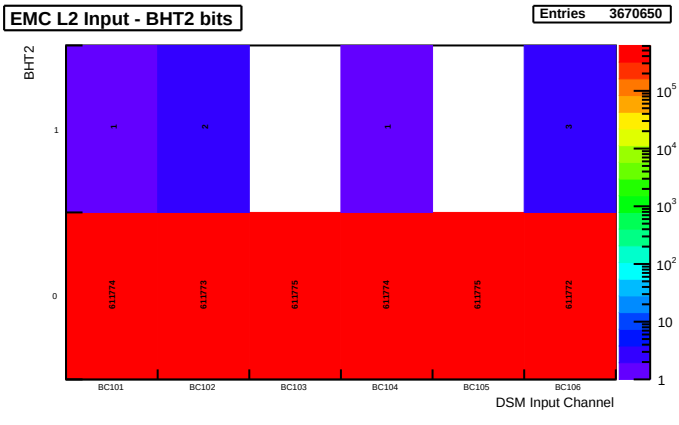

EMC L2 Input - Partial JP Sum
Entries 81
Partial JP Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

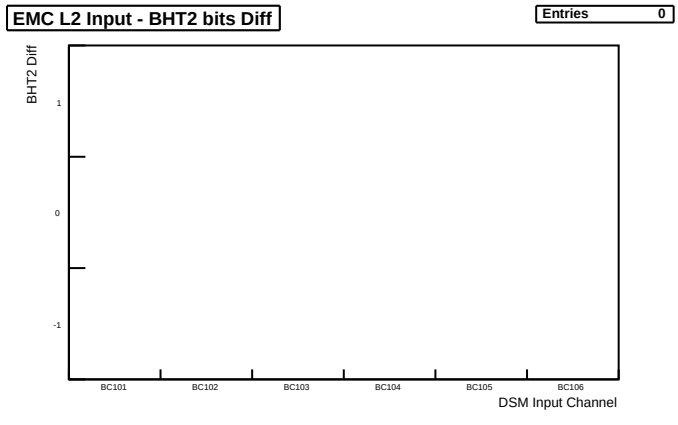

EMC L2 Input - BHT0 bits
Entries 3670650
BHT0
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

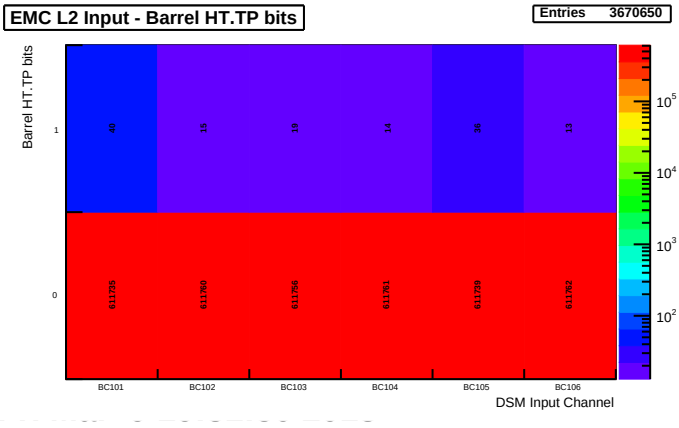

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

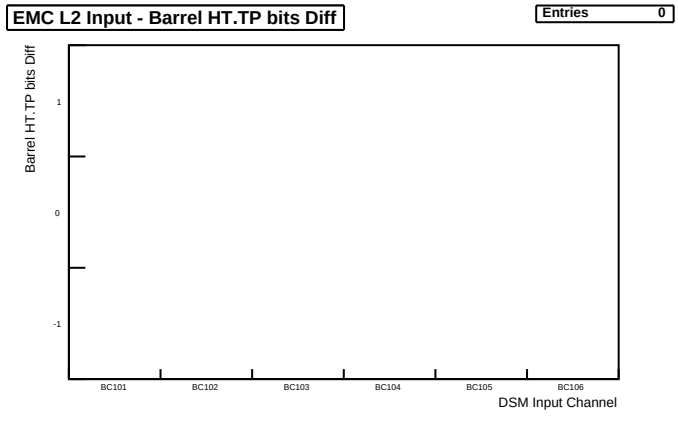

EMC L2 Input - BHT1 bits
Entries 3670650
BHT1
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

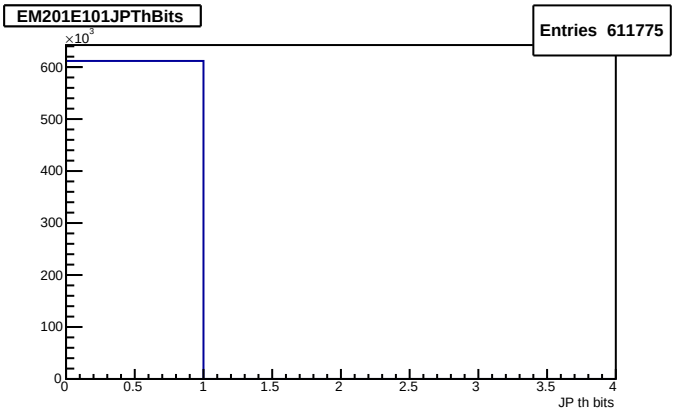

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

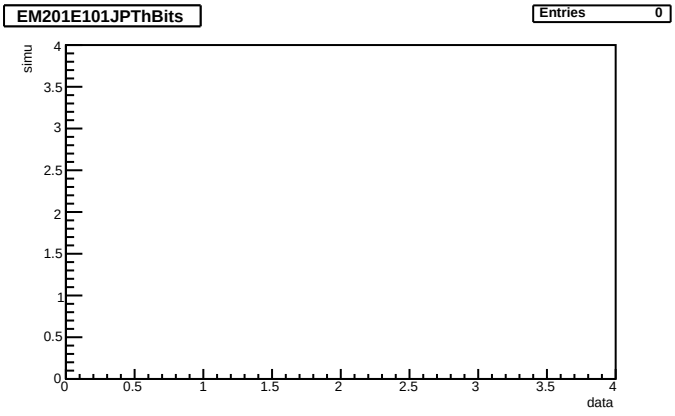

EMC L2 Input - BHT2 bits
Entries 3670650
BHT2
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

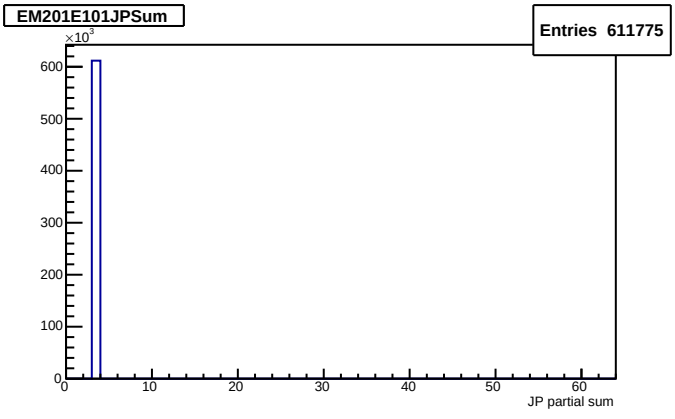

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

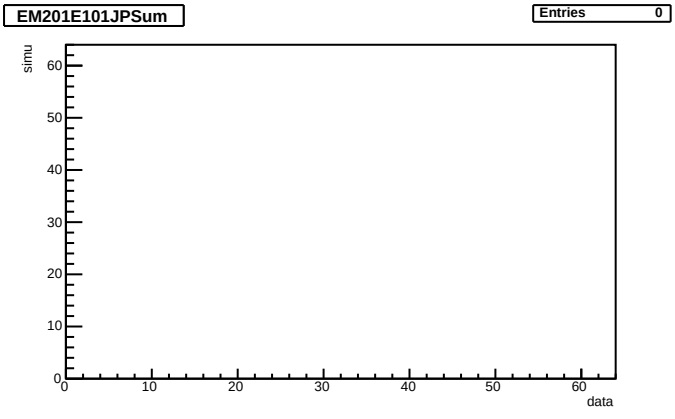

EMC L2 Input - Barrel HT.TP bits
Entries 3670650
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

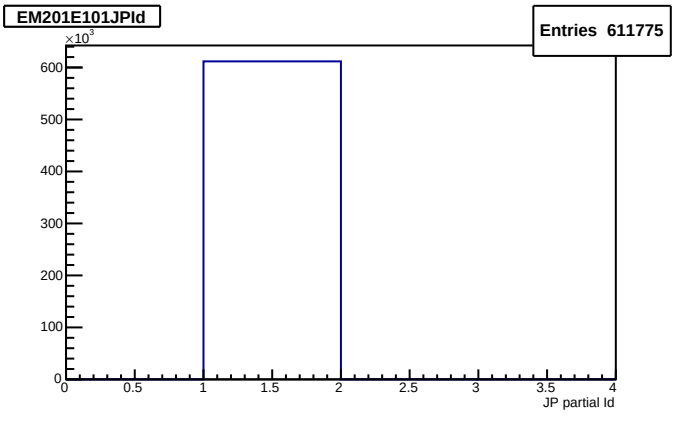

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

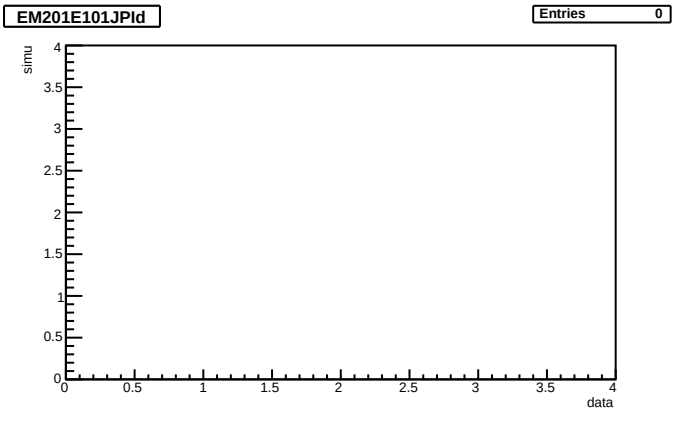

EM201E101JPThBits
Entries 611775
JP th bits

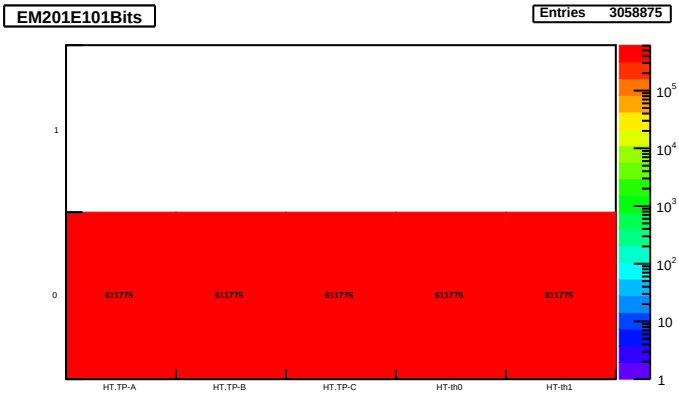

EM201E101JPThBits
Entries 0
simu
data

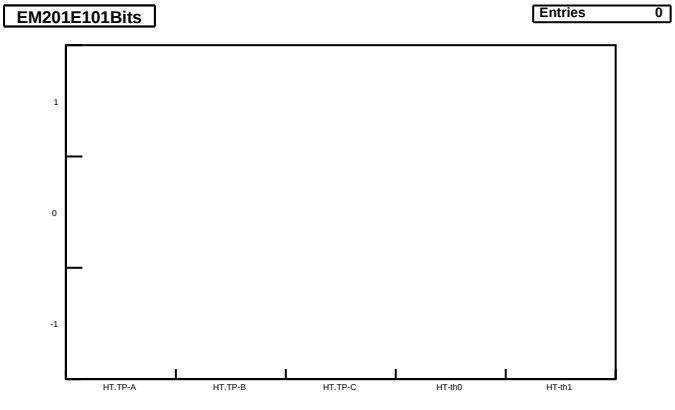

EM201E101JPSum
Entries 611775
JP partial sum

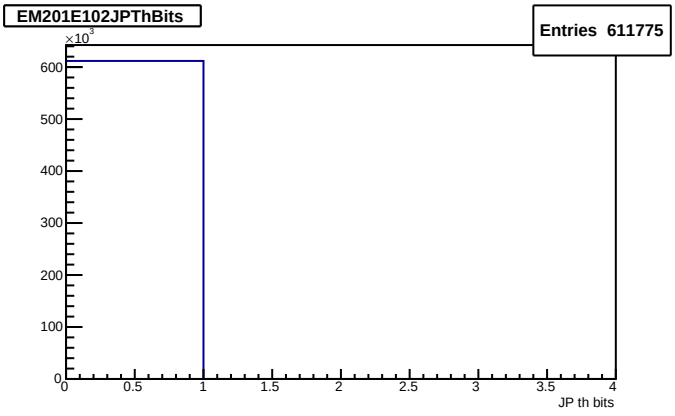

EM201E101JPSum
Entries 0
simu
data

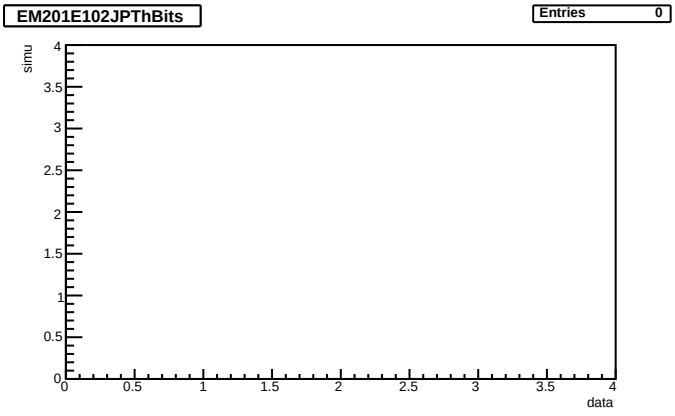

EM201E101JPId
Entries 611775
JP partial Id

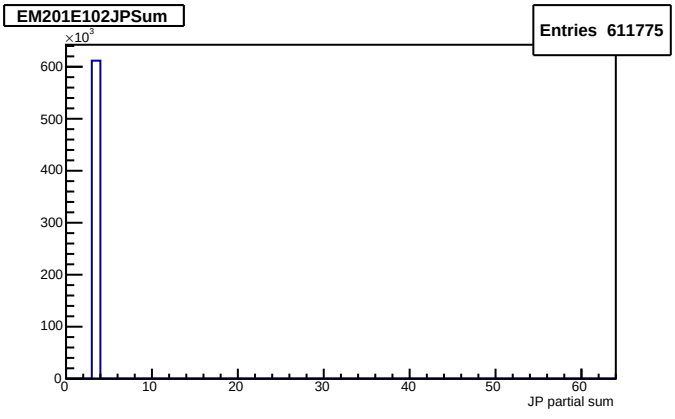

EM201E101JPId
Entries 0
simu
data

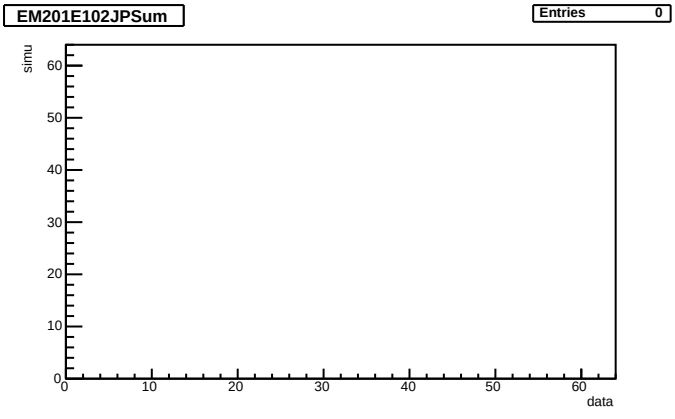

EM201E101Bits
Entries 3058875
611775
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

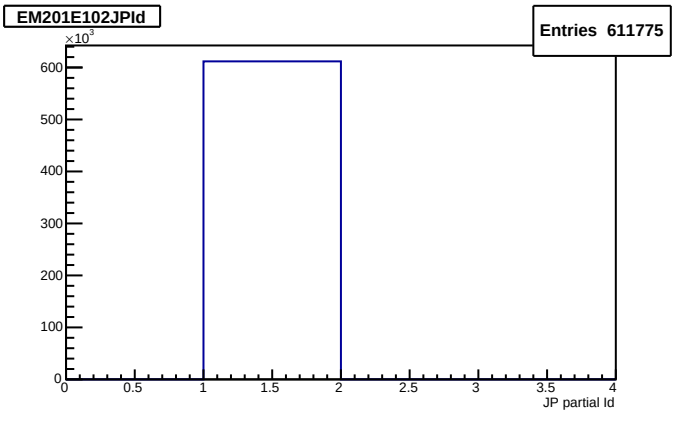

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

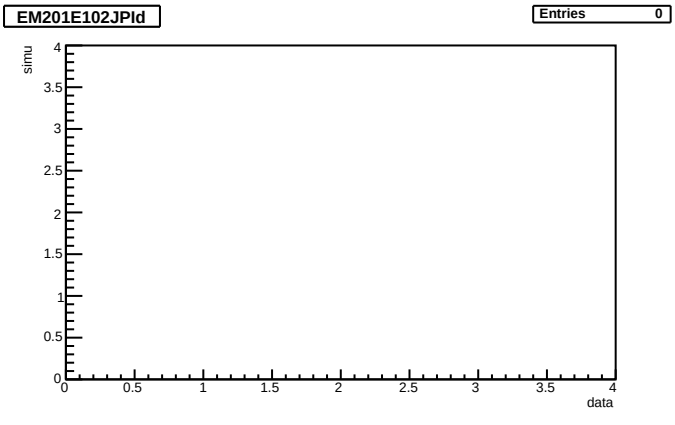

EM201E102JPThBits
Entries 611775
JP th bits

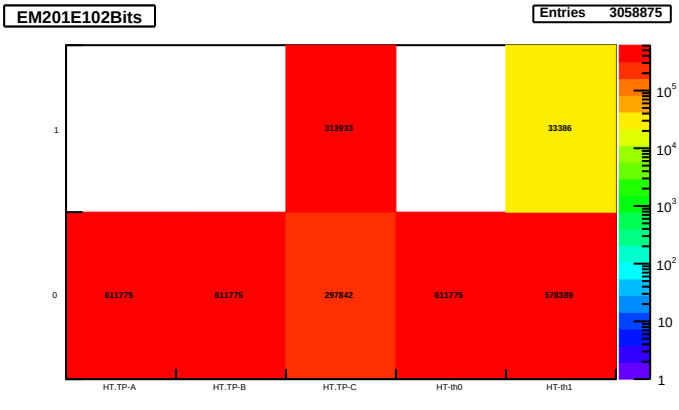

EM201E102JPThBits
Entries 0
simu
data

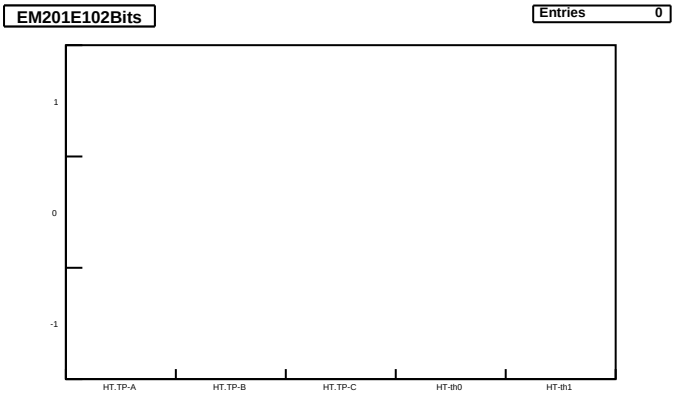

EM201E102JPSum
Entries 611775
JP partial sum

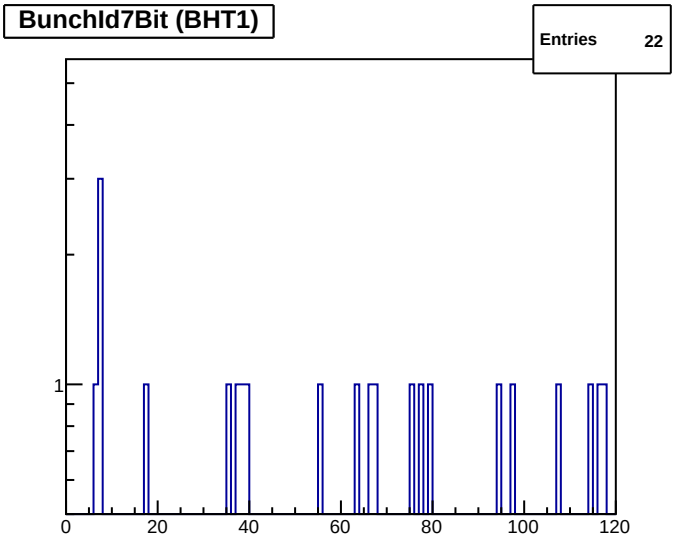

EM201E102JPSum
Entries 0
simu
data

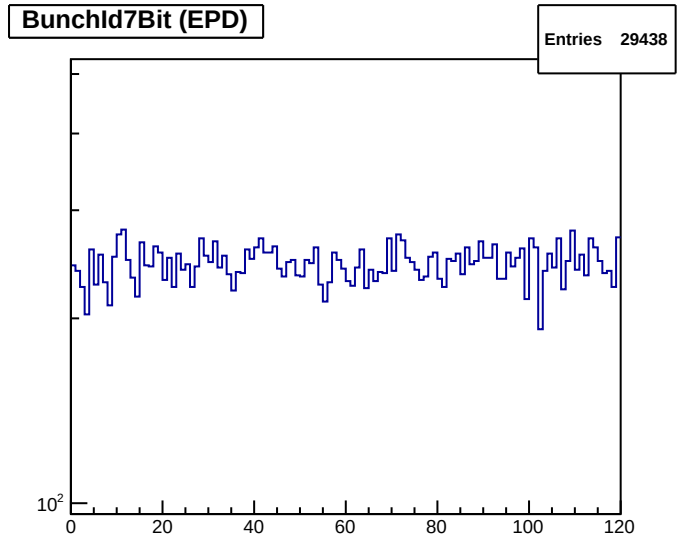

EM201E102JPId
Entries 611775
JP partial Id

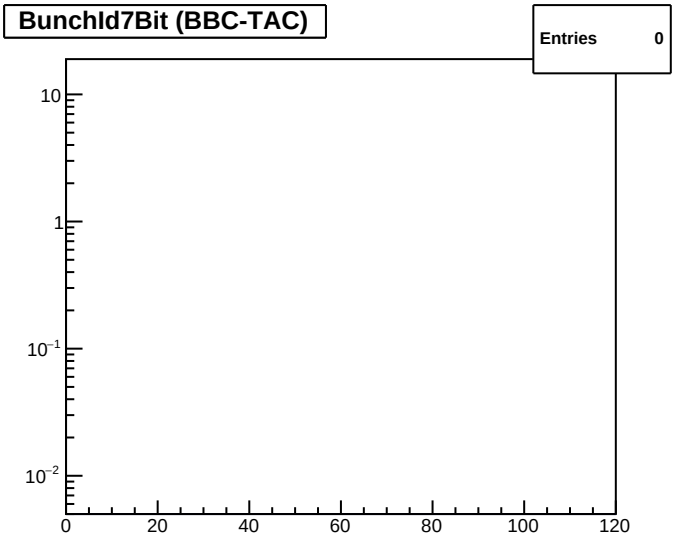

EM201E102JPId
Entries 0
simu
data

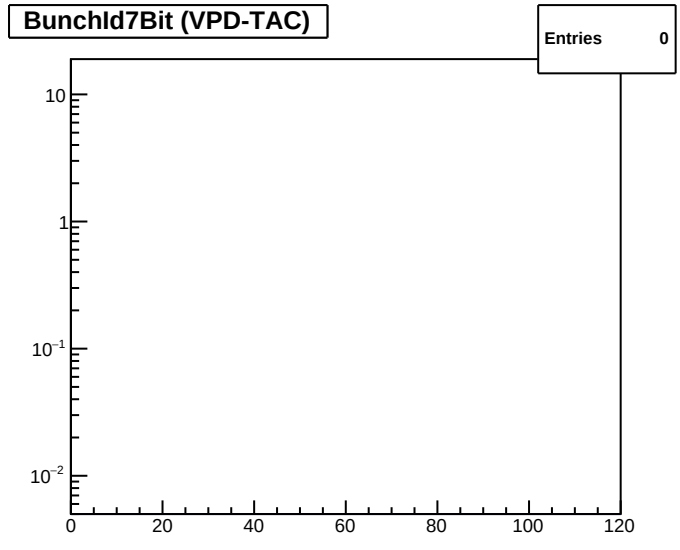

EM201E102Bits
Entries 3058875
313933
33386
611775
611775
297842
611775
578389
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

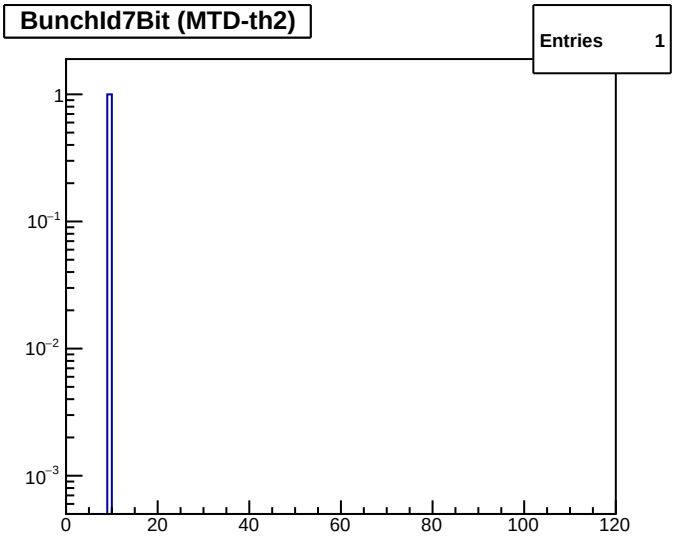

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

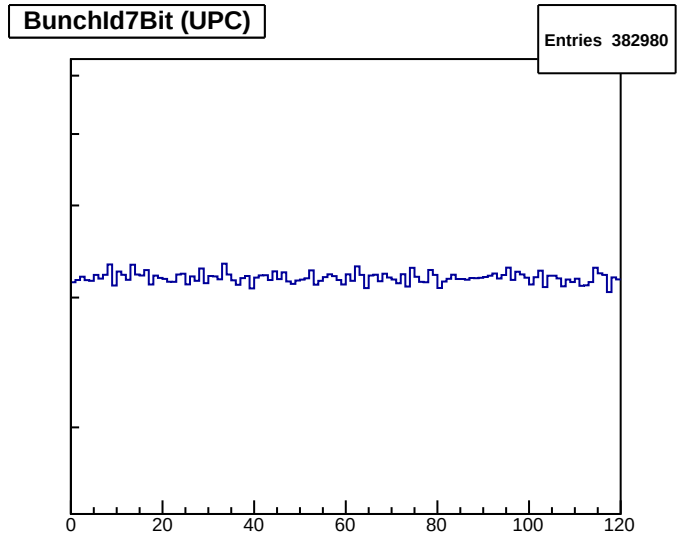

BunchId7Bit (BHT1)
Entries 22
1
0
20
40
60
80
100
120

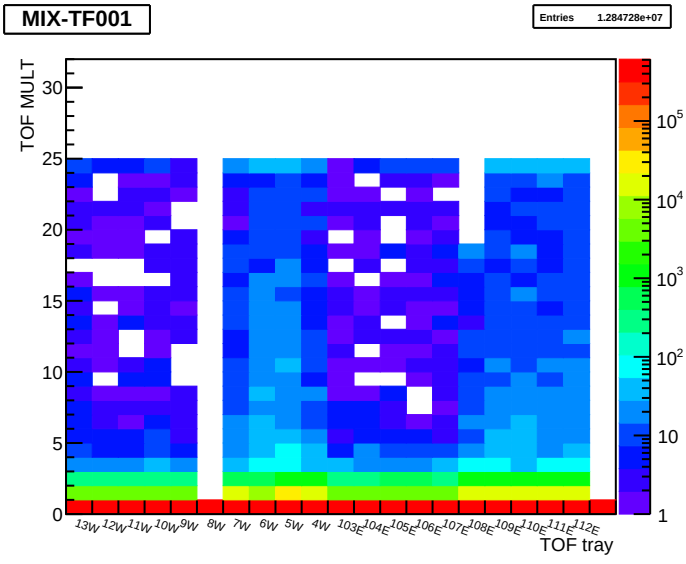

BunchId7Bit (EPD)
Entries 29438
10^2
0
20
40
60
80
100
120

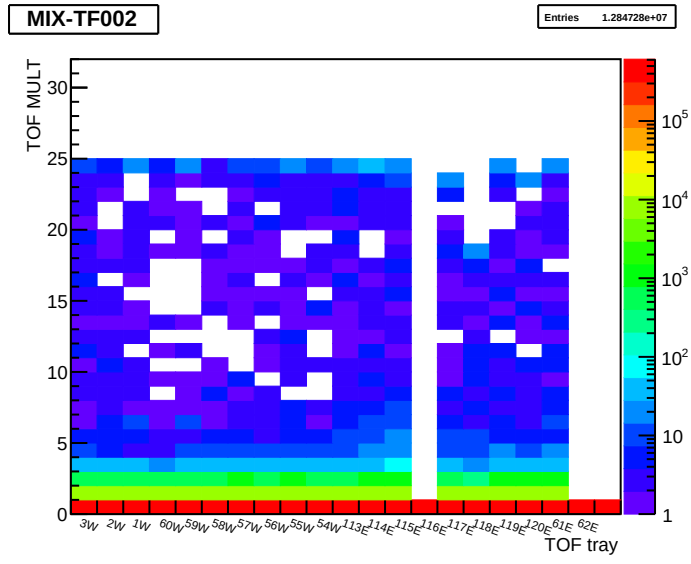

BunchId7Bit (BBC-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

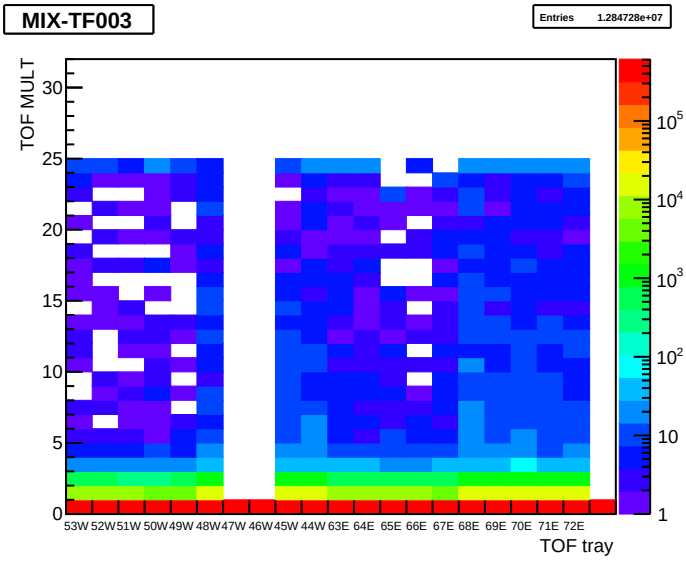

BunchId7Bit (VPD-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

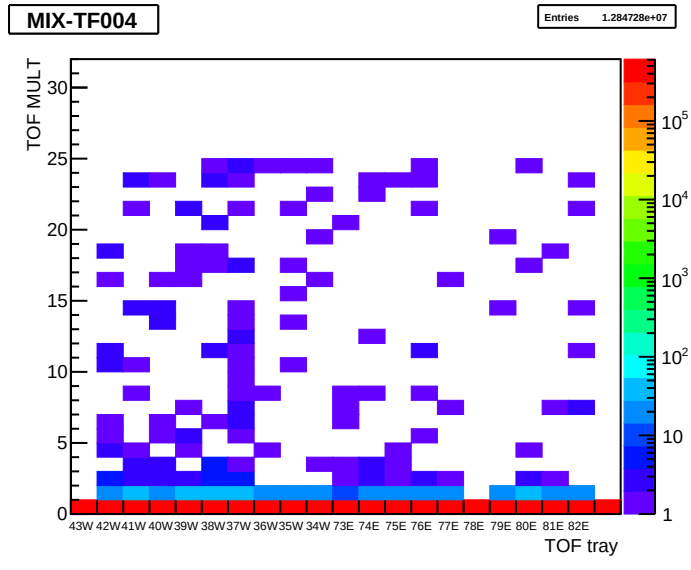

BunchId7Bit (MTD-th2)
Entries 1
1
10^{-1}
10^{-2}
10^{-3}
0
20
40
60
80
100
120

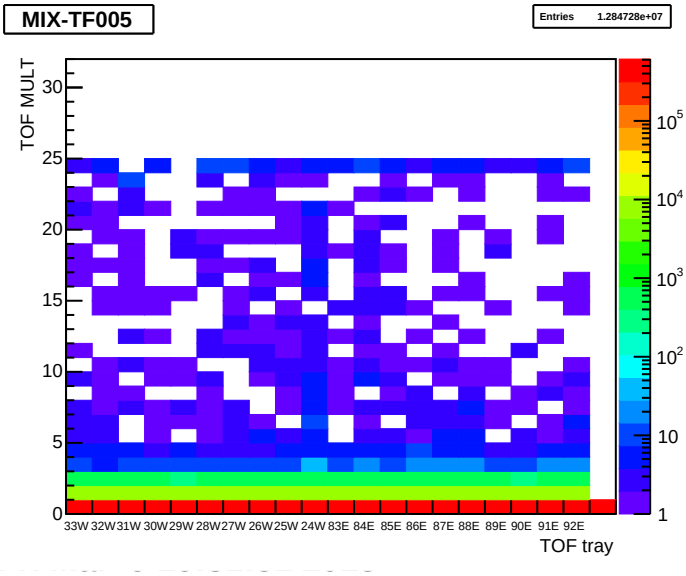

BunchId7Bit (UPC)
Entries 382980
0
20
40
60
80
100
120

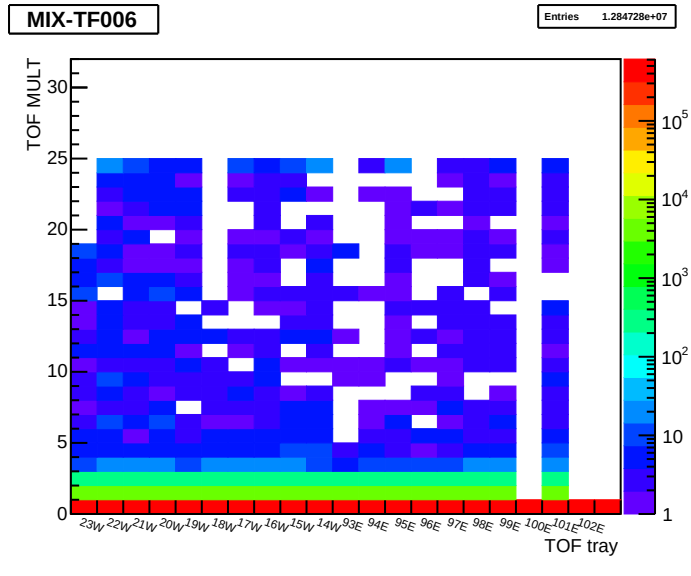

MIX-TF001
Entries 1.284728e+07
TOF MULT
TOF tray

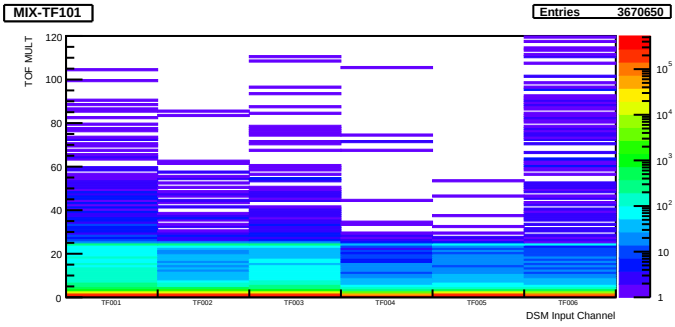

MIX-TF002
Entries 1.284728e+07
TOF MULT
TOF tray

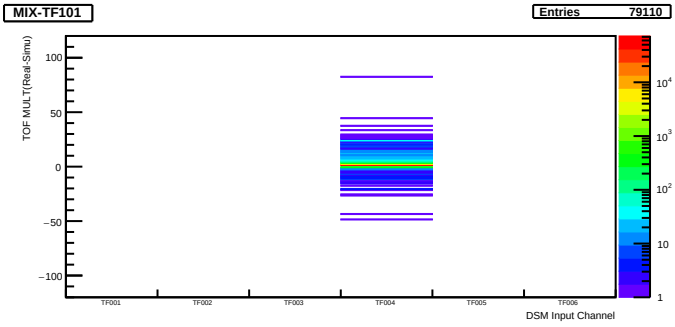

MIX-TF003
Entries 1.284728e+07
TOF MULT
TOF tray

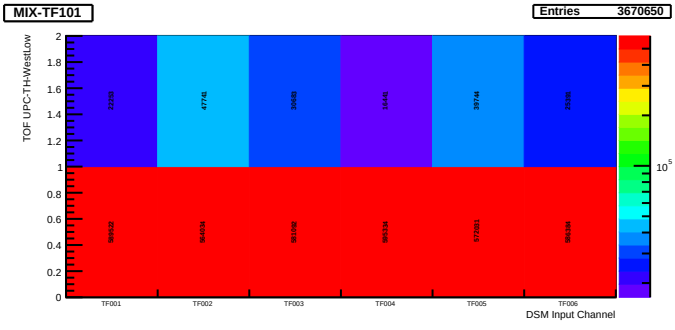

MIX-TF004
Entries 1.284728e+07
TOF MULT
TOF tray

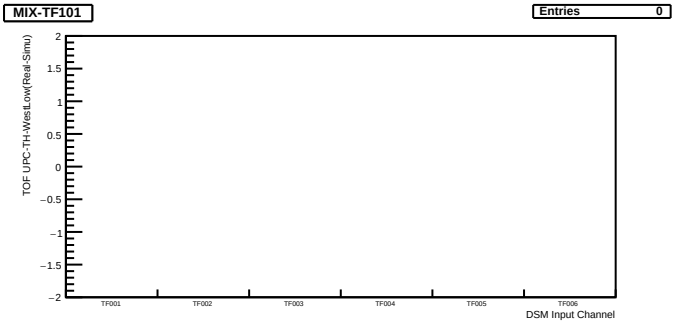

MIX-TF005
Entries 1.284728e+07
TOF MULT
TOF tray

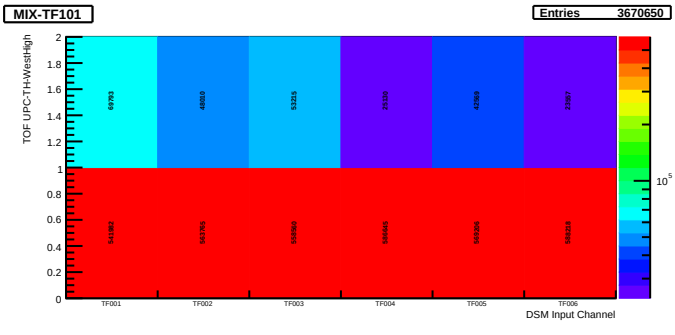

MIX-TF006
Entries 1.284728e+07
TOF MULT
TOF tray

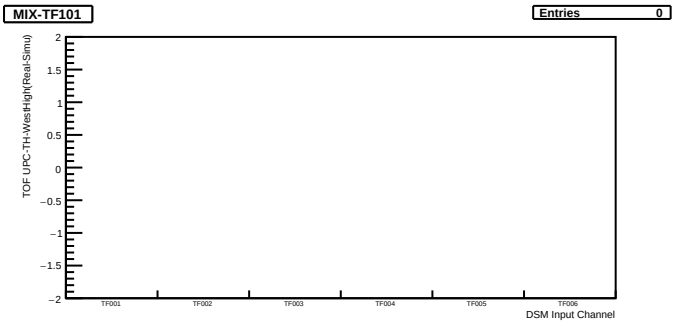

MIX-TF101
Entries 3670650
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

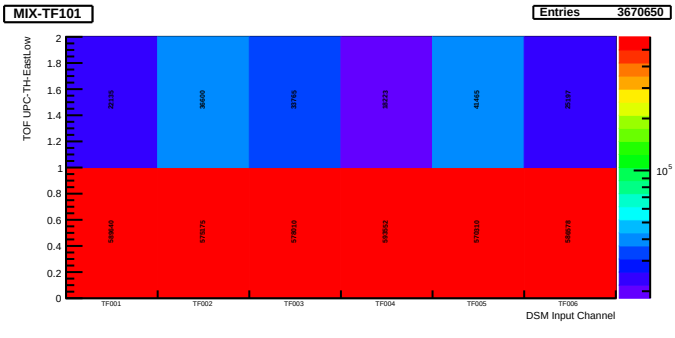

MIX-TF101
Entries 79110
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

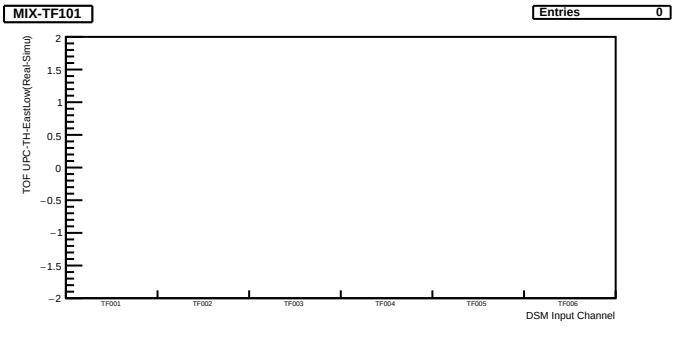

MIX-TF101
Entries 3670650
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

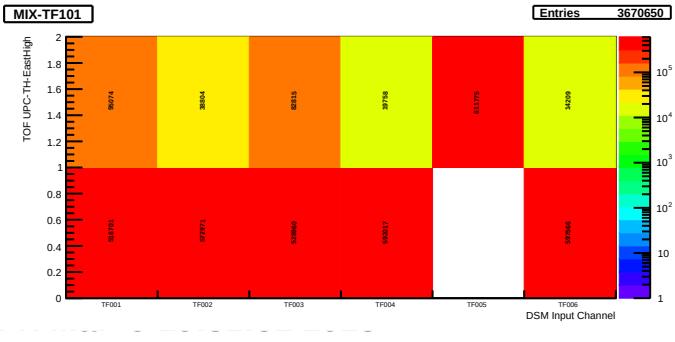

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

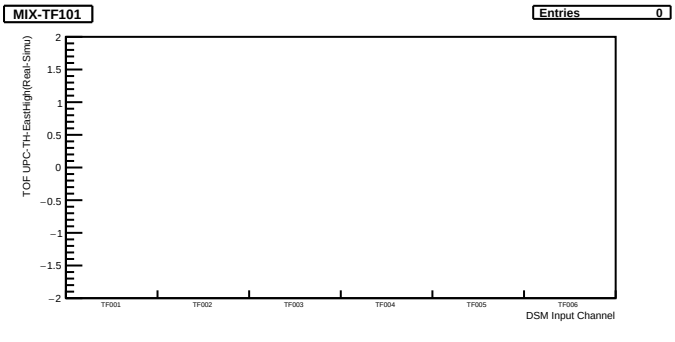

MIX-TF101
Entries 3670650
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

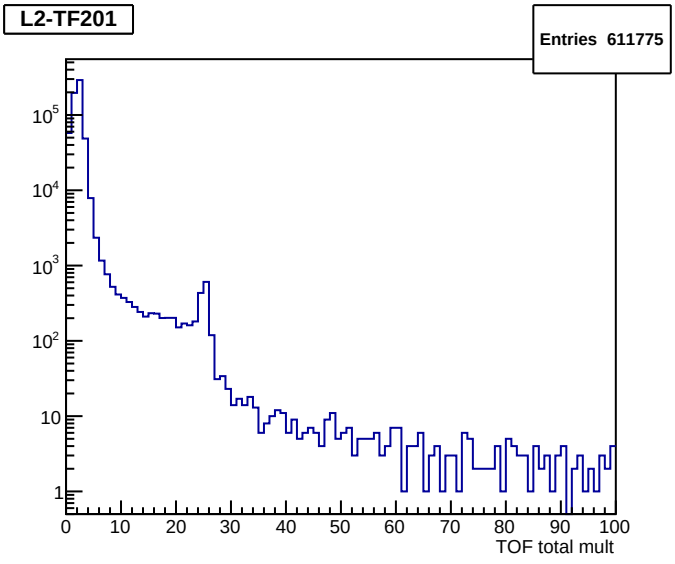

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

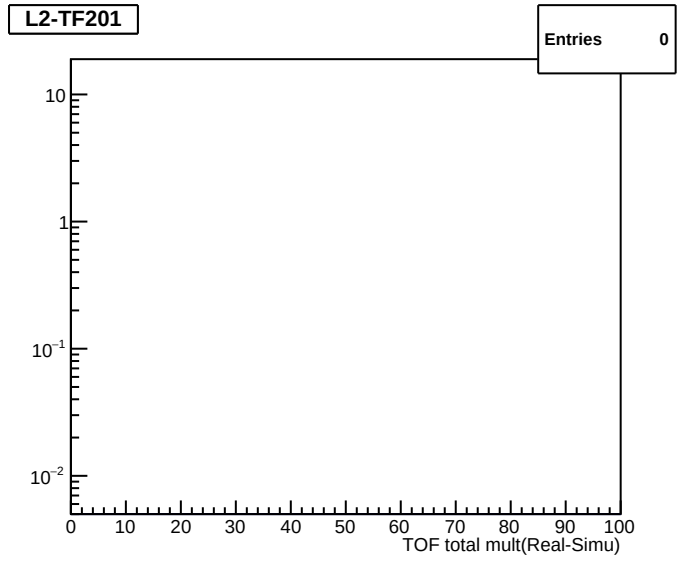

MIX-TF101
Entries 3670650
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

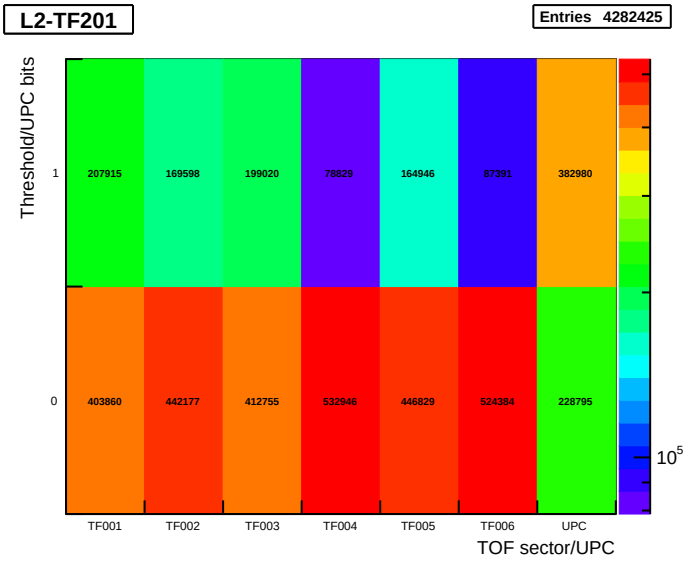

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

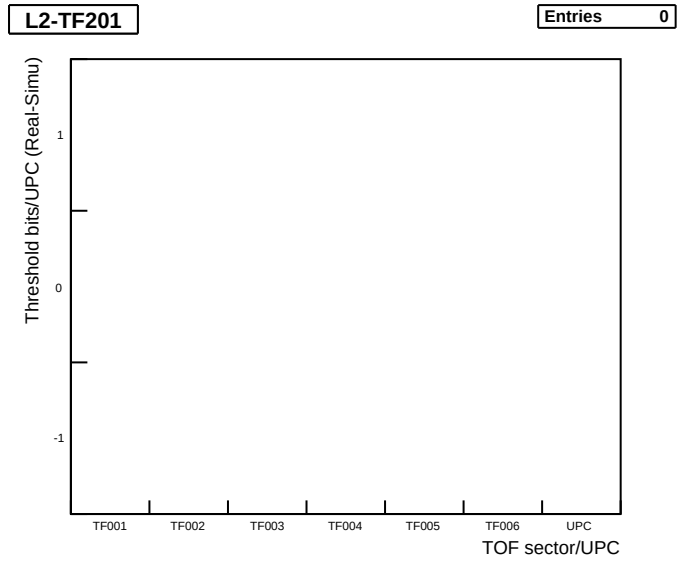

MIX-TF101
Entries 3670650
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

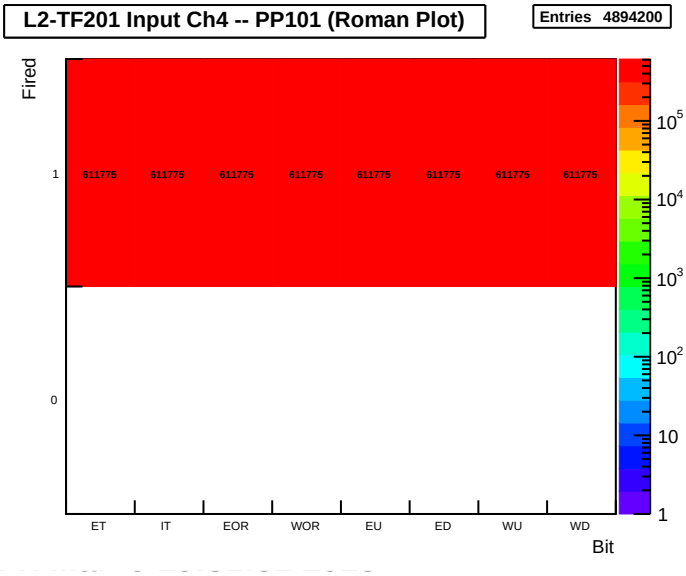

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

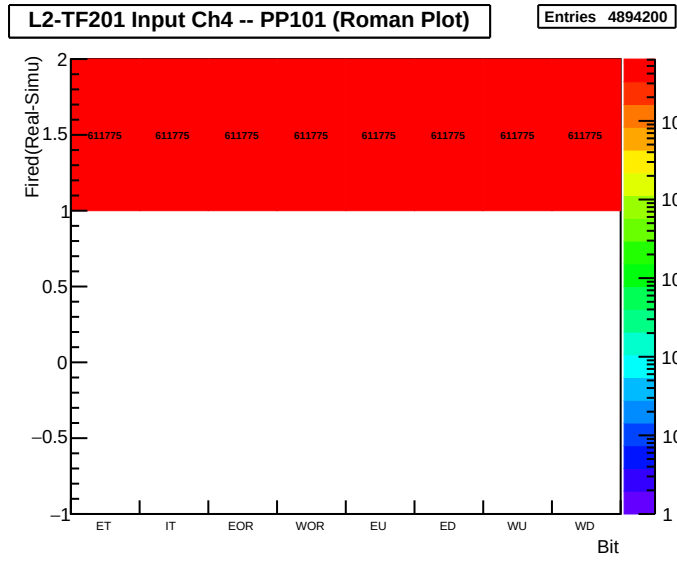

L2-TF201
Entries 611775
TOF total mult

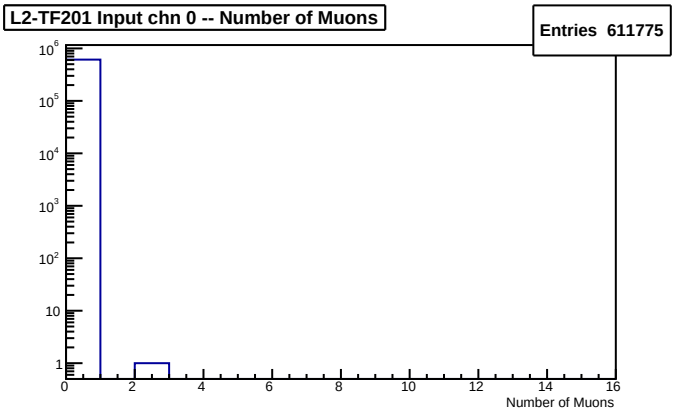

L2-TF201
Entries 0
TOF total mult(Real-Simu)

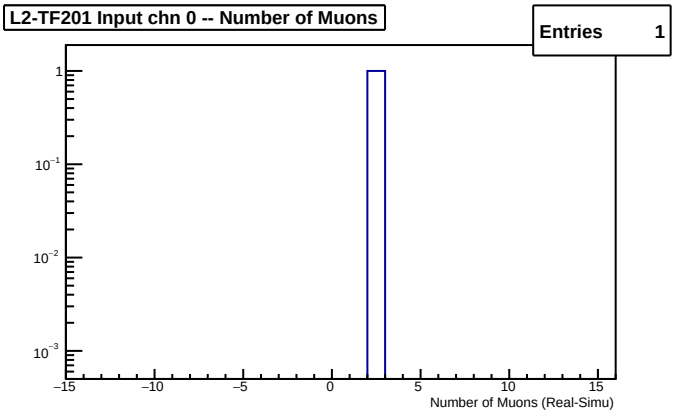

L2-TF201
Entries 4282425
Threshold/UPC bits
207915
169598
199020
78829
164946
87391
382980
403860
442177
412755
532946
446829
524384
228795
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

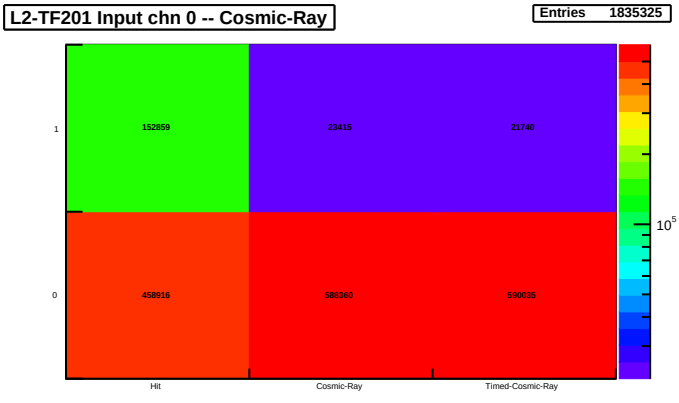

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

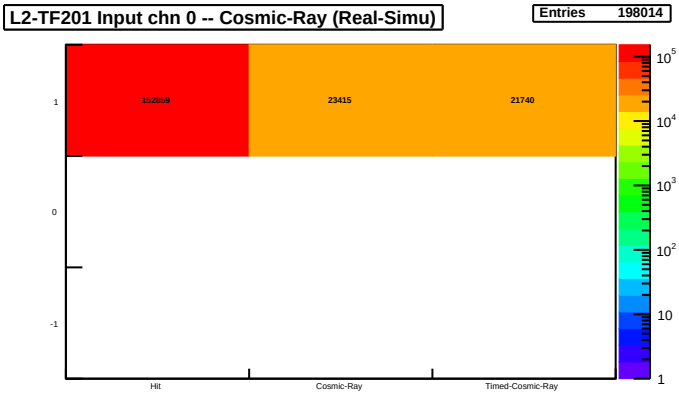

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 4894200
Fired
611775
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

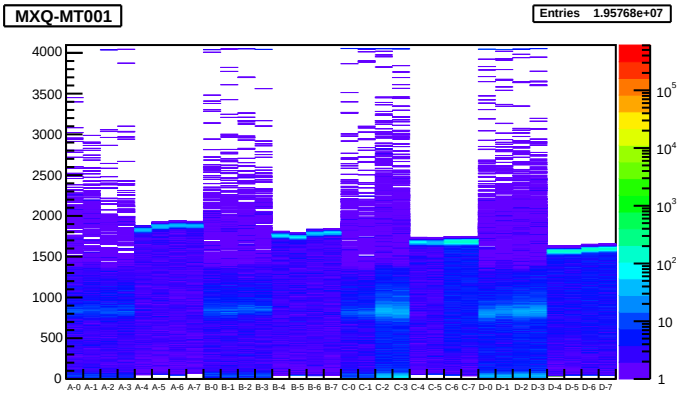

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 4894200
Fired(Real-Simu)
611775
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

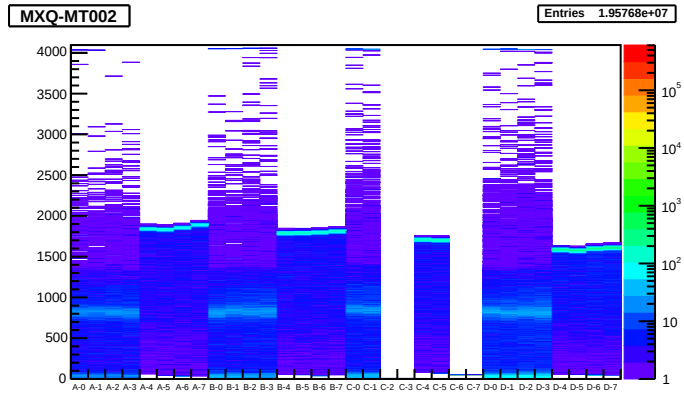

L2-TF201 Input chn 0 -- Number of Muons
Entries 611775
Number of Muons

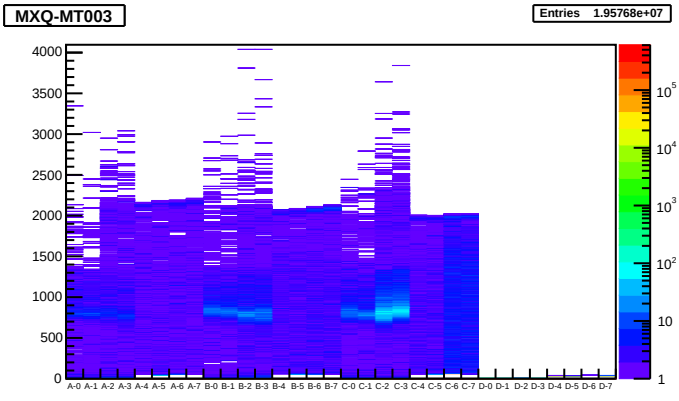

L2-TF201 Input chn 0 -- Number of Muons
Entries 1
Number of Muons (Real-Simu)

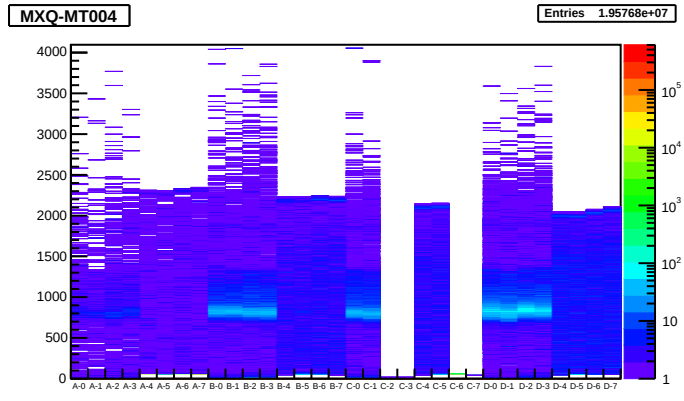

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 1835325
152859
23415
21740
458916
588360
590035
Hit
Cosmic-Ray
Timed-Cosmic-Ray

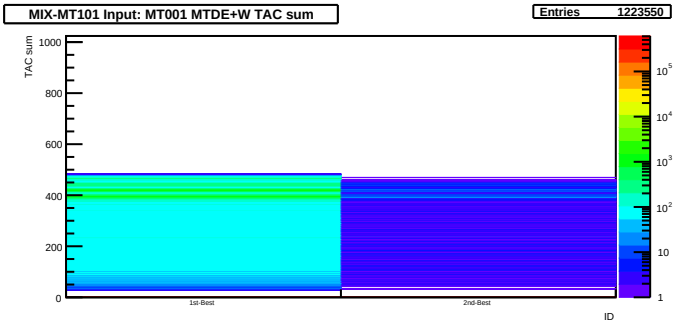

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 198014
152859
23415
21740
Hit
Cosmic-Ray
Timed-Cosmic-Ray

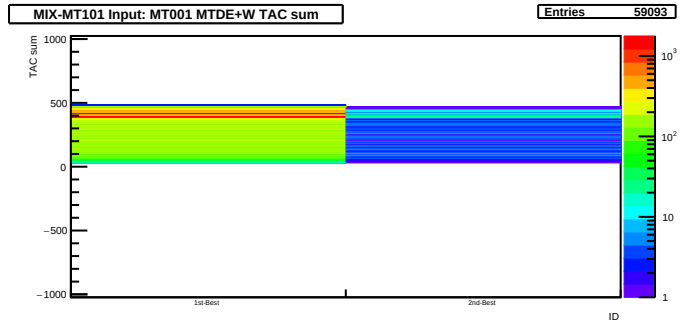

MXQ-MT001
Entries 1.95768e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

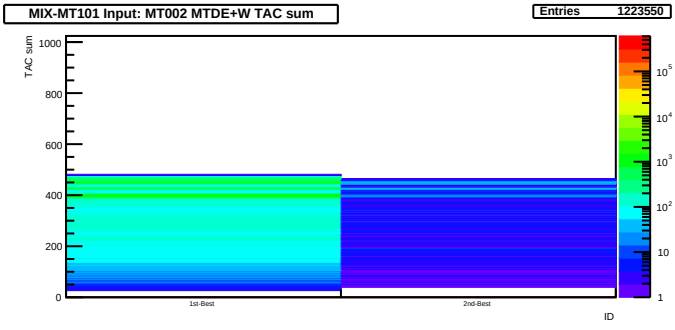

MXQ-MT002
Entries 1.95768e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

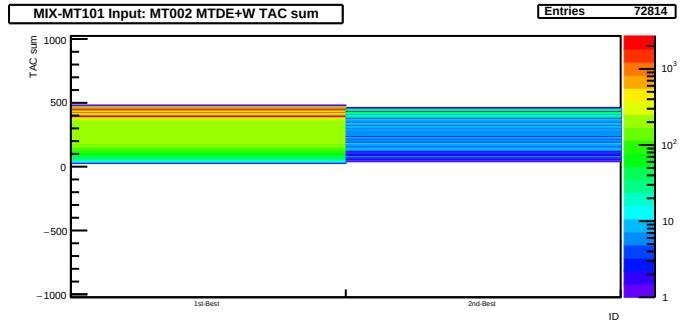

MXQ-MT003
Entries 1.95768e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

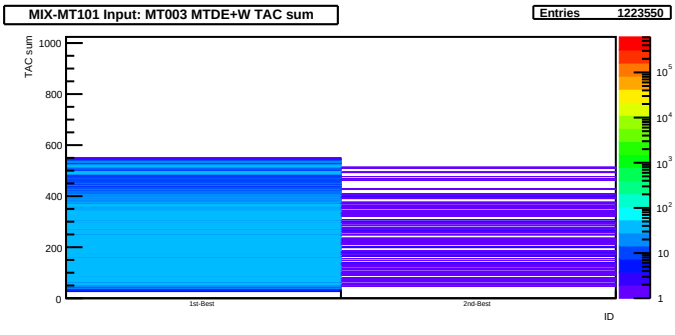

MXQ-MT004
Entries 1.95768e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

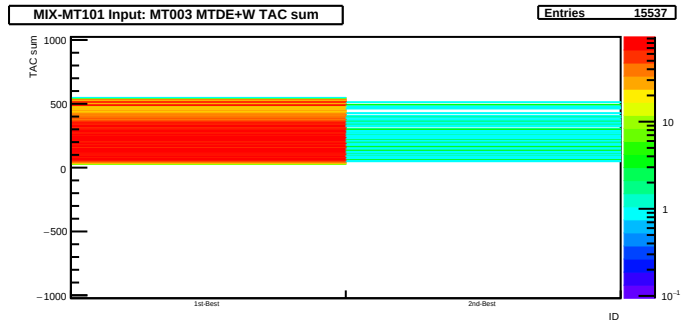

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 1223550
TAC sum
1st-Best
2nd-Best
ID

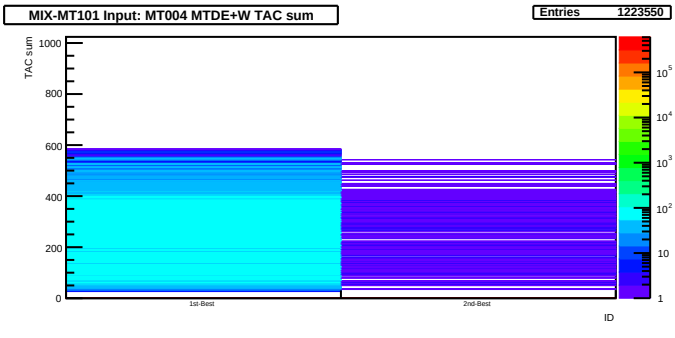

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 59093
TAC sum
1st-Best
2nd-Best
ID

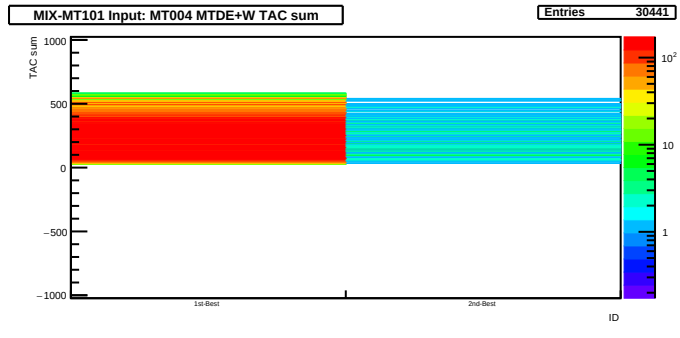

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 1223550
TAC sum
1st-Best
2nd-Best
ID

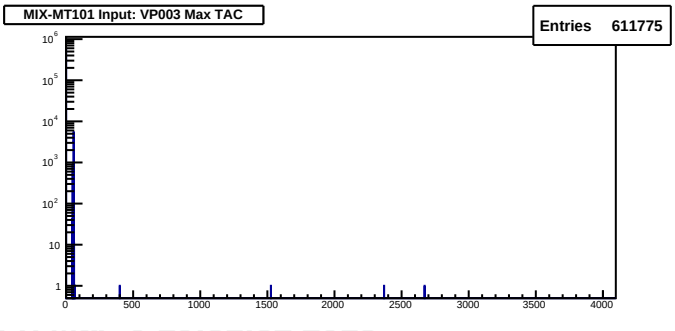

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 72814
TAC sum
1st-Best
2nd-Best
ID

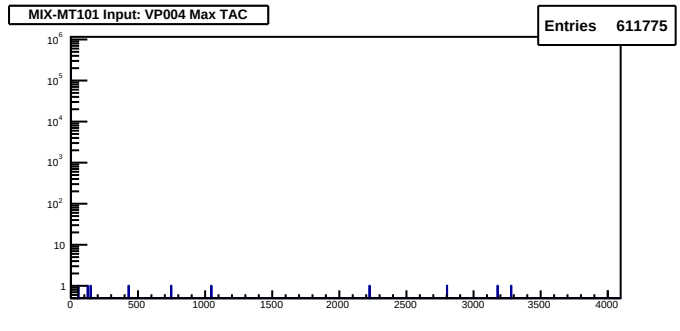

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 1223550
TAC sum
1st-Best
2nd-Best
ID

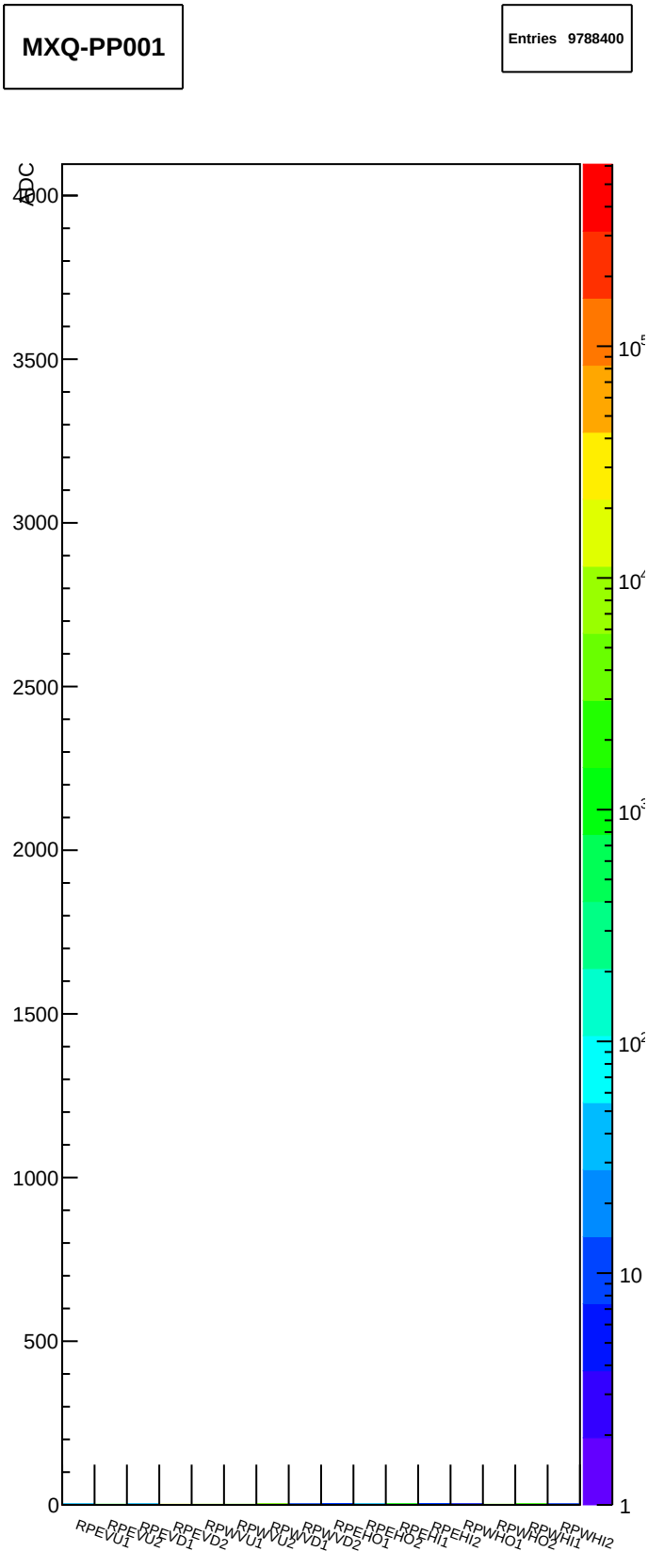

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 15537
TAC sum
1st-Best
2nd-Best
ID


MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 1223550
TAC sum
1st-Best
2nd-Best
ID


MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 30441
TAC sum
1st-Best
2nd-Best
ID


MIX-MT101 Input: VP003 Max TAC
Entries 611775


MIX-MT101 Input: VP004 Max TAC
Entries 611775


MXQ-PP001
Entries 9788400
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
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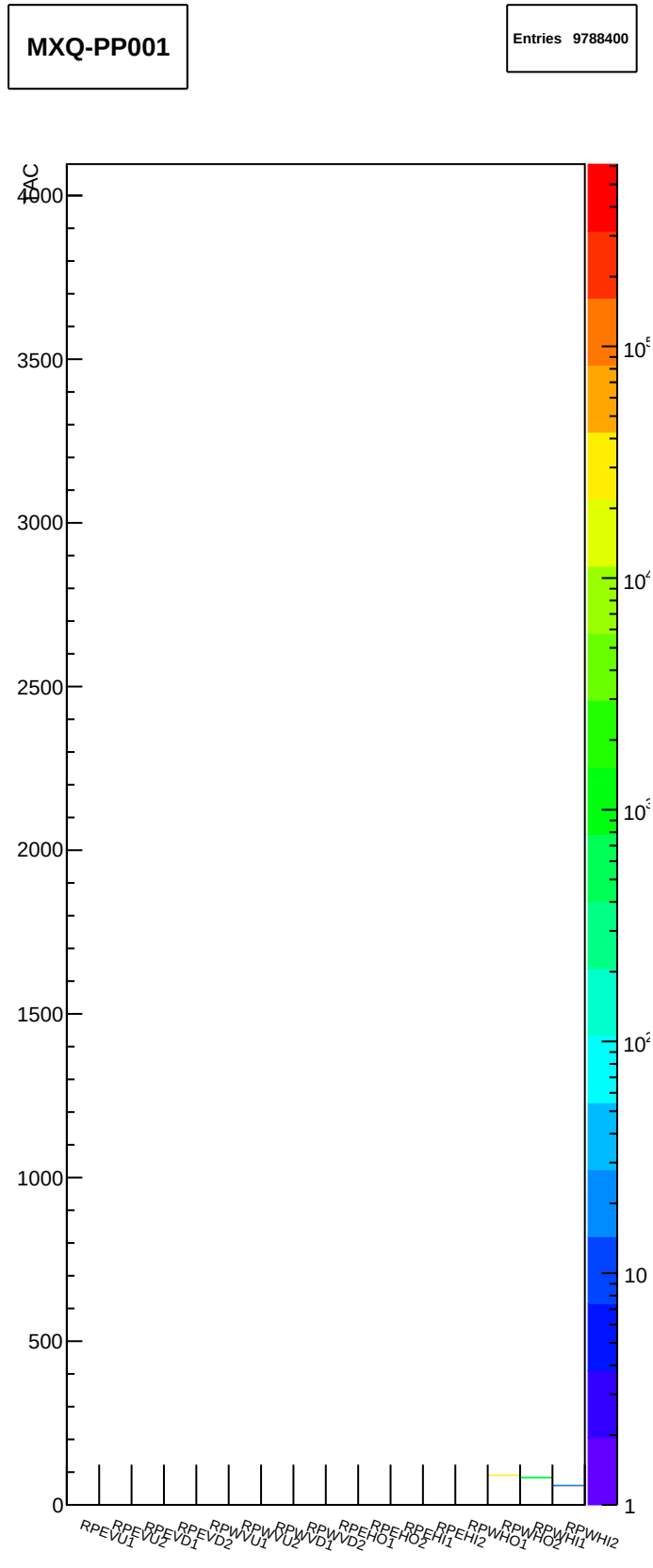
RPWHO2
RPWHI1
RPWHI2
10
1


MXQ-PP001
Entries 9788400
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
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RPEHO2
RPEHI1
RPEHI2
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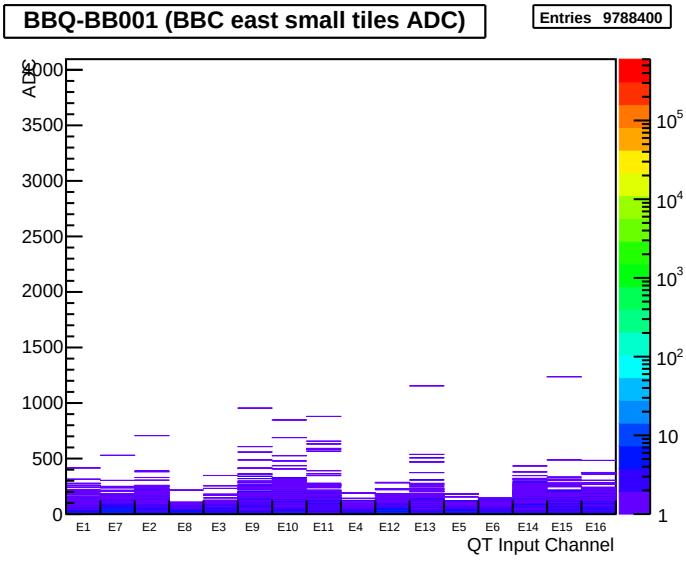
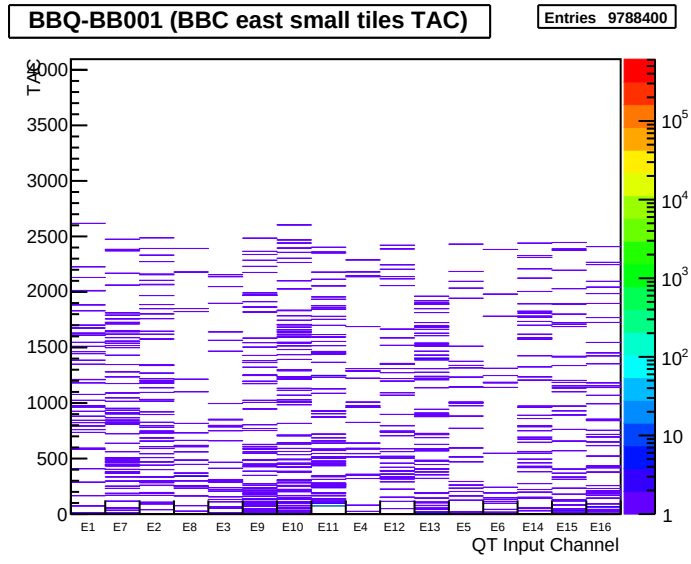
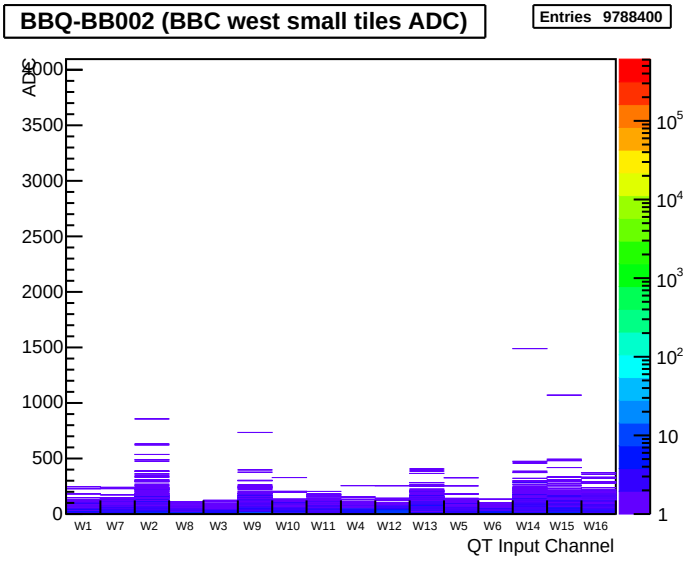
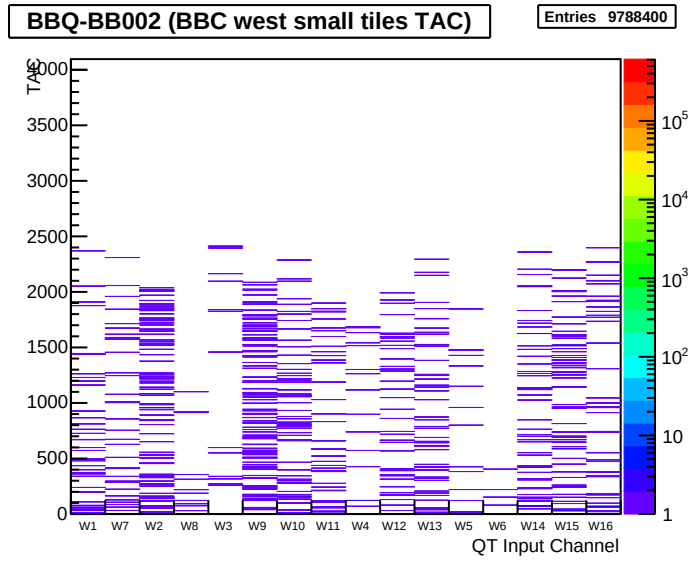
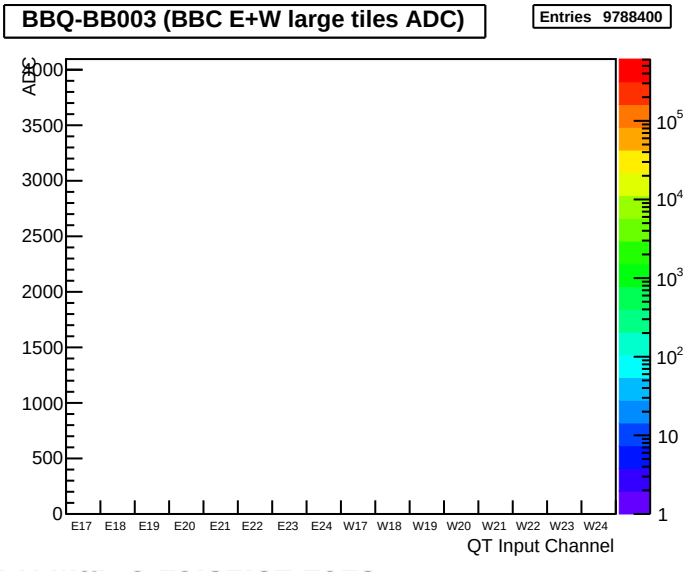
RPWHO2
RPWHI1
RPWHI2
10
1

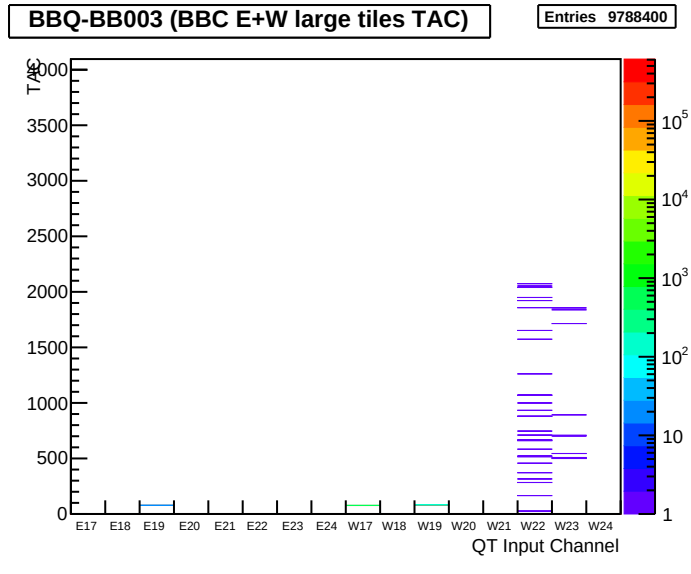

BBQ-BB001 (BBC east small tiles ADC)
Entries 9788400
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

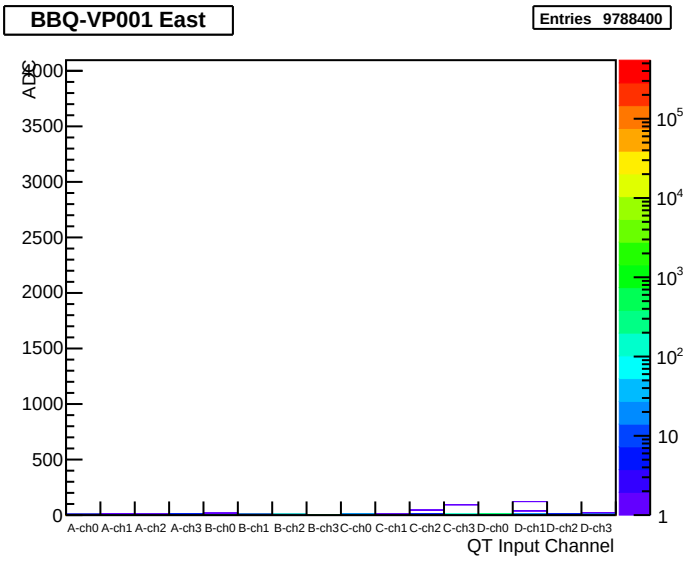

BBQ-BB001 (BBC east small tiles TAC)
Entries 9788400
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

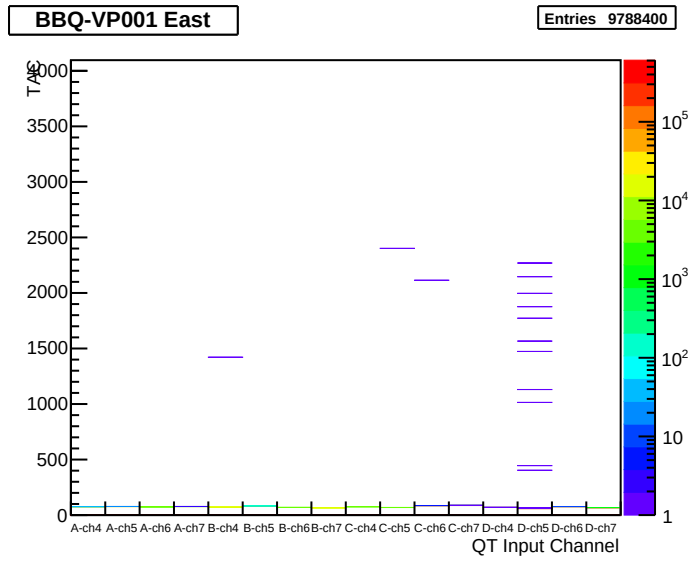

BBQ-BB002 (BBC west small tiles ADC)
Entries 9788400
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

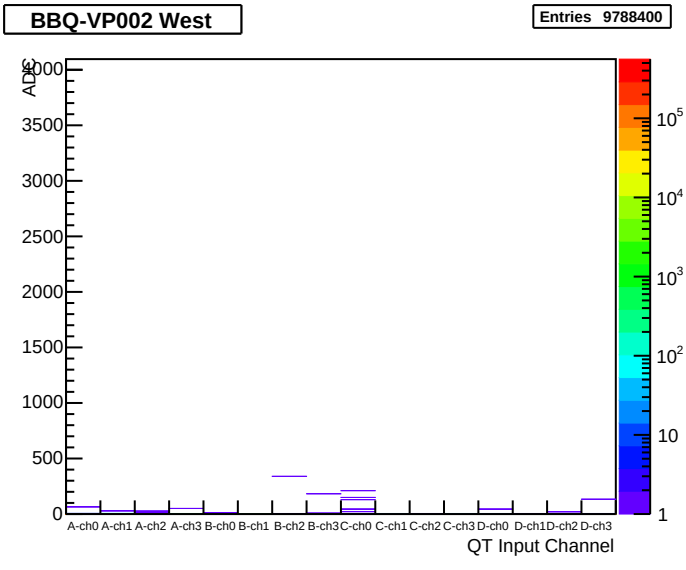

BBQ-BB002 (BBC west small tiles TAC)
Entries 9788400
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

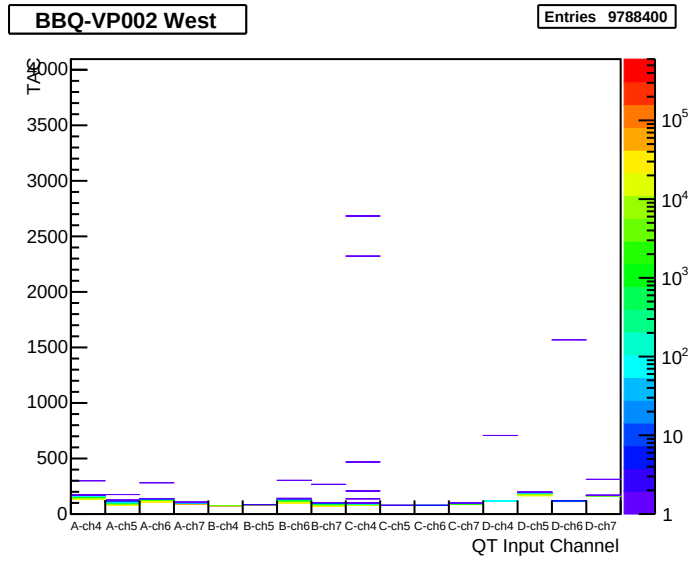

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9788400
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

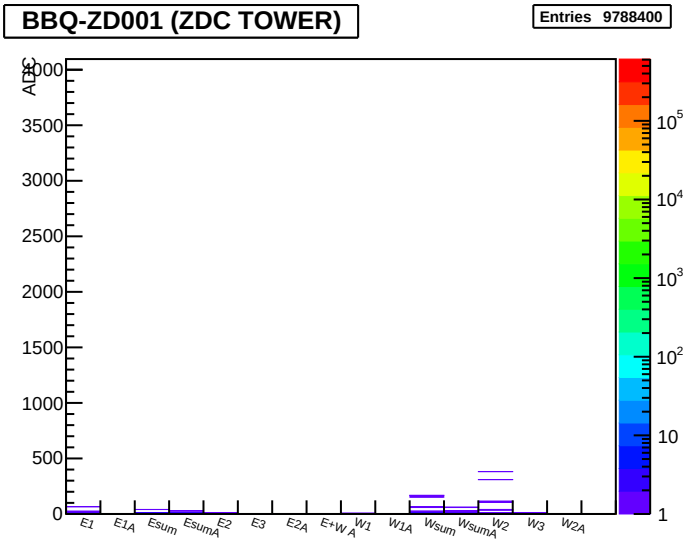

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9788400
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

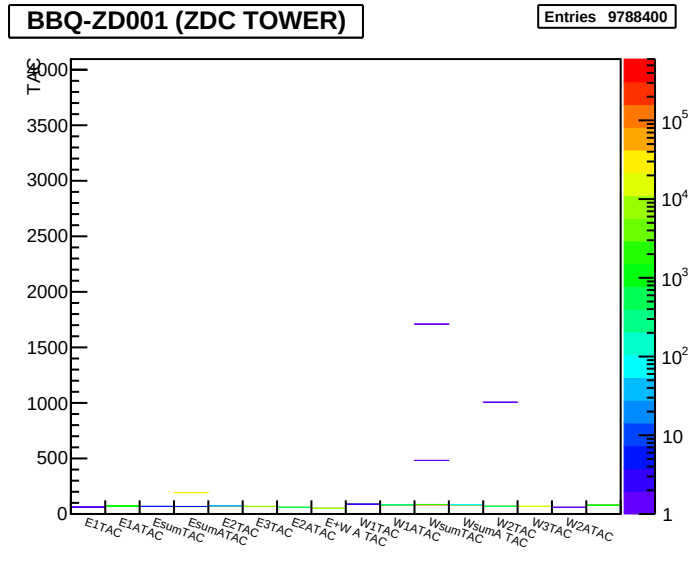

BBQ-VP001 East
Entries 9788400
ADC
QT Input Channel

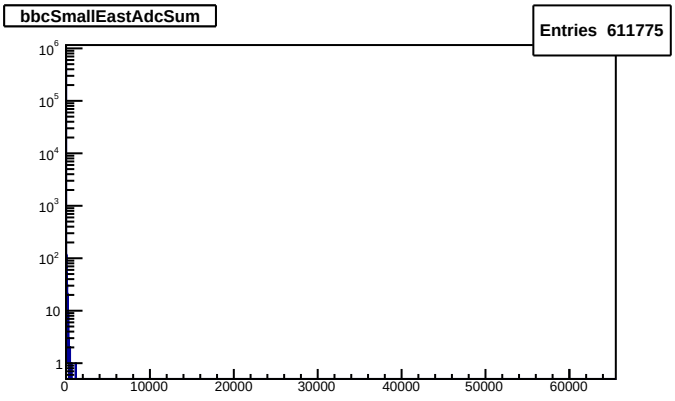

BBQ-VP001 East
Entries 9788400
TAC
QT Input Channel

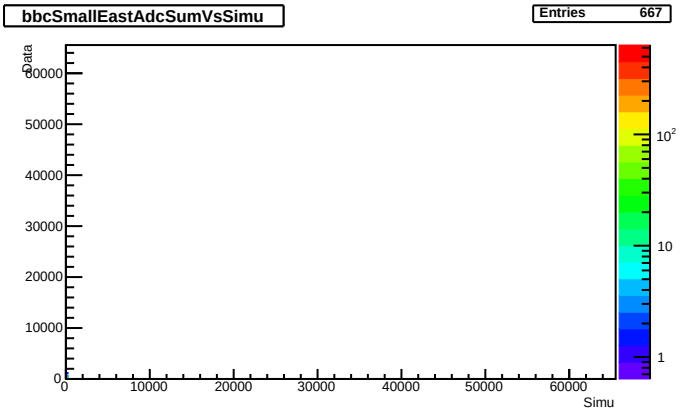

BBQ-VP002 West
Entries 9788400
ADC
QT Input Channel

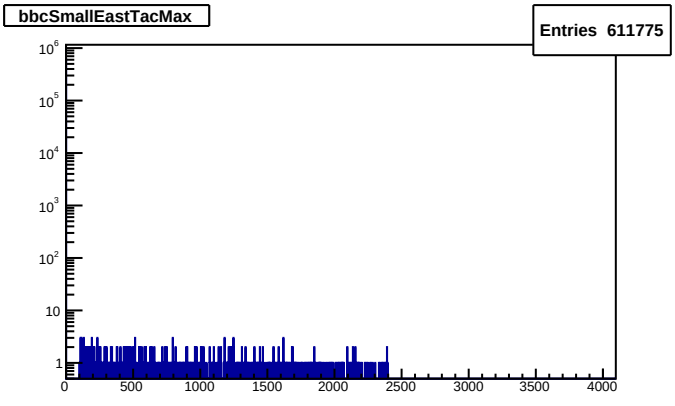

BBQ-VP002 West
Entries 9788400
TAC
QT Input Channel

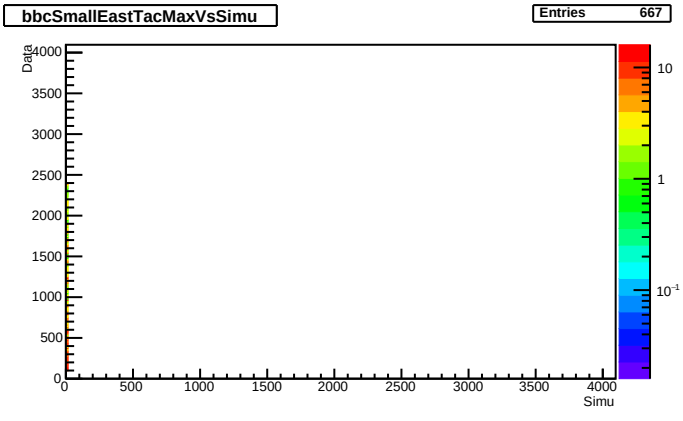

BBQ-ZD001 (ZDC TOWER)
Entries 9788400
ADC

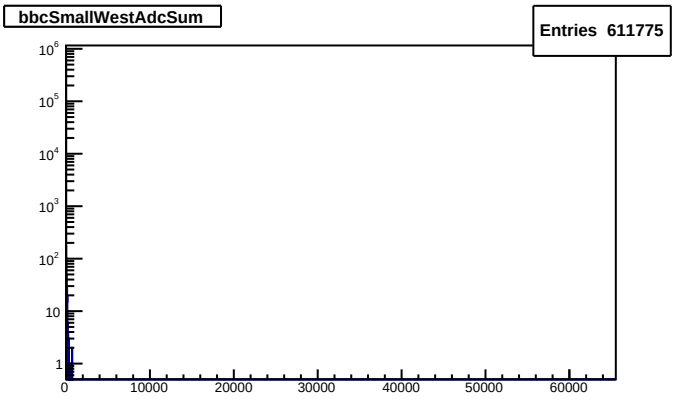

BBQ-ZD001 (ZDC TOWER)
Entries 9788400
TAC

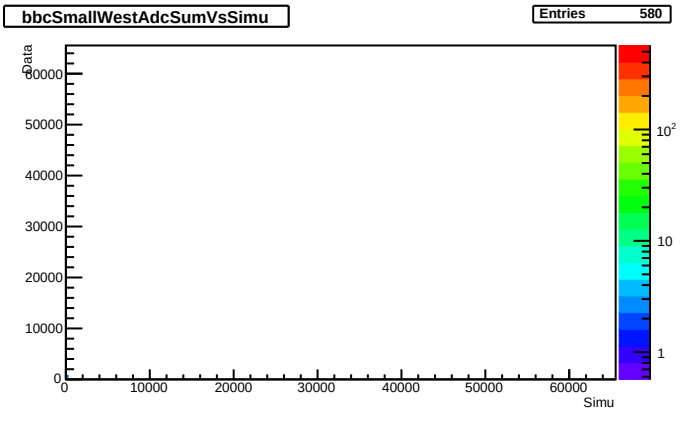

bbcSmallEastAdcSum
Entries 611775

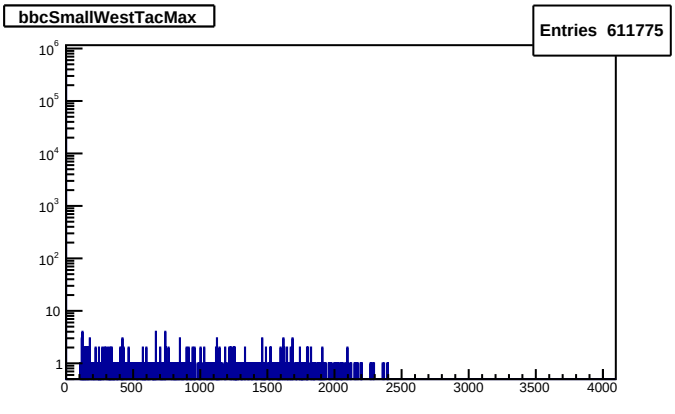

bbcSmallEastAdcSumVsSimu
Entries 667
Data
Simu

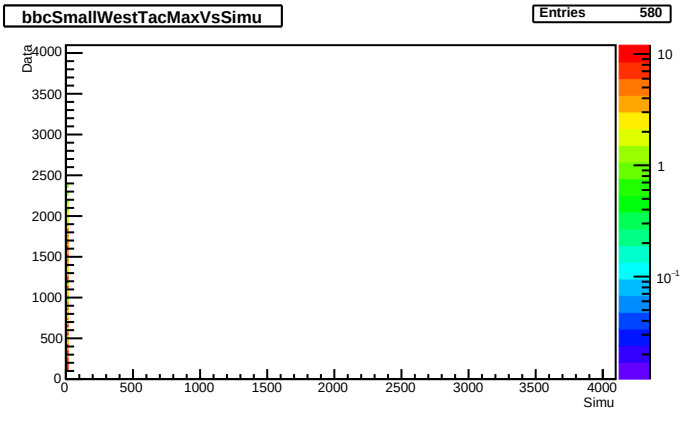

bbcSmallEastTacMax
Entries 611775

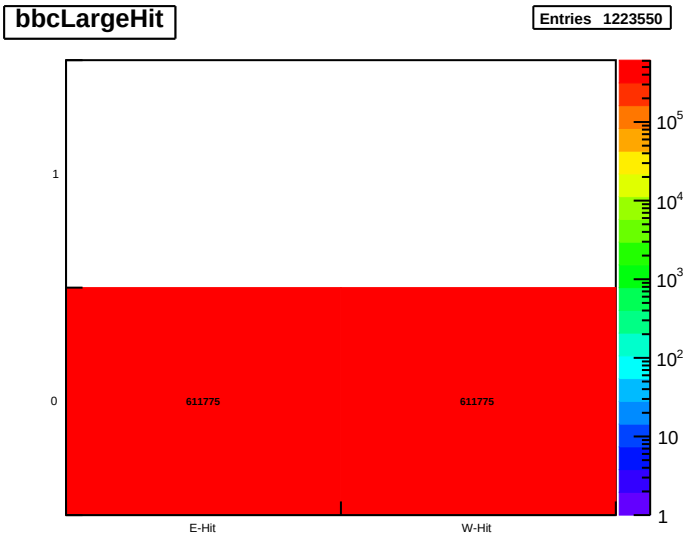

bbcSmallEastTacMaxVsSimu
Entries 667
Data
Simu

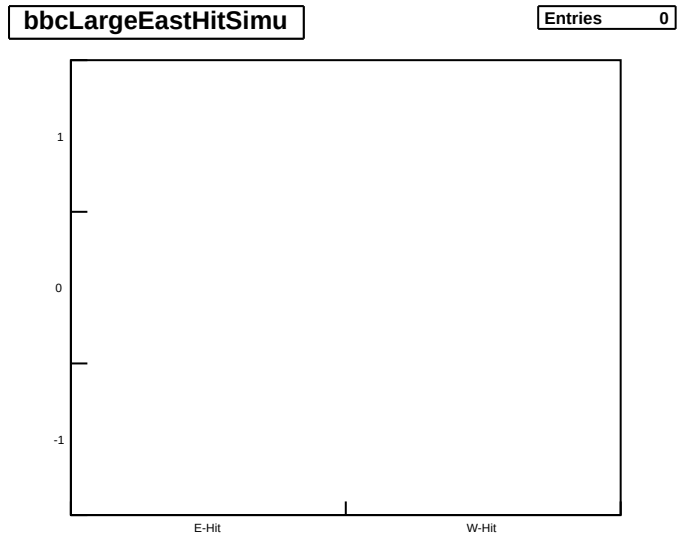

bbcSmallWestAdcSum
Entries 611775

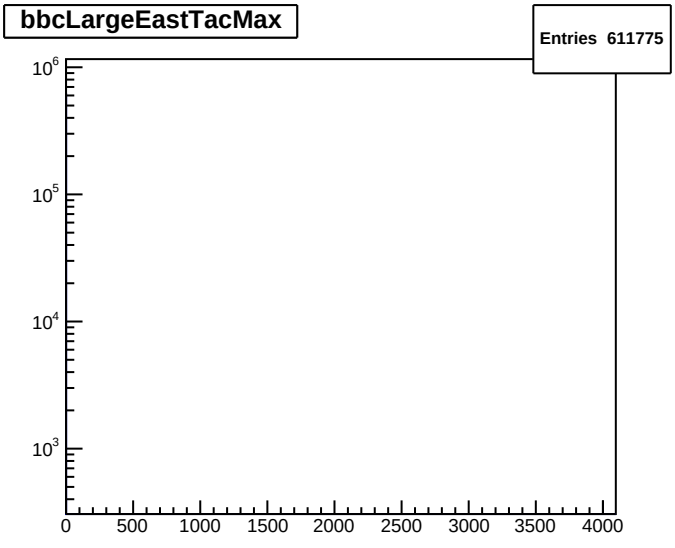

bbcSmallWestAdcSumVsSimu
Entries 580
Data
Simu

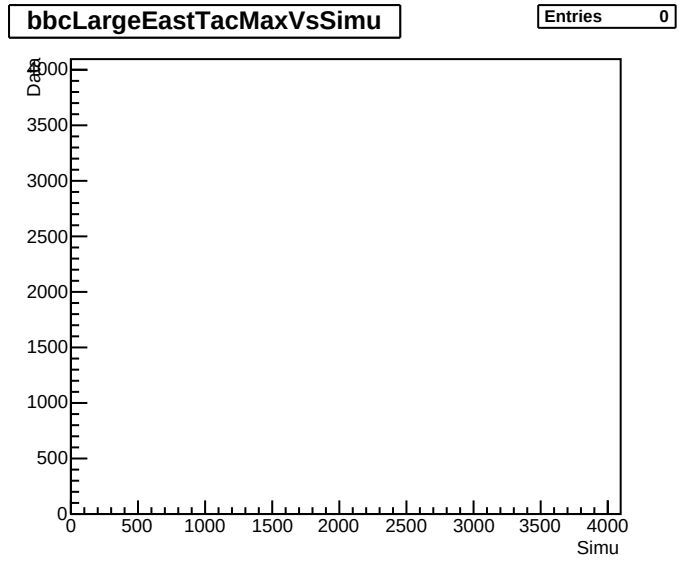

bbcSmallWestTacMax
Entries 611775

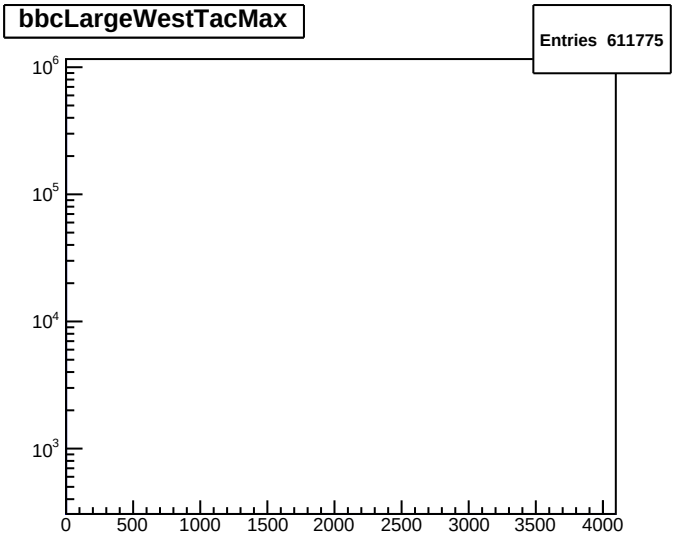

bbcSmallWestTacMaxVsSimu
Entries 580
Data
Simu

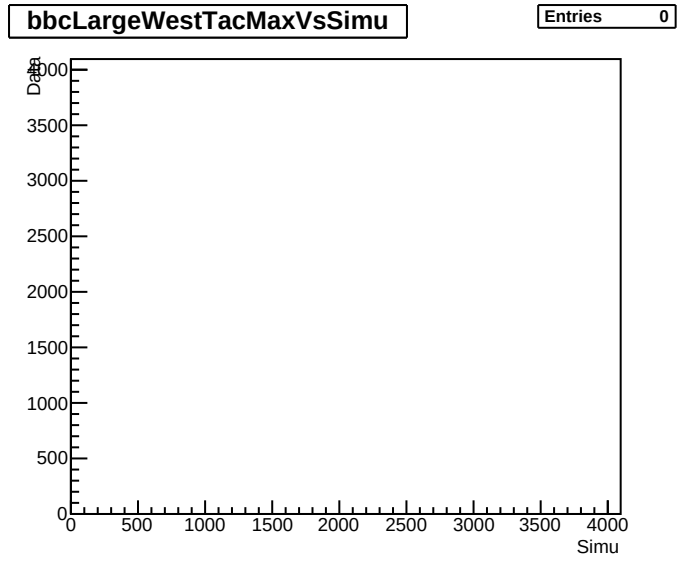

bbcLargeHit
Entries 1223550
611775
611775
E-Hit
W-Hit

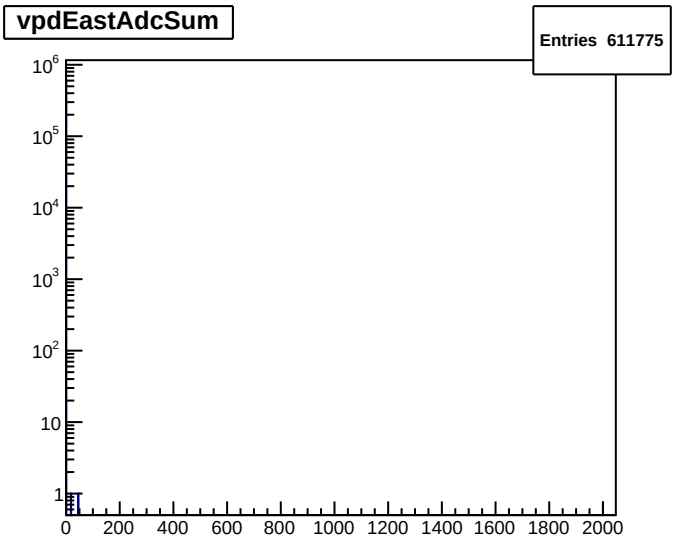

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

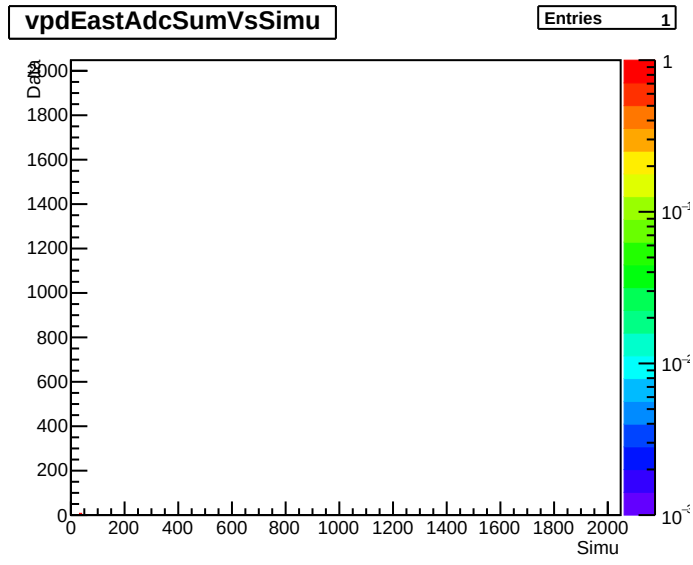

bbcLargeEastTacMax
Entries 611775

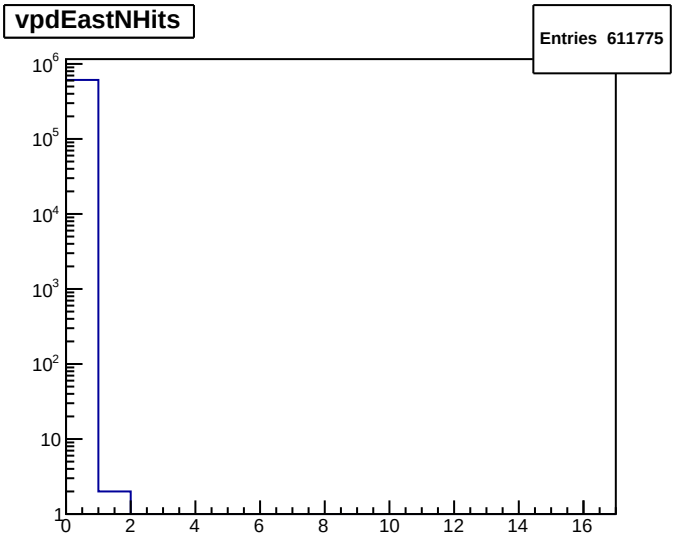

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

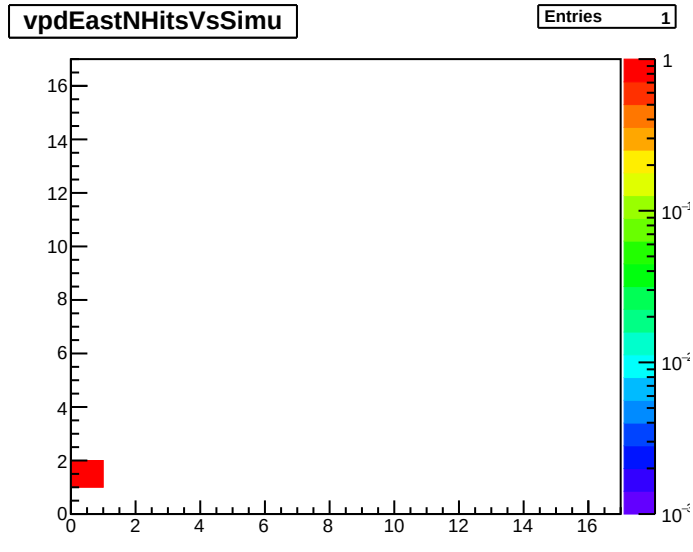

bbcLargeWestTacMax
Entries 611775

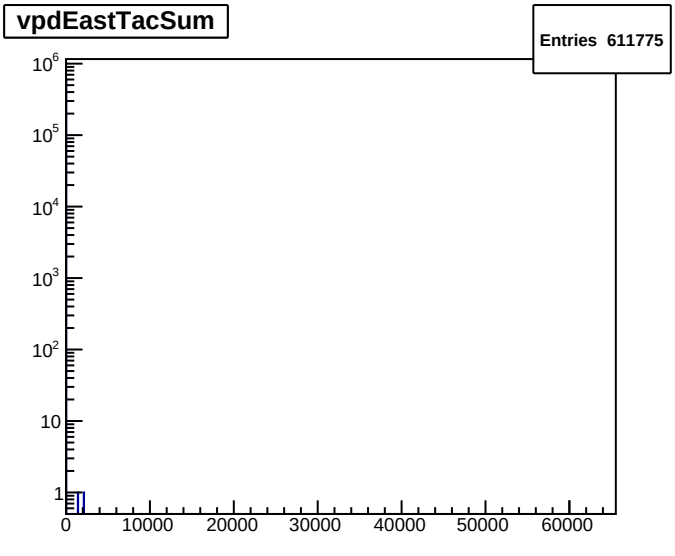

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

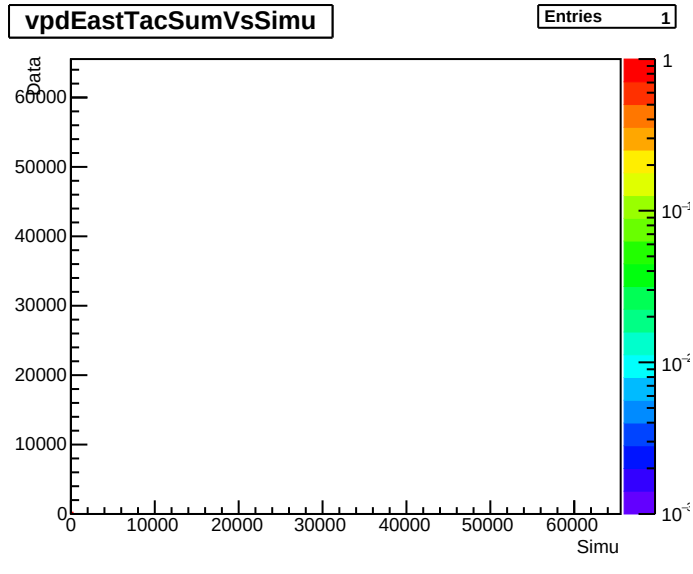

vpdEastAdcSum
Entries 611775

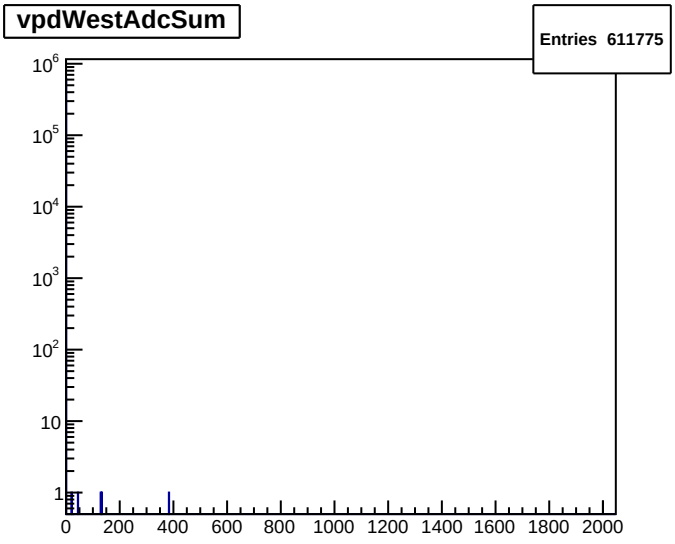

vpdEastAdcSumVsSimu
Entries 1
Data
Simu

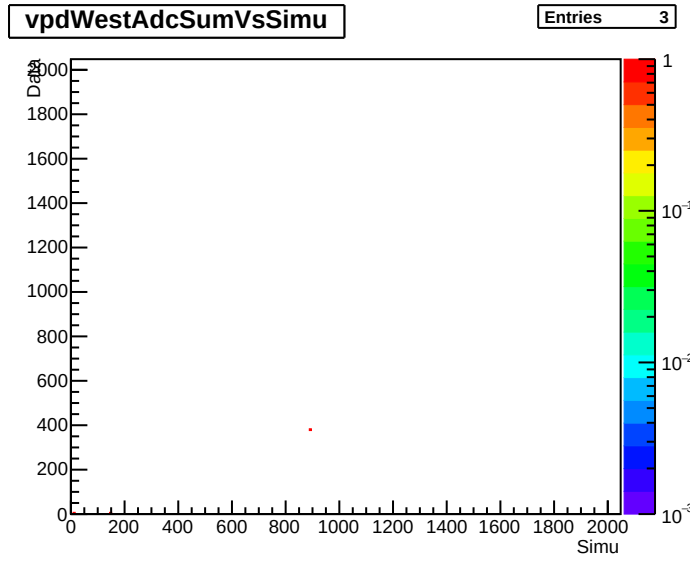

vpdEastNHits
Entries 611775

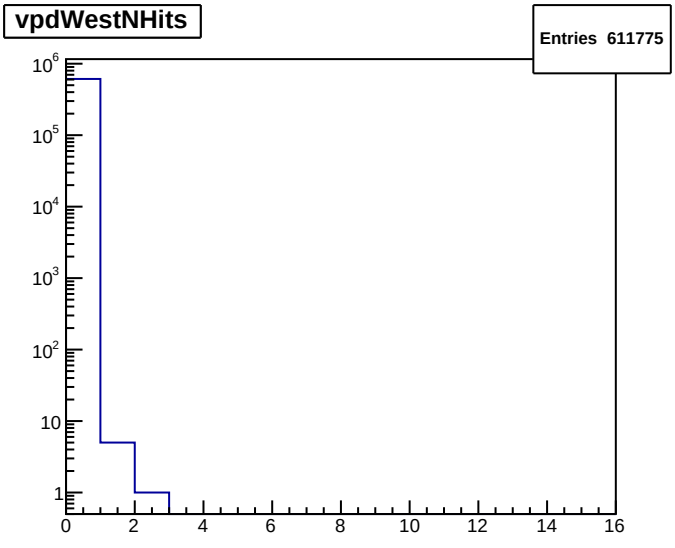

vpdEastNHitsVsSimu
Entries 1

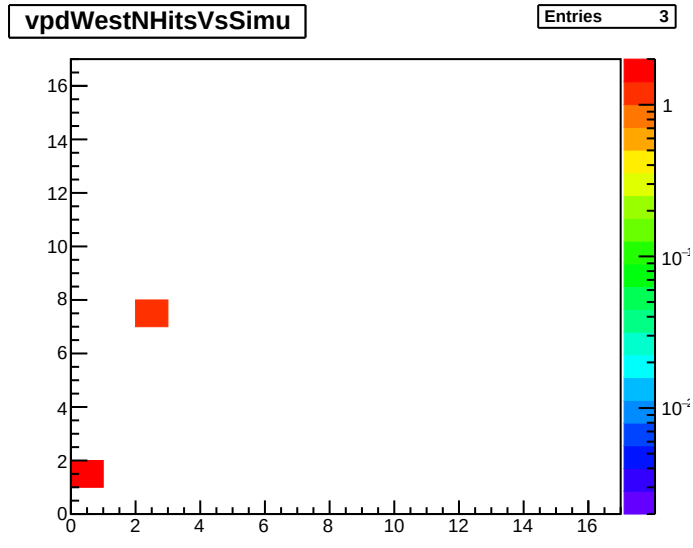

vpdEastTacSum
Entries 611775

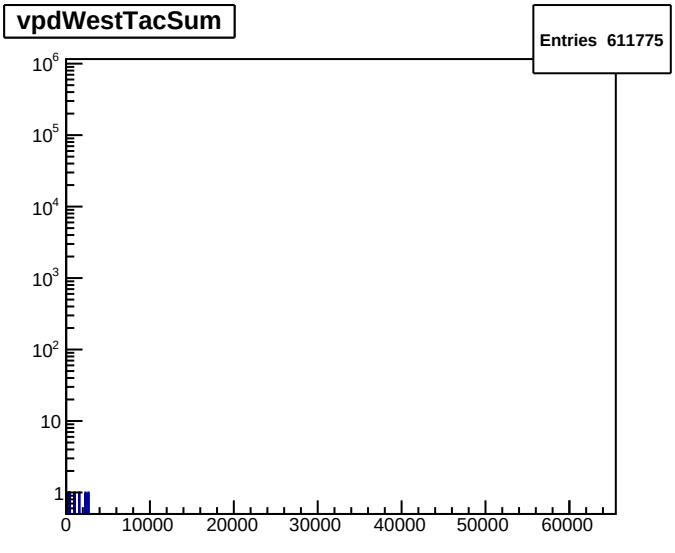

vpdEastTacSumVsSimu
Entries 1
Data
Simu

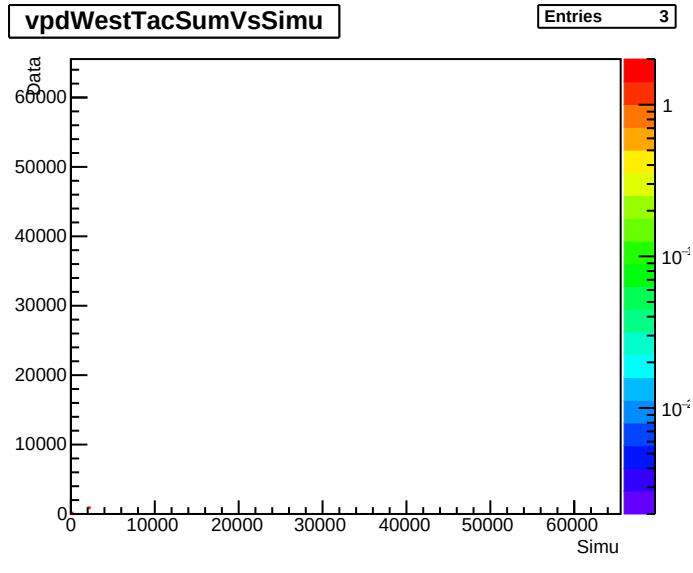

vpdWestAdcSum
Entries 611775

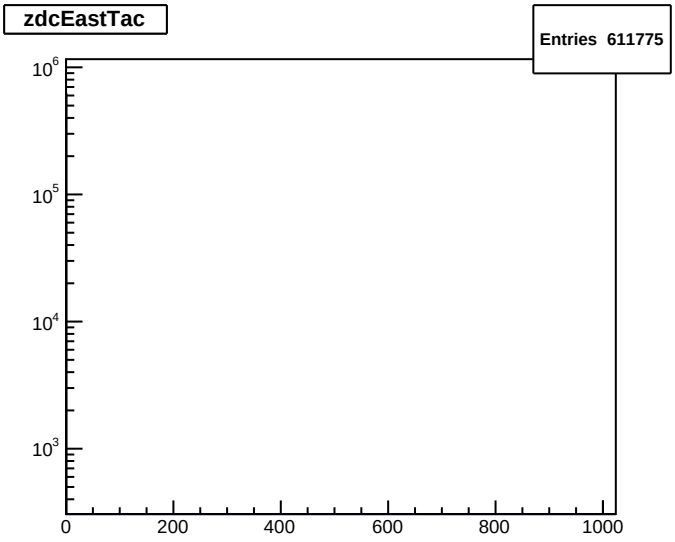

vpdWestAdcSumVsSimu
Entries 3
Data
Simu

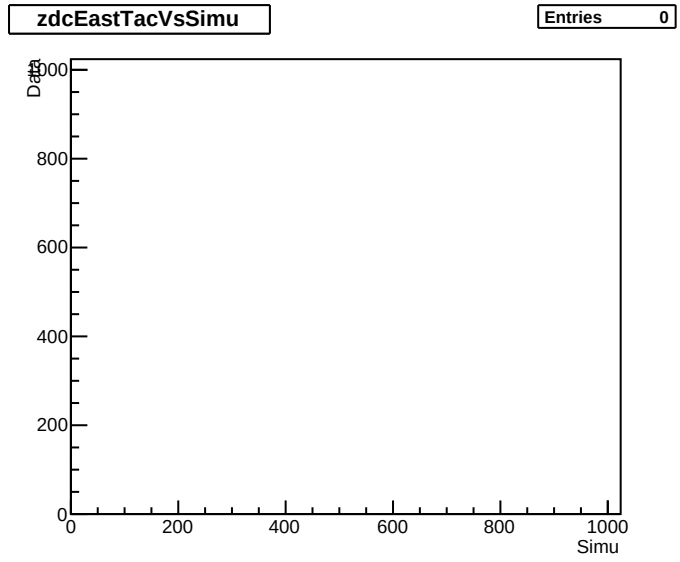

vpdWestNHits
Entries 611775

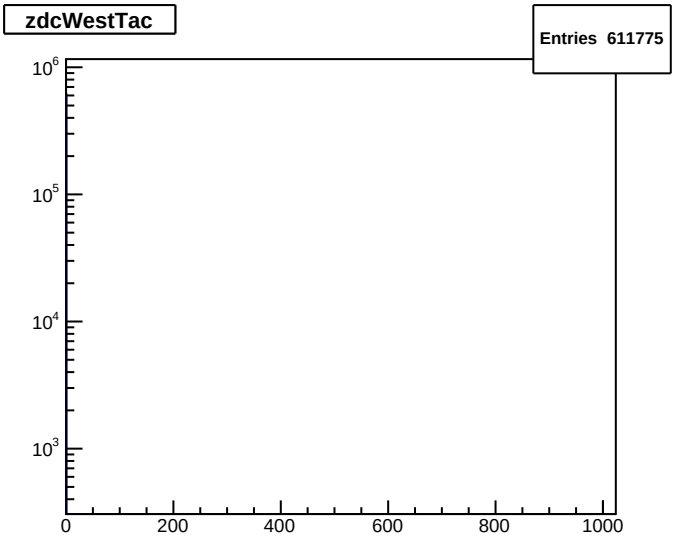

vpdWestNHitsVsSimu
Entries 3

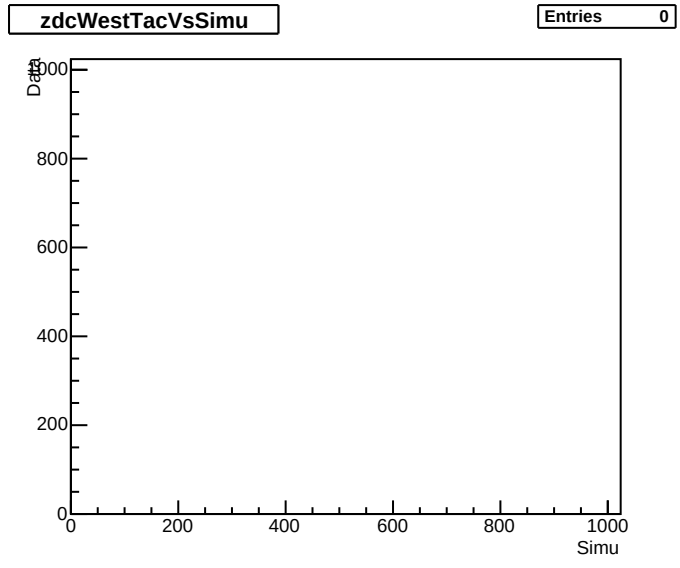

vpdWestTacSum
Entries 611775

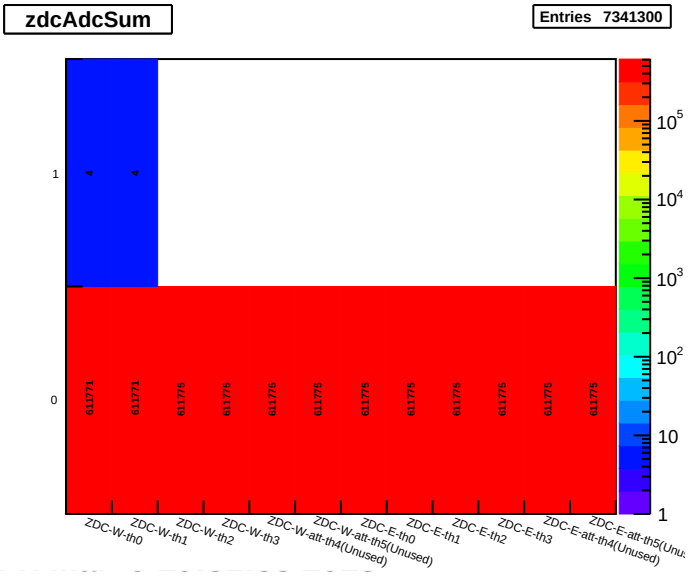

vpdWestTacSumVsSimu
Entries 3
Data
Simu

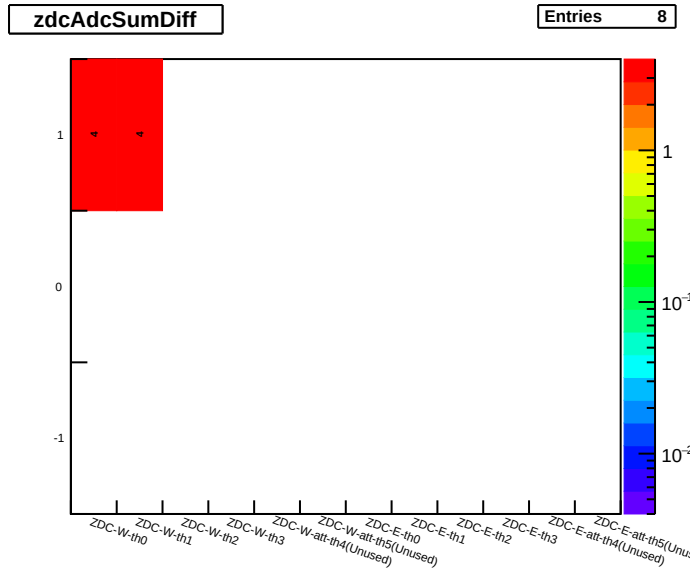

zdcEastTac
Entries 611775
10^6
10^5
10^4
10^3
0
200
400
600
800
1000

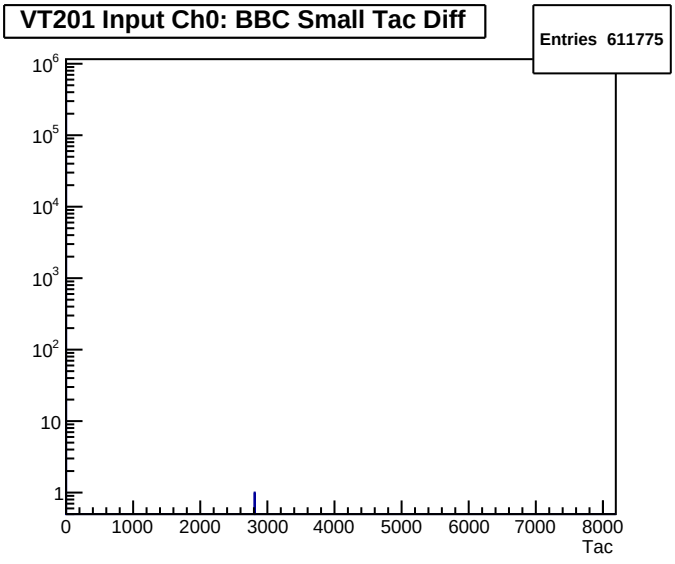

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

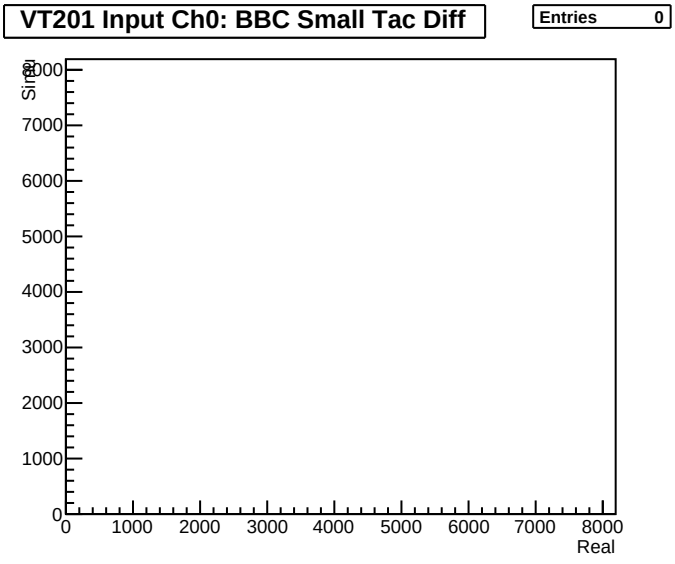

zdcWestTac
Entries 611775
10^6
10^5
10^4
10^3
0
200
400
600
800
1000

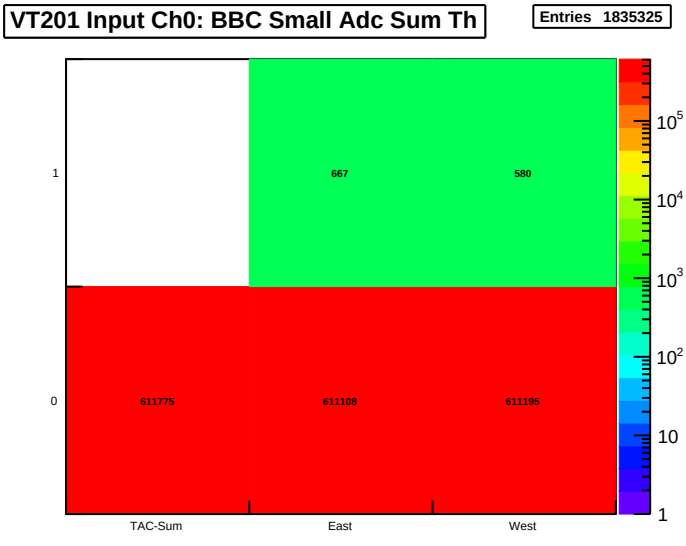

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

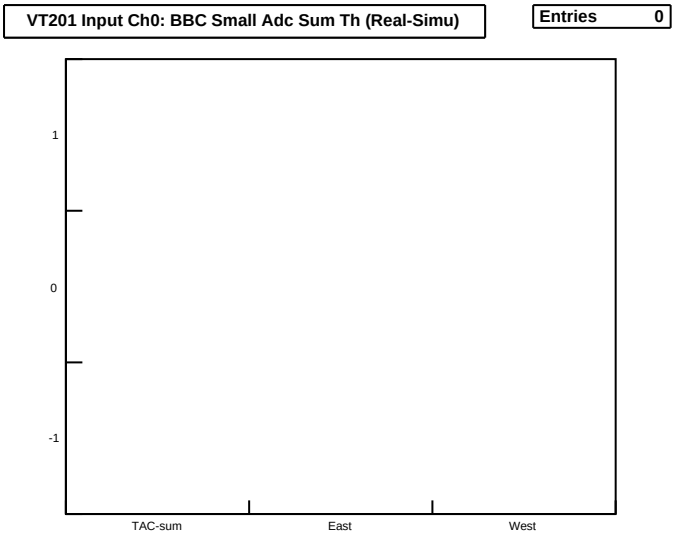

zdcAdcSum
Entries 7341300
1
0
4
4
611771
611771
611775
611775
611775
611775
611775
611775
611775
611775
611775
611775
10^5
10^4
10^3
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

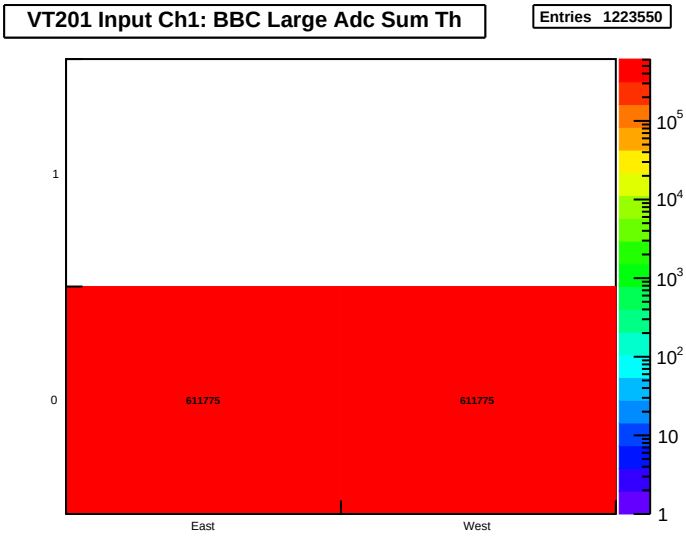

zdcAdcSumDiff
Entries 8
1
0
-1
4
4
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

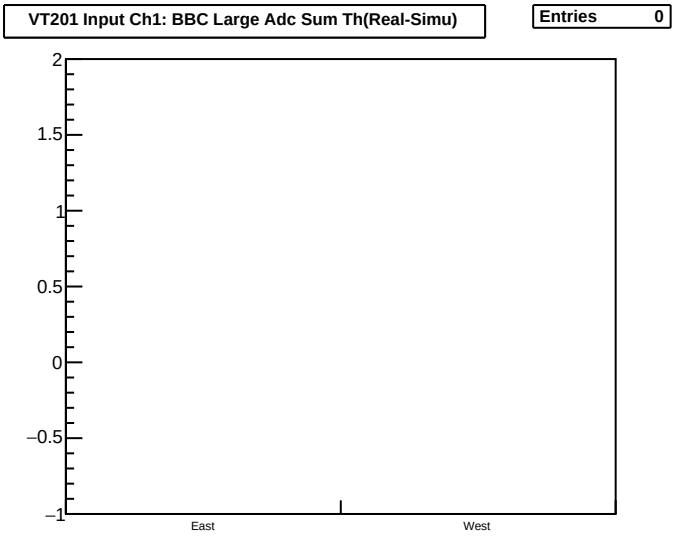

VT201 Input Ch0: BBC Small Tac Diff
Entries 611775
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

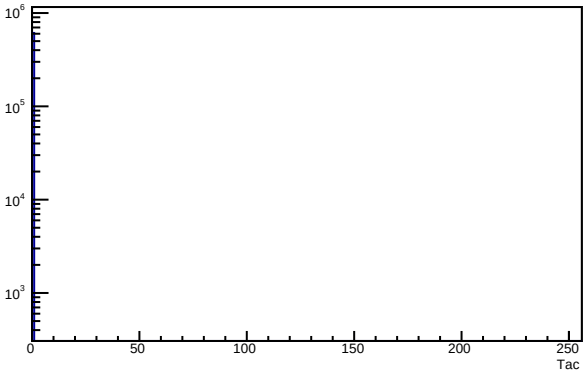

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 1835325
667
580
611775
611108
611195
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

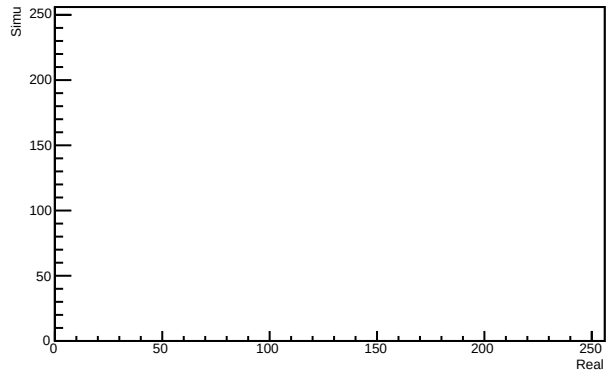

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 1223550
611775
611775
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

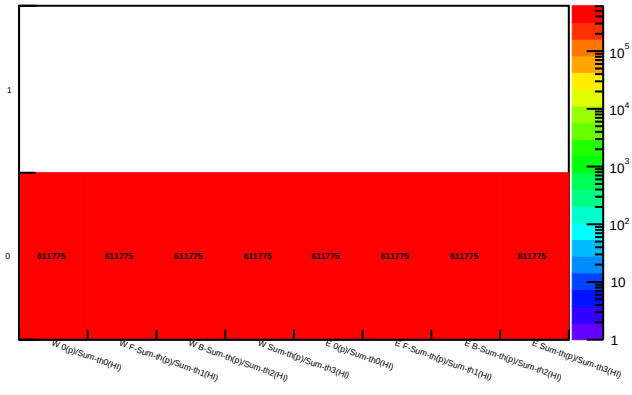
VT201 Input Ch2: ZDC Tac Diff



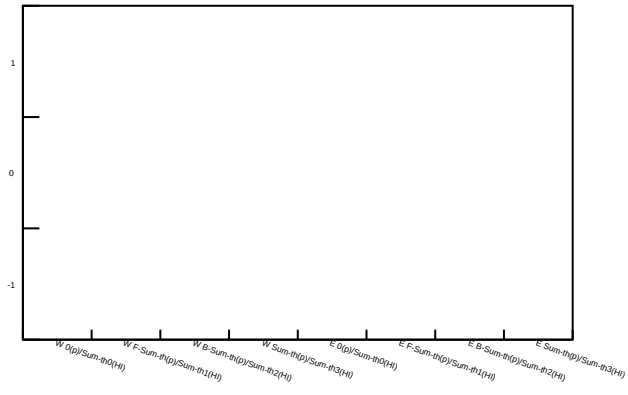
VT201 Input Ch2: ZDC Tac Diff



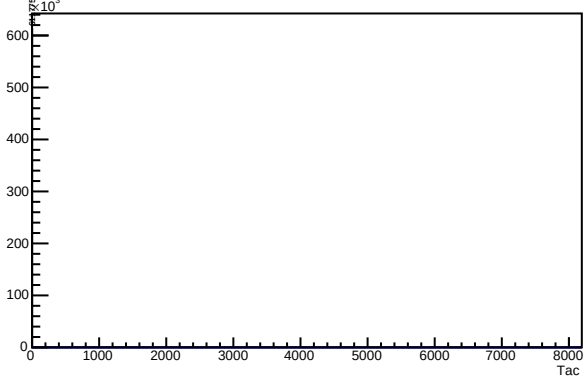
VT201 Input Ch2: ZDC Adc Sum Th



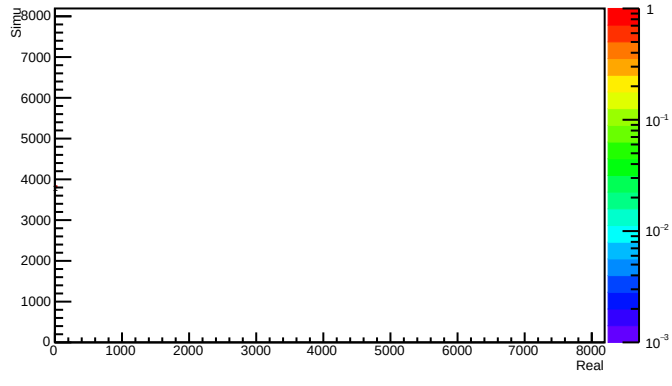
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



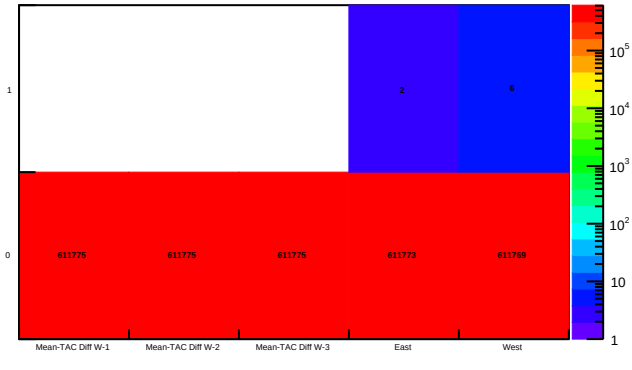
VT201 Input Ch4: VPD Tac Diff



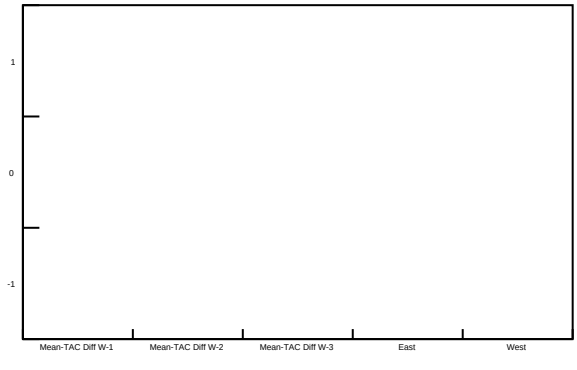
VT201 Input Ch4: VPD Tac Diff

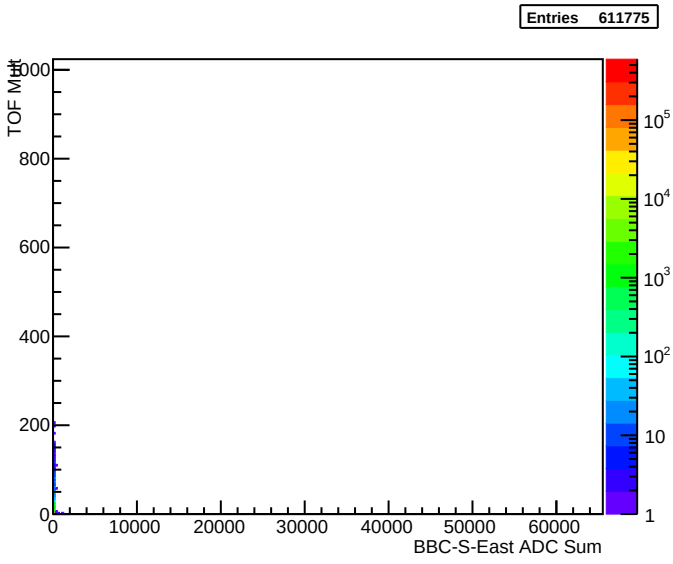


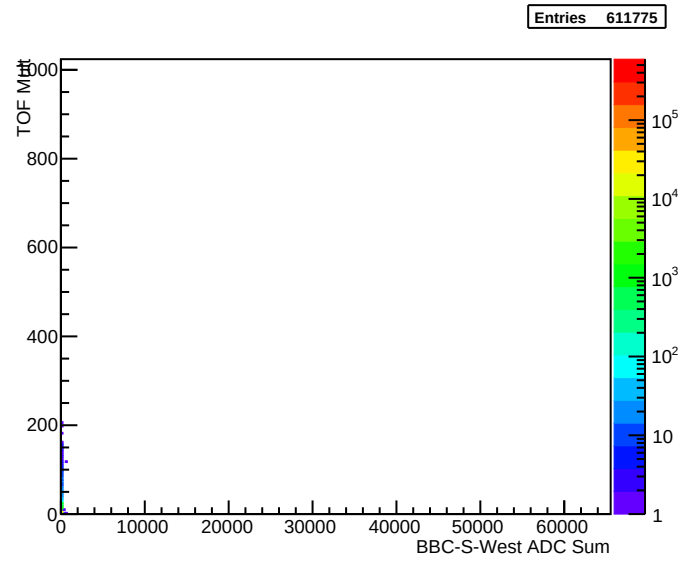
VT201 Input Ch4: VPD Adc Sum Th

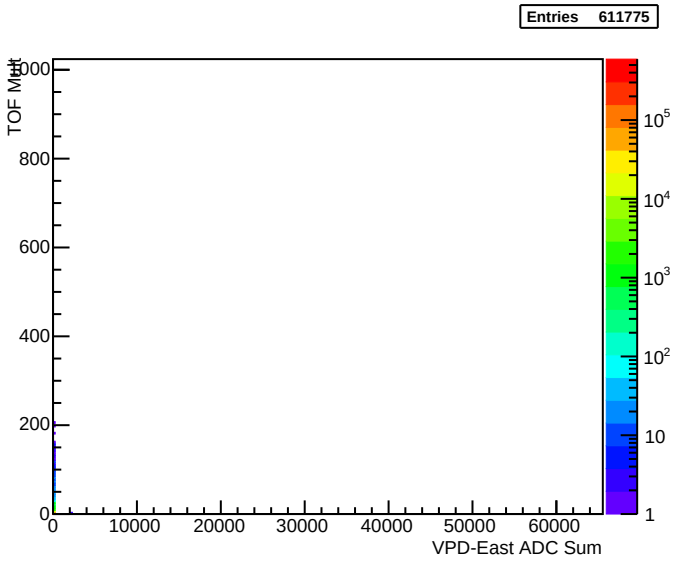


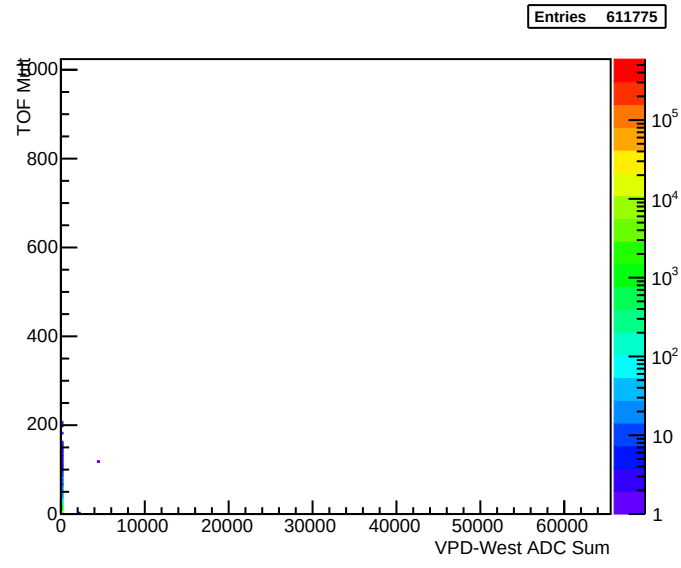
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

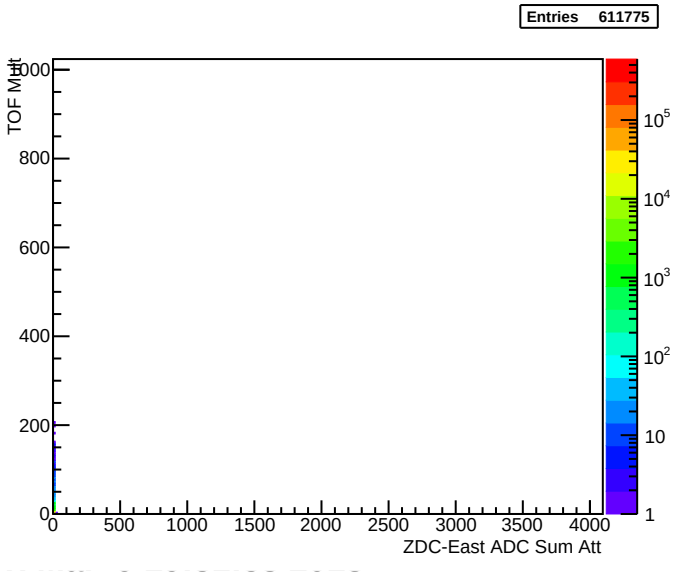


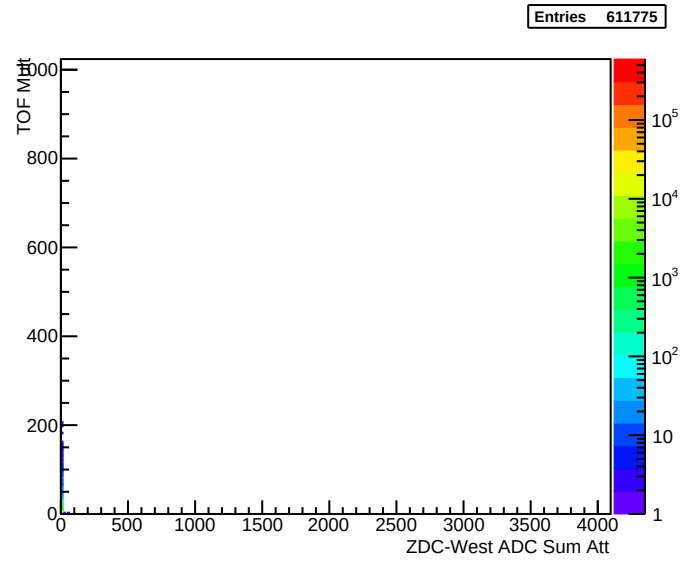

Entries 611775
TOF Mult
BBC-S-East ADC Sum

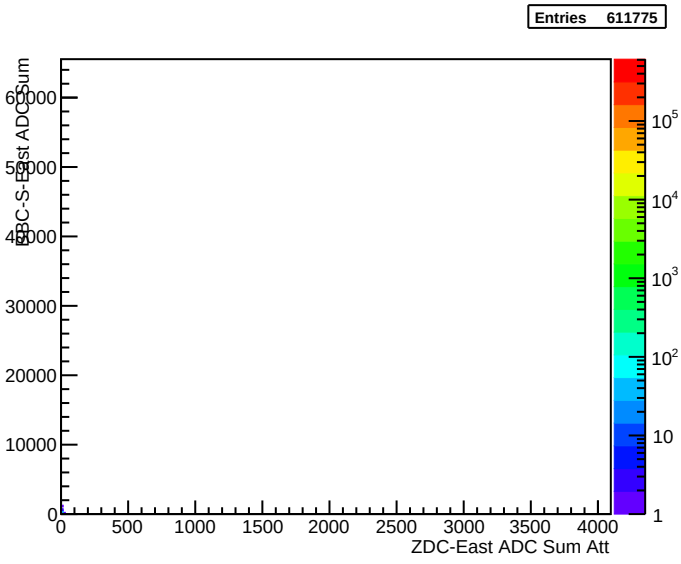

Entries 611775
TOF Mult
BBC-S-West ADC Sum

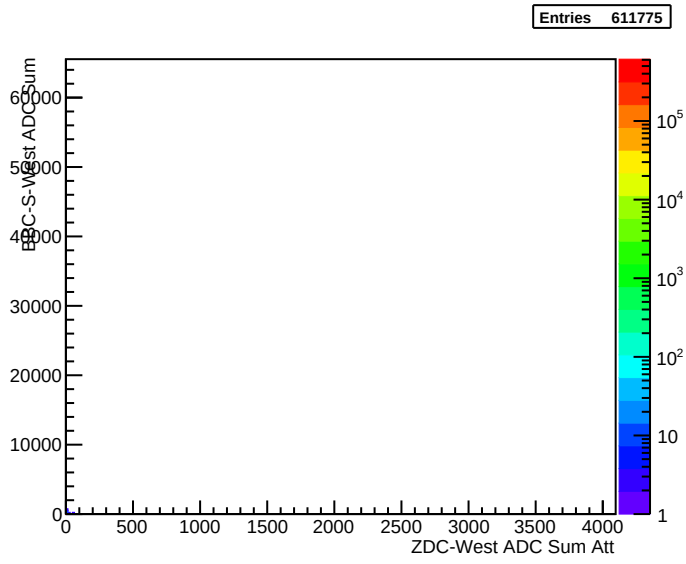

Entries 611775
TOF Mult
VPD-East ADC Sum

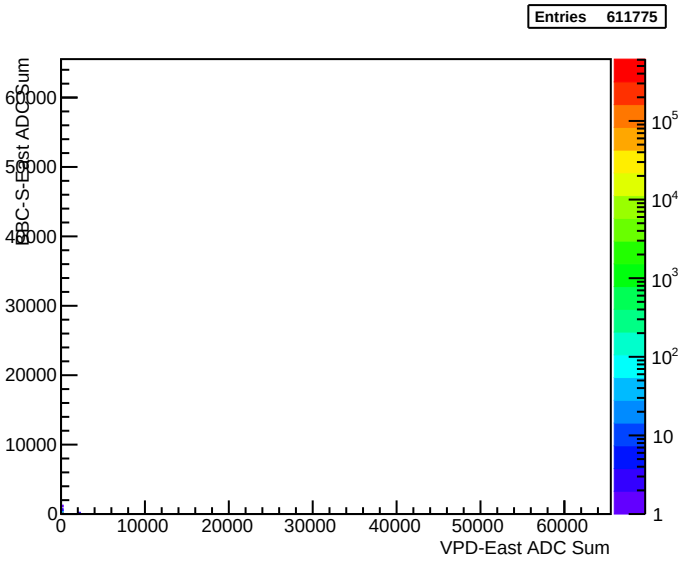

Entries 611775
TOF Mult
VPD-West ADC Sum

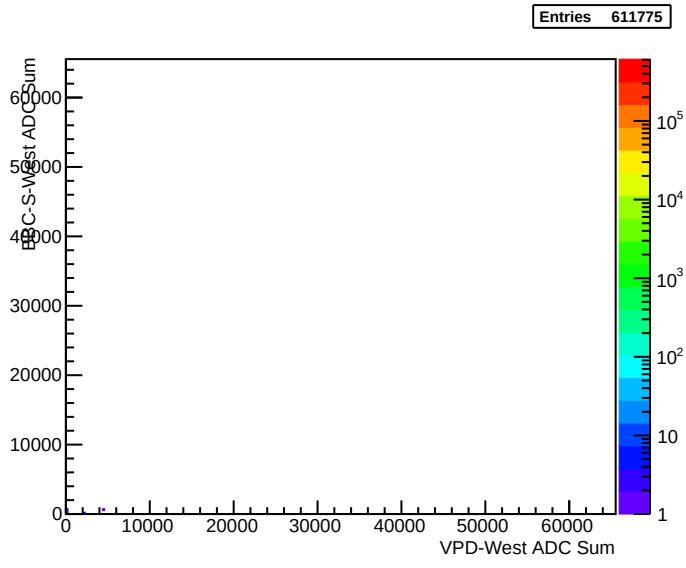

Entries 611775
TOF Mult
ZDC-East ADC Sum Att

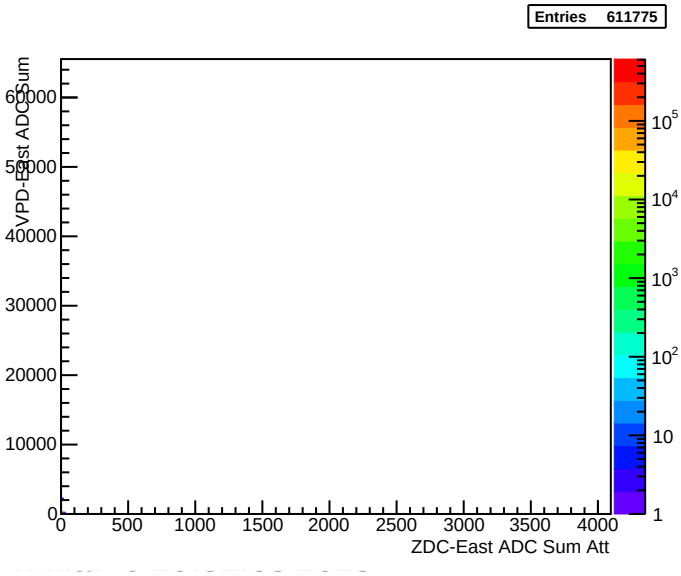

Entries 611775
TOF Mult
ZDC-West ADC Sum Att

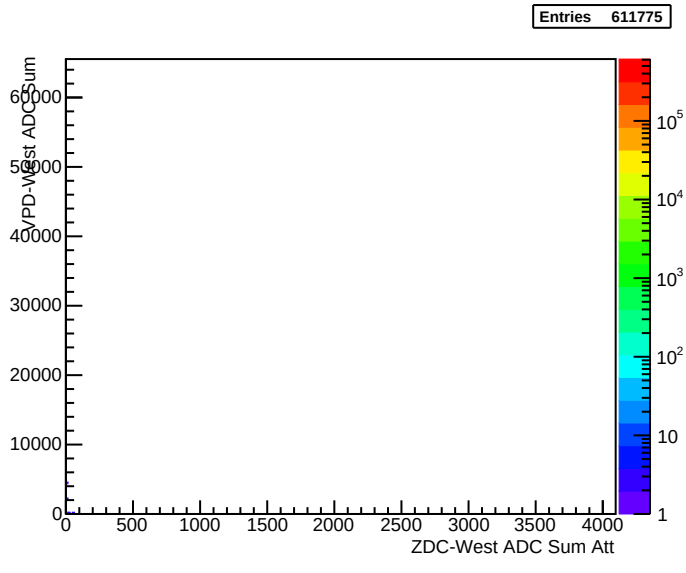

Entries 611775
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

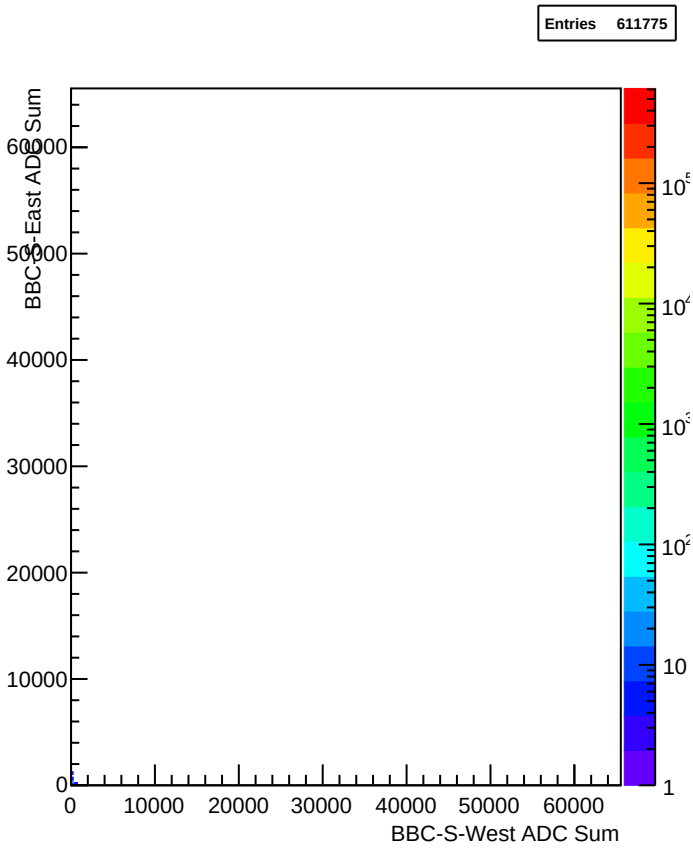

Entries 611775
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

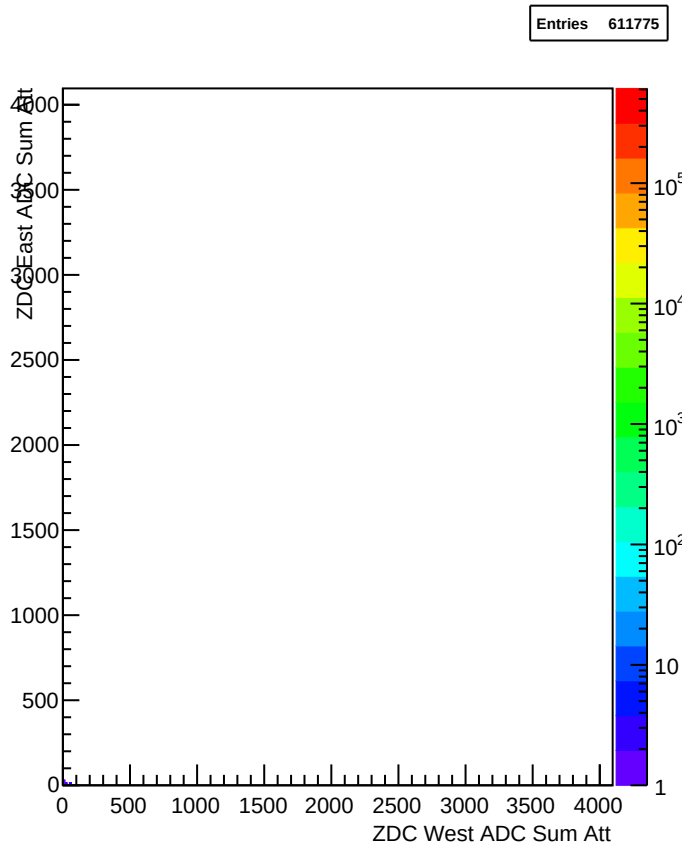

Entries 611775
BBC-S-East ADC Sum
VPD-East ADC Sum

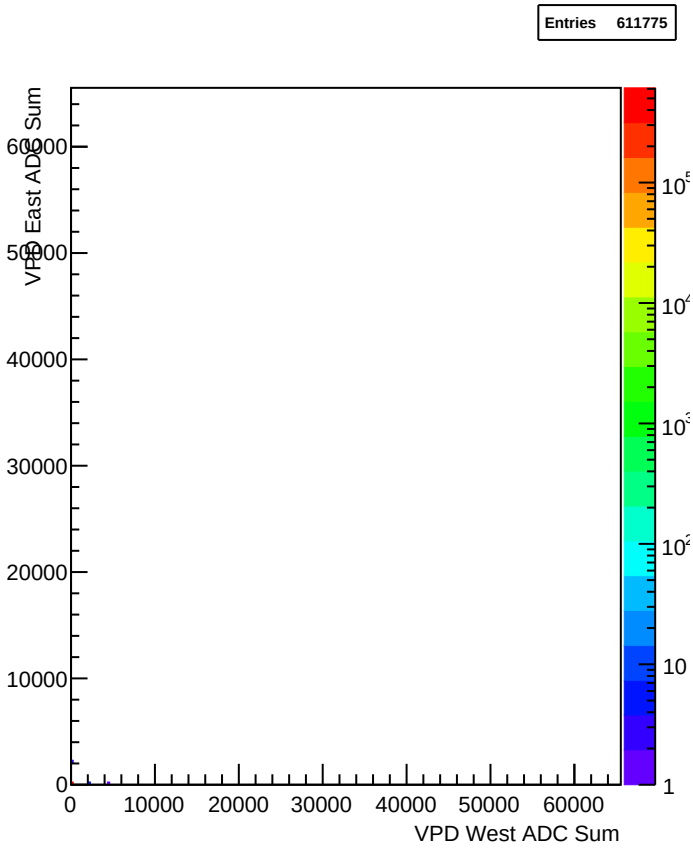

Entries 611775
BBC-S-West ADC Sum
VPD-West ADC Sum

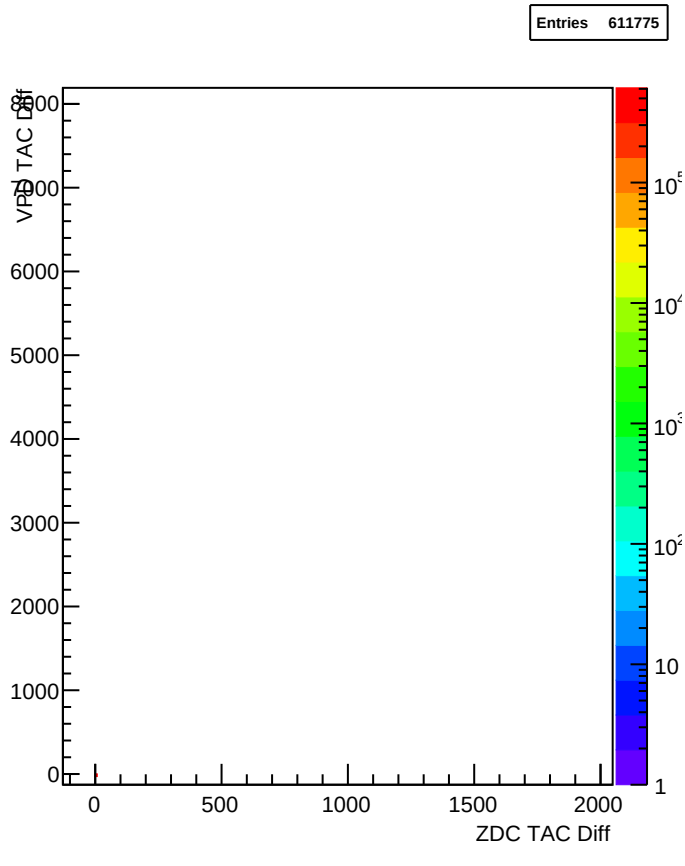

Entries 611775
VPD-East ADC Sum
ZDC-East ADC Sum Att

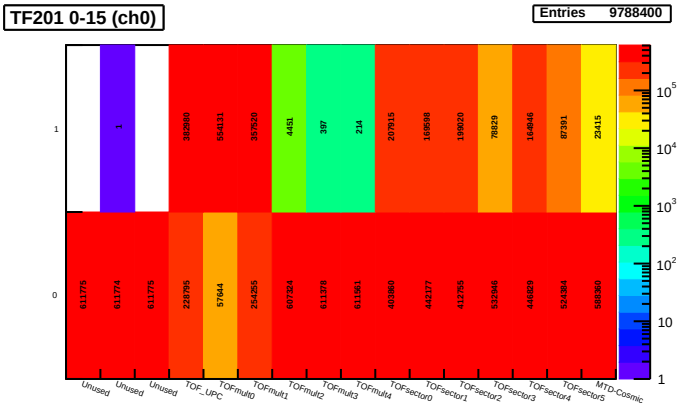

Entries 611775
VPD-West ADC Sum
ZDC-West ADC Sum Att

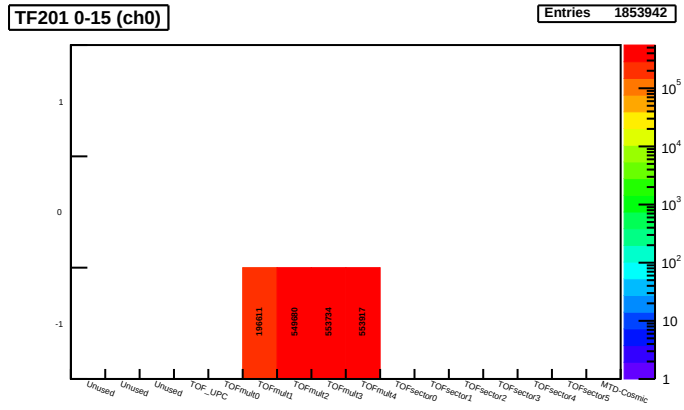

Entries 611775
BBC-S-East ADC Sum
BBC-S-West ADC Sum
0
10000
20000
30000
40000
50000
60000
1
10
10^2
10^3
10^4
10^5

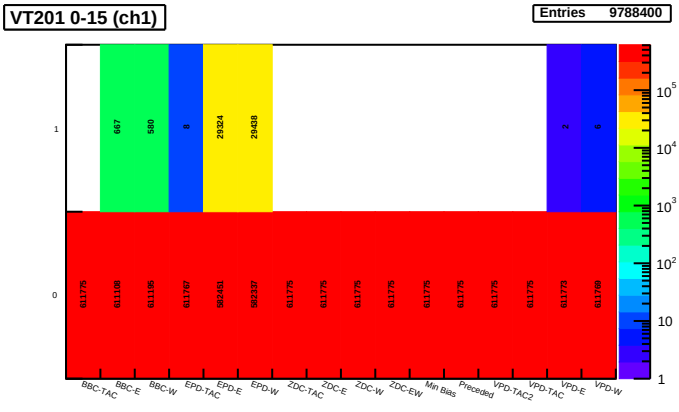

Entries 611775
ZDC East ADC Sum Att
ZDC West ADC Sum Att
0
500
1000
1500
2000
2500
3000
3500
4000
1
10
10^2
10^3
10^4
10^5

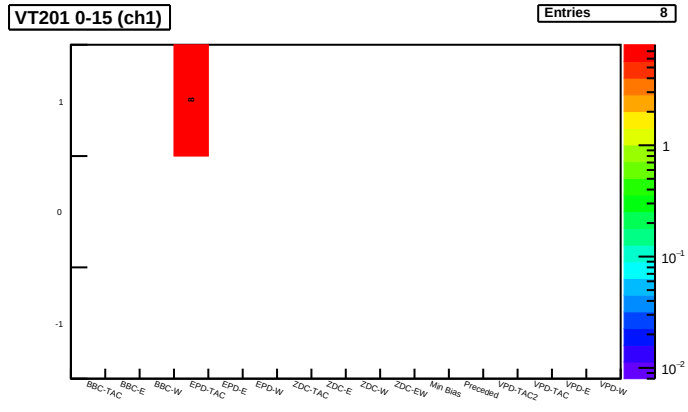

Entries 611775
VPD East ADC Sum
VPD West ADC Sum
0
10000
20000
30000
40000
50000
60000
1
10
10^2
10^3
10^4
10^5

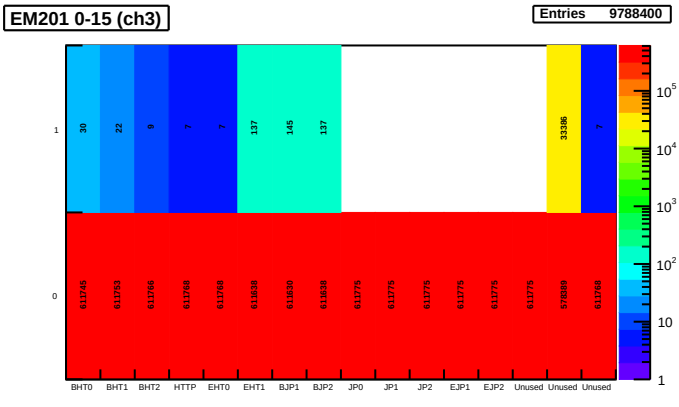

Entries 611775
VPD TAC Diff
ZDC TAC Diff
0
500
1000
1500
2000
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2
10^3
10^4
10^5

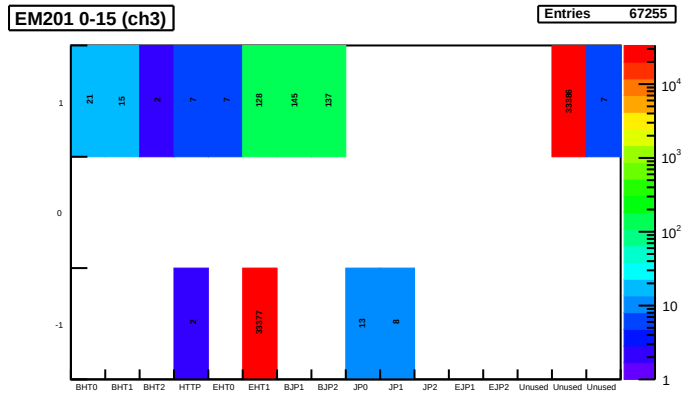

TF201 0-15 (ch0)
Entries 9788400
1 382980 554131 357520 4451 397 214 207915 169598 139020 78829 164846 87391 23415
0 611775 611774 611775 228795 57644 254255 607324 611378 611561 403860 442177 412755 532946 446829 524384 588360
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic
1 10 10^2 10^3 10^4 10^5


TF201 0-15 (ch0)
Entries 1853942
1
0
-1
196611 548680 553734 553917
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic
1 10 10^2 10^3 10^4 10^5


VT201 0-15 (ch1)
Entries 9788400
1 667 580 8 29324 29438 2 6
0 611775 611108 611185 611767 592451 592337 611775 611775 611775 611775 611775 611775 611775 611773 611789
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W
1 10 10^2 10^3 10^4 10^5


VT201 0-15 (ch1)
Entries 8
1 8
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W
1 10^-1 10^-2


EM201 0-15 (ch3)
Entries 9788400
1 30 22 9 7 7 137 145 137 33386 7
0 611745 611753 611766 611768 611768 611638 611638 611638 611775 611775 611775 611775 611775 611775 576889 611768
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused
1 10 10^2 10^3 10^4 10^5


EM201 0-15 (ch3)
Entries 67255
1 21 15 2 7 7 128 145 137 33386 7
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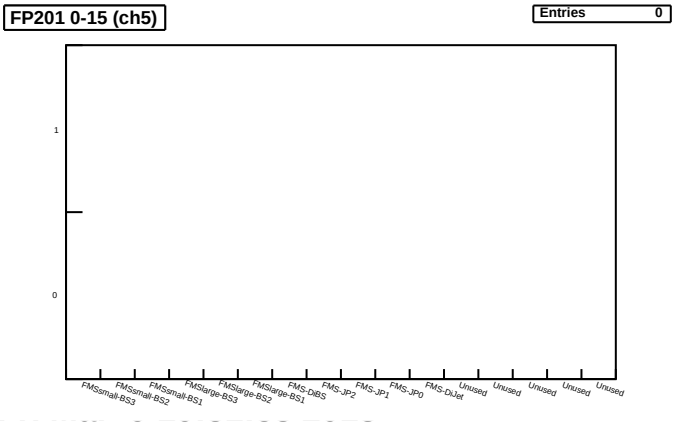
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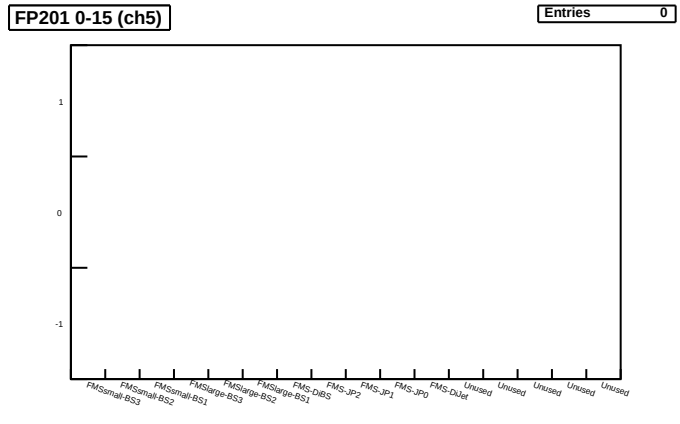
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0
-1
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