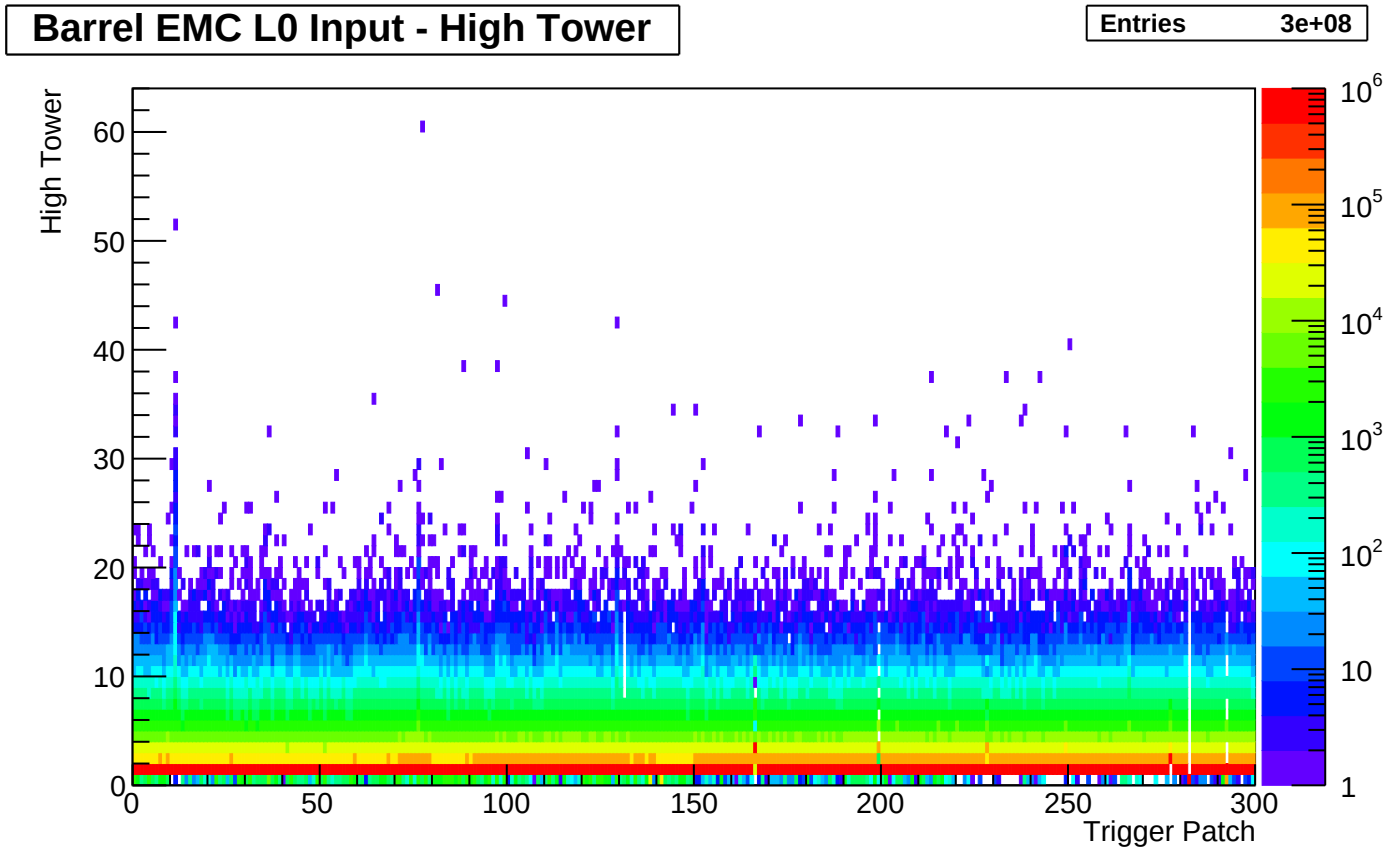
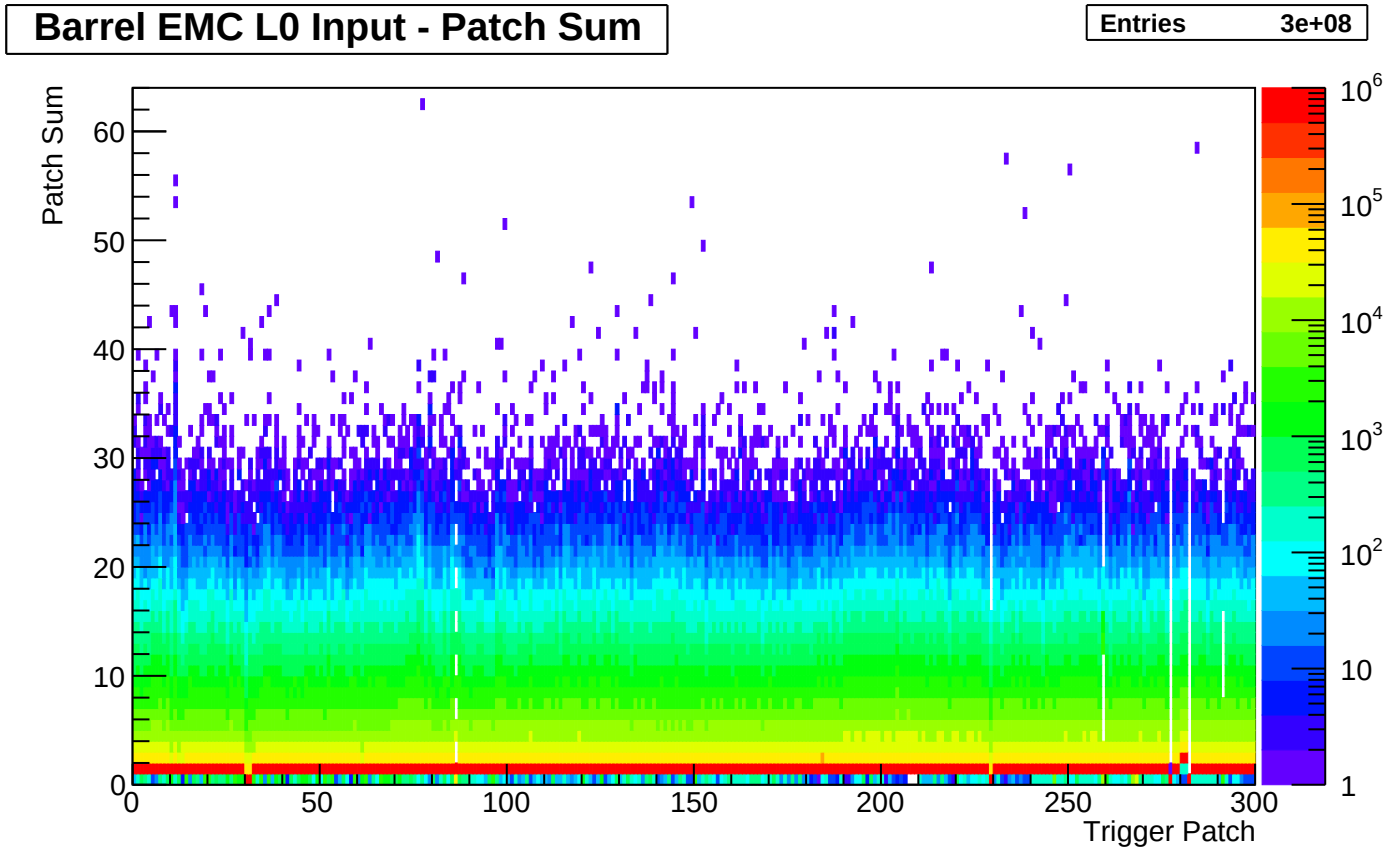
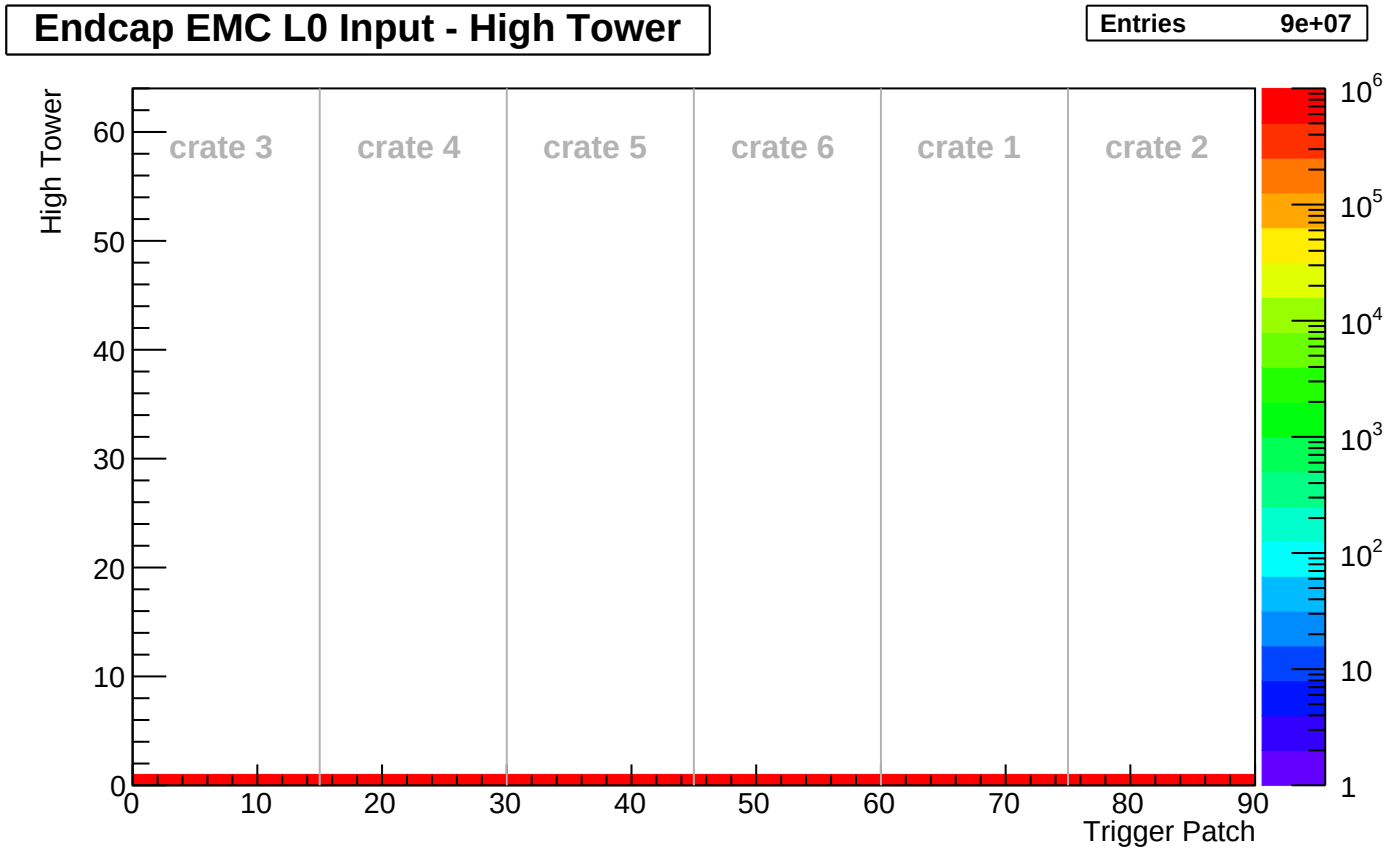
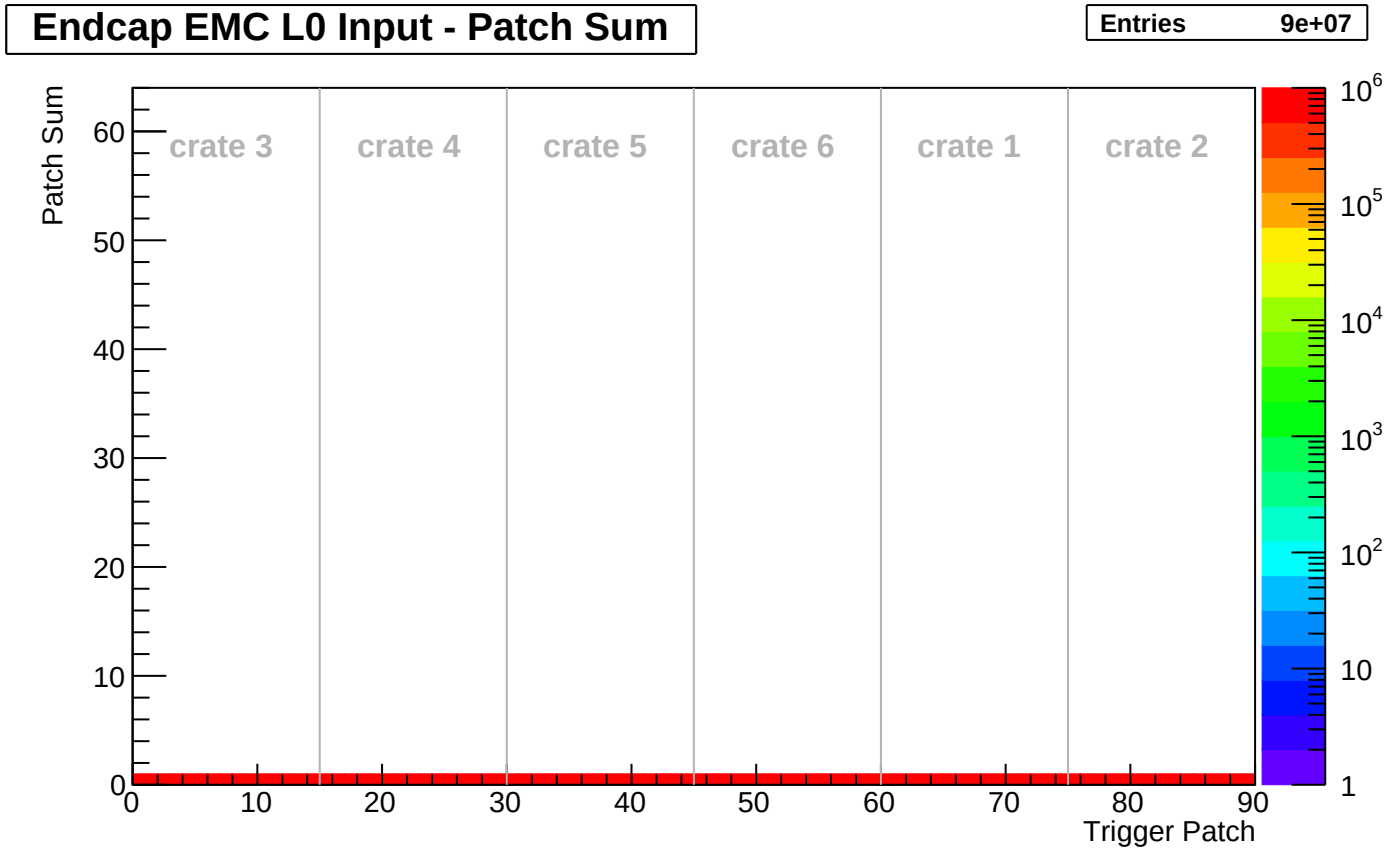
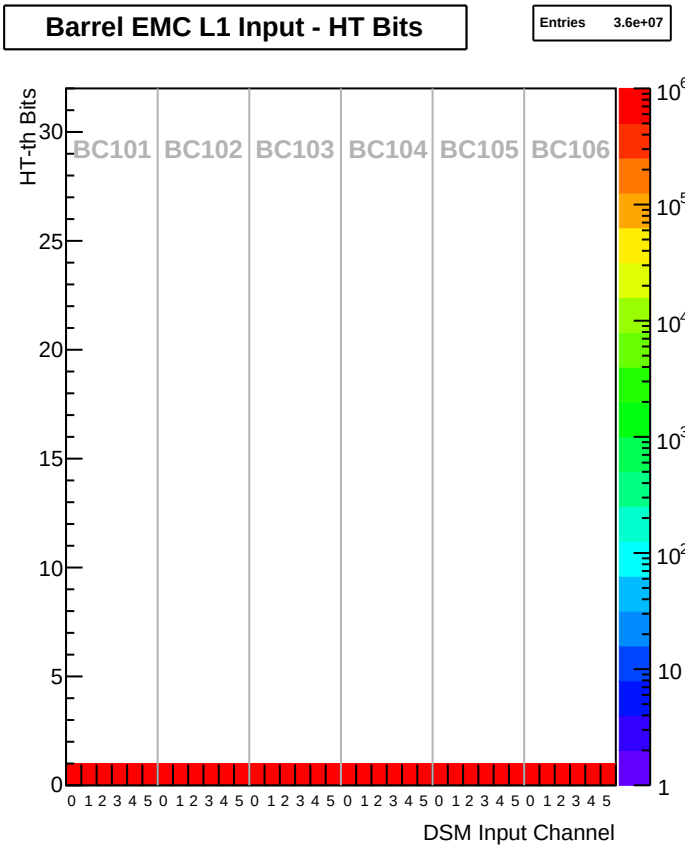
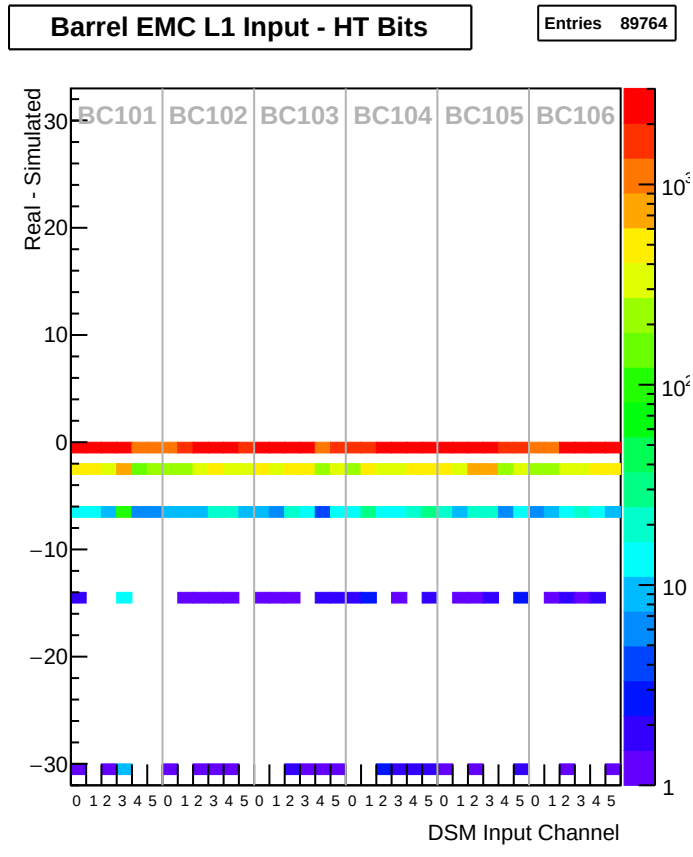

Barrel EMC L0 Input - High Tower
Entries 3e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

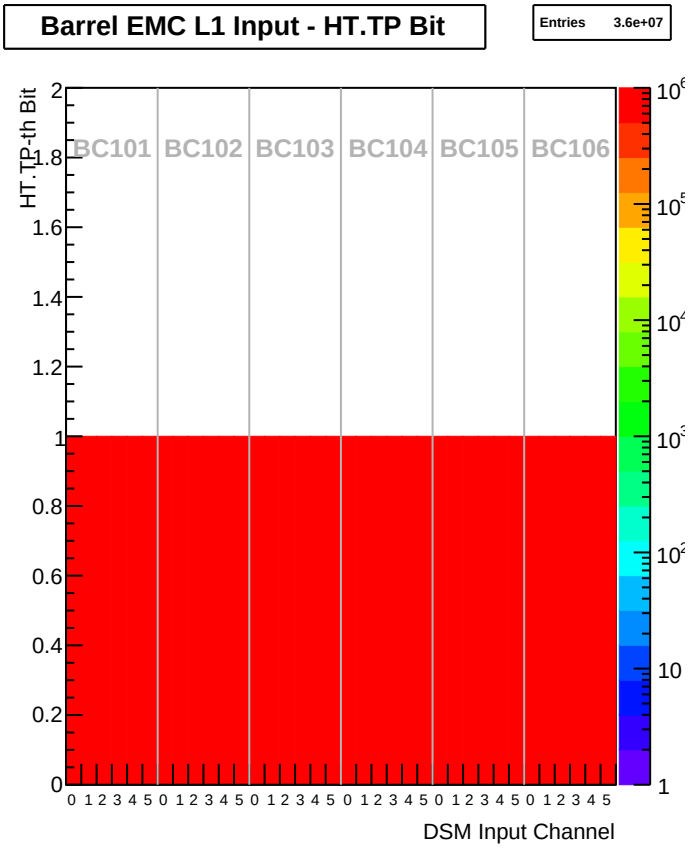

Barrel EMC L0 Input - Patch Sum
Entries 3e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

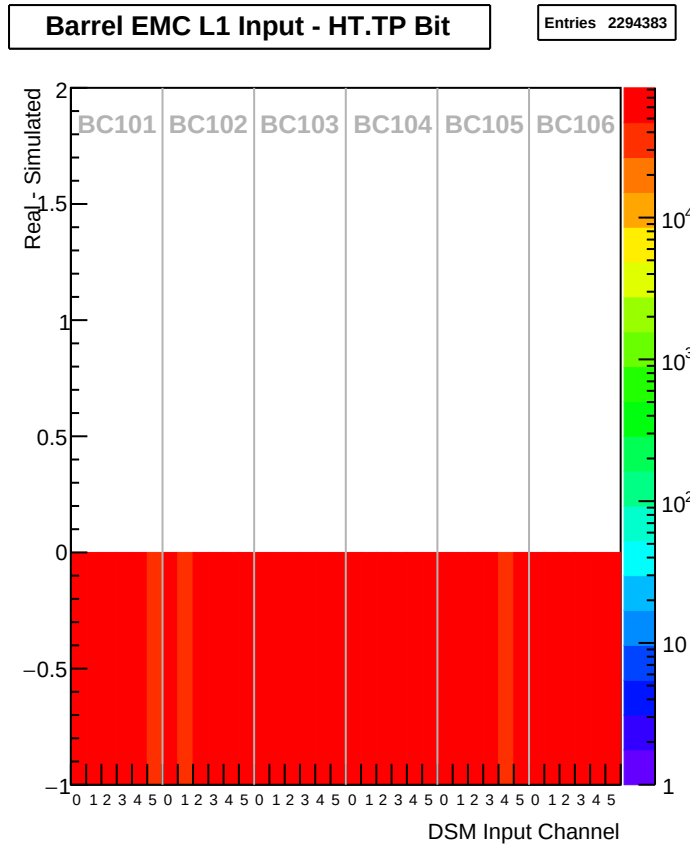

Endcap EMC L0 Input - High Tower
Entries 9e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

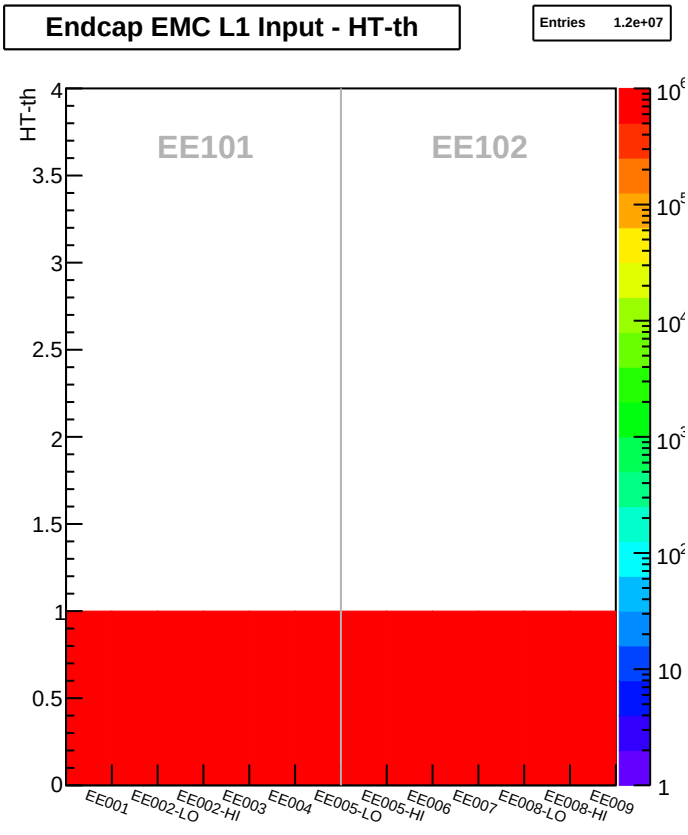

Endcap EMC L0 Input - Patch Sum
Entries 9e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

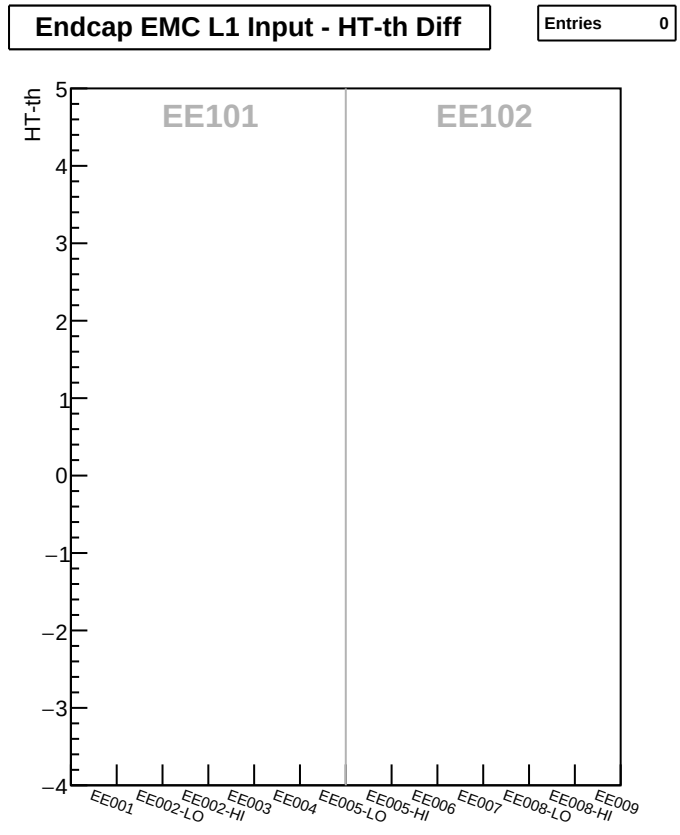

Barrel EMC L1 Input - HT Bits
Entries 3.6e+07
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

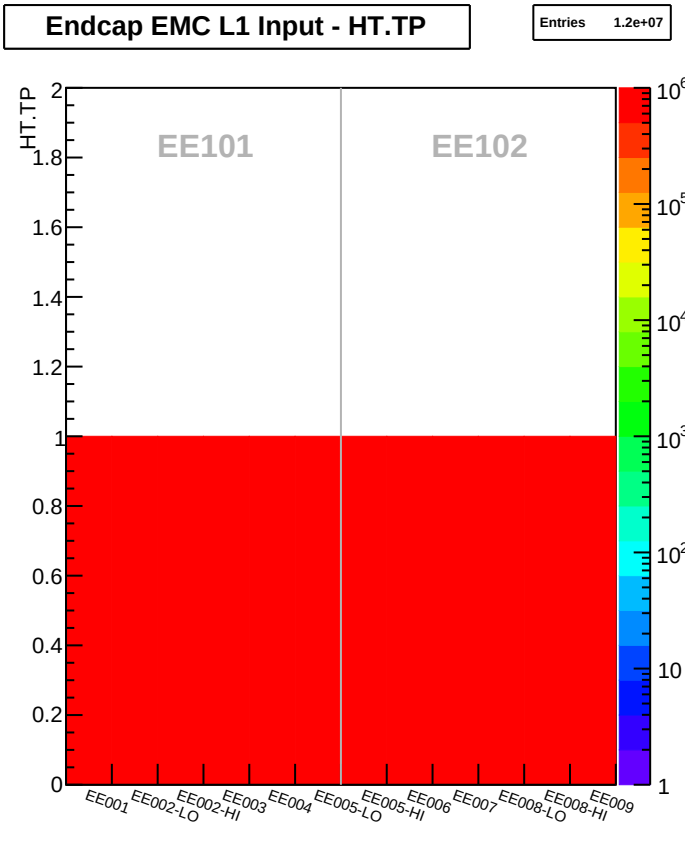

Barrel EMC L1 Input - HT Bits
Entries 89764
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

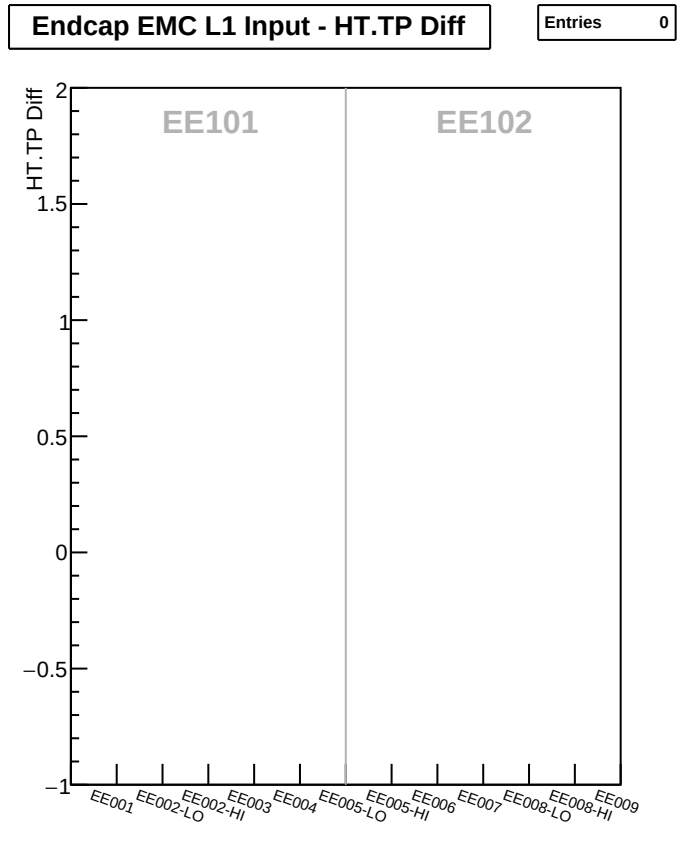

Barrel EMC L1 Input - HT.TP Bit
Entries 3.6e+07
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

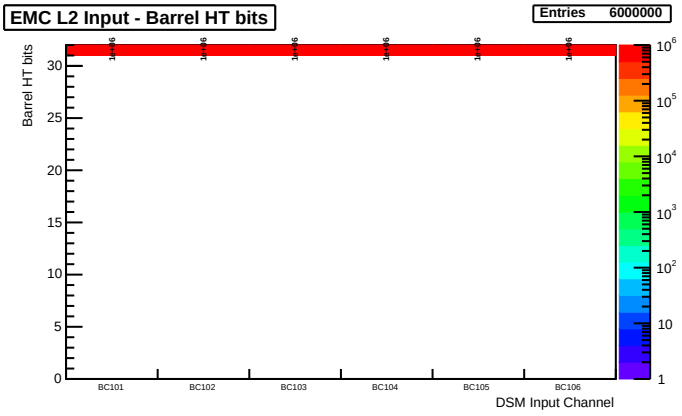

Barrel EMC L1 Input - HT.TP Bit
Entries 2294383
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

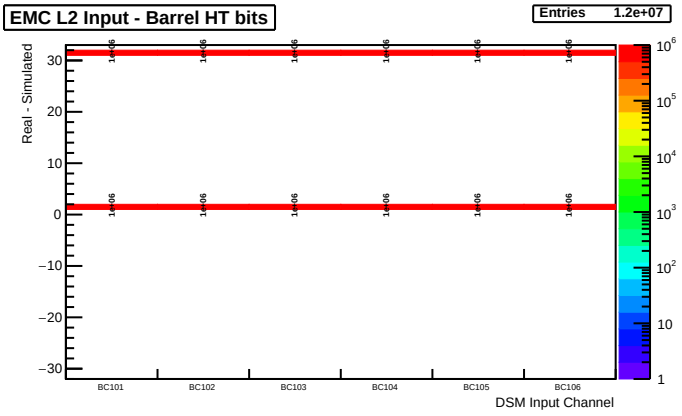

Endcap EMC L1 Input - HT-th
Entries 1.2e+07
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

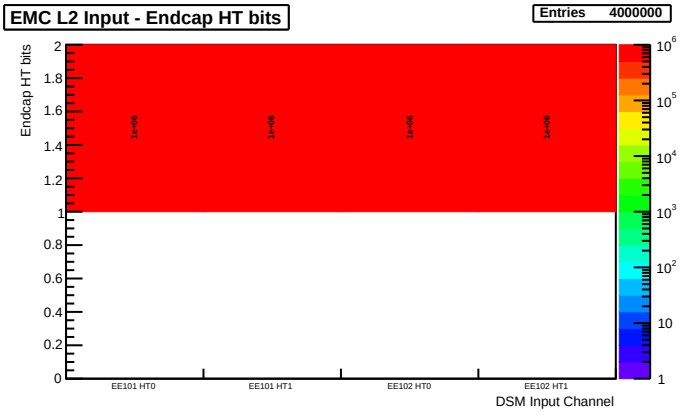

Endcap EMC L1 Input - HT-th Diff
Entries 0
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

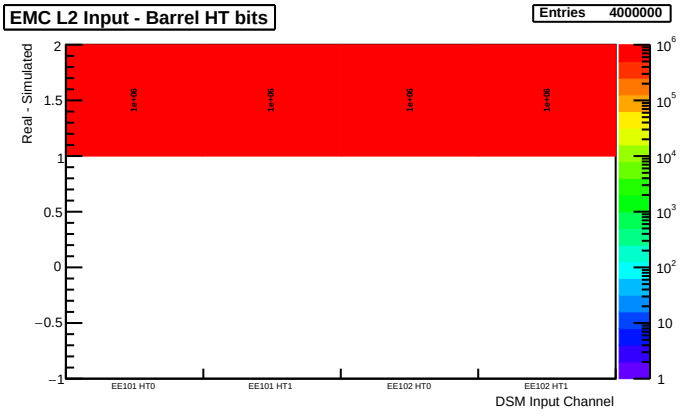

Endcap EMC L1 Input - HT.TP
Entries 1.2e+07
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

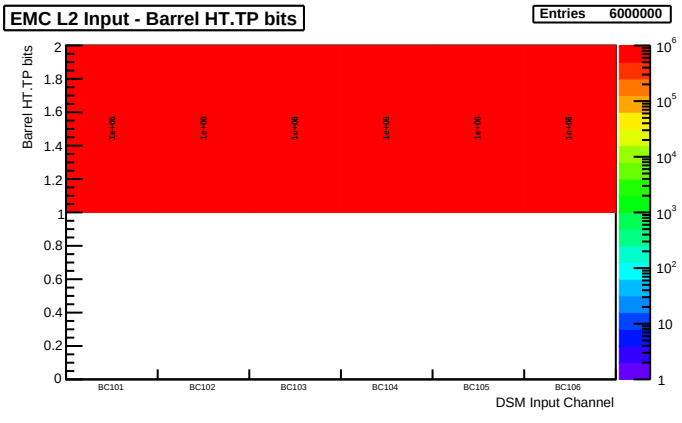

Endcap EMC L1 Input - HT.TP Diff
Entries 0
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

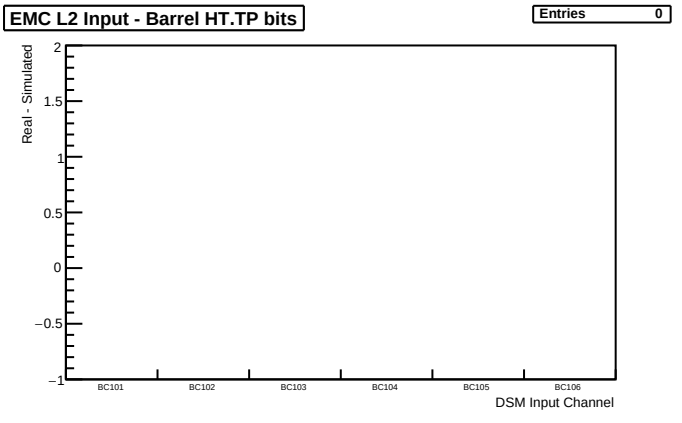

EMC L2 Input - Barrel HT bits
Entries 6000000
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

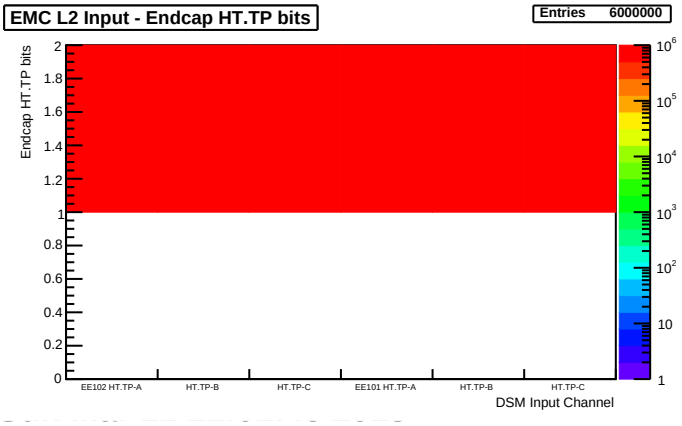

EMC L2 Input - Barrel HT bits
Entries 1.2e+07
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

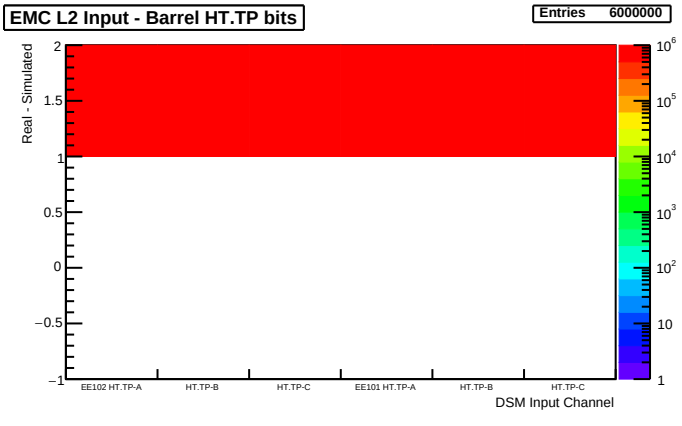

EMC L2 Input - Endcap HT bits
Entries 4000000
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

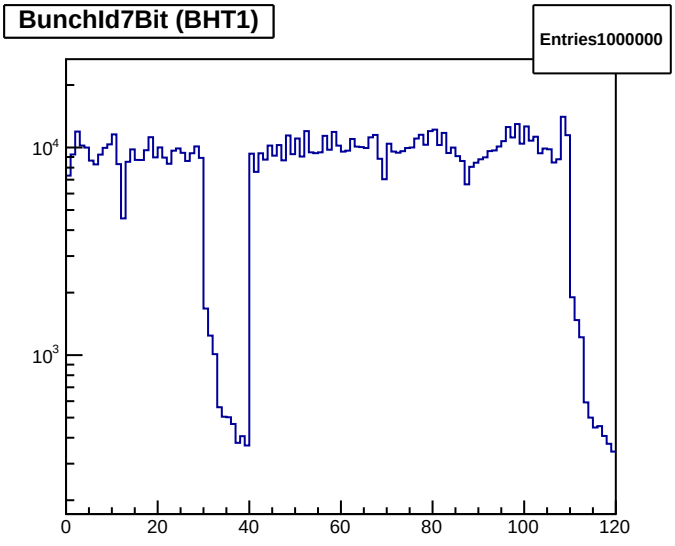

EMC L2 Input - Barrel HT bits
Entries 4000000
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

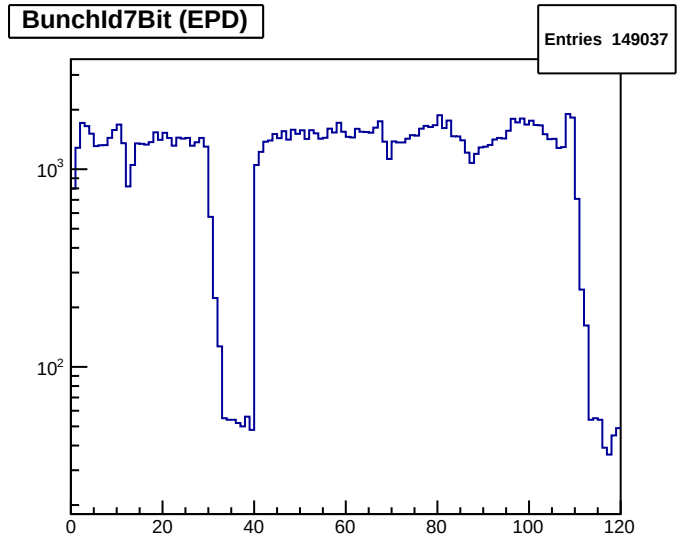

EMC L2 Input - Barrel HT.TP bits
Entries 6000000
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

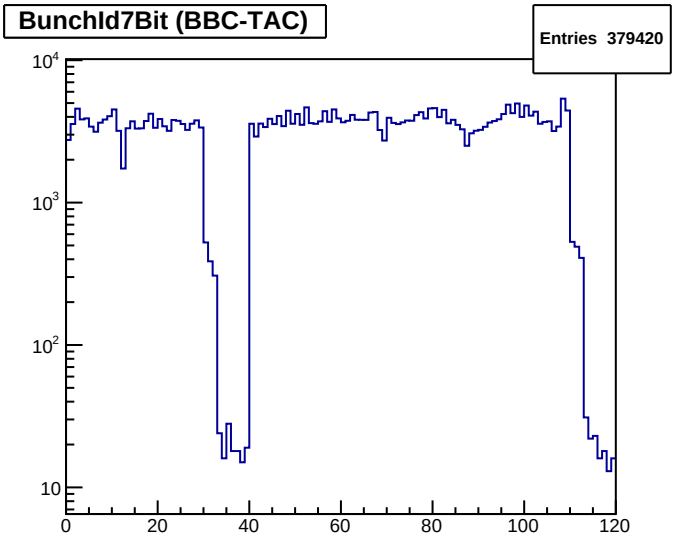

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

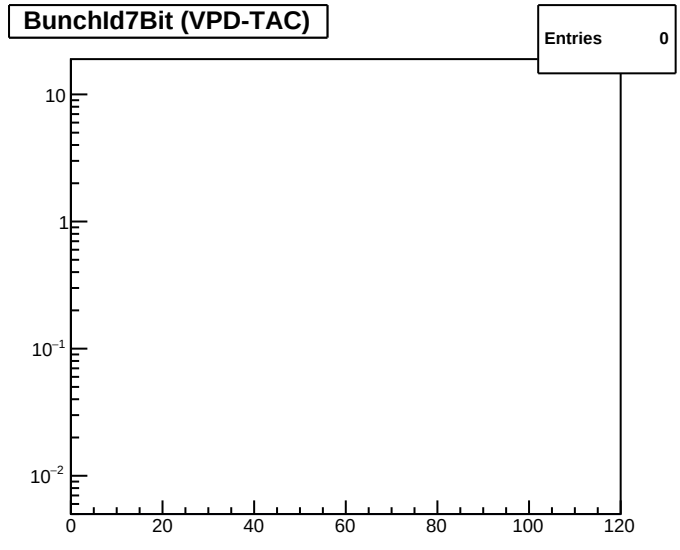

EMC L2 Input - Endcap HT.TP bits
Entries 6000000
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

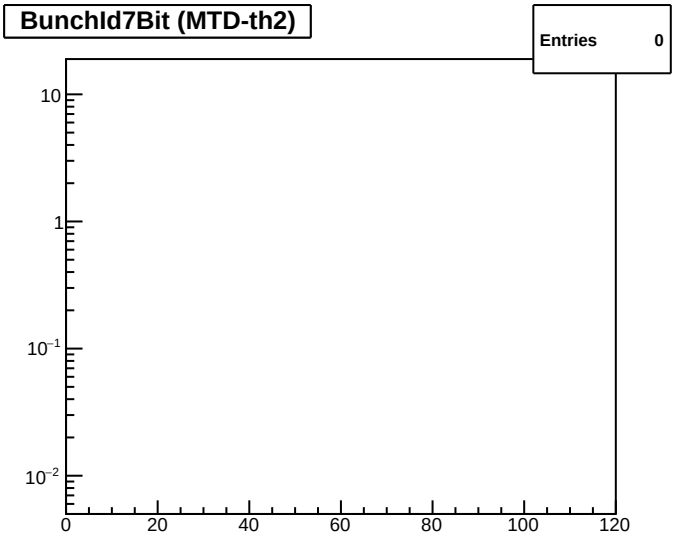

EMC L2 Input - Barrel HT.TP bits
Entries 6000000
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

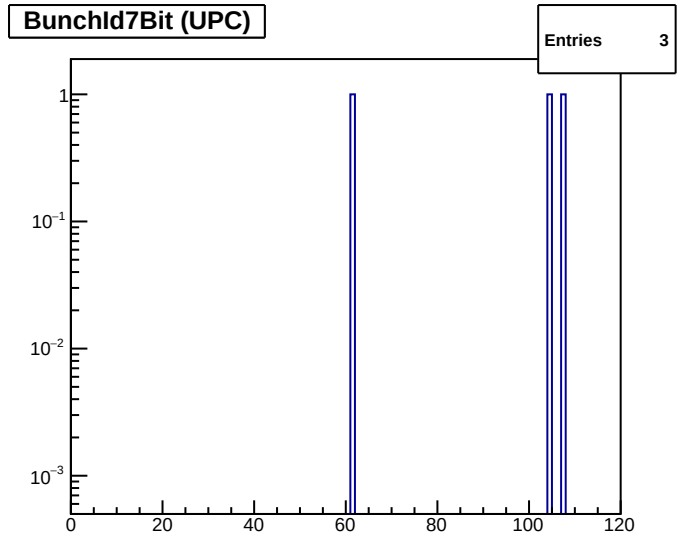

BunchId7Bit (BHT1)
Entries1000000
10^4
10^3
0 20 40 60 80 100 120

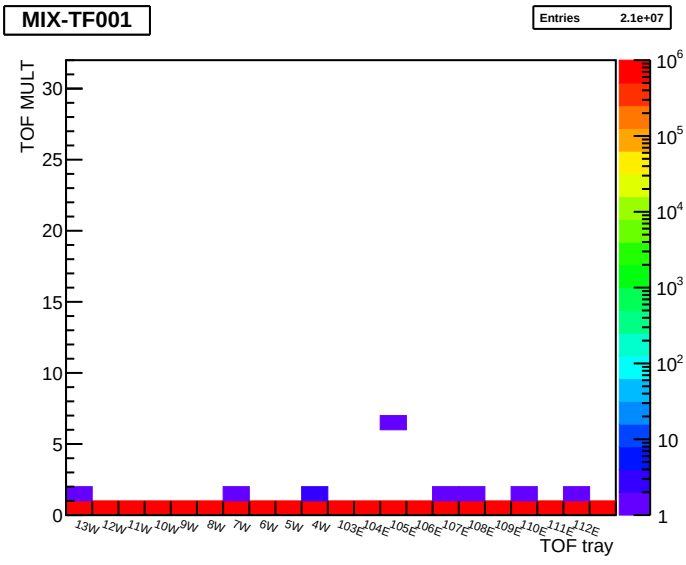

BunchId7Bit (EPD)
Entries 149037
10^3
10^2
0 20 40 60 80 100 120

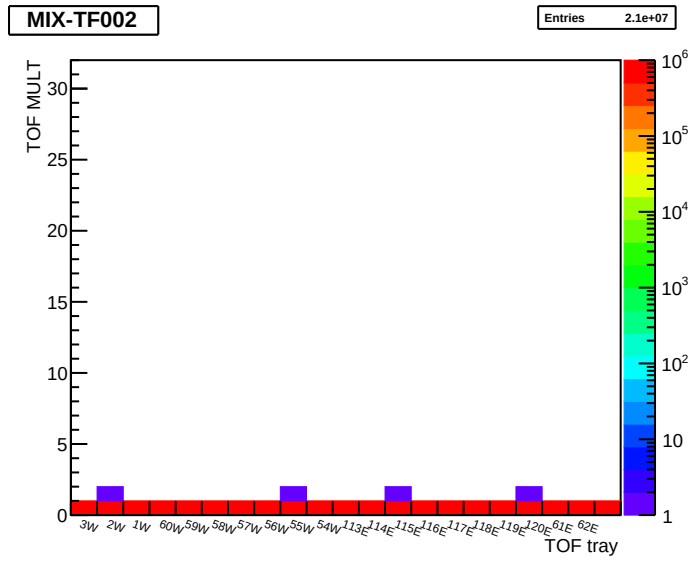

BunchId7Bit (BBC-TAC)
Entries 379420
10^4
10^3
10^2
10
0 20 40 60 80 100 120

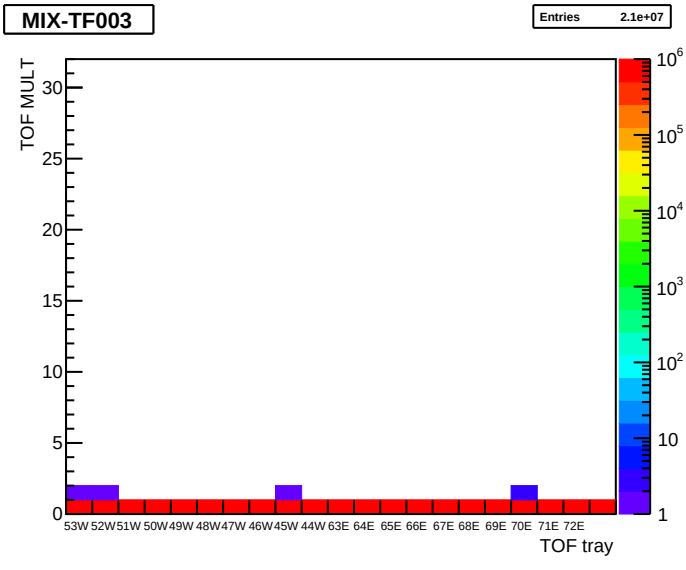

BunchId7Bit (VPD-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

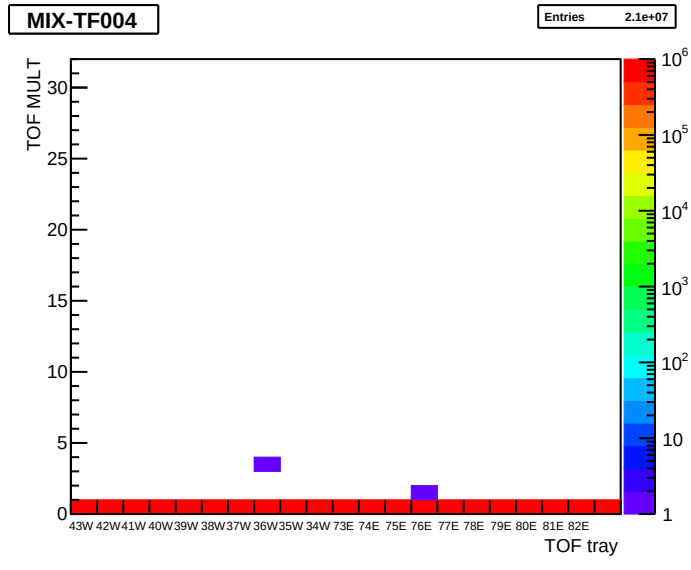

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

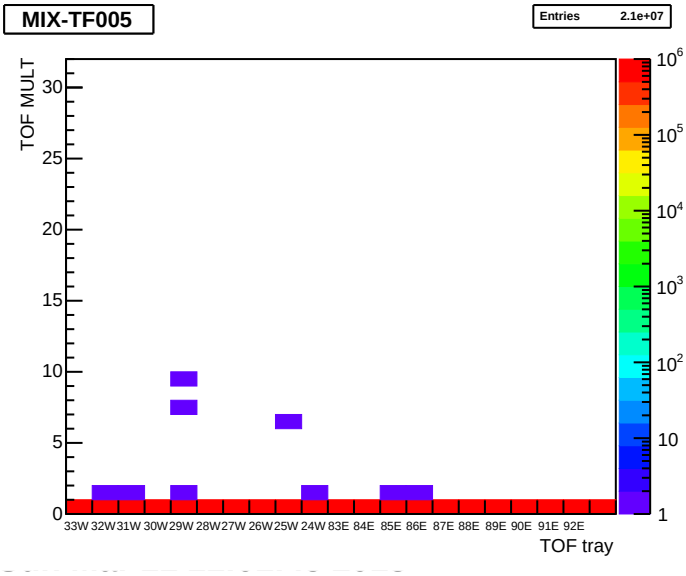

BunchId7Bit (UPC)
Entries 3
1
10^{-1}
10^{-2}
10^{-3}
0 20 40 60 80 100 120

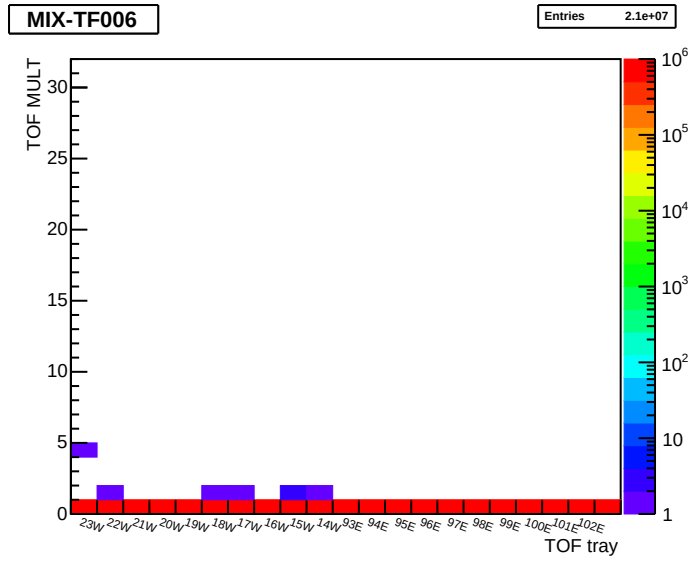

MIX-TF001
Entries 2.1e+07
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

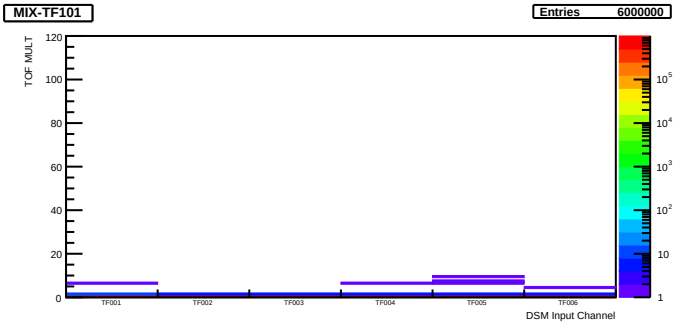

MIX-TF002
Entries 2.1e+07
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

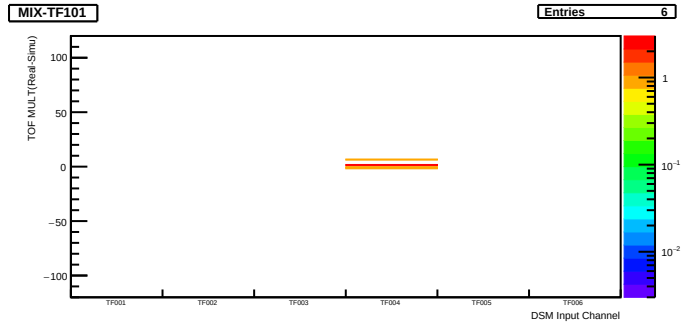

MIX-TF003
Entries 2.1e+07
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

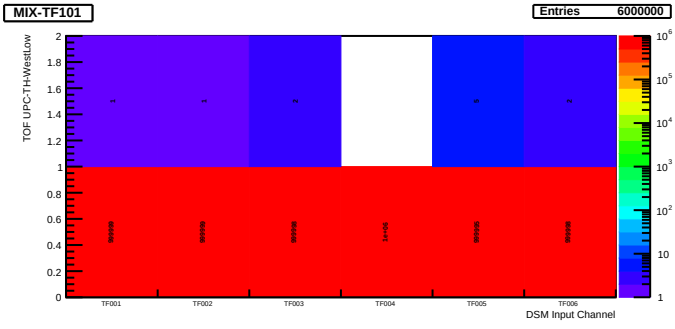

MIX-TF004
Entries 2.1e+07
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

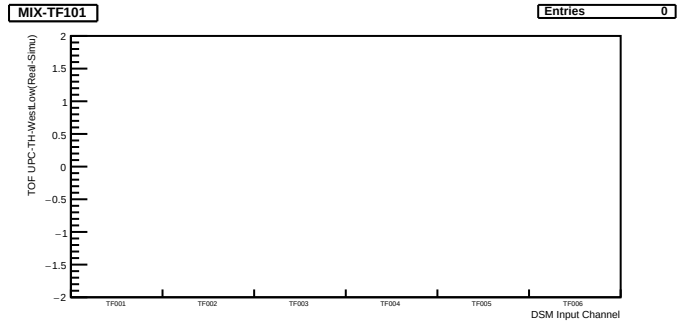

MIX-TF005
Entries 2.1e+07
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

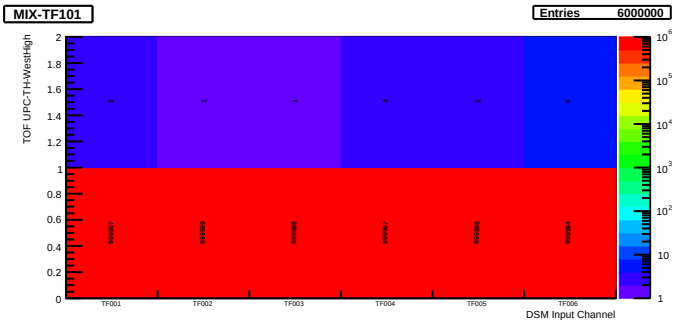

MIX-TF006
Entries 2.1e+07
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

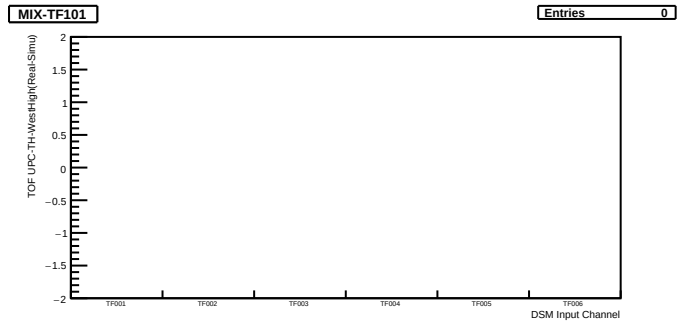

MIX-TF101
Entries 6000000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

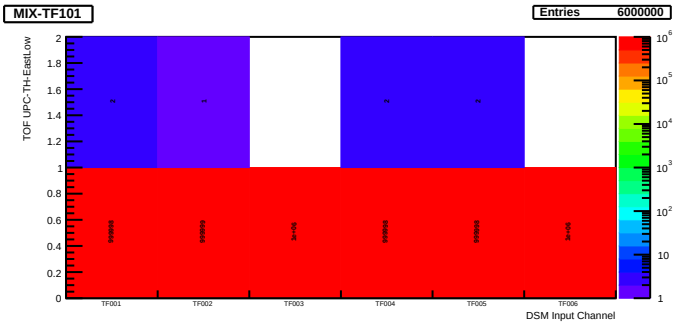

MIX-TF101
Entries 6
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

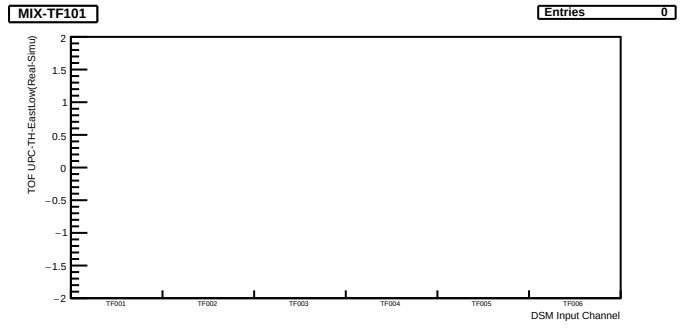

MIX-TF101
Entries 6000000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

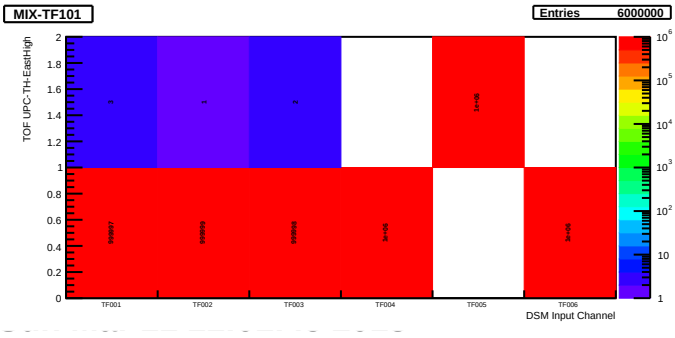

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

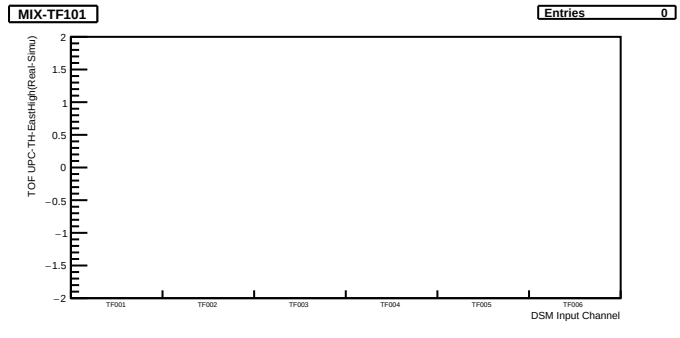

MIX-TF101
Entries 6000000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

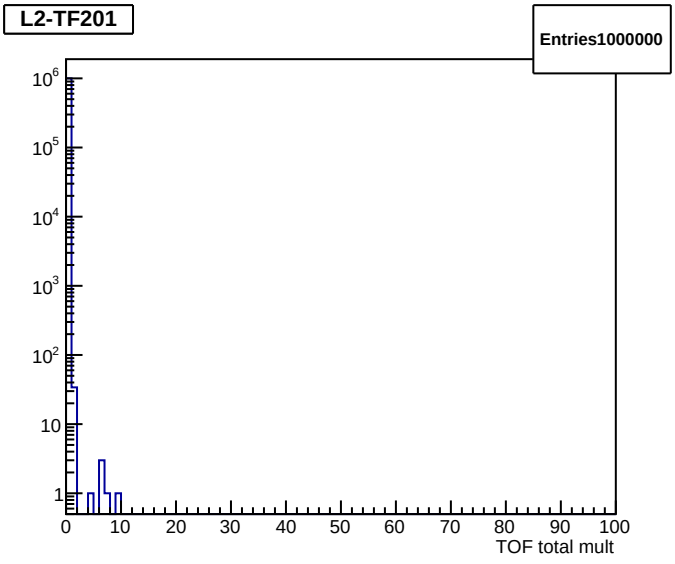

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

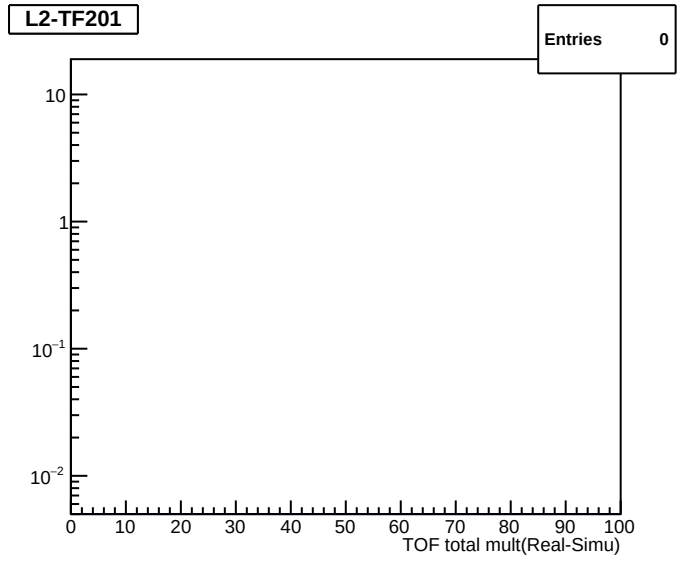

MIX-TF101
Entries 6000000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

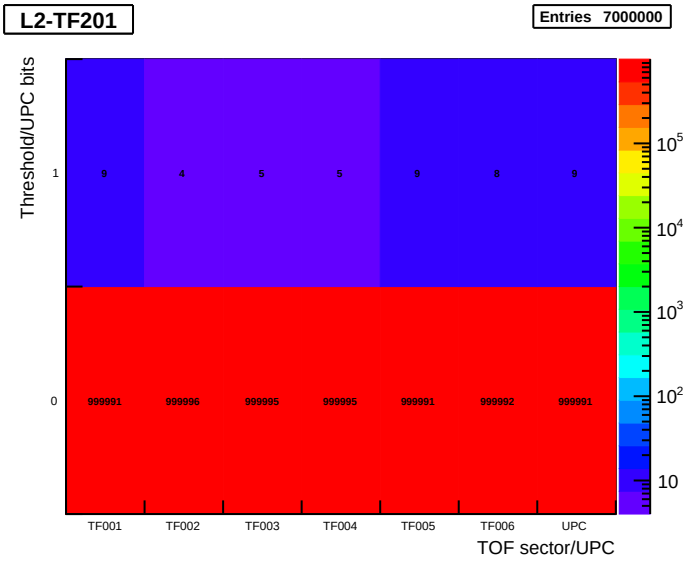

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

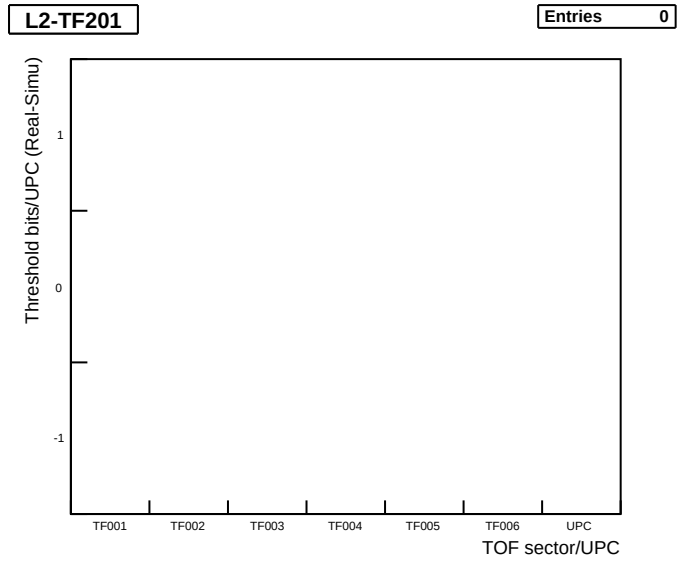

MIX-TF101
Entries 6000000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

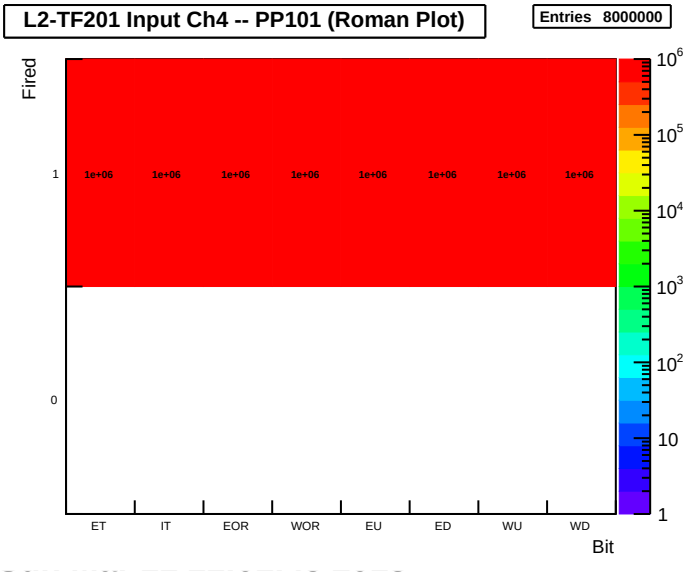

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

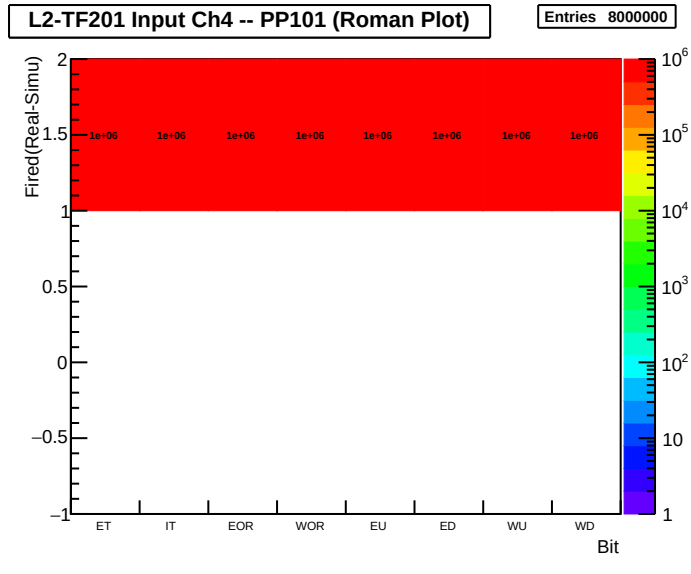

L2-TF201
Entries1000000
TOF total mult

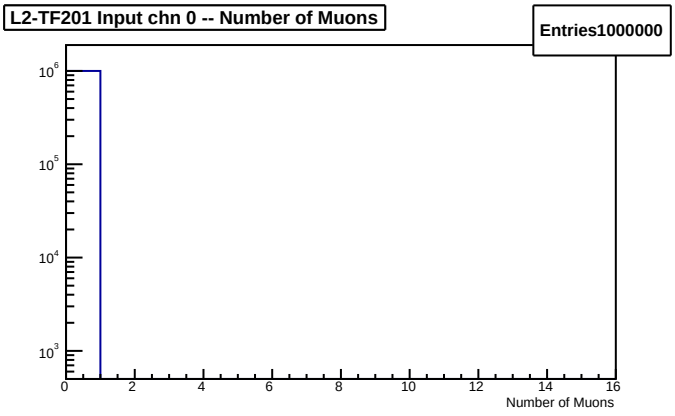

L2-TF201
Entries 0
TOF total mult(Real-Simu)

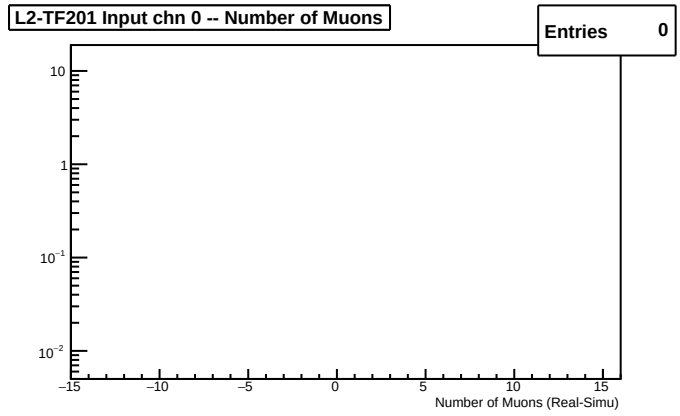

L2-TF201
Entries 7000000
Threshold/UPC bits
9 4 5 5 9 8 9
999991 999996 999995 999995 999991 999992 999991
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

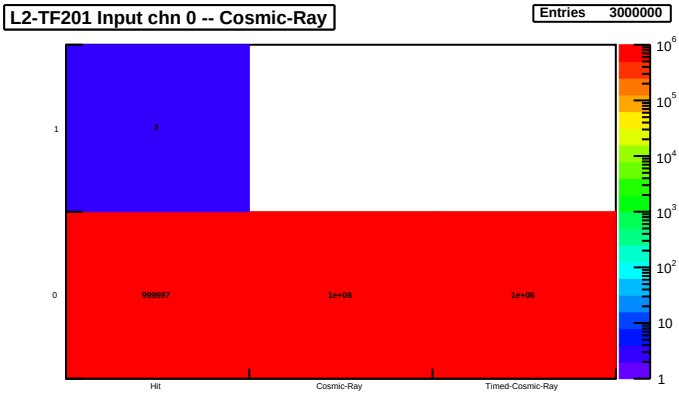

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

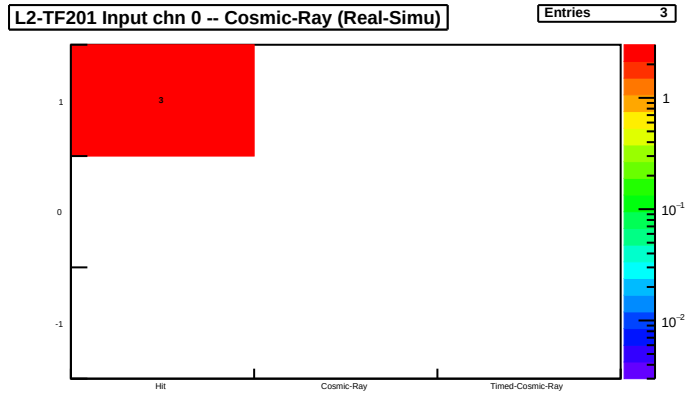

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 8000000
Fired
1e+06 1e+06 1e+06 1e+06 1e+06 1e+06 1e+06 1e+06
ET IT EOR WOR EU ED WU WD
Bit

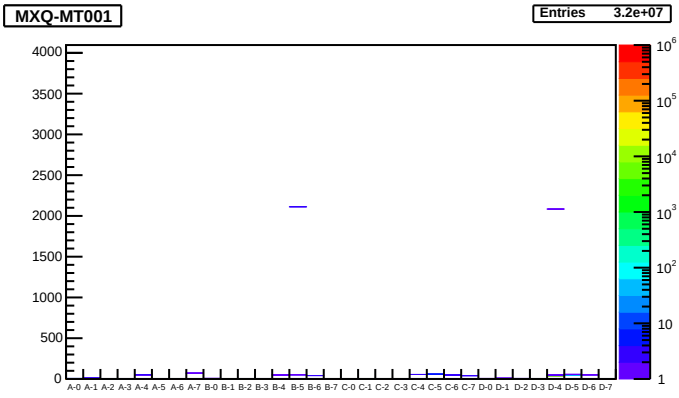

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 8000000
Fired(Real-Simu)
1e+06 1e+06 1e+06 1e+06 1e+06 1e+06 1e+06 1e+06
ET IT EOR WOR EU ED WU WD
Bit

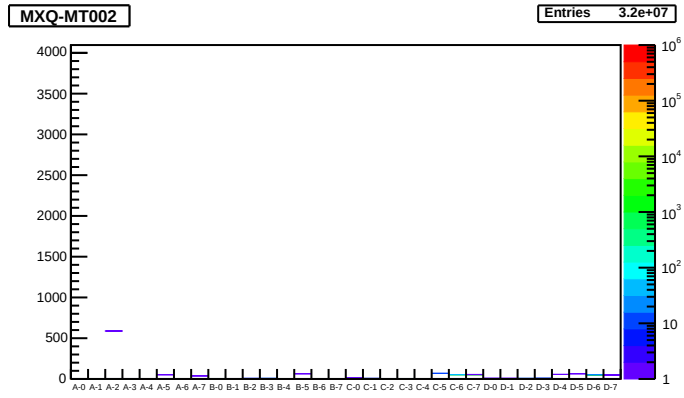

L2-TF201 Input chn 0 -- Number of Muons
Entries1000000
Number of Muons

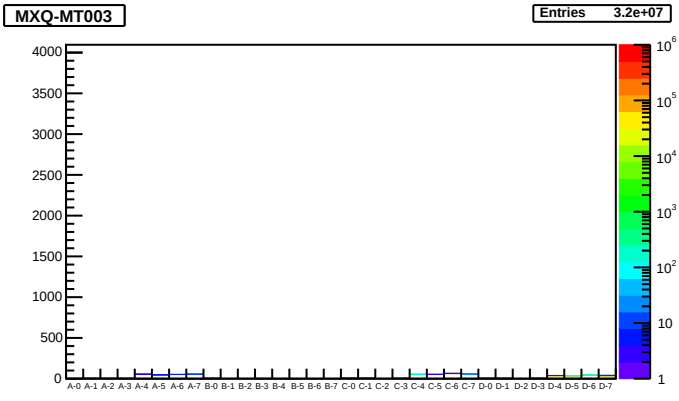

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

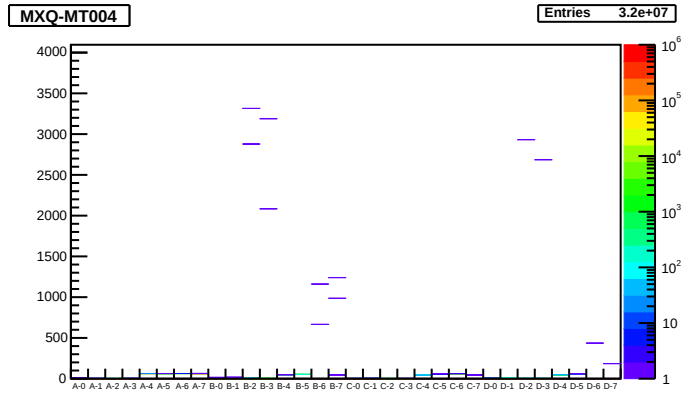

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 3000000
3
999997
1e+06
1e+06
Hit
Cosmic-Ray
Timed-Cosmic-Ray

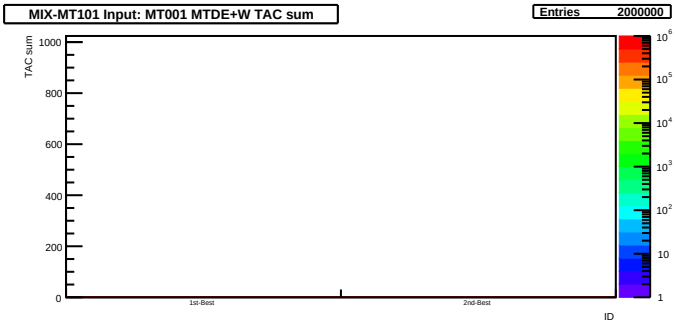

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 3
3
Hit
Cosmic-Ray
Timed-Cosmic-Ray

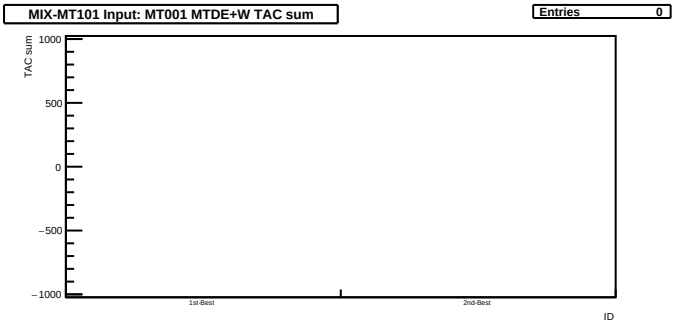

MXQ-MT001
Entries 3.2e+07

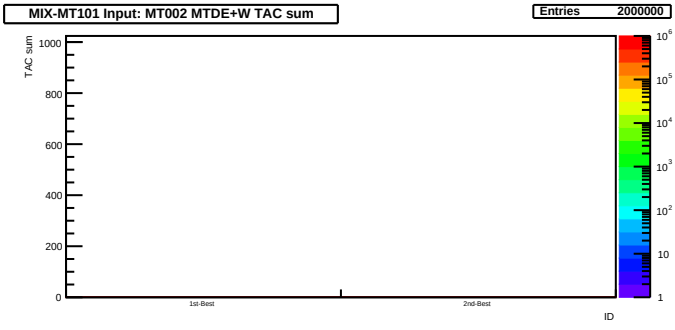

MXQ-MT002
Entries 3.2e+07

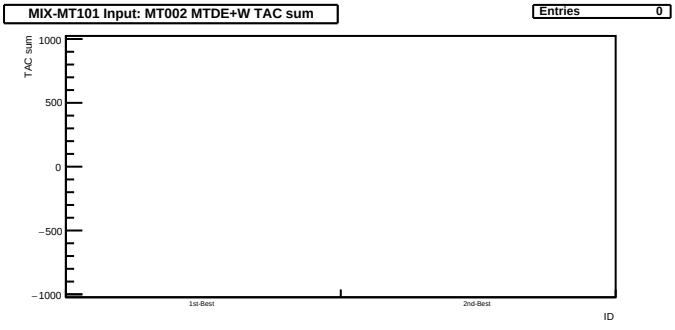

MXQ-MT003
Entries 3.2e+07

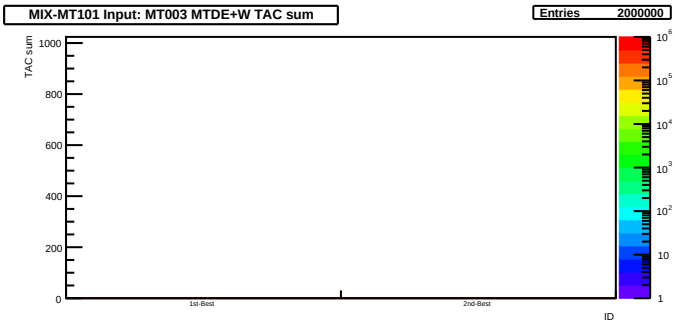

MXQ-MT004
Entries 3.2e+07

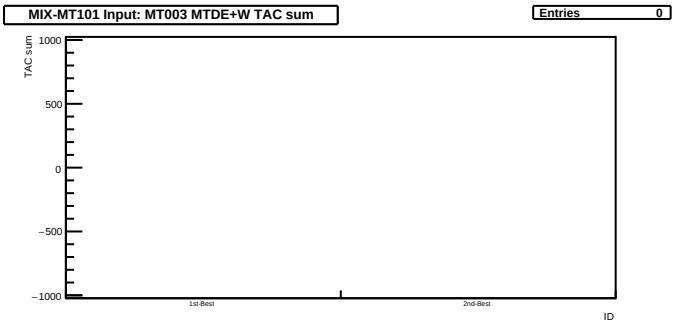

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

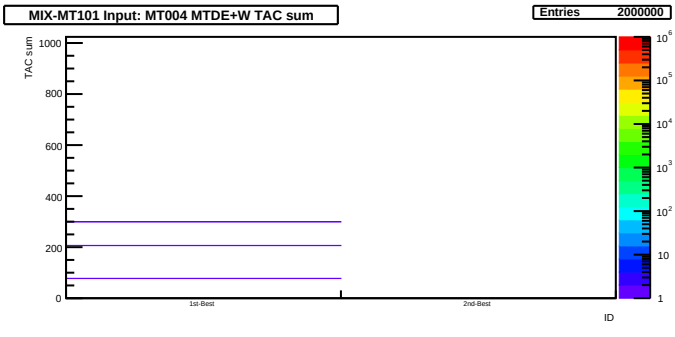

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 0
TAC sum
1st-Best
2nd-Best
ID

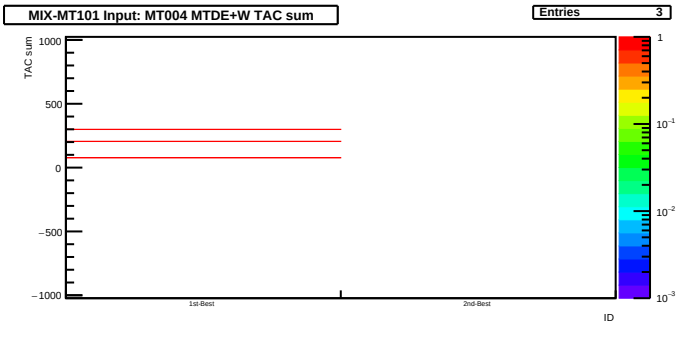

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

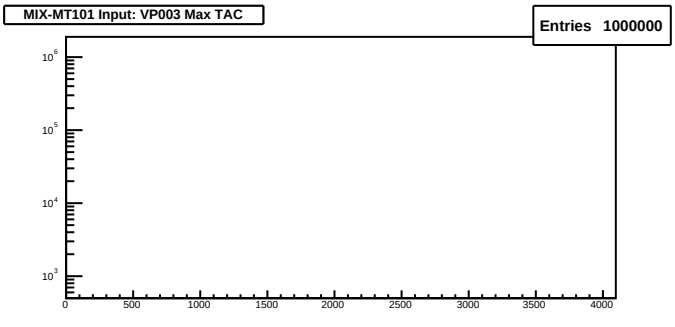

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 0
TAC sum
1st-Best
2nd-Best
ID

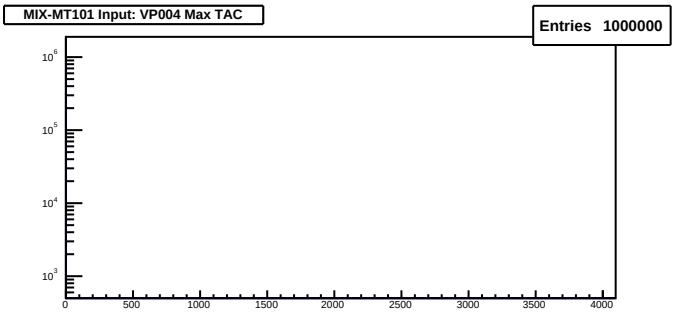

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

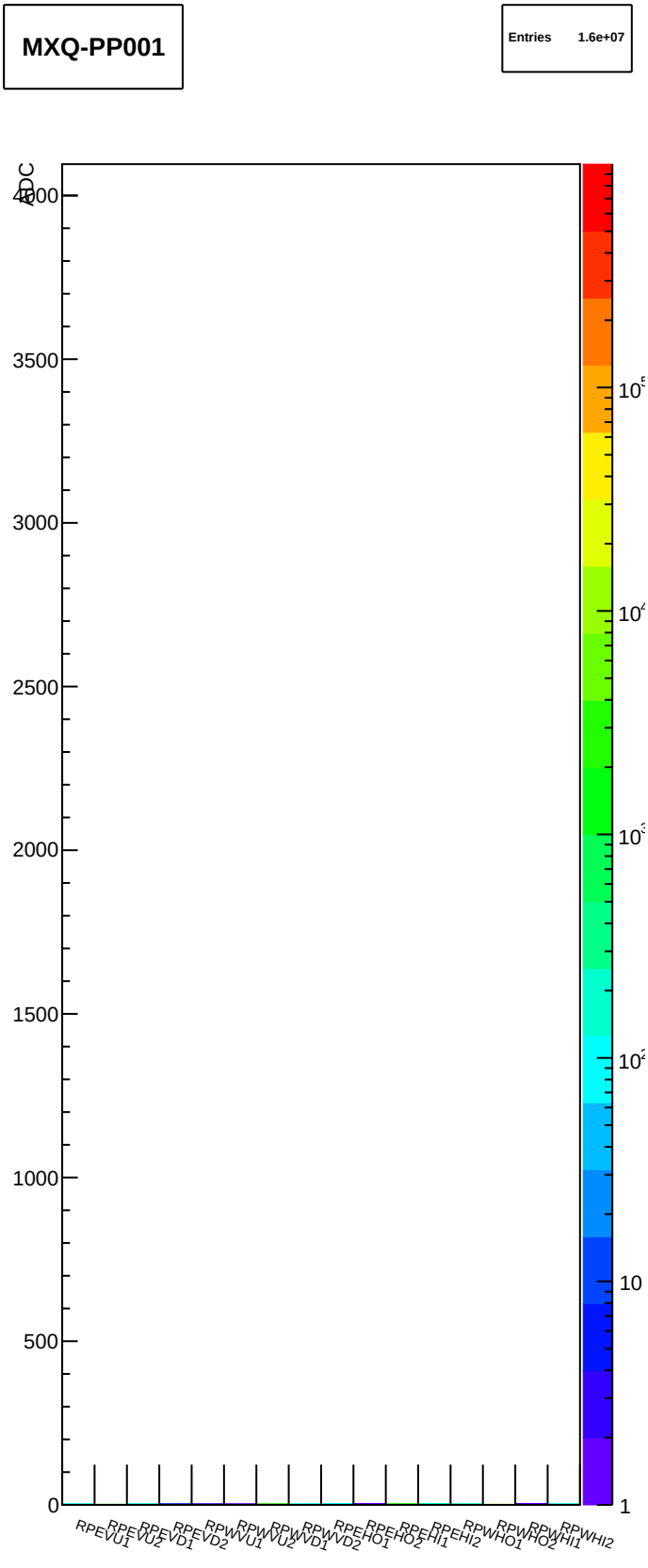

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 0
TAC sum
1st-Best
2nd-Best
ID

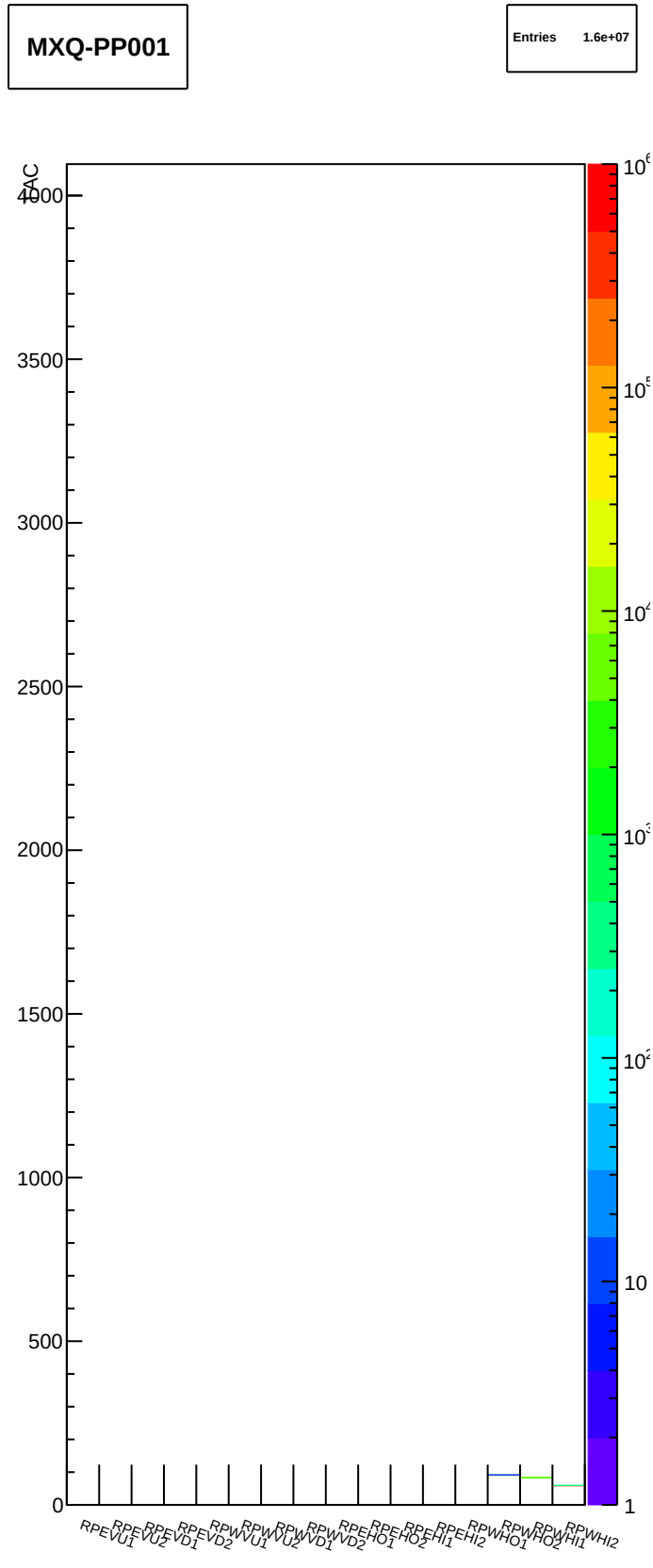

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

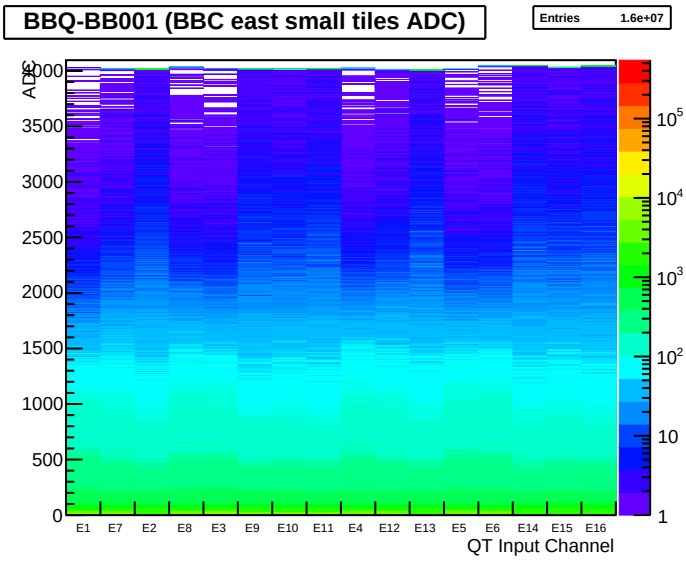

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 3
TAC sum
1st-Best
2nd-Best
ID

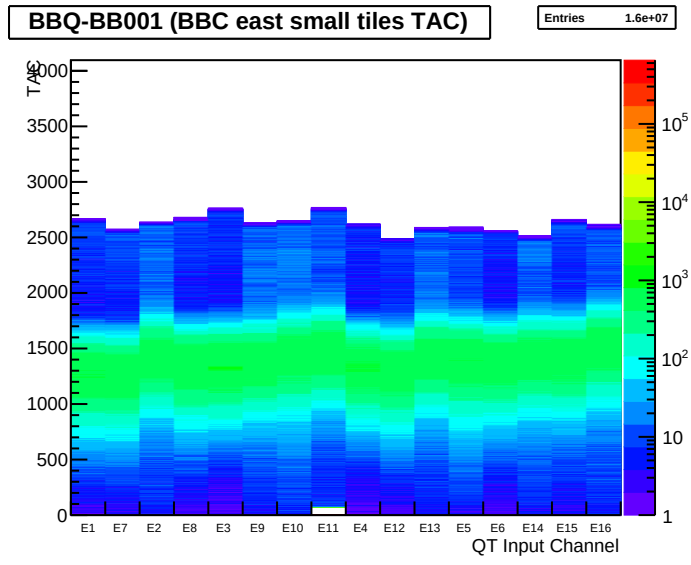

MIX-MT101 Input: VP003 Max TAC
Entries 1000000

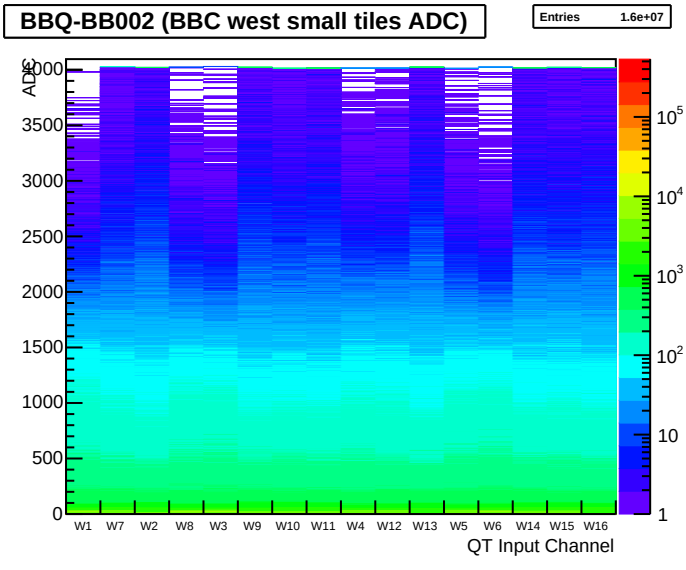

MIX-MT101 Input: VP004 Max TAC
Entries 1000000

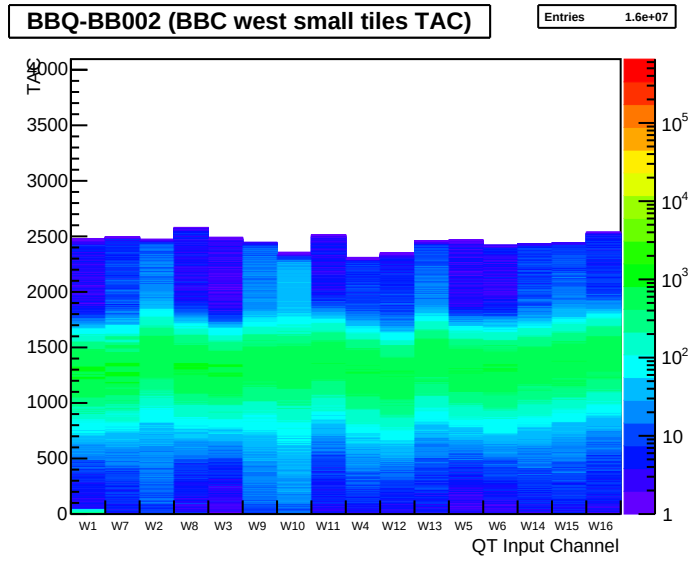

MXQ-PP001
Entries 1.6e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
1
10

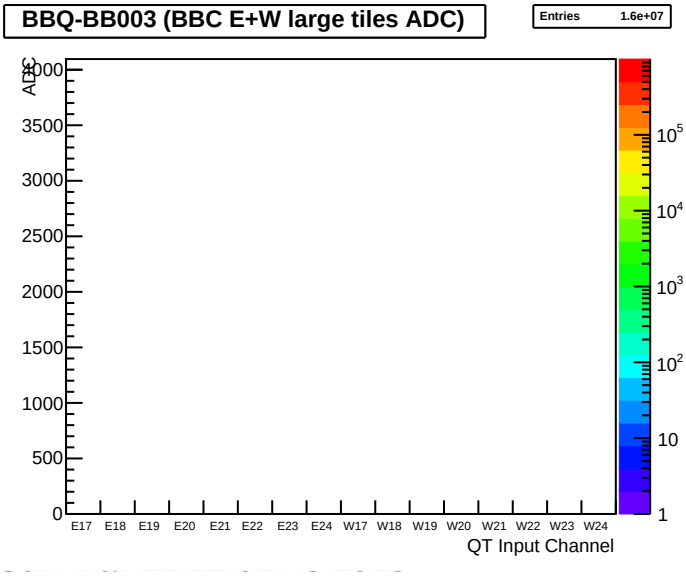

MXQ-PP001
Entries 1.6e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
1
10

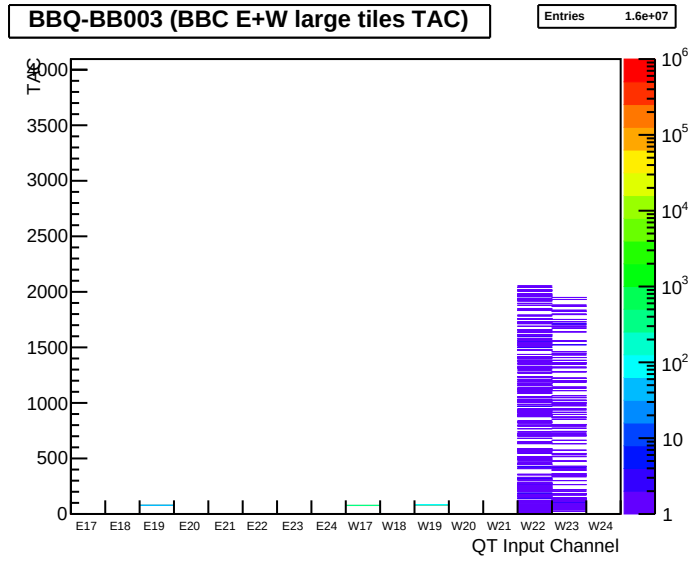

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.6e+07
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

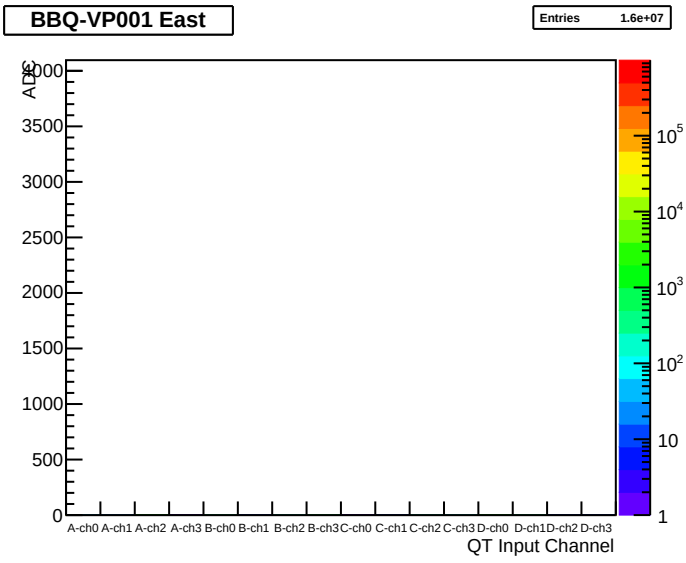

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.6e+07
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

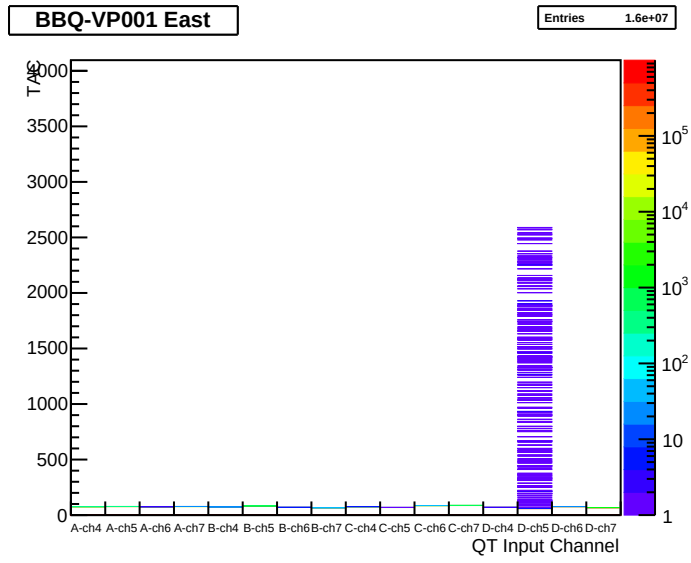

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.6e+07
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

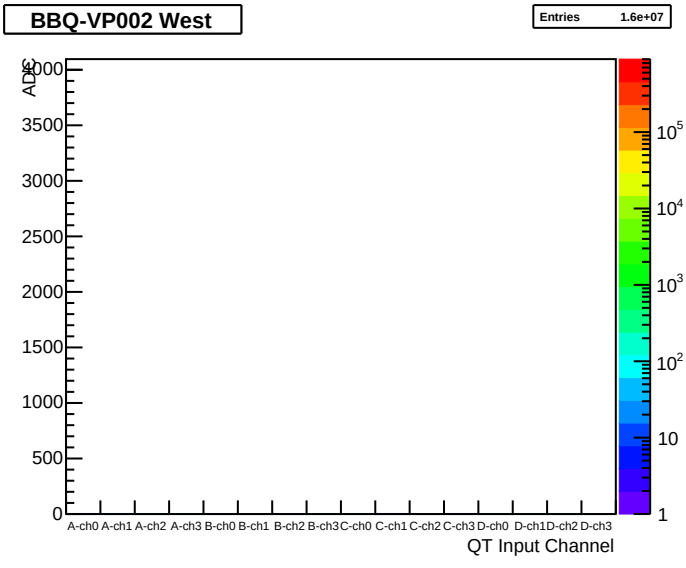

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.6e+07
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

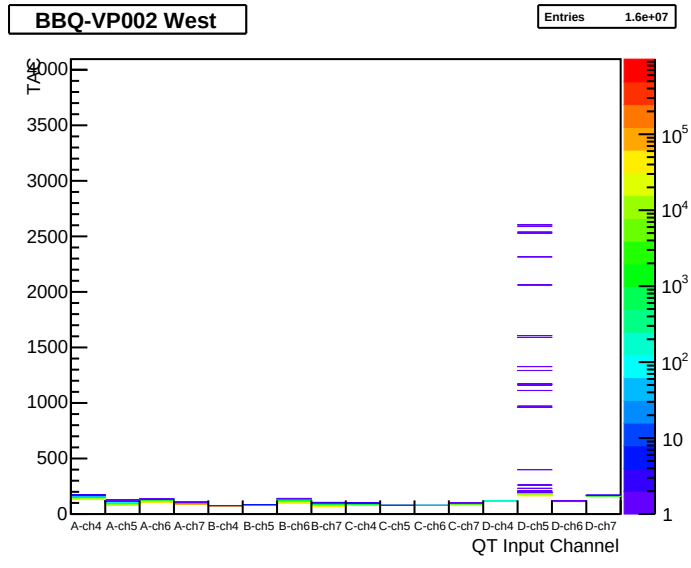

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.6e+07
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

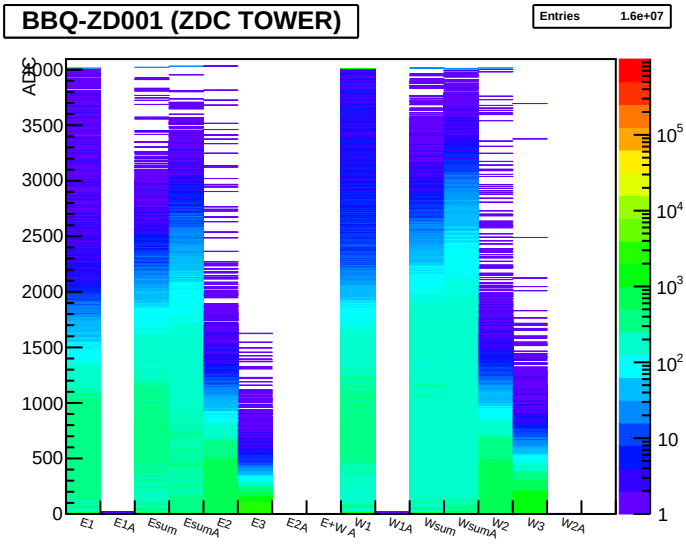

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.6e+07
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

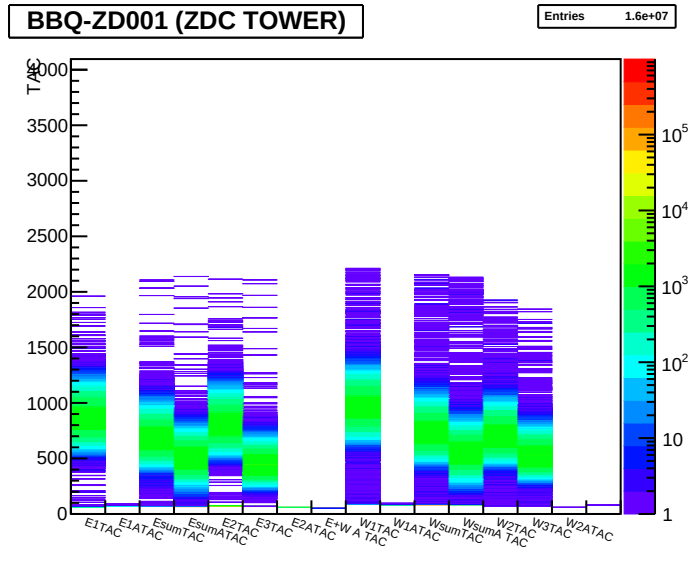

BBQ-VP001 East
Entries 1.6e+07
ADC
QT Input Channel

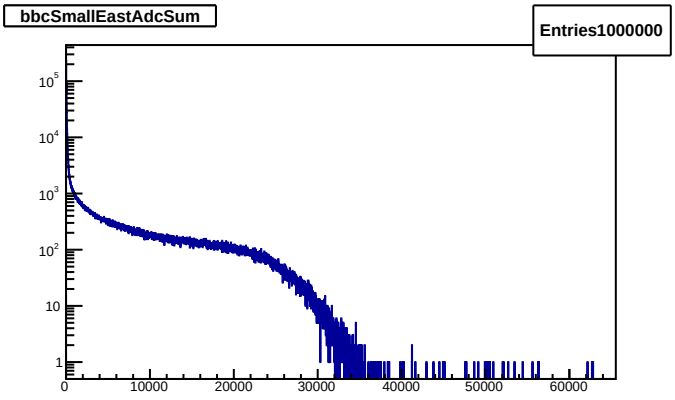

BBQ-VP001 East
Entries 1.6e+07
TAC
QT Input Channel

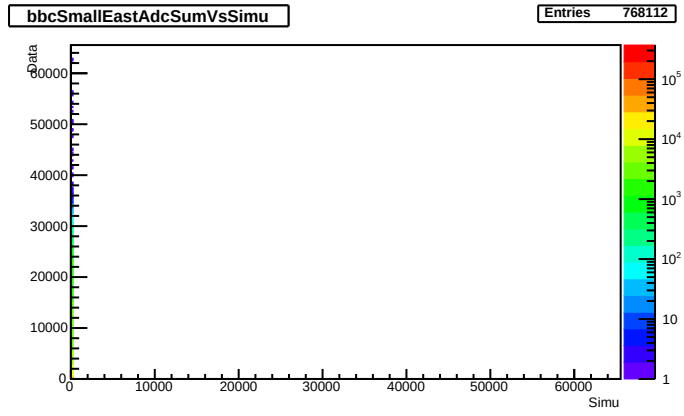

BBQ-VP002 West
Entries 1.6e+07
ADC
QT Input Channel

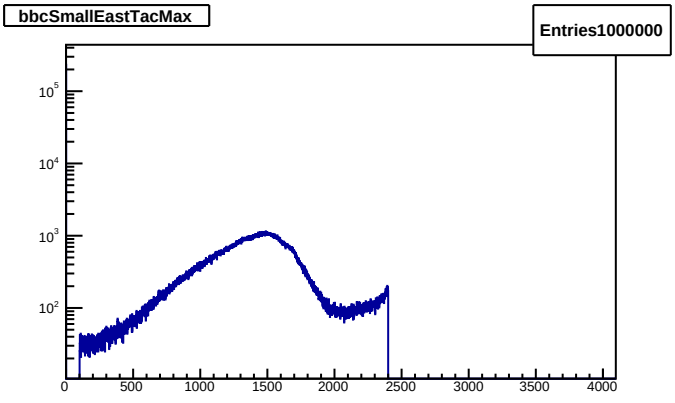

BBQ-VP002 West
Entries 1.6e+07
TAC
QT Input Channel

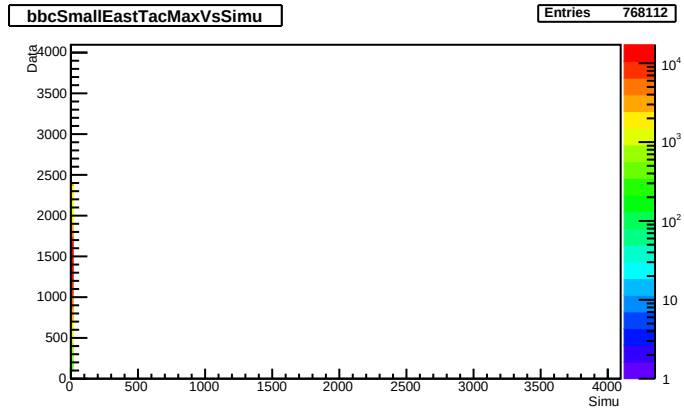

BBQ-ZD001 (ZDC TOWER)
Entries 1.6e+07
ADC

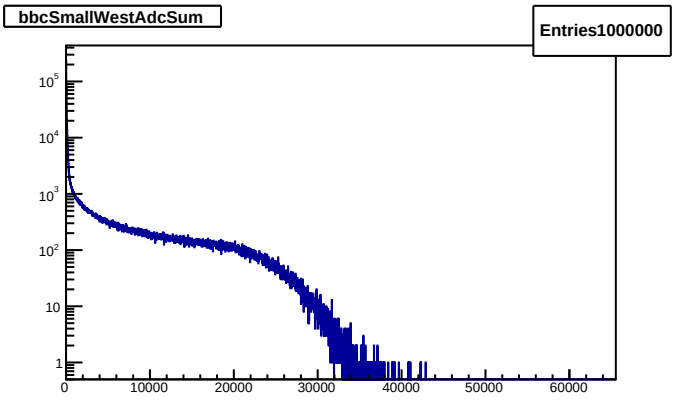

BBQ-ZD001 (ZDC TOWER)
Entries 1.6e+07
TAC

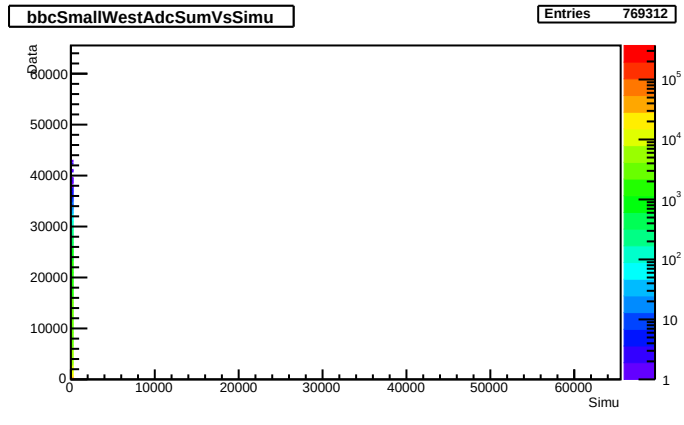

bbcSmallEastAdcSum
Entries1000000

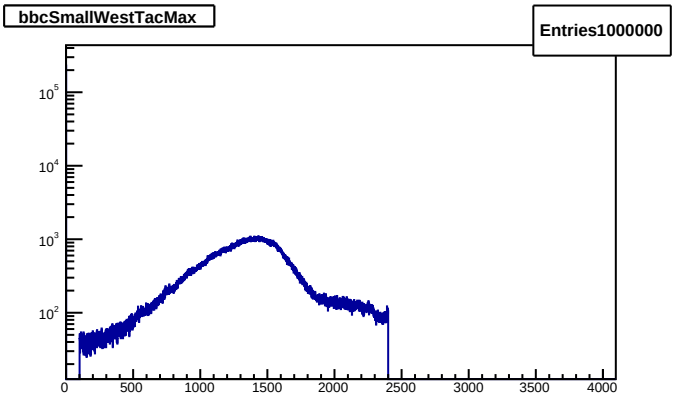

bbcSmallEastAdcSumVsSimu
Entries 768112
Data
Simu

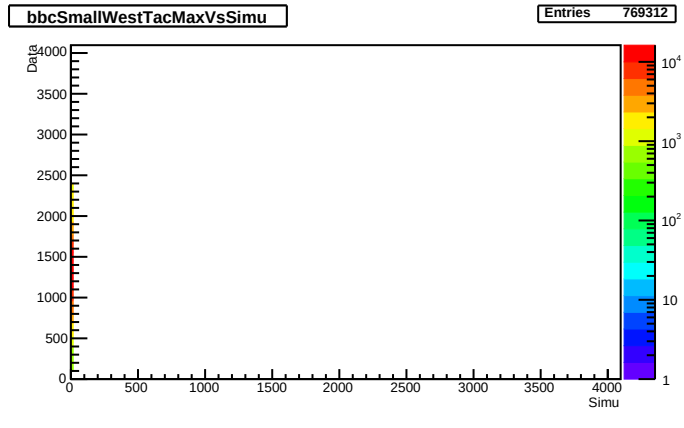

bbcSmallEastTacMax
Entries1000000

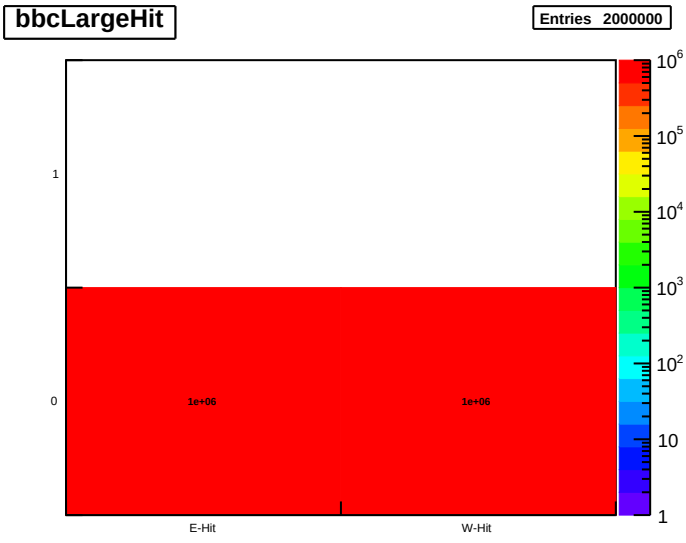

bbcSmallEastTacMaxVsSimu
Entries 768112
Data
Simu

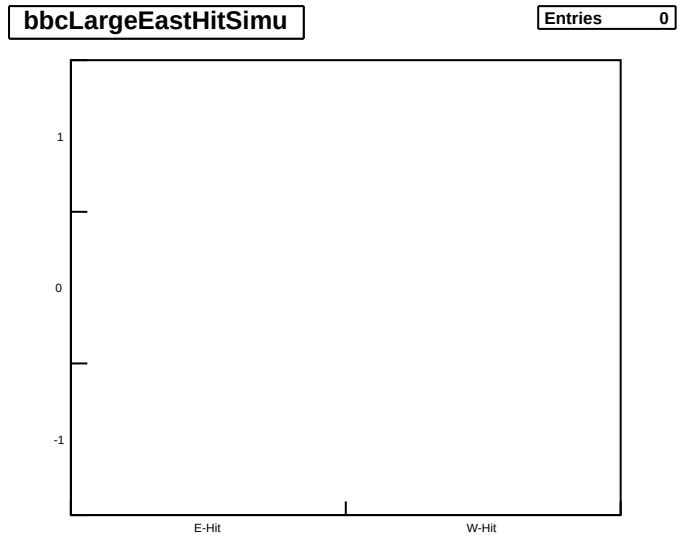

bbcSmallWestAdcSum
Entries1000000

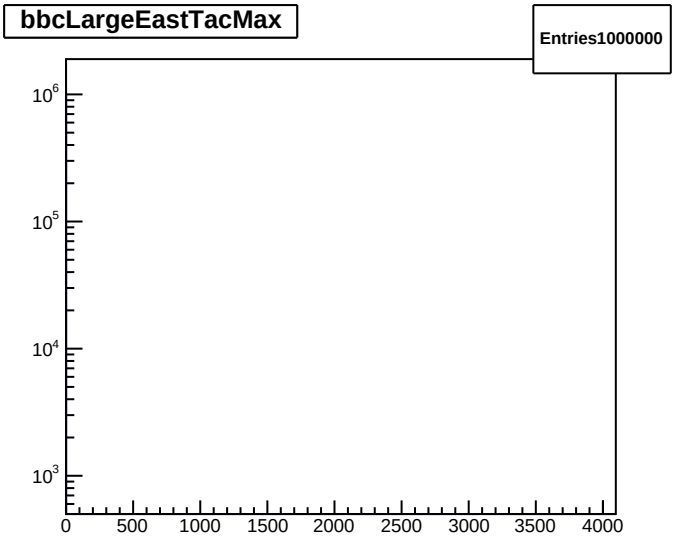

bbcSmallWestAdcSumVsSimu
Entries 769312
Data
Simu

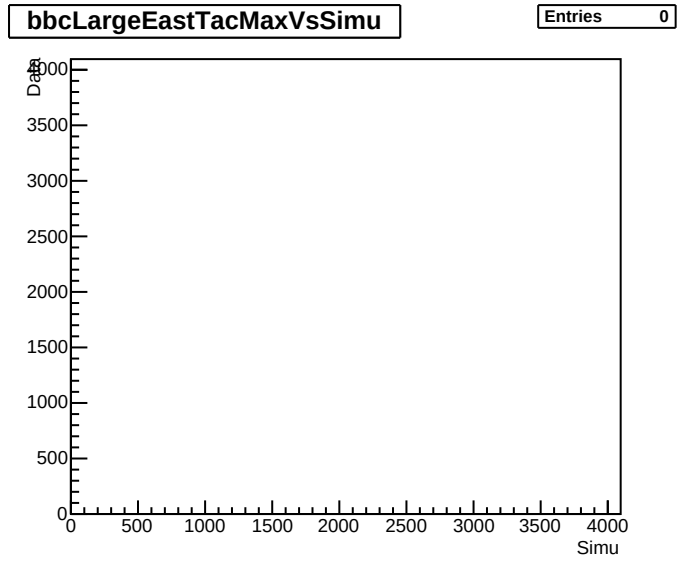

bbcSmallWestTacMax
Entries1000000

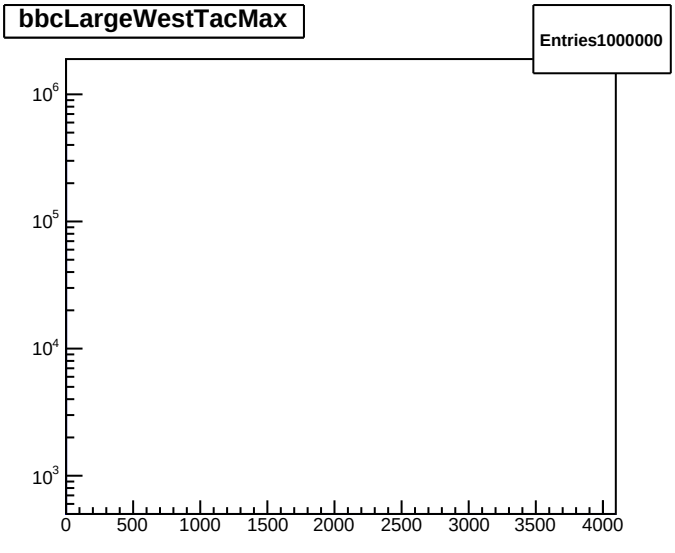

bbcSmallWestTacMaxVsSimu
Entries 769312
Data
Simu

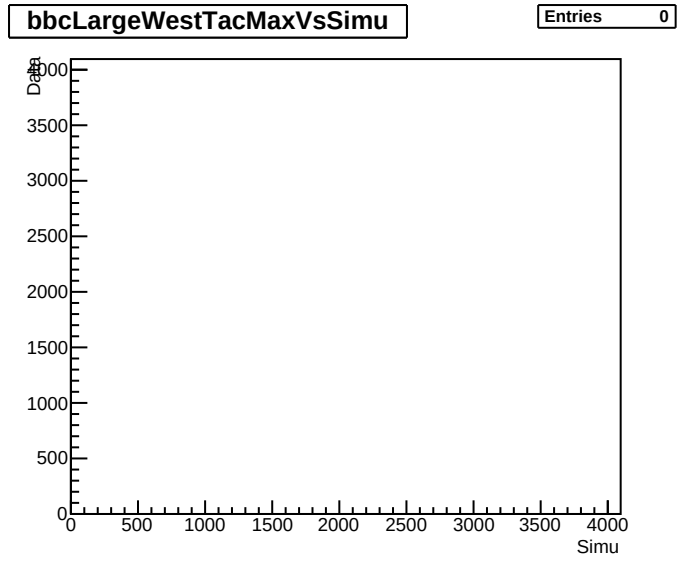

bbcLargeHit
Entries 2000000
1
0
1e+06
1e+06
E-Hit
W-Hit
10^6
10^5
10^4
10^3
10^2
10
1

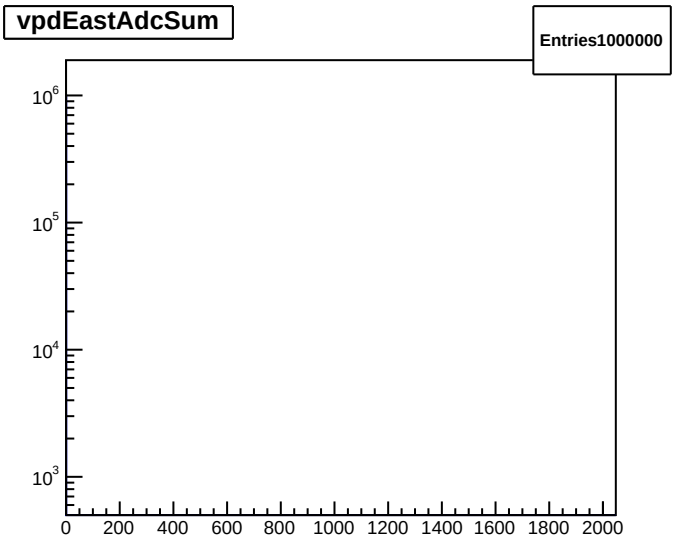

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

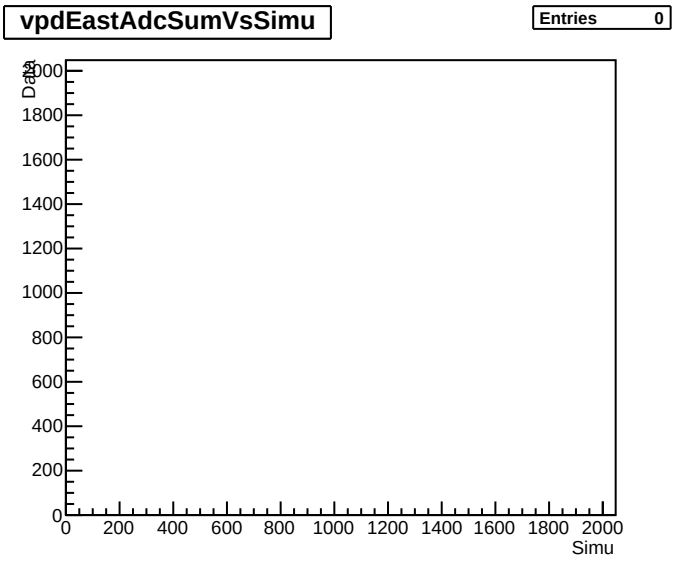

bbcLargeEastTacMax
Entries1000000
10^6
10^5
10^4
10^3
0
500
1000
1500
2000
2500
3000
3500
4000

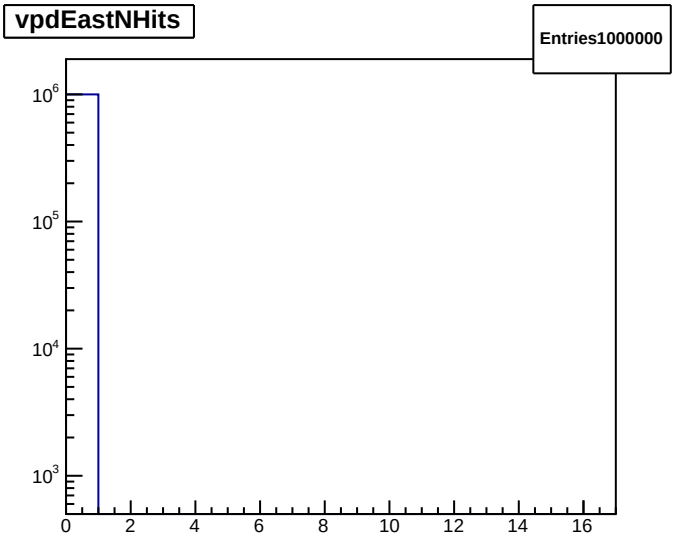

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu
0
500
1000
1500
2000
2500
3000
3500
4000


bbcLargeWestTacMax
Entries1000000
10^6
10^5
10^4
10^3
0
500
1000
1500
2000
2500
3000
3500
4000


bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu
0
500
1000
1500
2000
2500
3000
3500
4000


vpdEastAdcSum
Entries1000000
10^6
10^5
10^4
10^3
0 200 400 600 800 1000 1200 1400 1600 1800 2000


vpdEastAdcSumVsSimu
Entries 0
Data
2000 1800 1600 1400 1200 1000 800 600 400 200 0
0 200 400 600 800 1000 1200 1400 1600 1800 2000
Simu


vpdEastNHits
Entries1000000
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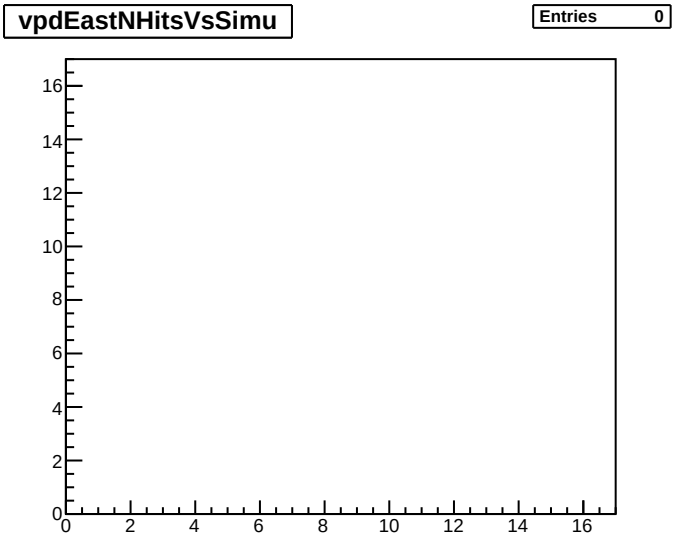
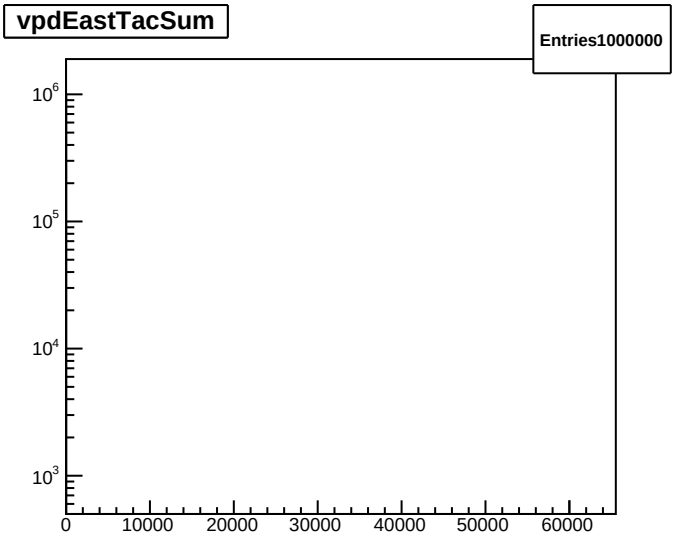
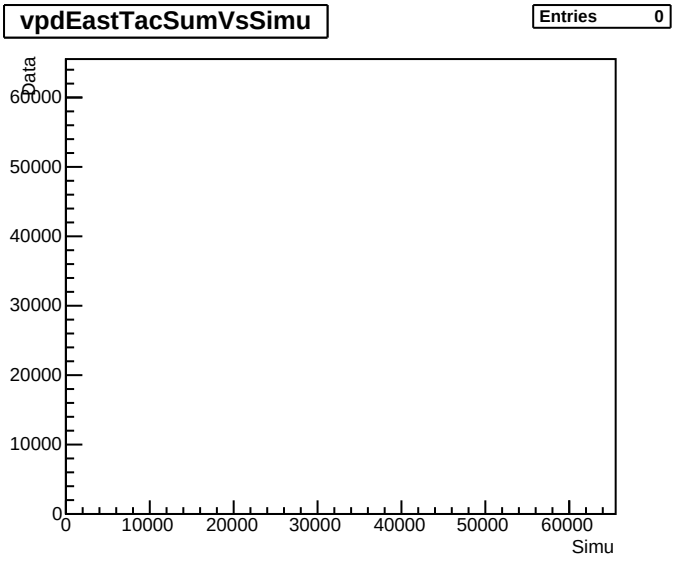
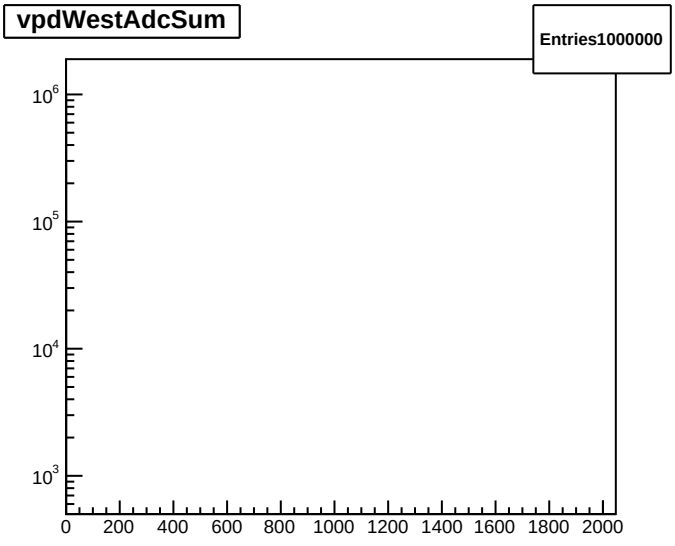
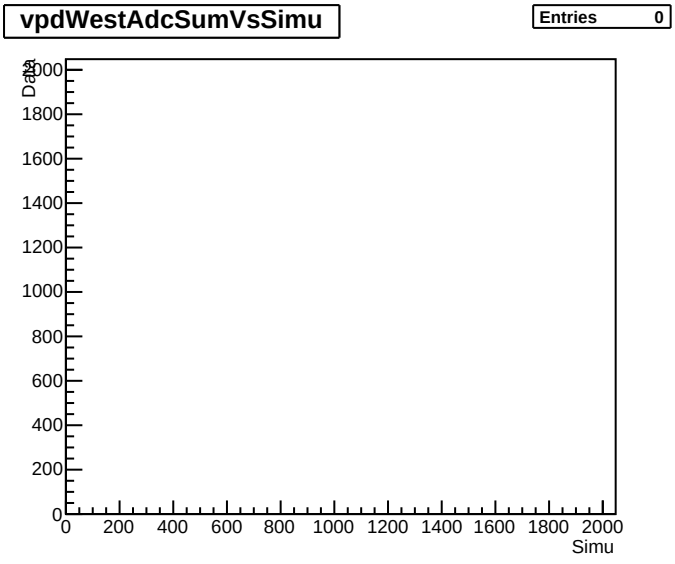
10^5
10^4
10^3
0 2 4 6 8 10 12 14 16

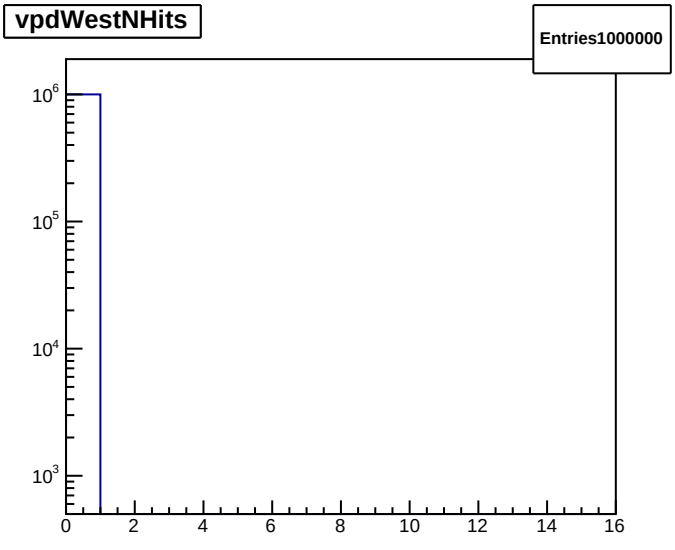

vpdEastNHitsVsSimu
Entries 0
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16

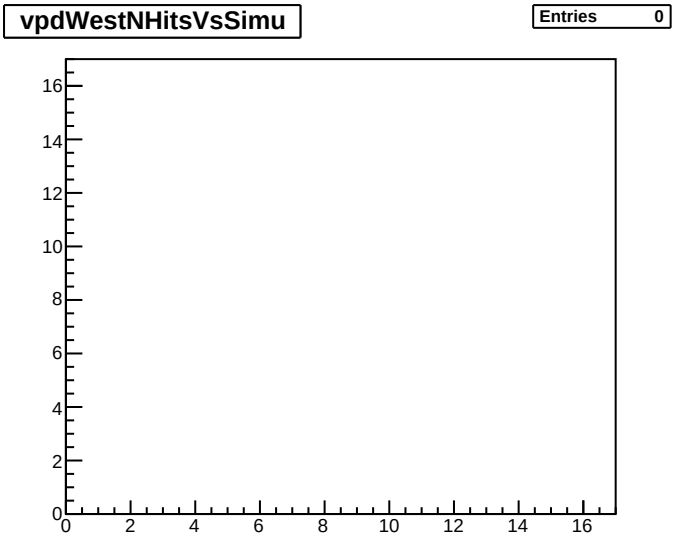

vpdEastTacSum
Entries1000000
10^6
10^5
10^4
10^3
0 10000 20000 30000 40000 50000 60000

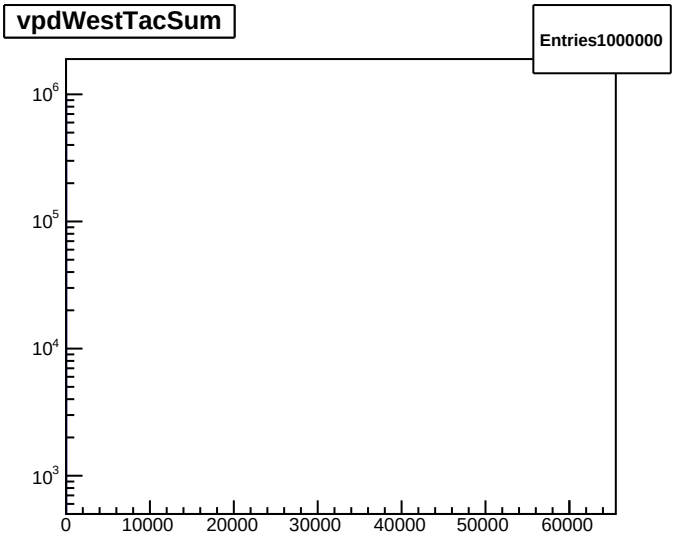

vpdEastTacSumVsSimu
Entries 0
Data
60000 50000 40000 30000 20000 10000 0
0 10000 20000 30000 40000 50000 60000
Simu

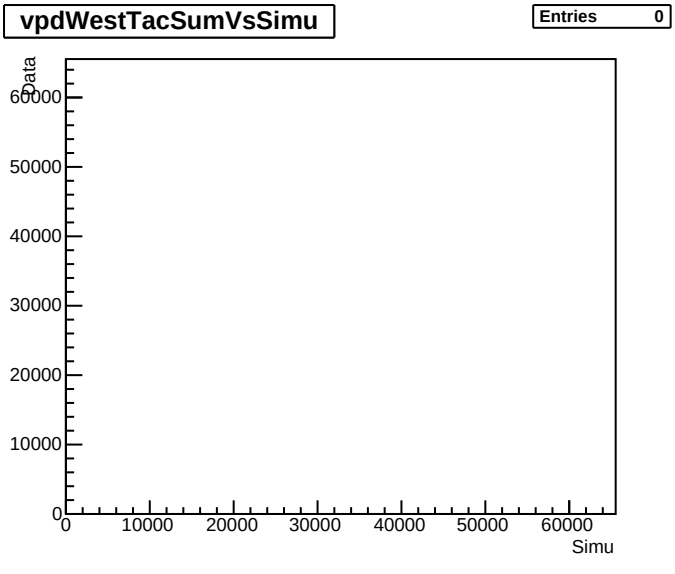

vpdWestAdcSum
Entries1000000
10^6
10^5
10^4
10^3
0 200 400 600 800 1000 1200 1400 1600 1800 2000

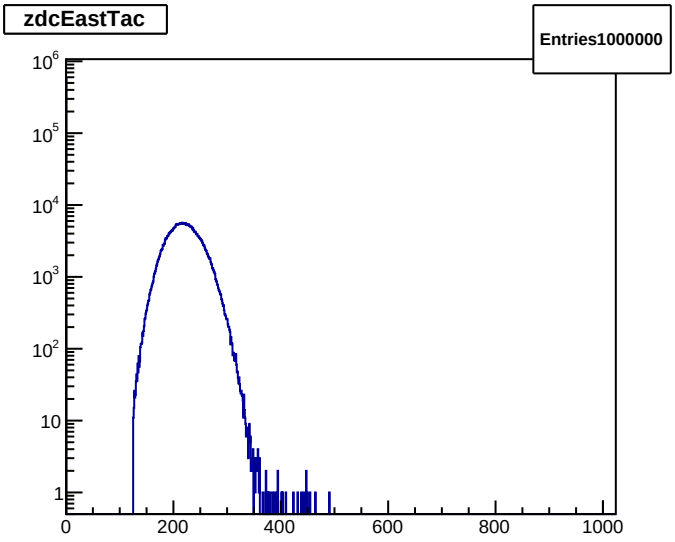

vpdWestAdcSumVsSimu
Entries 0
Data
2000 1800 1600 1400 1200 1000 800 600 400 200 0
0 200 400 600 800 1000 1200 1400 1600 1800 2000
Simu

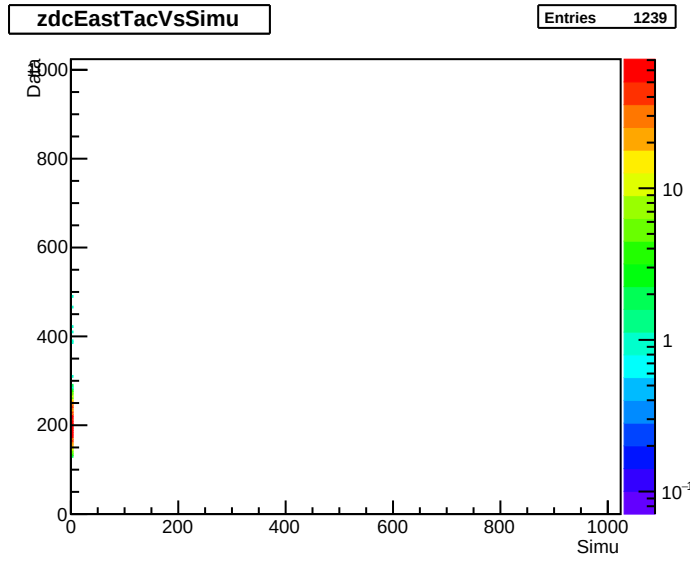

vpdWestNHits
Entries1000000
10^6
10^5
10^4
10^3
0 2 4 6 8 10 12 14 16

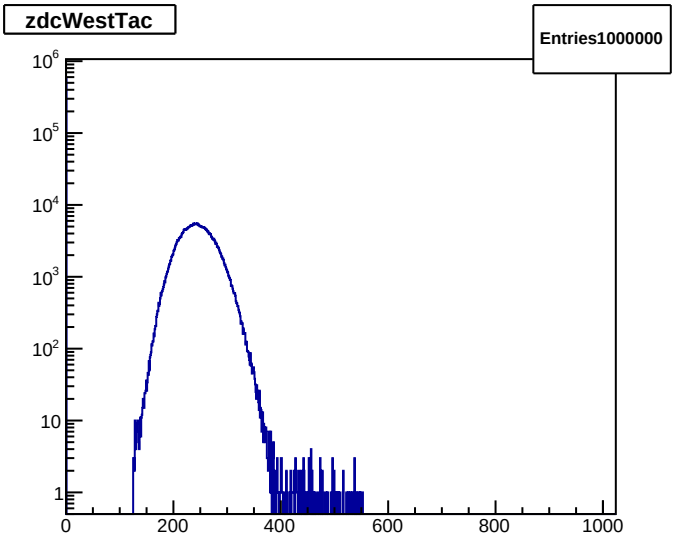

vpdWestNHitsVsSimu
Entries 0
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16

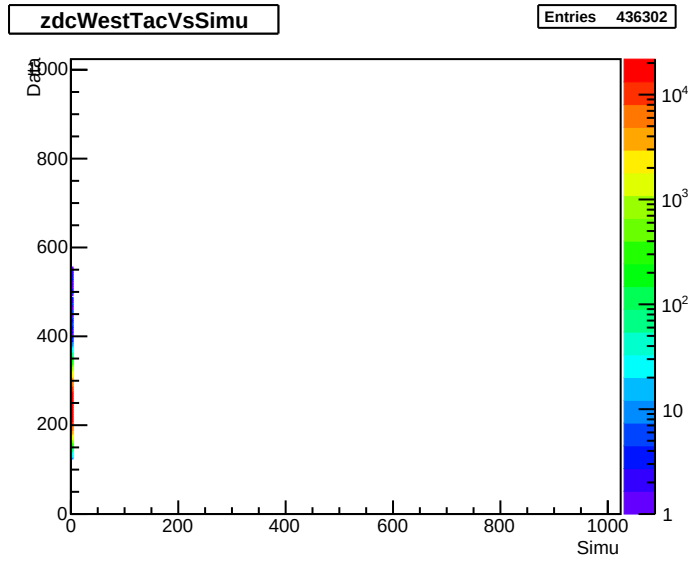

vpdWestTacSum
Entries1000000
10^6
10^5
10^4
10^3
0 10000 20000 30000 40000 50000 60000

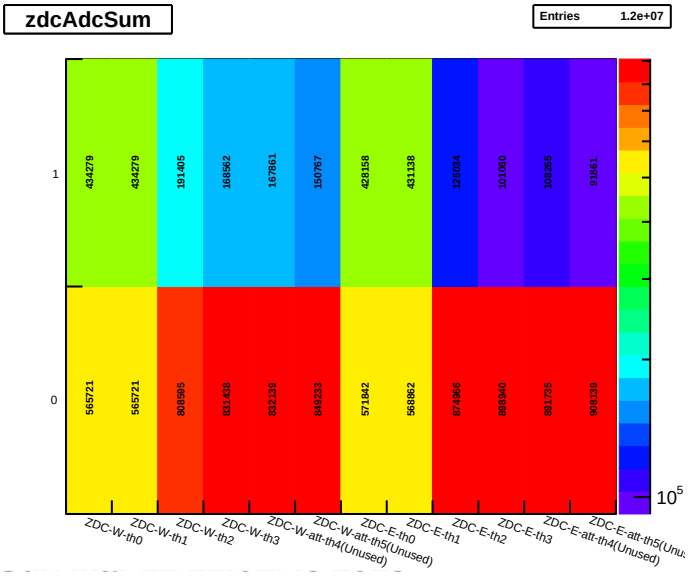

vpdWestTacSumVsSimu
Entries 0
Data
60000 50000 40000 30000 20000 10000 0
0 10000 20000 30000 40000 50000 60000
Simu

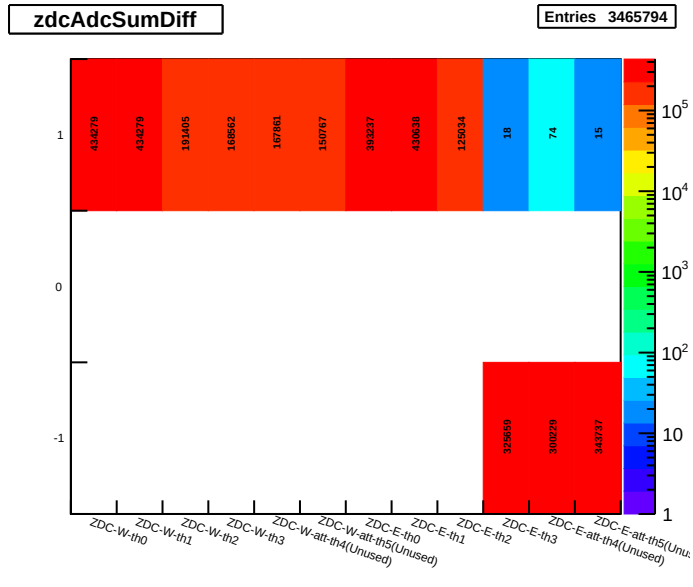

zdcEastTac
Entries1000000

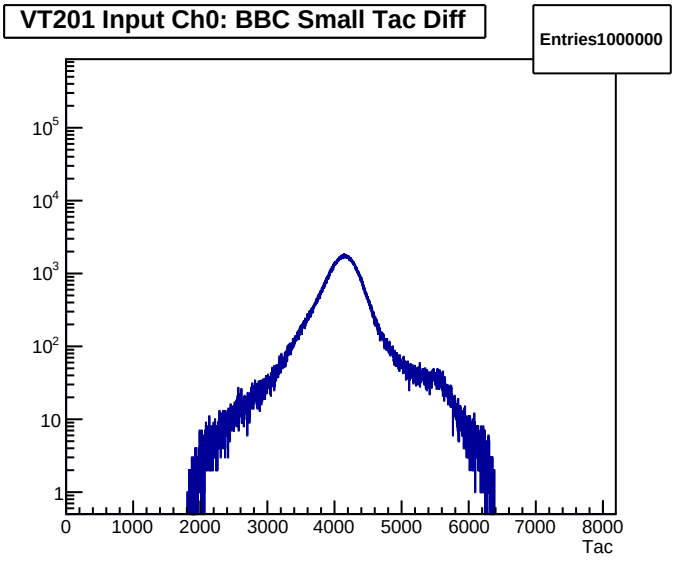

zdcEastTacVsSimu
Entries 1239
Data
Simu

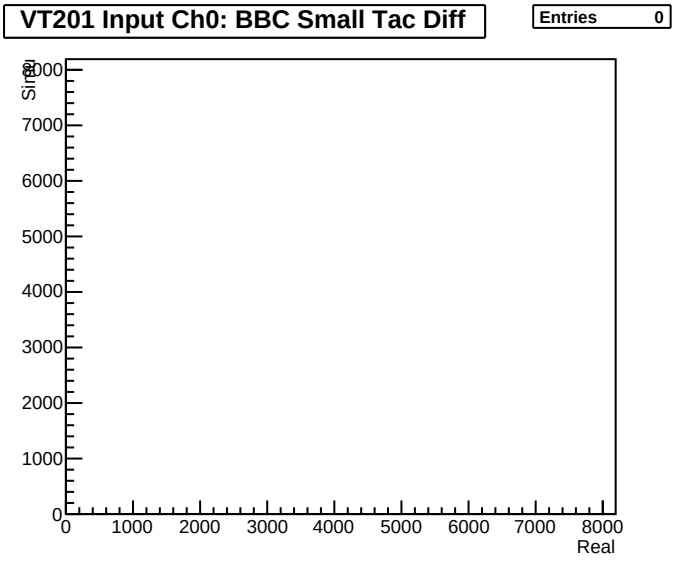

zdcWestTac
Entries1000000

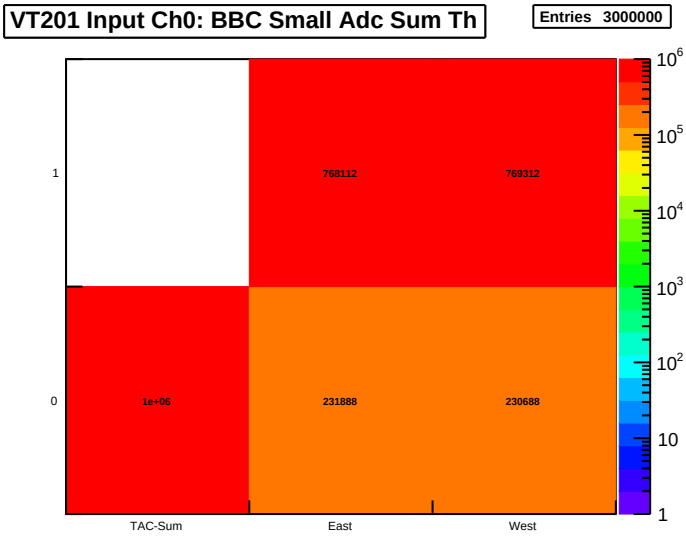

zdcWestTacVsSimu
Entries 436302
Data
Simu

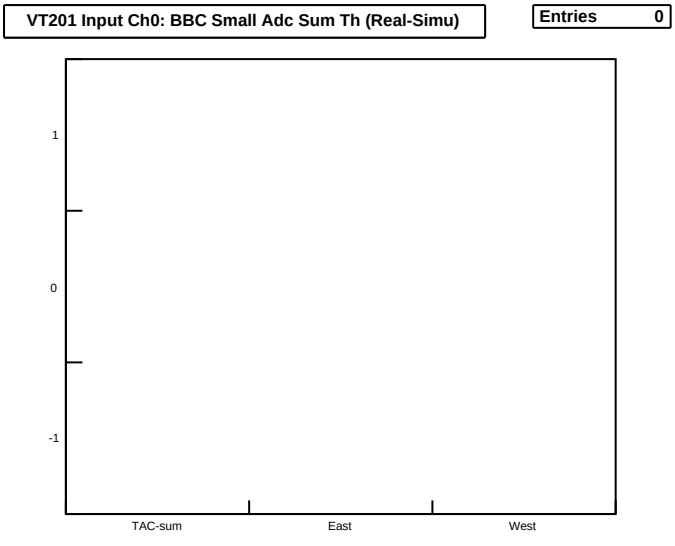

zdcAdcSum
Entries 1.2e+07
434279
434279
191405
168562
167861
150767
428158
431138
125034
101060
105285
91861
565721
565721
808595
831438
832139
849233
571842
568862
874066
888940
891735
908139
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

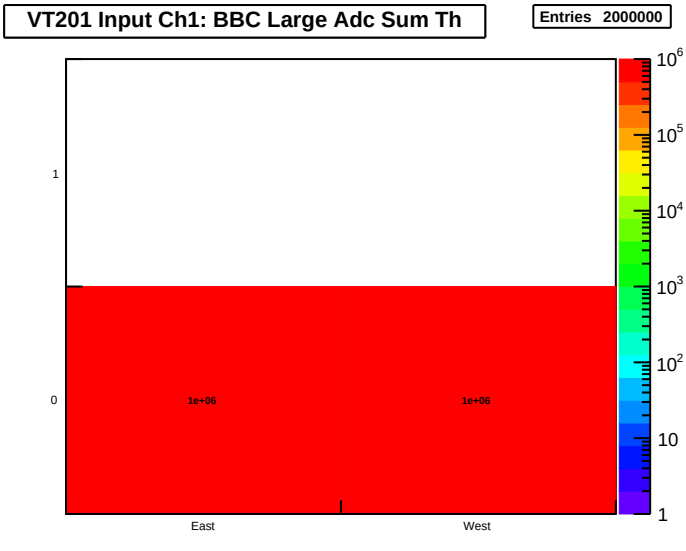

zdcAdcSumDiff
Entries 3465794
434279
434279
191405
168562
167861
150767
392237
430638
129034
18
74
15
325659
300229
343737
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

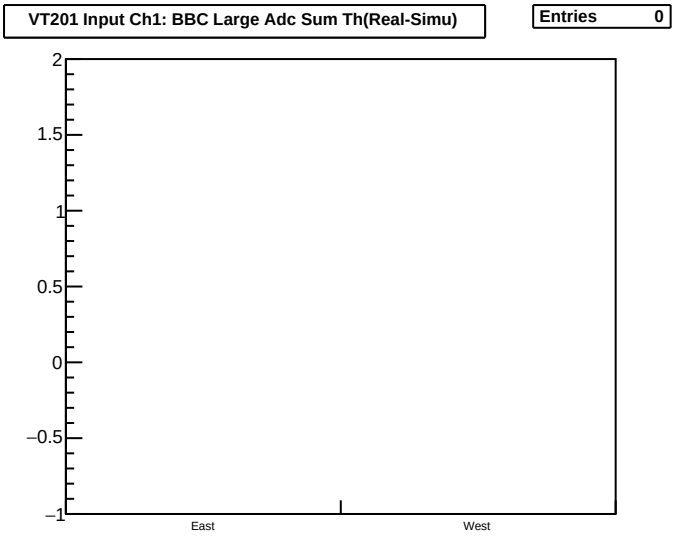

VT201 Input Ch0: BBC Small Tac Diff
Entries1000000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

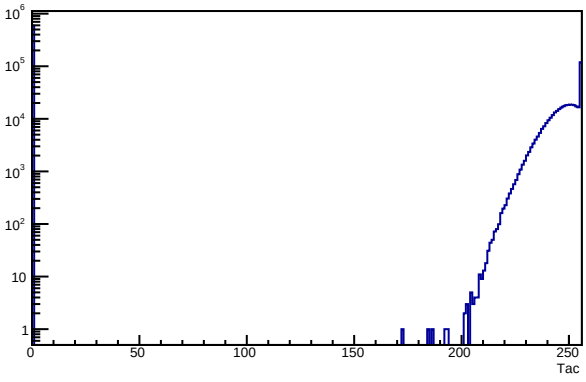

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 3000000
768112
769312
1e+06
231888
230688
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

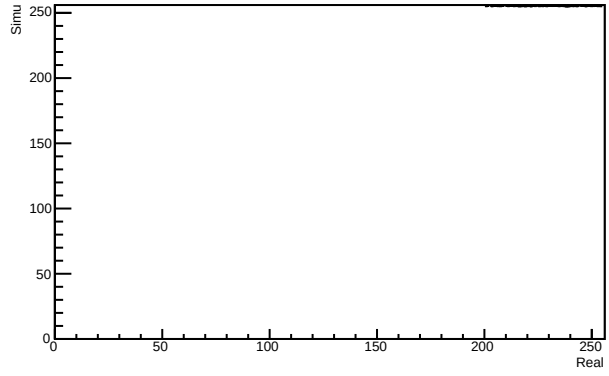

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 2000000
1e+06
1e+06
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

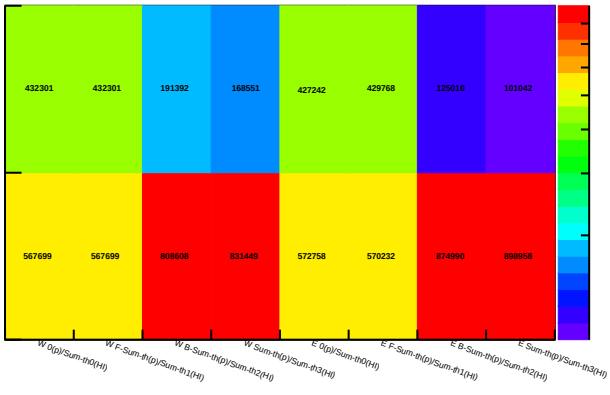
VT201 Input Ch2: ZDC Tac Diff



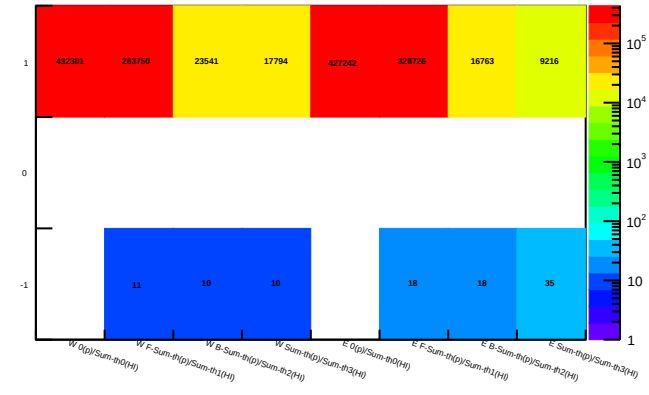
VT201 Input Ch2: ZDC Tac Diff



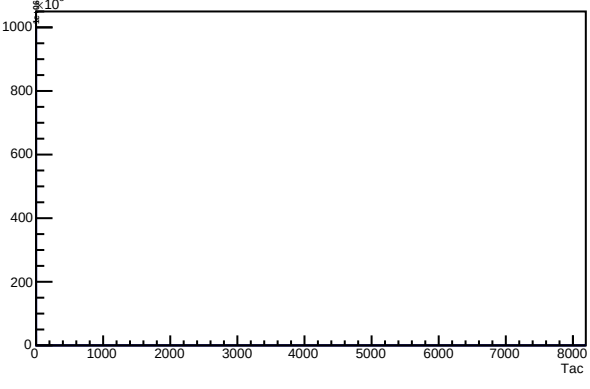
VT201 Input Ch2: ZDC Adc Sum Th



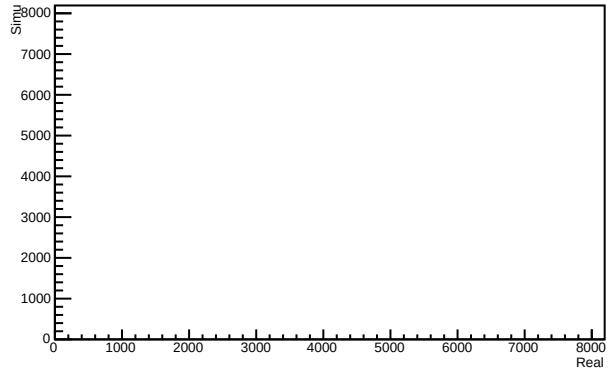
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



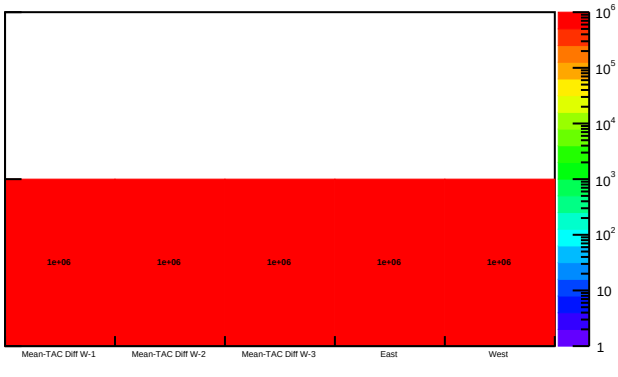
VT201 Input Ch4: VPD Tac Diff



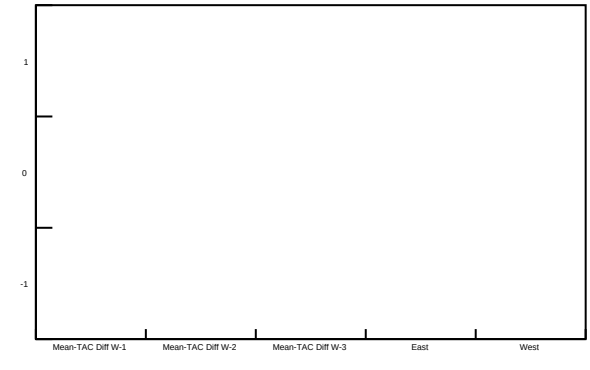
VT201 Input Ch4: VPD Tac Diff

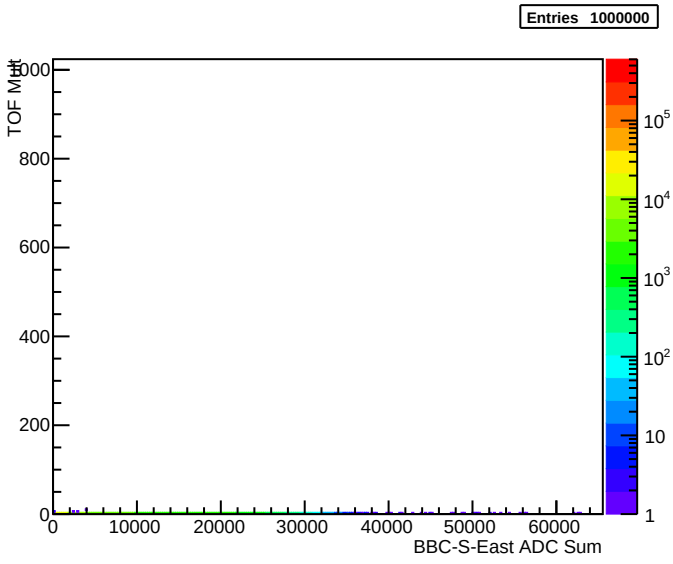


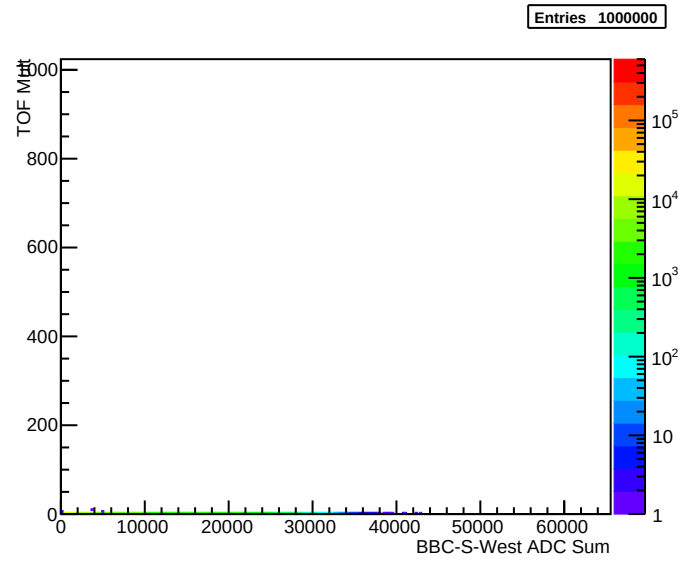
VT201 Input Ch4: VPD Adc Sum Th

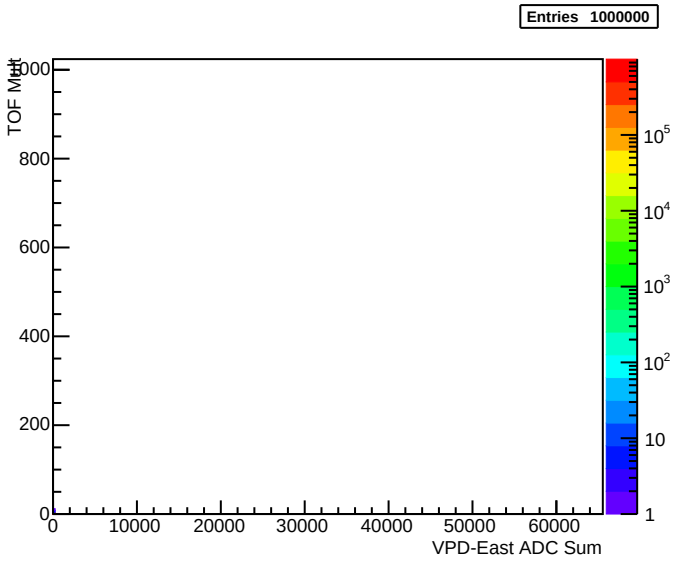


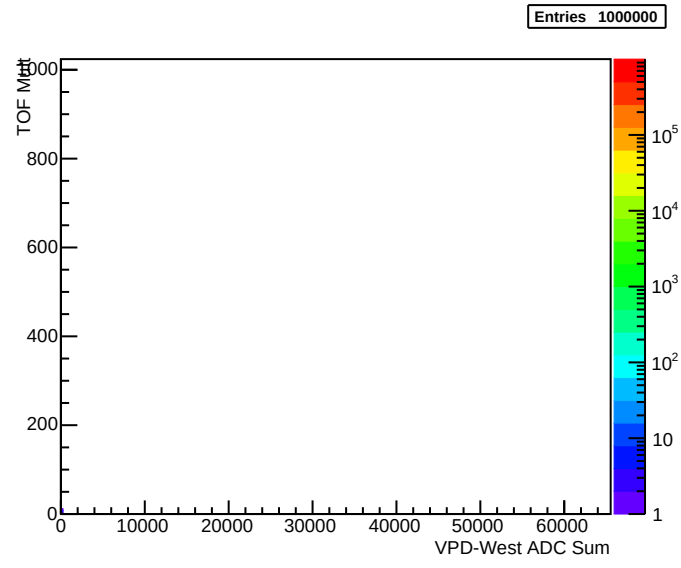
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

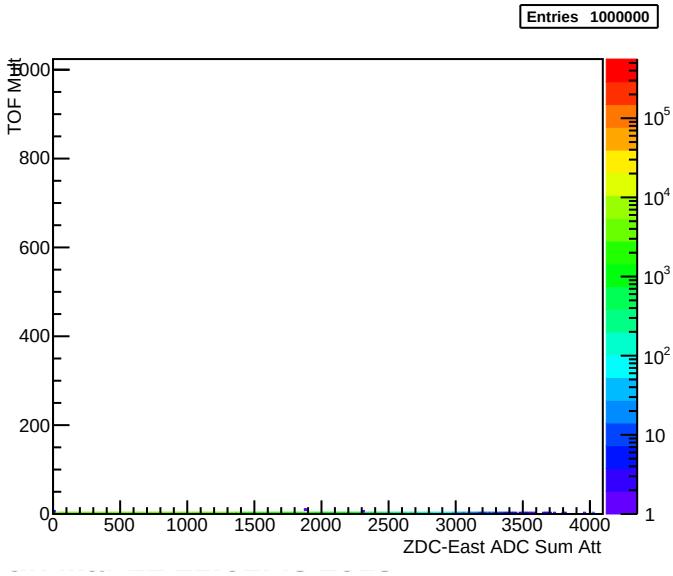


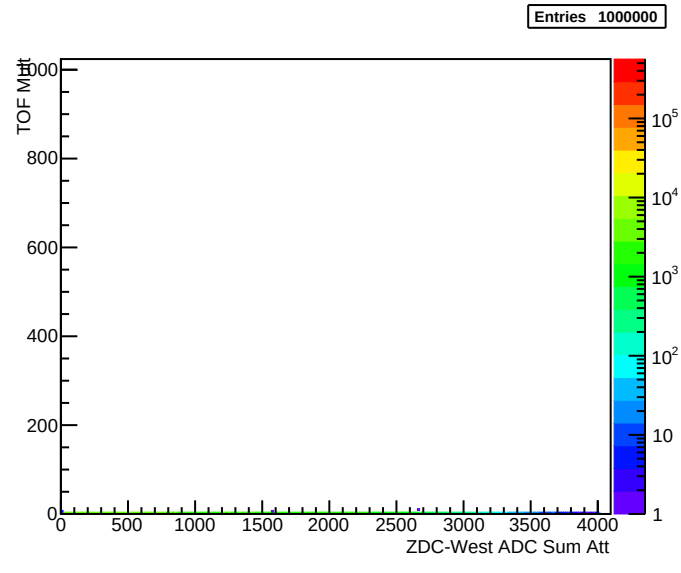

Entries 1000000
TOF Mult
BBC-S-East ADC Sum

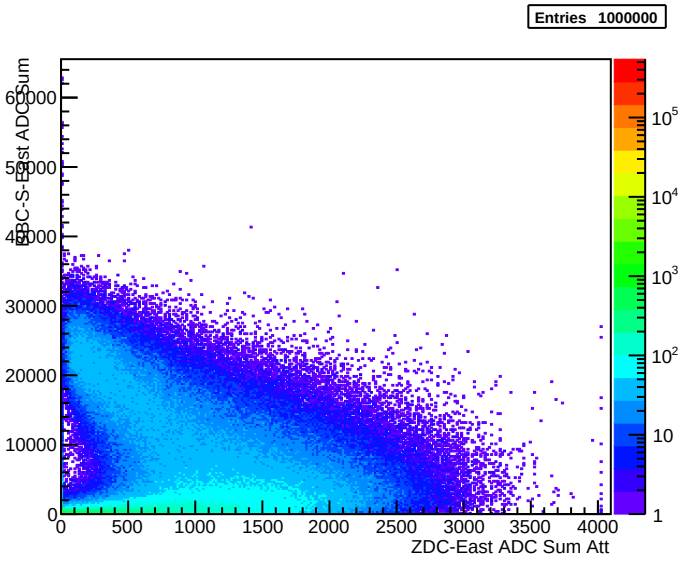

Entries 1000000
TOF Mult
BBC-S-West ADC Sum

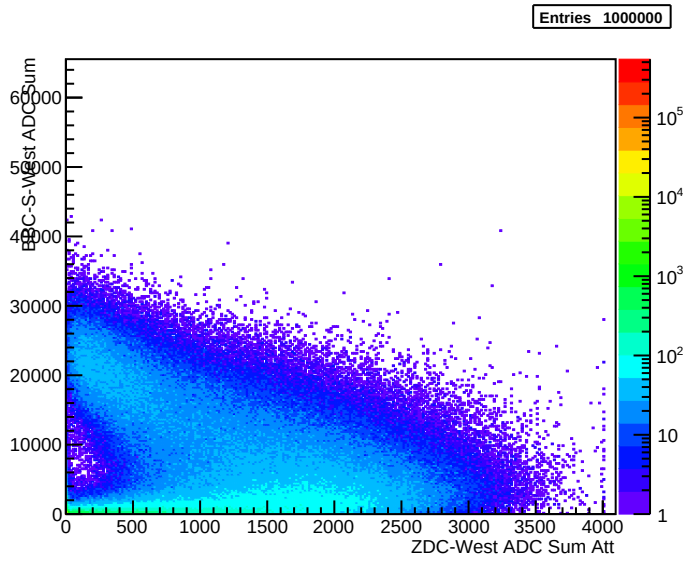

Entries 1000000
TOF Mult
VPD-East ADC Sum

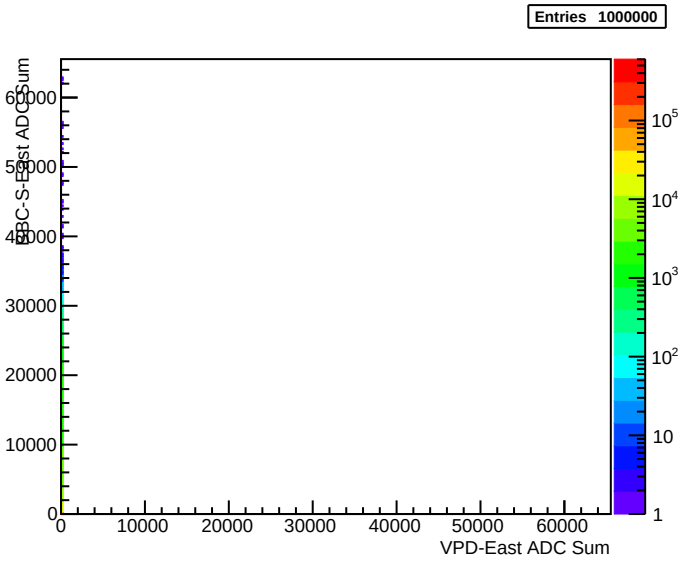

Entries 1000000
TOF Mult
VPD-West ADC Sum

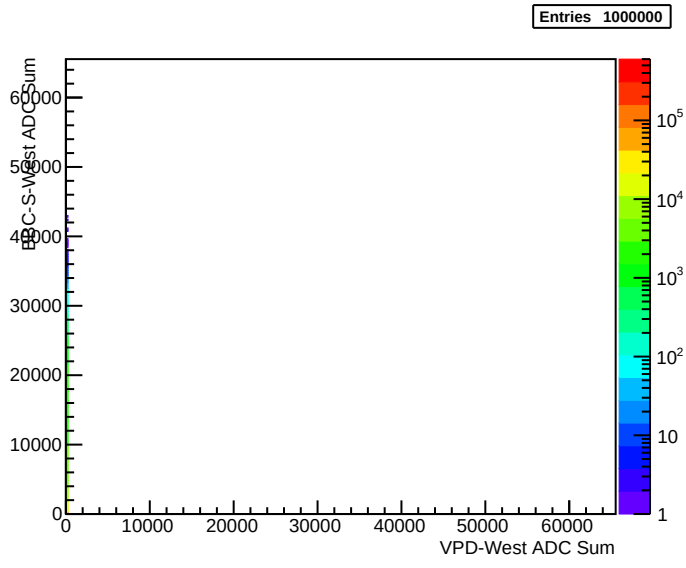

Entries 1000000
TOF Mult
ZDC-East ADC Sum Att

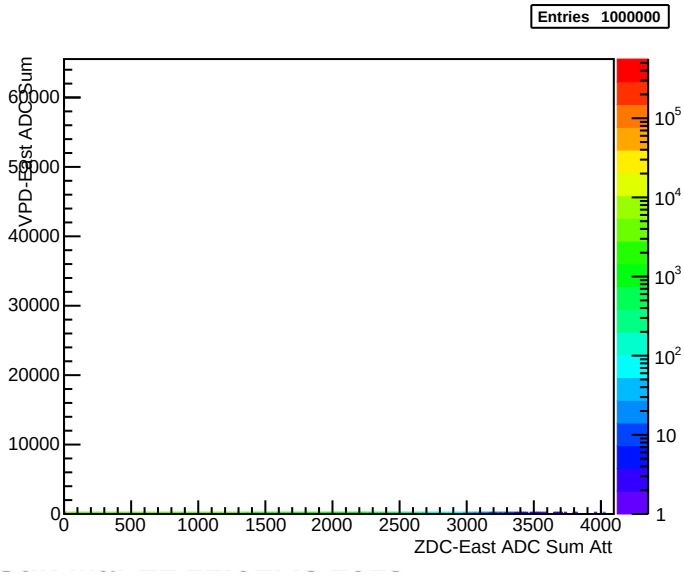

Entries 1000000
TOF Mult
ZDC-West ADC Sum Att

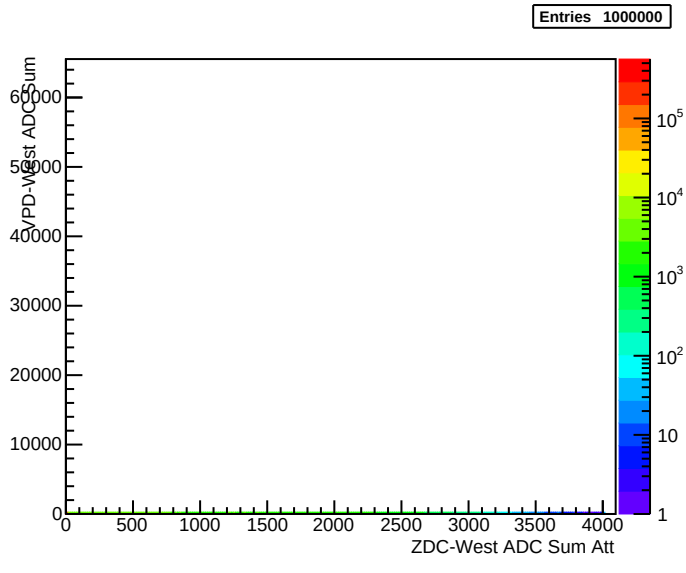

Entries 1000000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

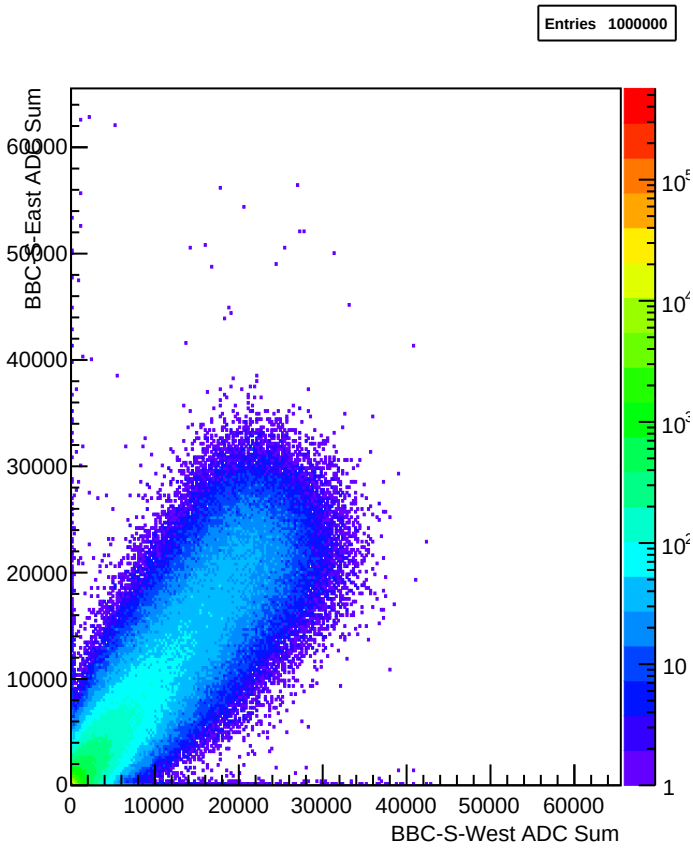

Entries 1000000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

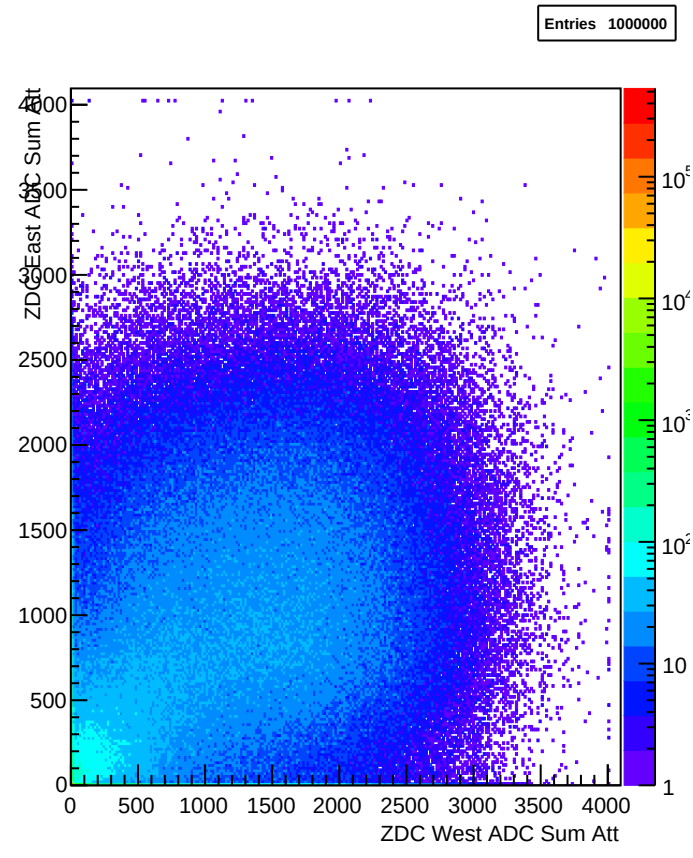

Entries 1000000
BBC-S-East ADC Sum
VPD-East ADC Sum

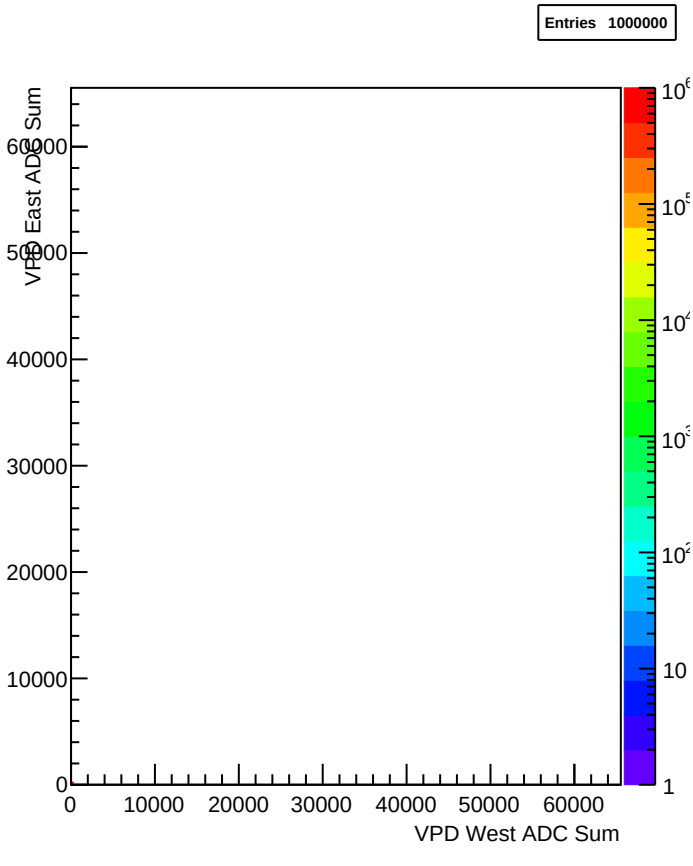

Entries 1000000
BBC-S-West ADC Sum
VPD-West ADC Sum

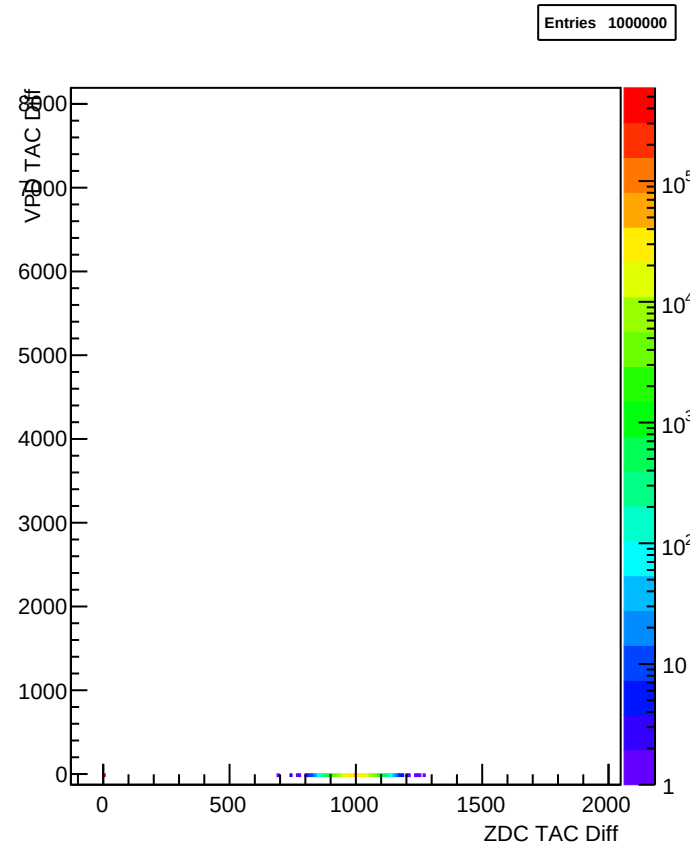

Entries 1000000
VPD-East ADC Sum
ZDC-East ADC Sum Att

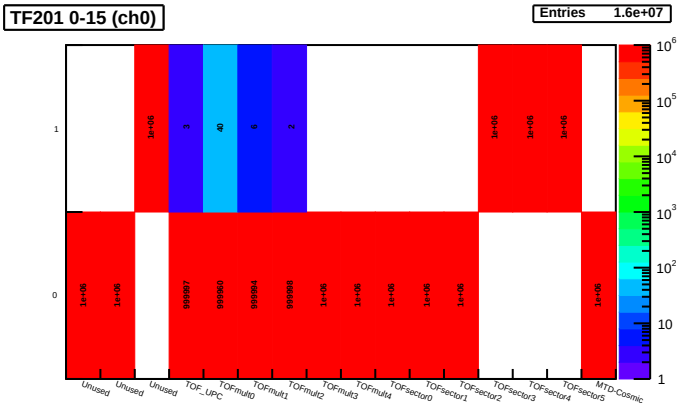

Entries 1000000
VPD-West ADC Sum
ZDC-West ADC Sum Att

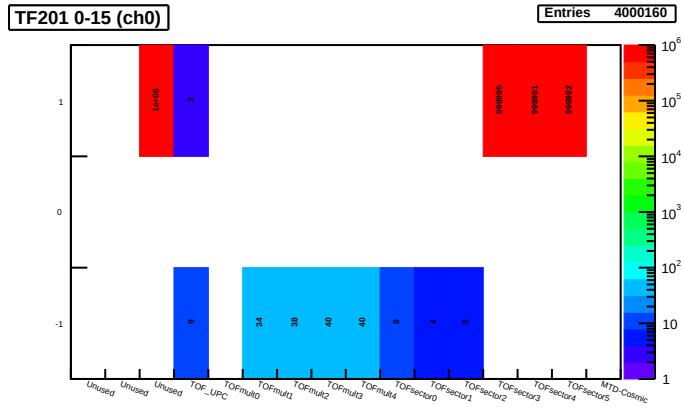

Entries 1000000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

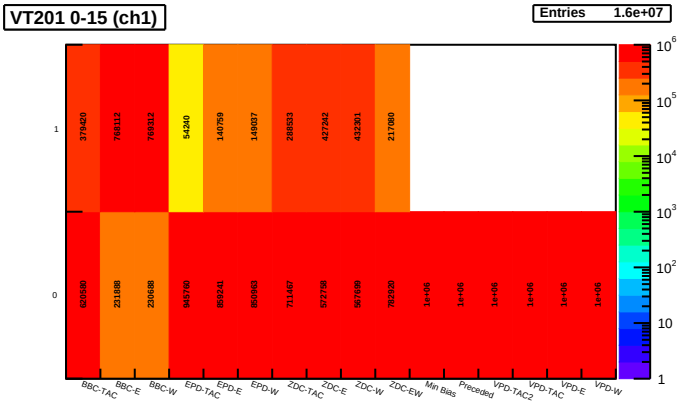

Entries 1000000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

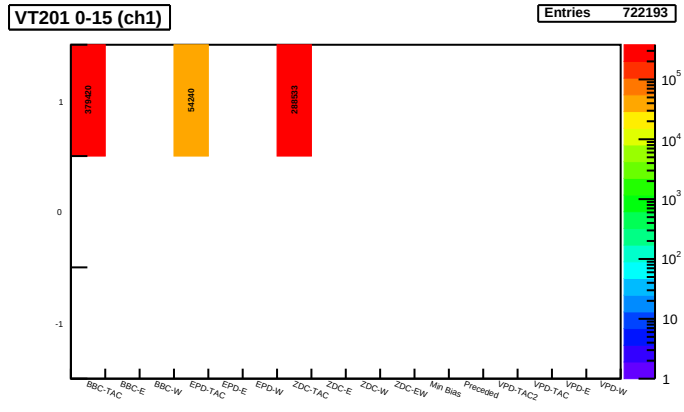

Entries 1000000
VPD East ADC Sum
VPD West ADC Sum

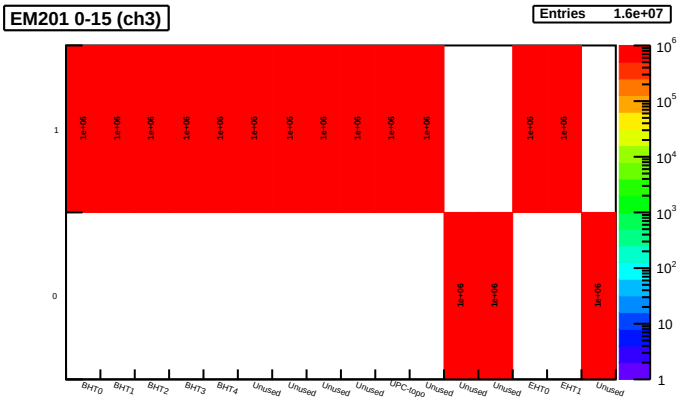

Entries 1000000
VPD TAC Diff
ZDC TAC Diff

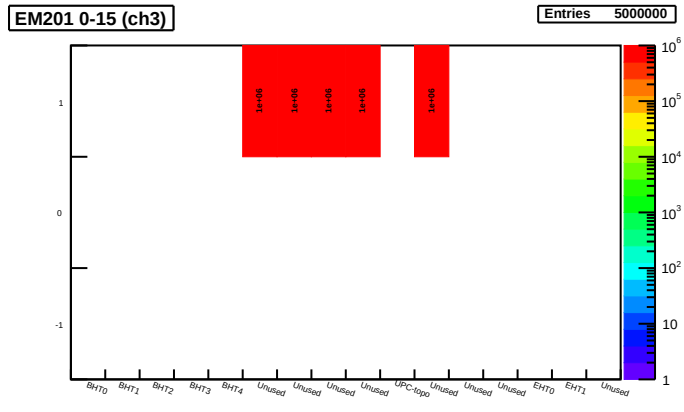

TF201 0-15 (ch0)
Entries 1.6e+07
Unused
Unused
Unused
TOF_UPC
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5
MTD-Cosmic

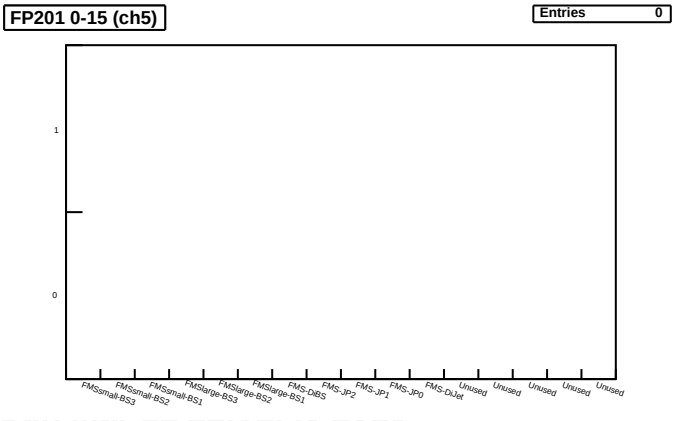

TF201 0-15 (ch0)
Entries 4000160
Unused
Unused
Unused
TOF_UPC
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5
MTD-Cosmic

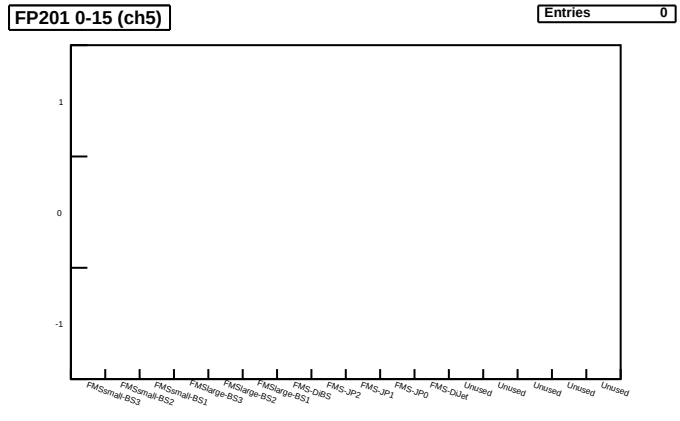

VT201 0-15 (ch1)
Entries 1.6e+07
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-EW
Min Bias
Preceded
VPD-TAC2
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 722193
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-EW
Min Bias
Preceded
VPD-TAC2
VPD-TAC
VPD-E
VPD-W


EM201 0-15 (ch3)
Entries 1.6e+07
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
Unused
Unused
Unused
UPC-topo
Unused
Unused
Unused
EHT0
EHT1
Unused


EM201 0-15 (ch3)
Entries 5000000
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
Unused
Unused
Unused
UPC-topo
Unused
Unused
Unused
EHT0
EHT1
Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused
Unused
Unused
Unused
Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused
Unused
Unused
Unused
Unused