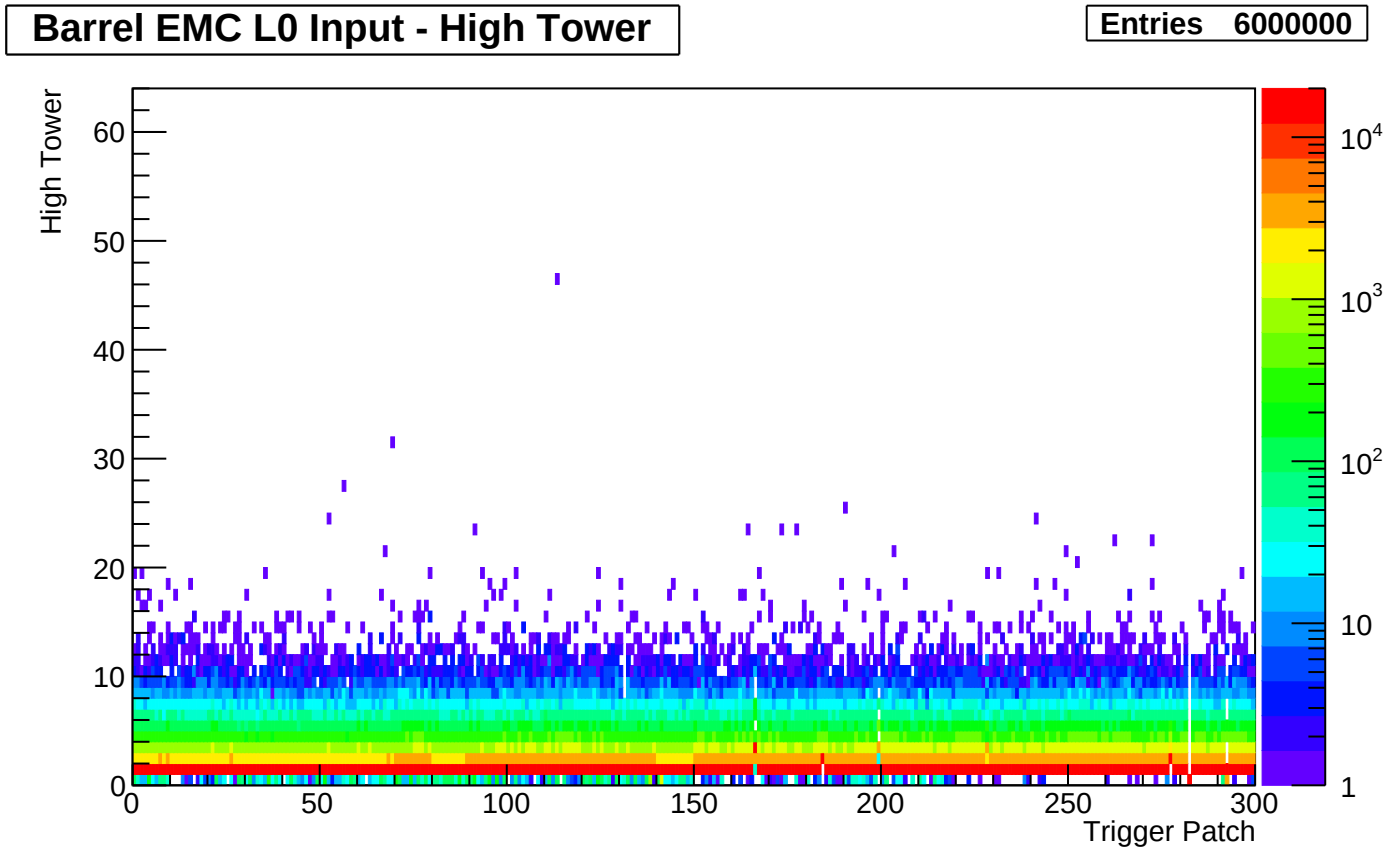
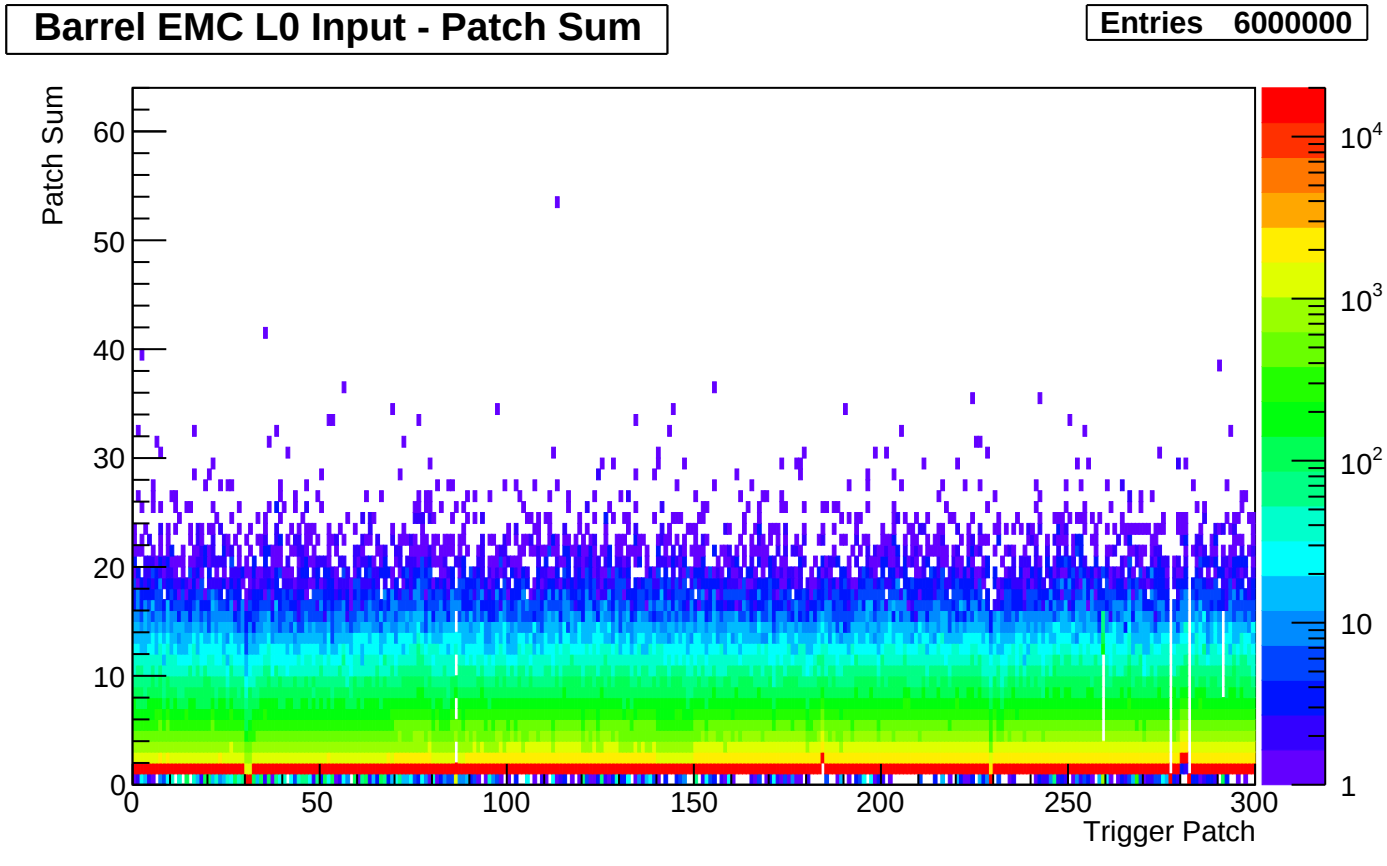
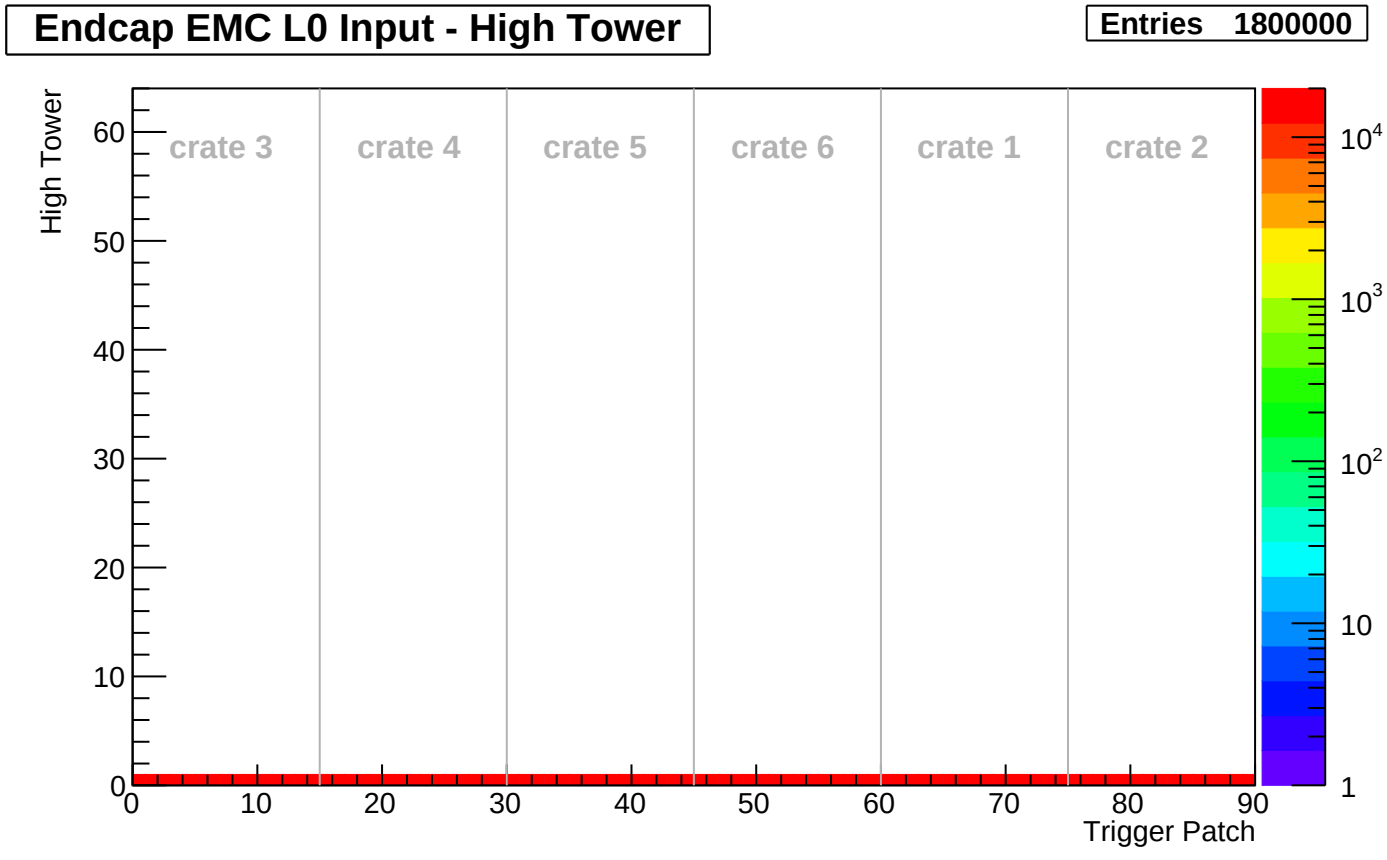
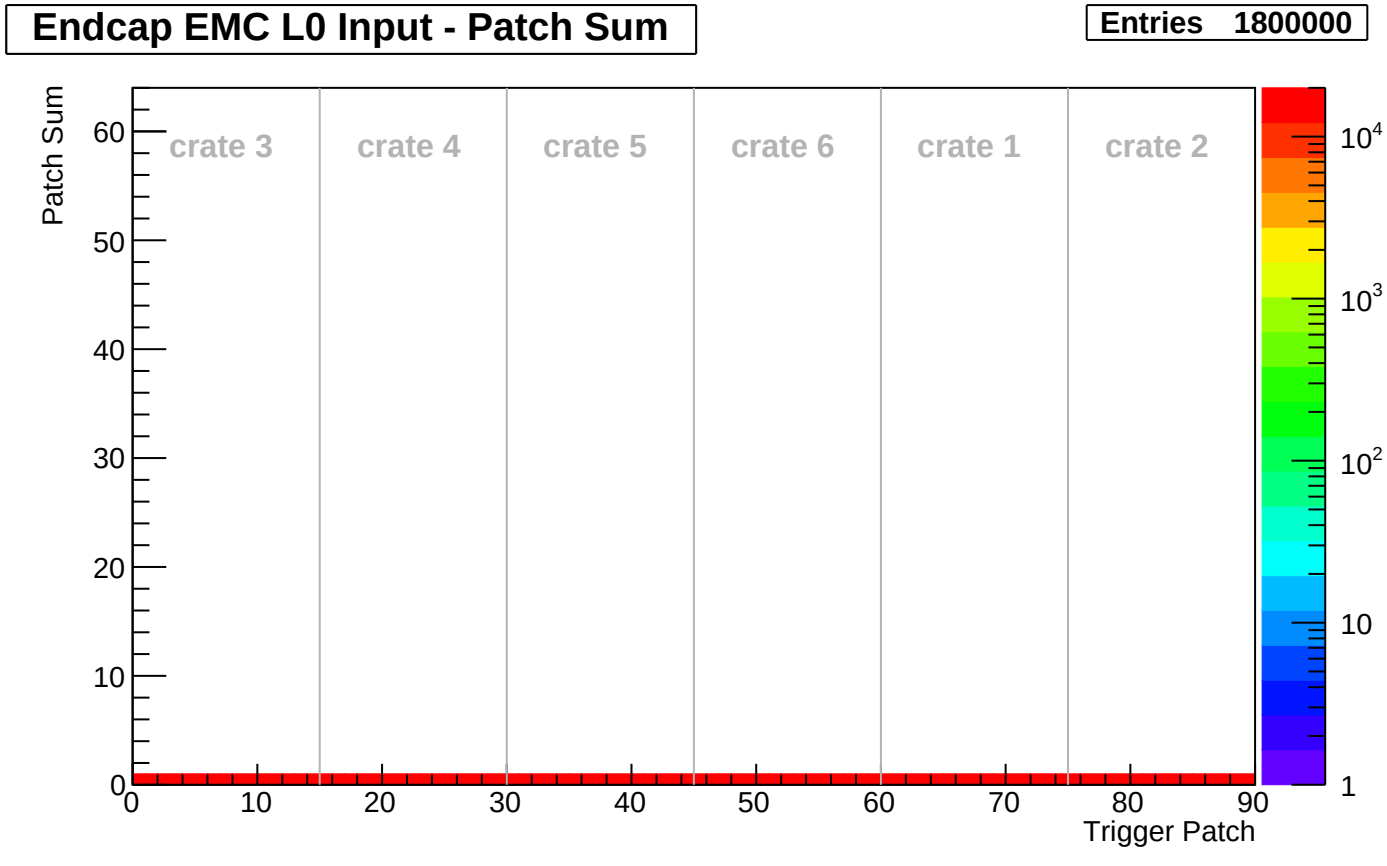
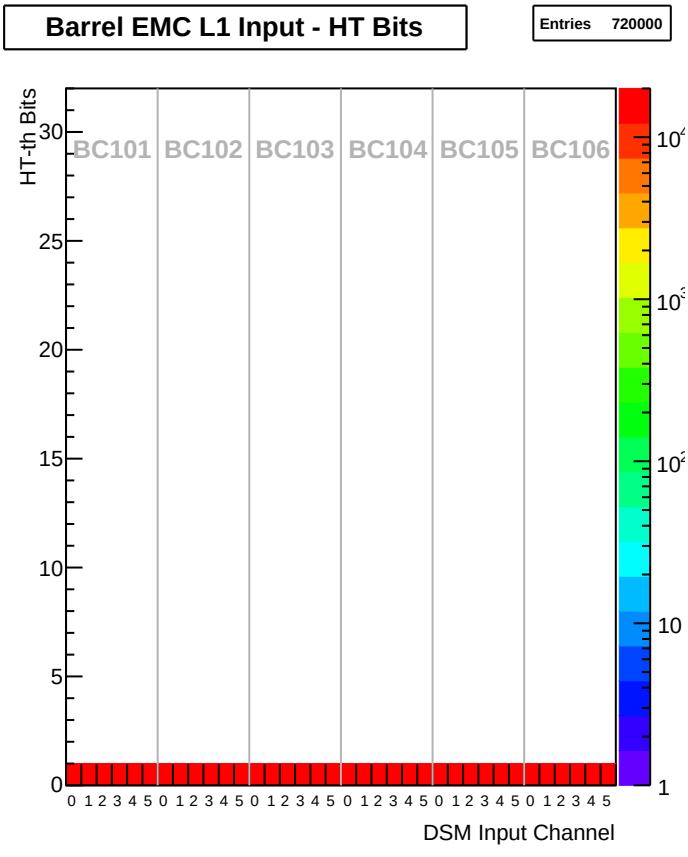
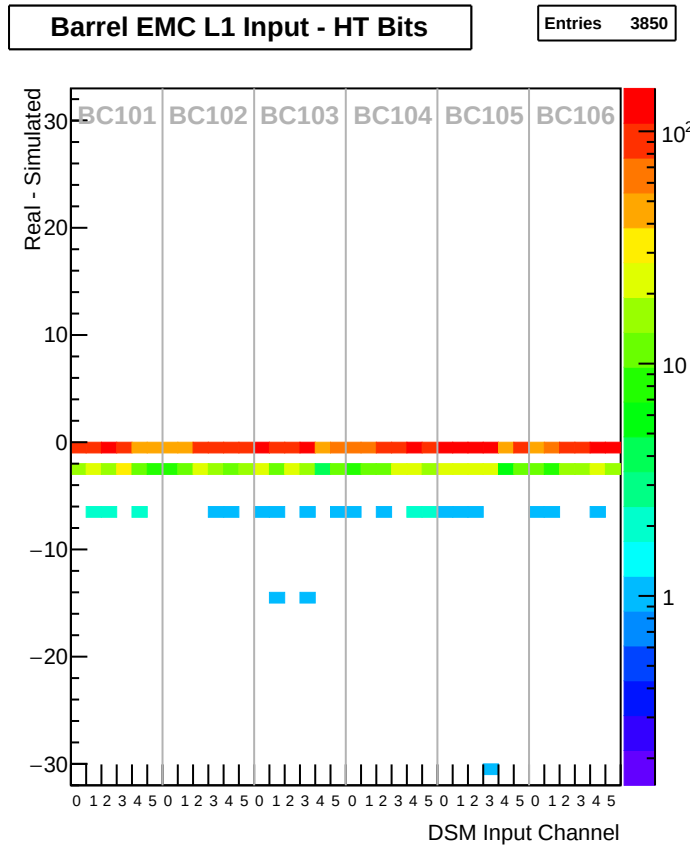

Barrel EMC L0 Input - High Tower
Entries 6000000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

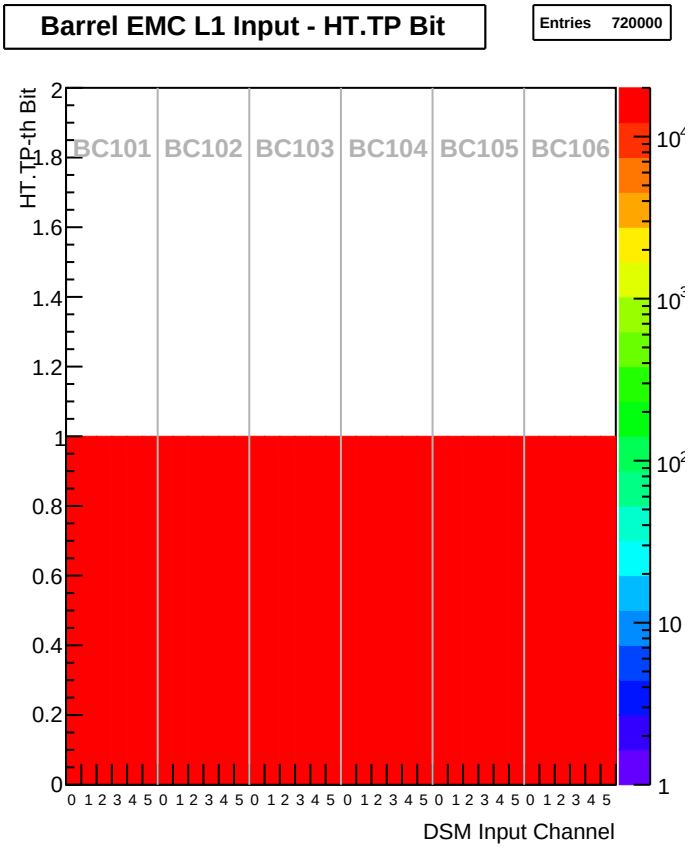

Barrel EMC L0 Input - Patch Sum
Entries 6000000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

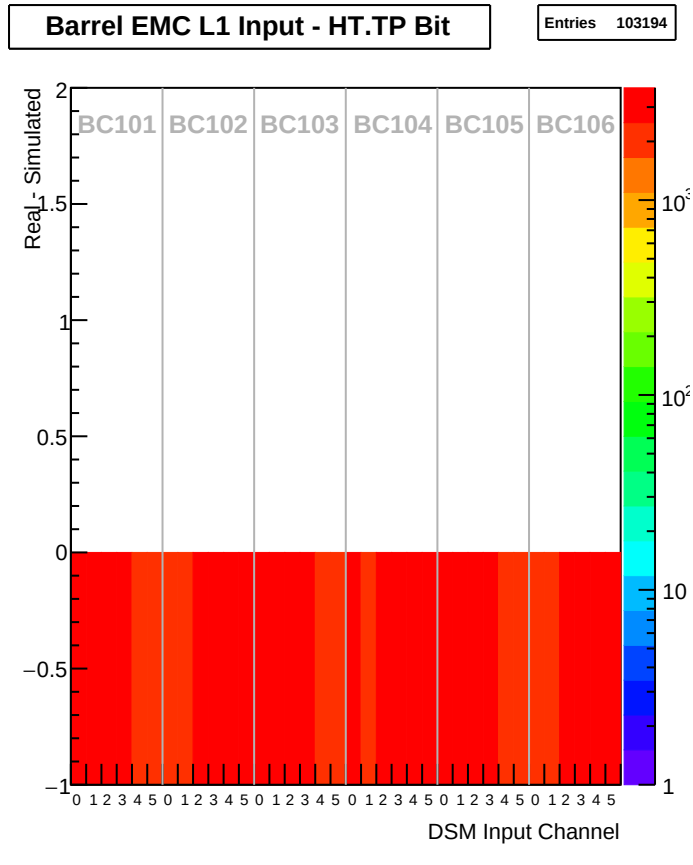

Endcap EMC L0 Input - High Tower
Entries 1800000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

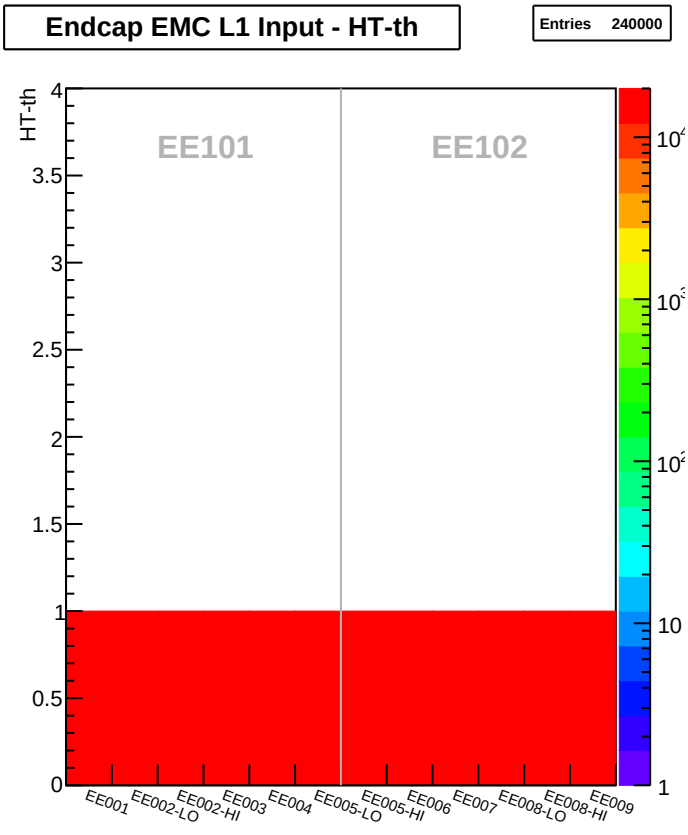

Endcap EMC L0 Input - Patch Sum
Entries 1800000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

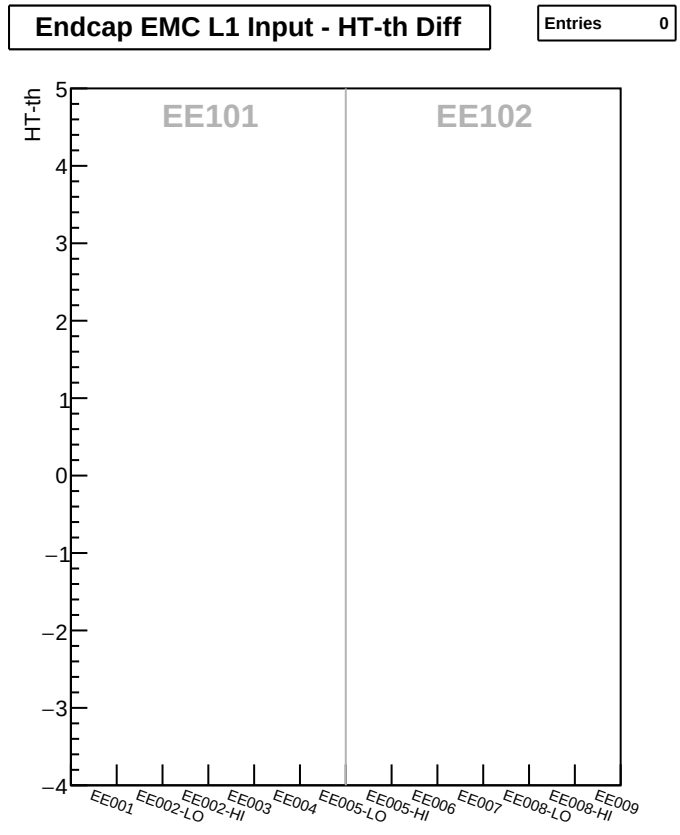

Barrel EMC L1 Input - HT Bits
Entries 720000
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

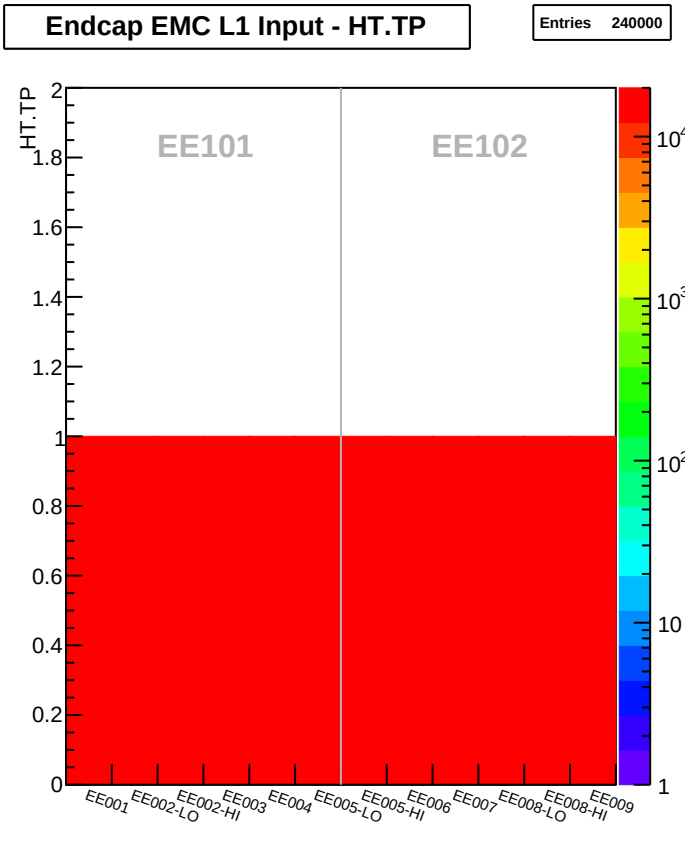

Barrel EMC L1 Input - HT Bits
Entries 3850
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

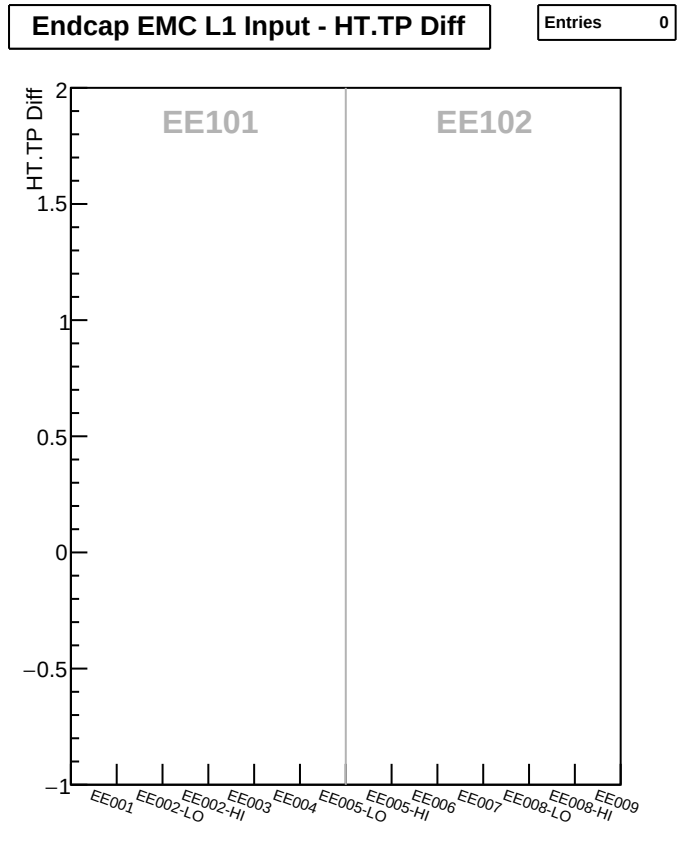

Barrel EMC L1 Input - HT.TP Bit
Entries 720000
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

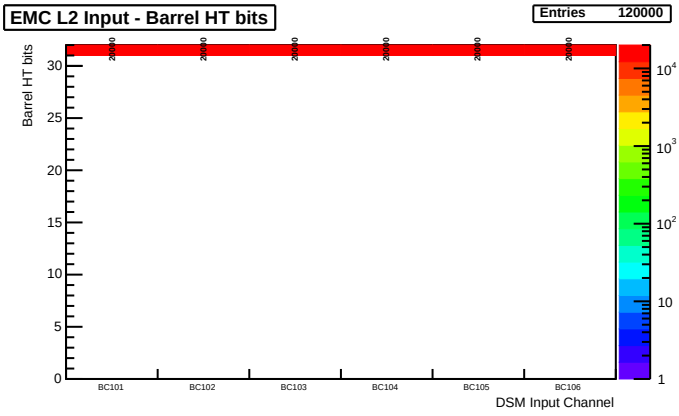

Barrel EMC L1 Input - HT.TP Bit
Entries 103194
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

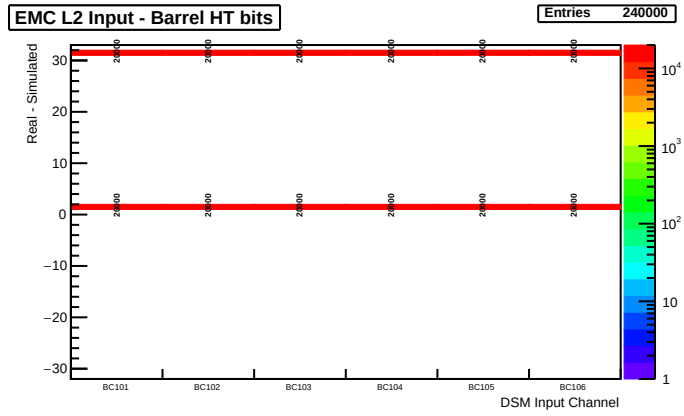

Endcap EMC L1 Input - HT-th
Entries 240000
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

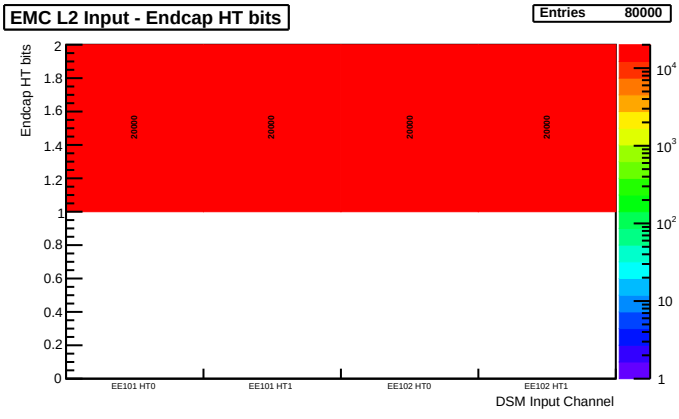

Endcap EMC L1 Input - HT-th Diff
Entries 0
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

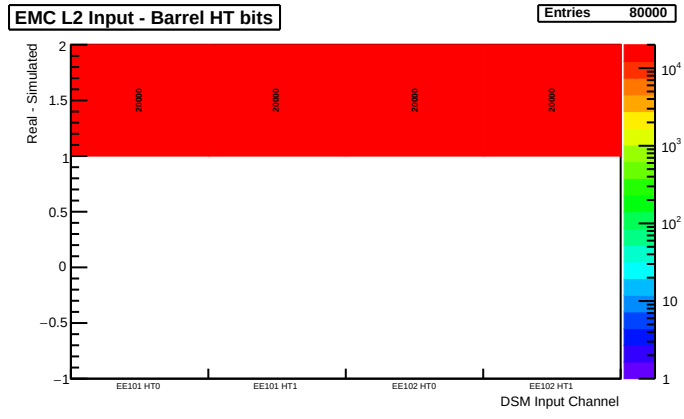

Endcap EMC L1 Input - HT.TP
Entries 240000
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

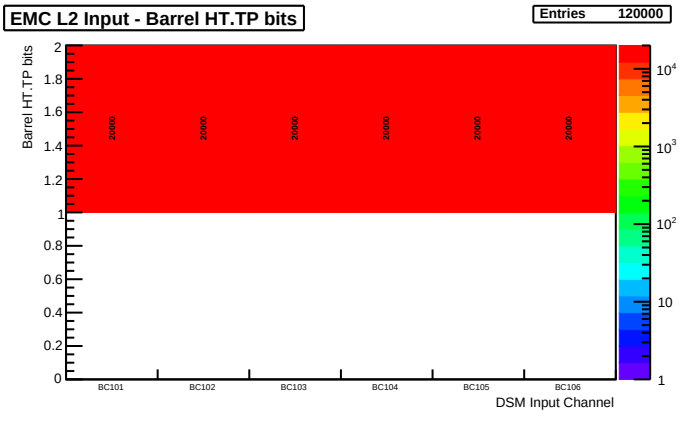

Endcap EMC L1 Input - HT.TP Diff
Entries 0
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

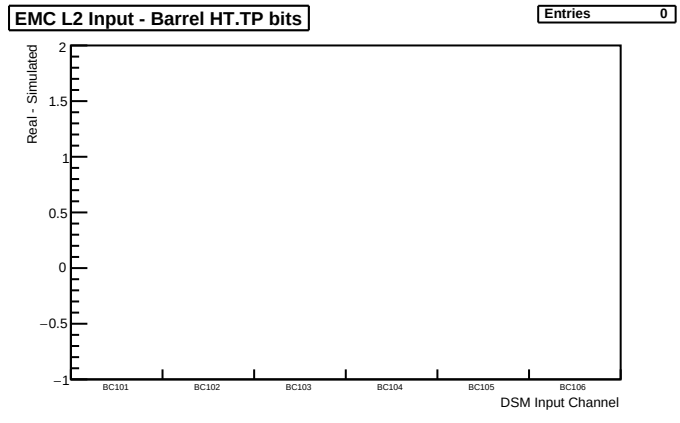

EMC L2 Input - Barrel HT bits
Entries 120000
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

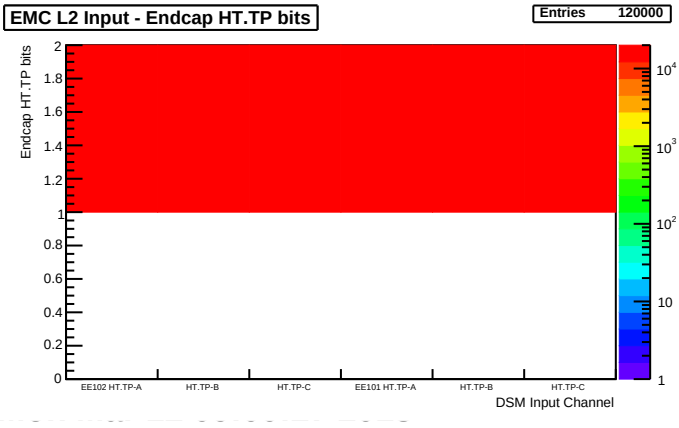

EMC L2 Input - Barrel HT bits
Entries 240000
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

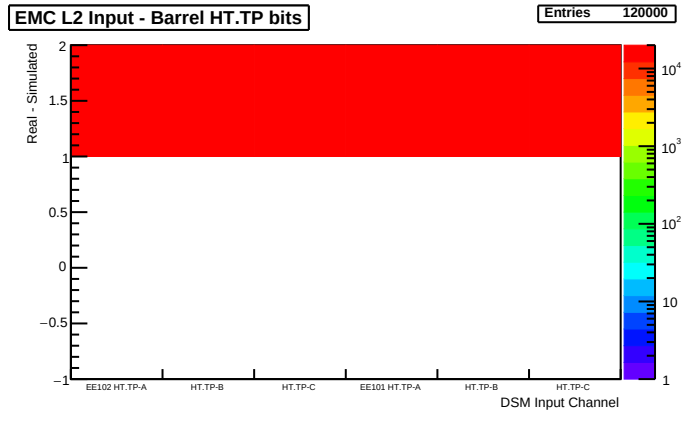

EMC L2 Input - Endcap HT bits
Entries 80000
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

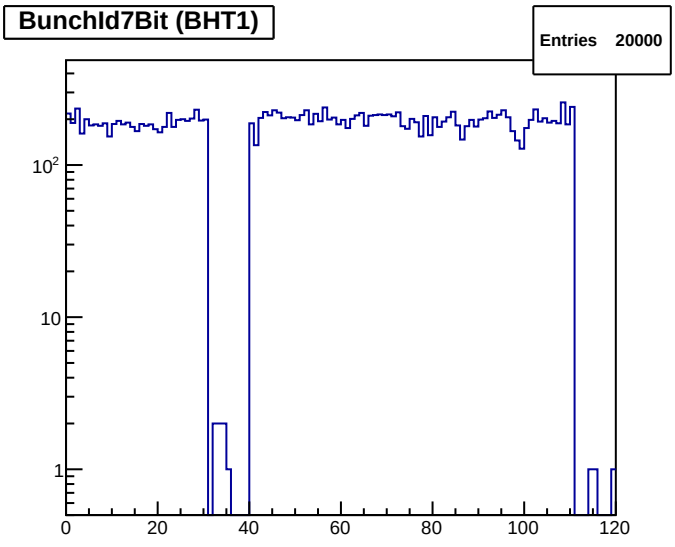

EMC L2 Input - Barrel HT bits
Entries 80000
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

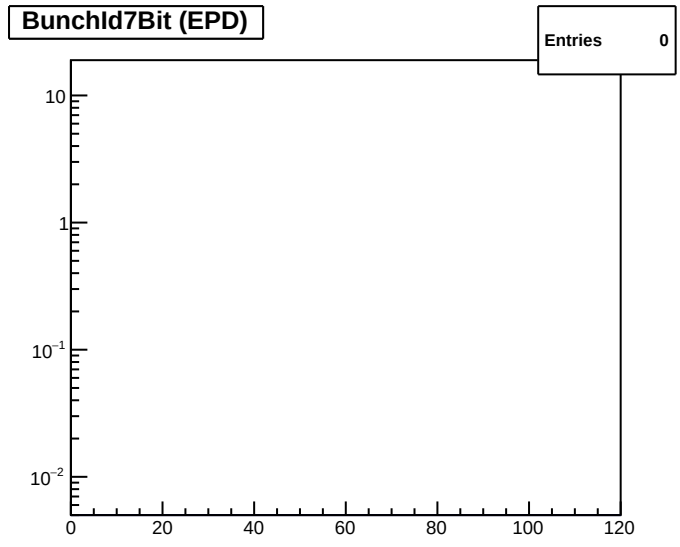

EMC L2 Input - Barrel HT.TP bits
Entries 120000
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

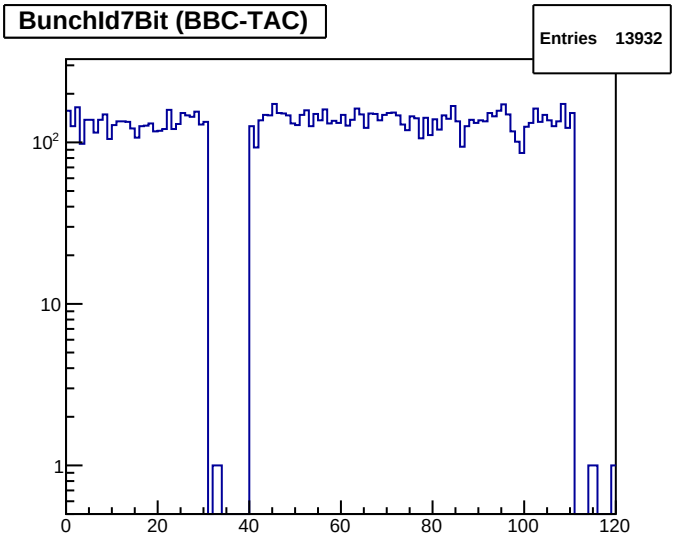

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

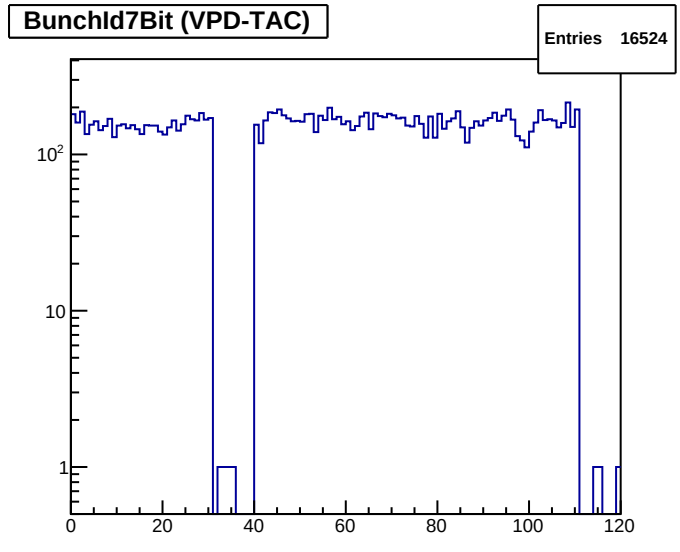

EMC L2 Input - Endcap HT.TP bits
Entries 120000
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

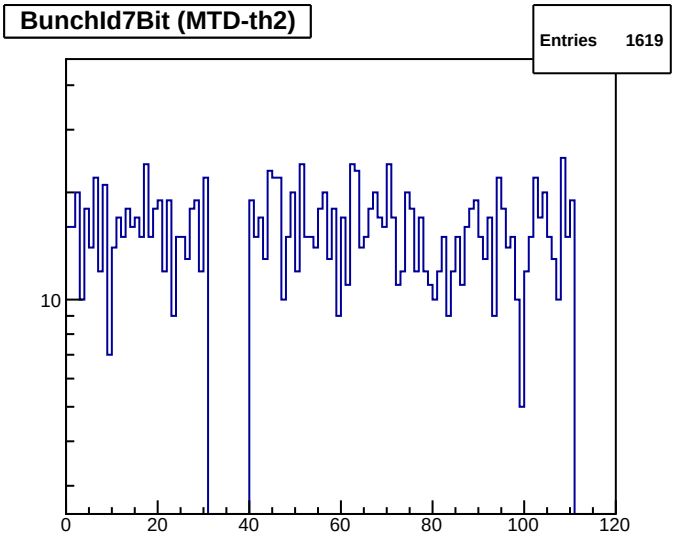

EMC L2 Input - Barrel HT.TP bits
Entries 120000
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

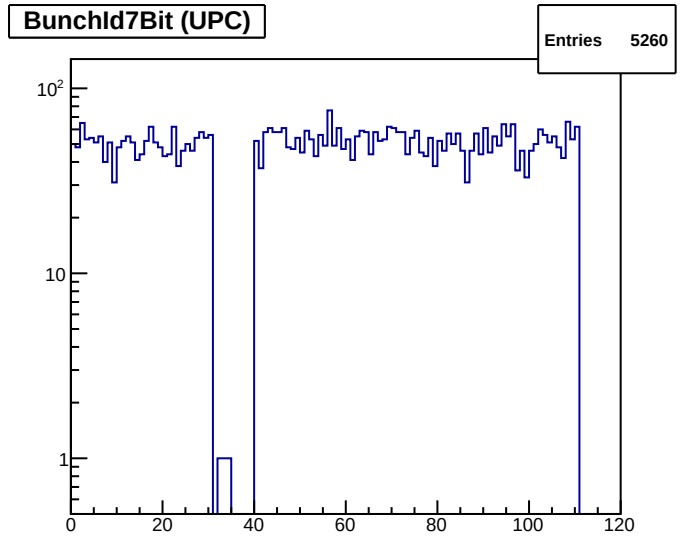

BunchId7Bit (BHT1)
Entries 20000

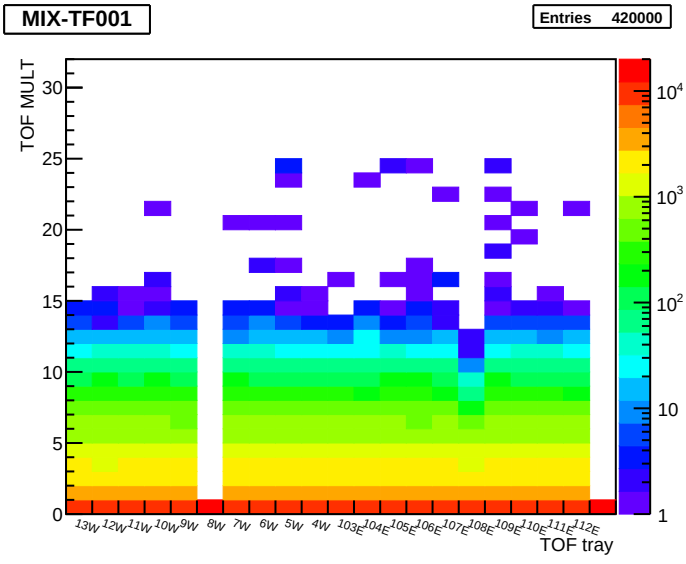

BunchId7Bit (EPD)
Entries 0

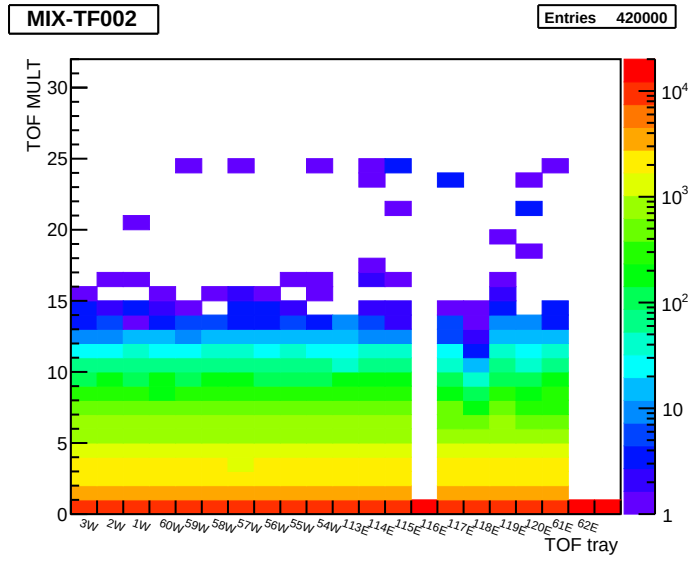

BunchId7Bit (BBC-TAC)
Entries 13932

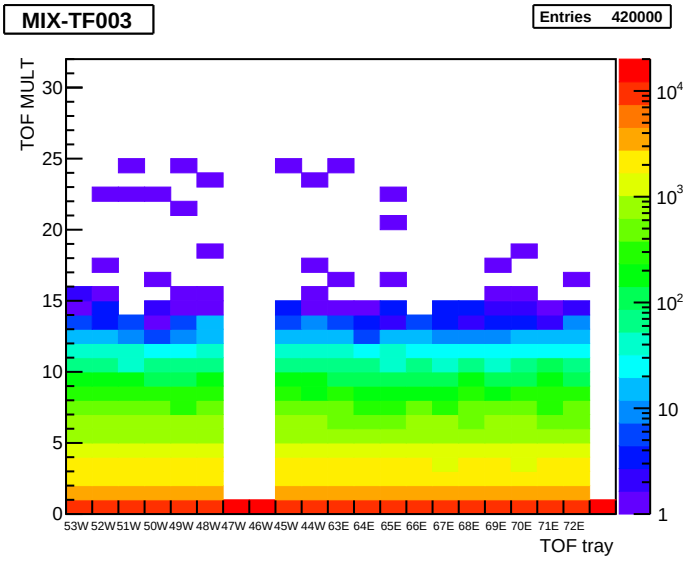

BunchId7Bit (VPD-TAC)
Entries 16524

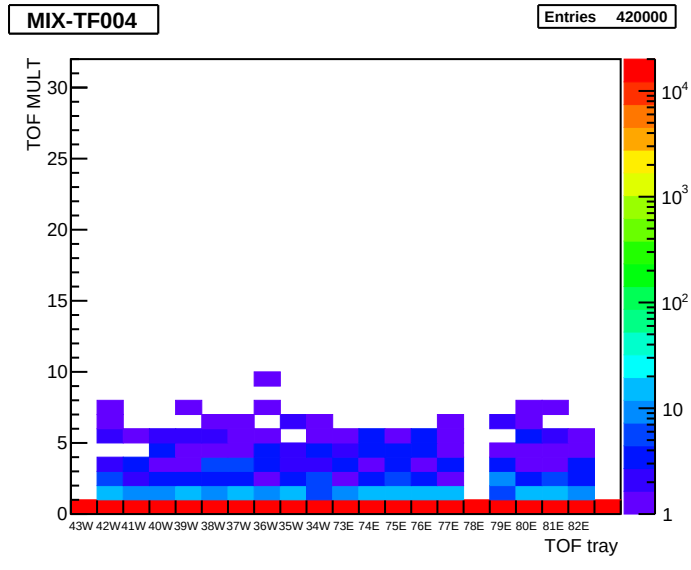

BunchId7Bit (MTD-th2)
Entries 1619

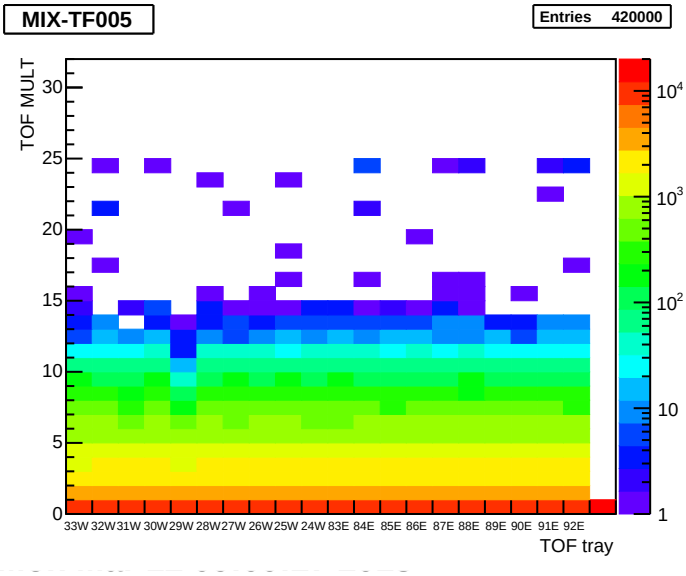

BunchId7Bit (UPC)
Entries 5260

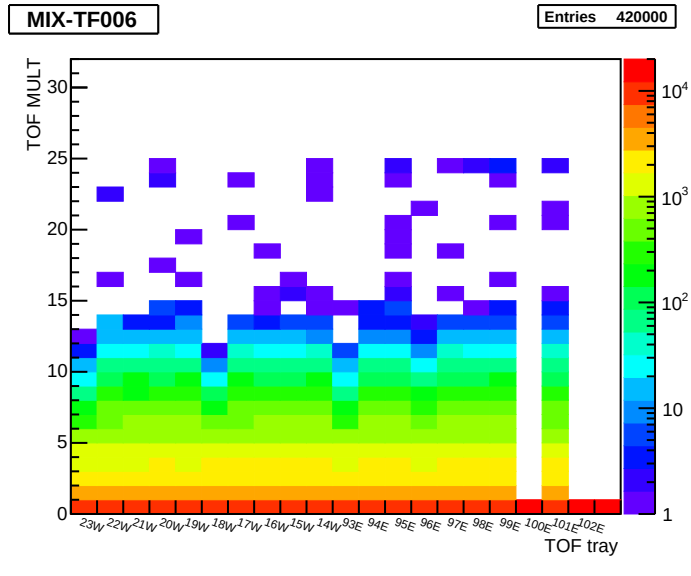

MIX-TF001
Entries 420000
TOF MULT
TOF tray

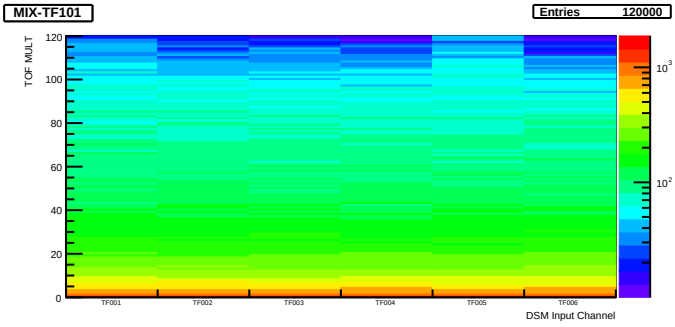

MIX-TF002
Entries 420000
TOF MULT
TOF tray

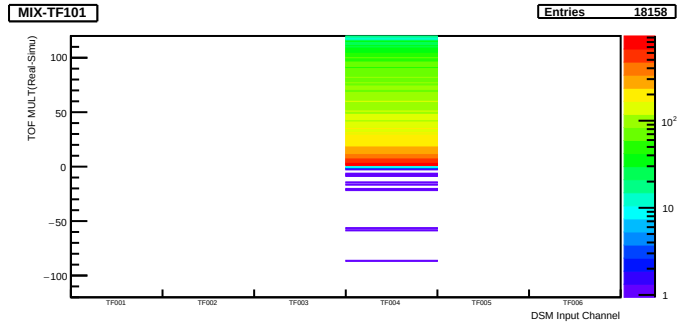

MIX-TF003
Entries 420000
TOF MULT
TOF tray

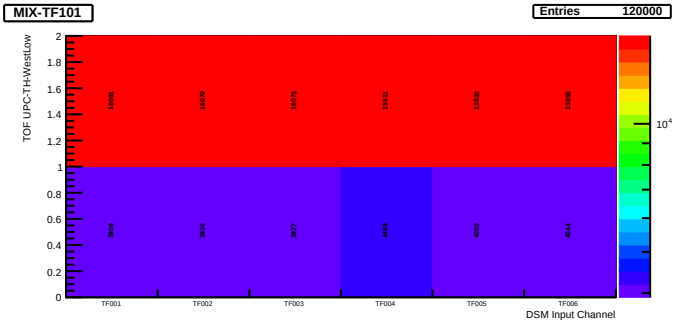

MIX-TF004
Entries 420000
TOF MULT
TOF tray

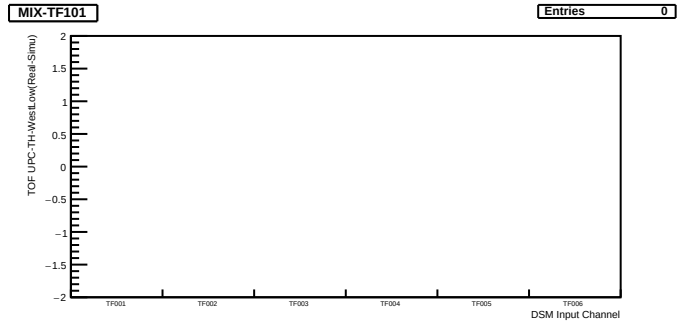

MIX-TF005
Entries 420000
TOF MULT
TOF tray

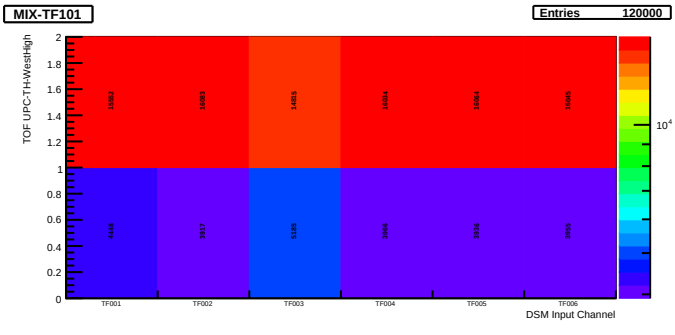

MIX-TF006
Entries 420000
TOF MULT
TOF tray

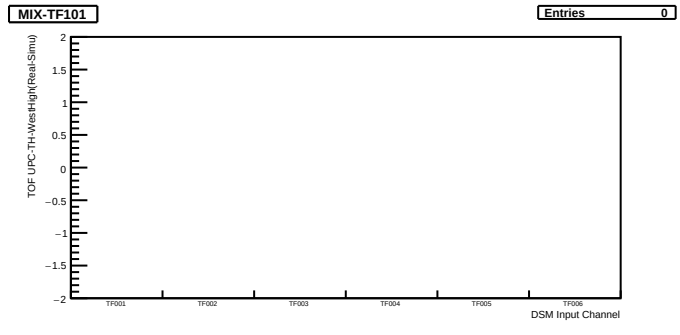

MIX-TF101
Entries 120000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

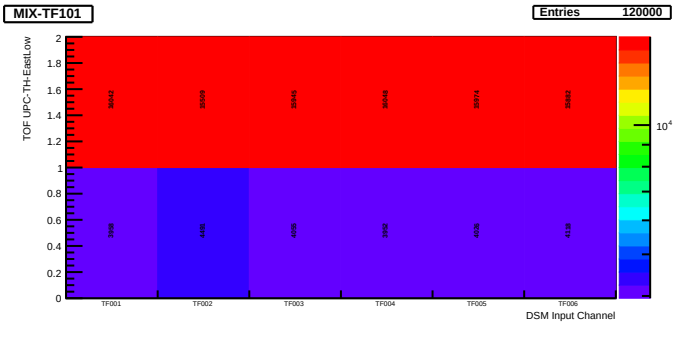

MIX-TF101
Entries 18158
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

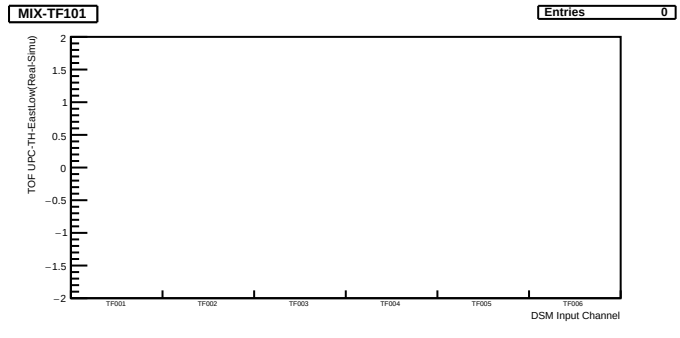

MIX-TF101
Entries 120000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

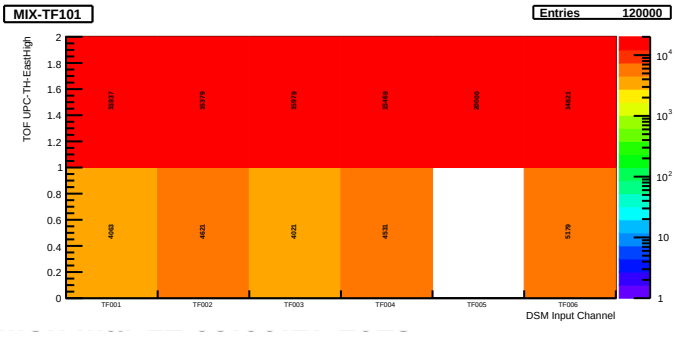

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

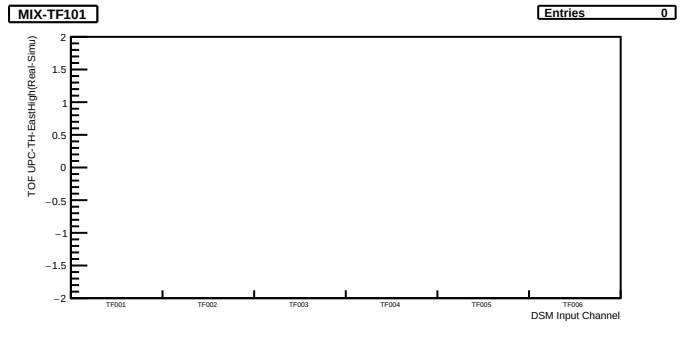

MIX-TF101
Entries 120000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

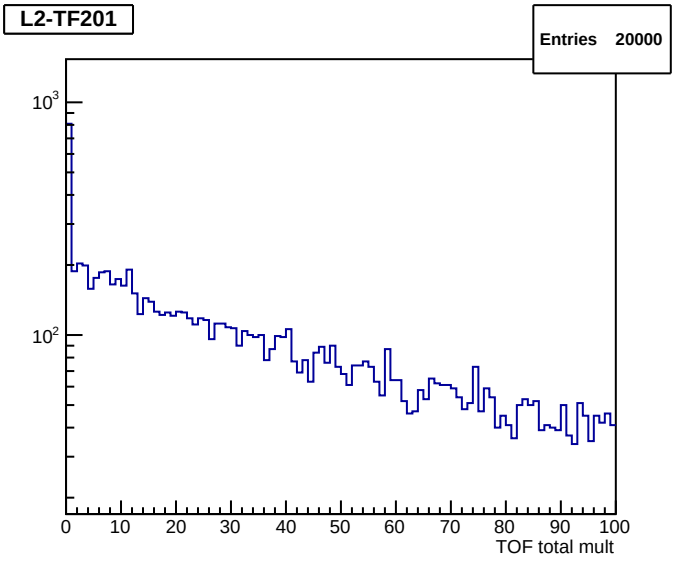

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

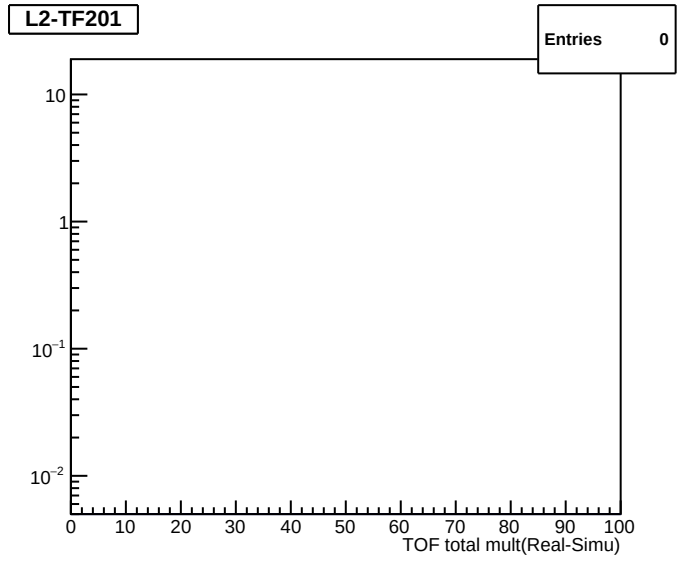

MIX-TF101
Entries 120000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

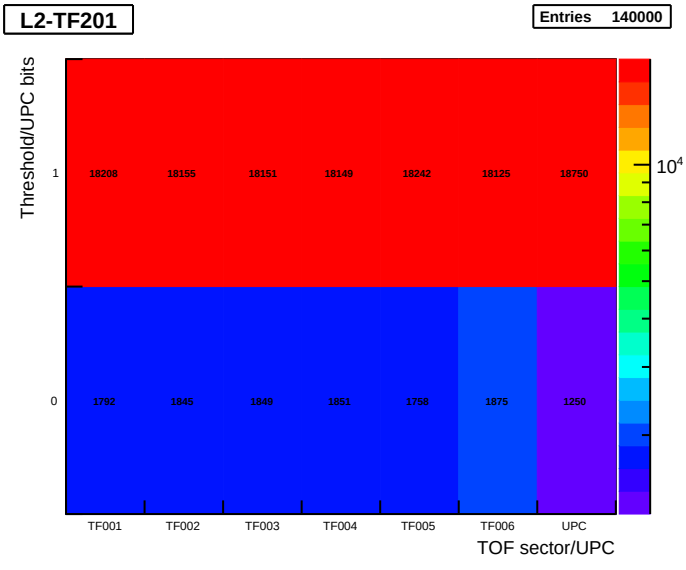

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

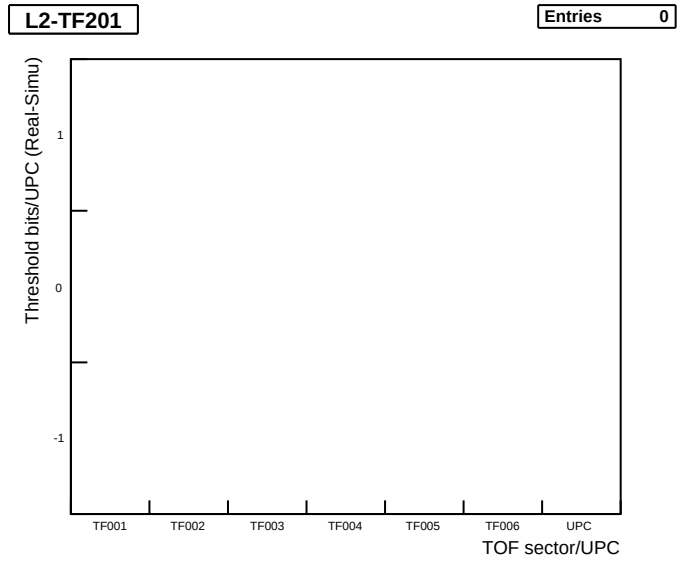

MIX-TF101
Entries 120000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

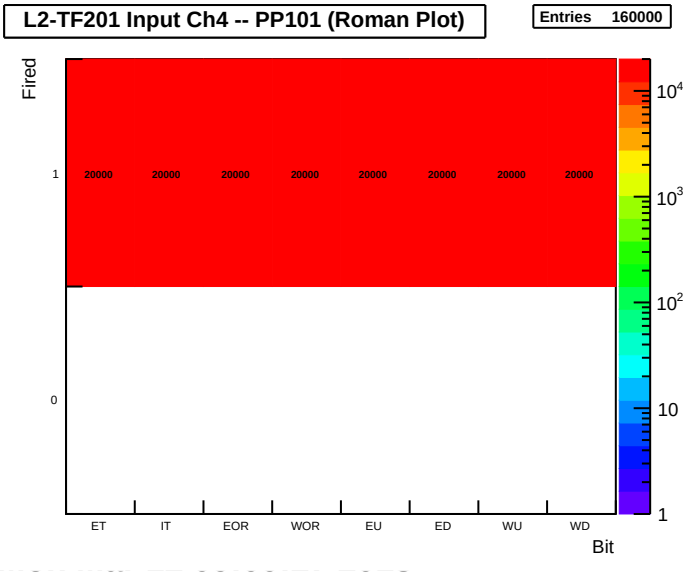

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

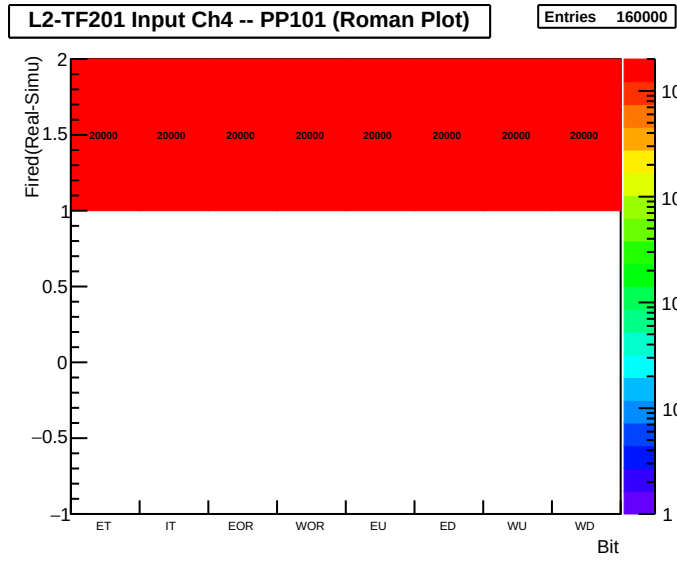

L2-TF201
Entries 20000
TOF total mult

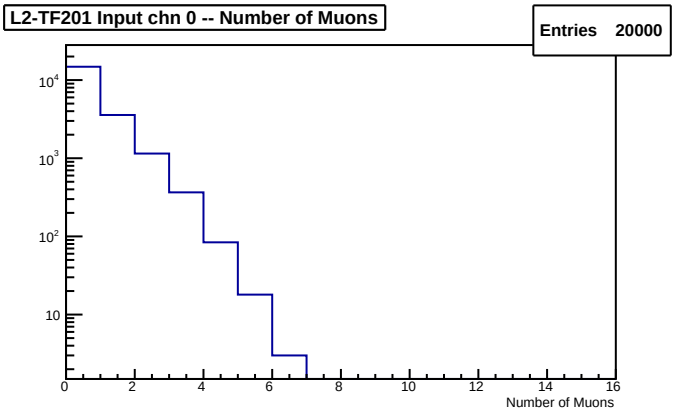

L2-TF201
Entries 0
TOF total mult(Real-Simu)

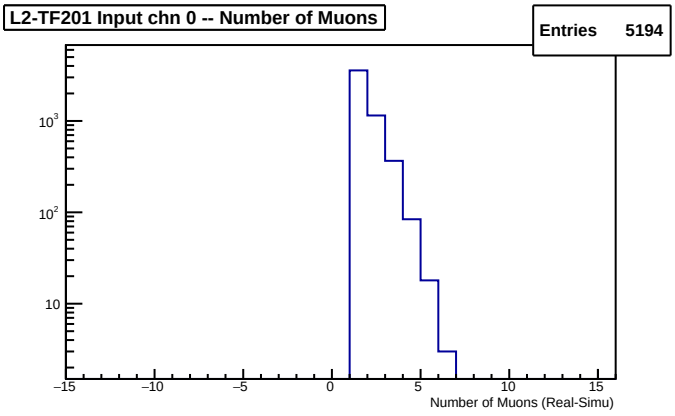

L2-TF201
Entries 140000
Threshold/UPC bits
18208 18155 18151 18149 18242 18125 18750
1792 1845 1849 1851 1758 1875 1250
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

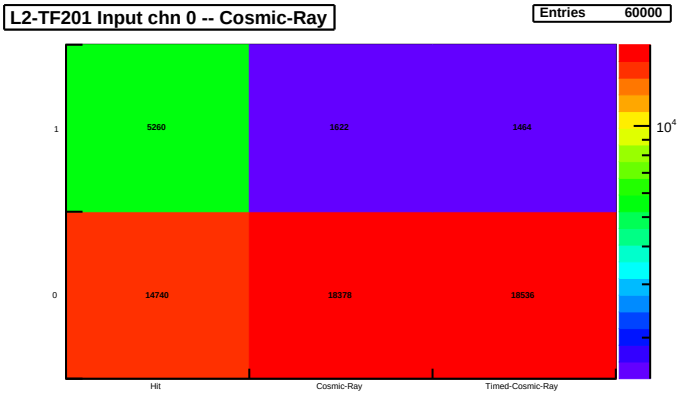

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

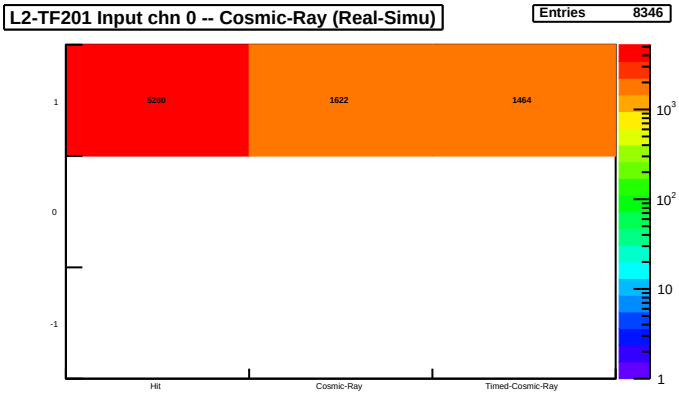

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 160000
Fired
20000 20000 20000 20000 20000 20000 20000 20000
ET IT EOR WOR EU ED WU WD
Bit

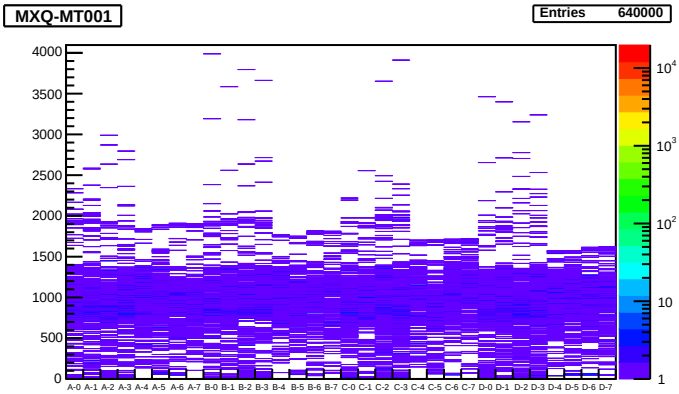

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 160000
Fired(Real-Simu)
20000 20000 20000 20000 20000 20000 20000 20000
ET IT EOR WOR EU ED WU WD
Bit

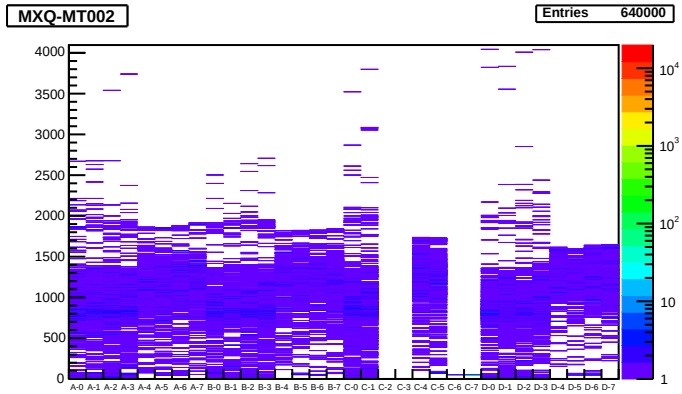

L2-TF201 Input chn 0 -- Number of Muons
Entries 20000
Number of Muons

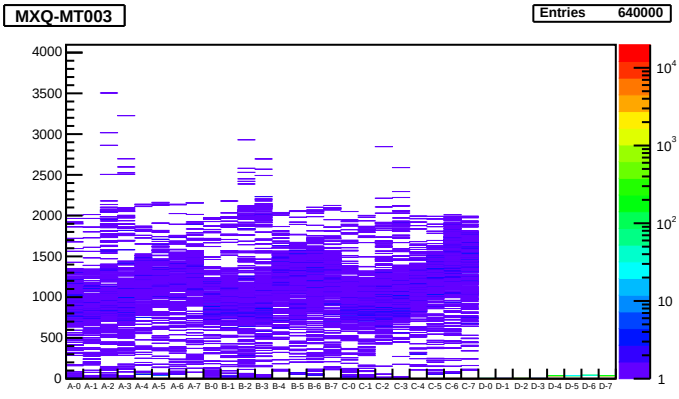

L2-TF201 Input chn 0 -- Number of Muons
Entries 5194
Number of Muons (Real-Simu)

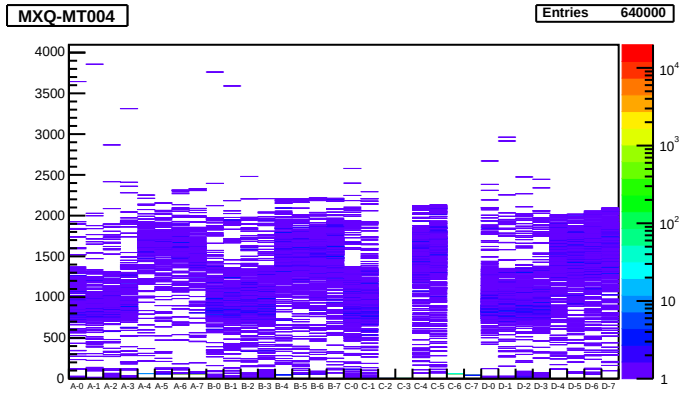

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 60000
5260
1622
1464
14740
18378
18536
Hit
Cosmic-Ray
Timed-Cosmic-Ray

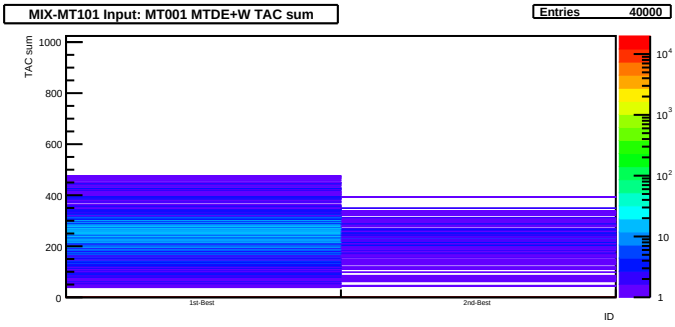

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 8346
5260
1622
1464
Hit
Cosmic-Ray
Timed-Cosmic-Ray

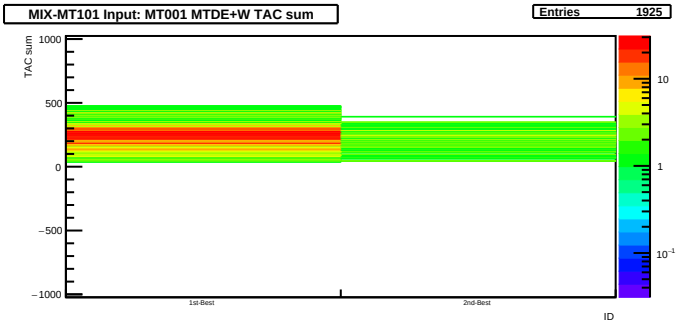

MXQ-MT001
Entries 640000

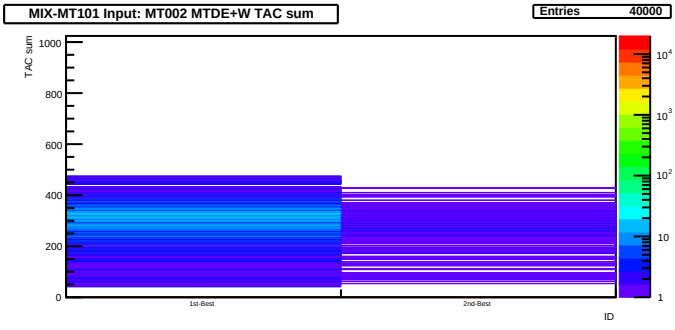

MXQ-MT002
Entries 640000

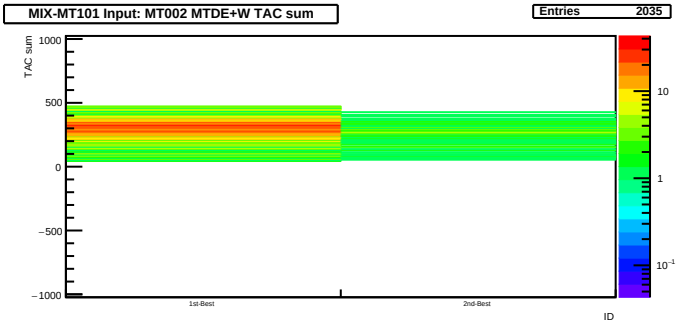

MXQ-MT003
Entries 640000

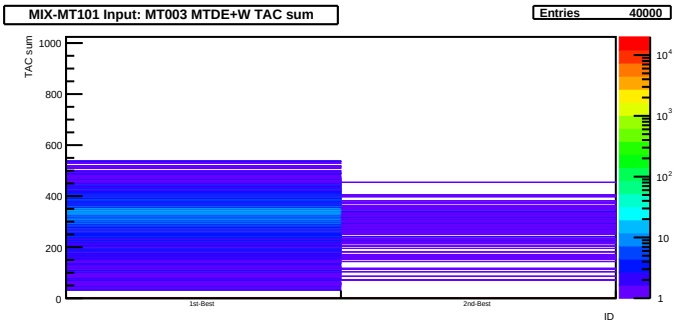

MXQ-MT004
Entries 640000

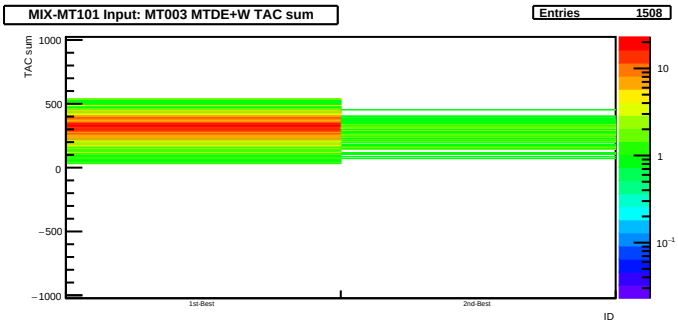

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 40000
TAC sum
1st-Best
2nd-Best
ID

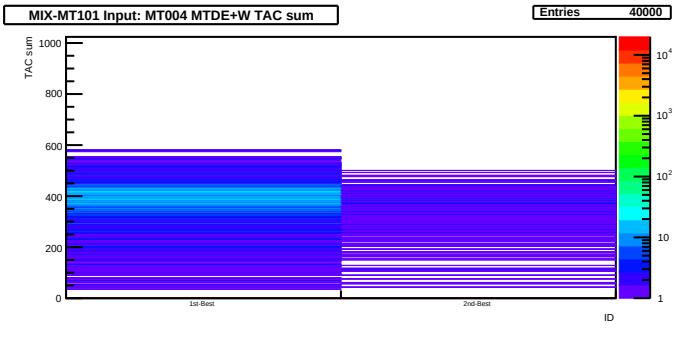

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 1925
TAC sum
1st-Best
2nd-Best
ID

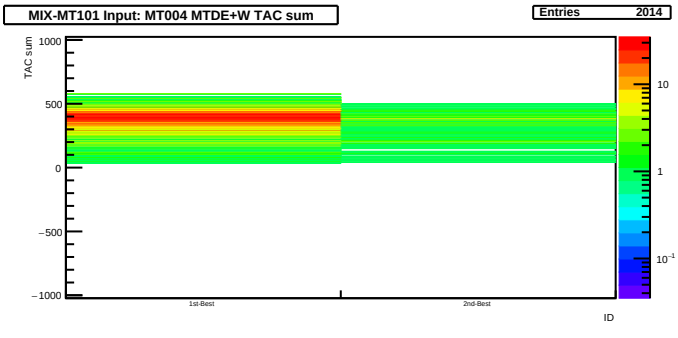

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 40000
TAC sum
1st-Best
2nd-Best
ID

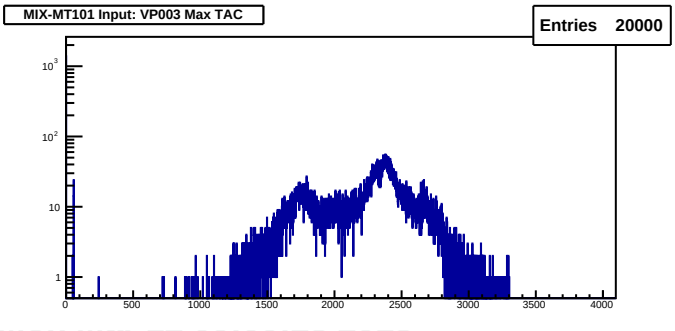

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 2035
TAC sum
1st-Best
2nd-Best
ID

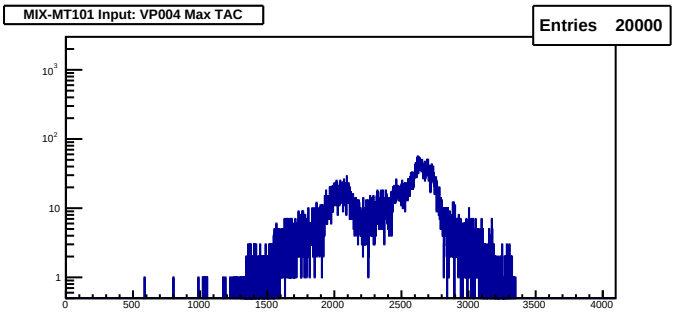

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 40000
TAC sum
1st-Best
2nd-Best
ID

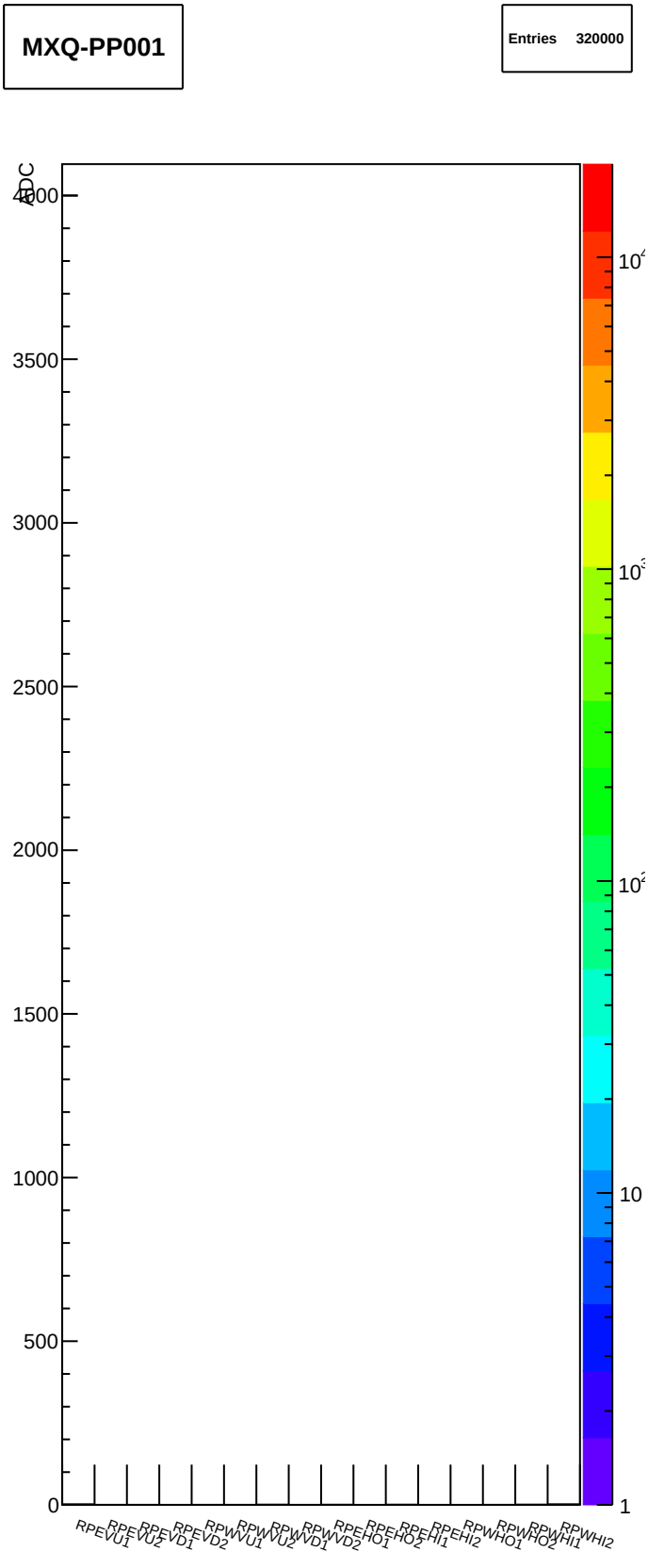

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 1508
TAC sum
1st-Best
2nd-Best
ID

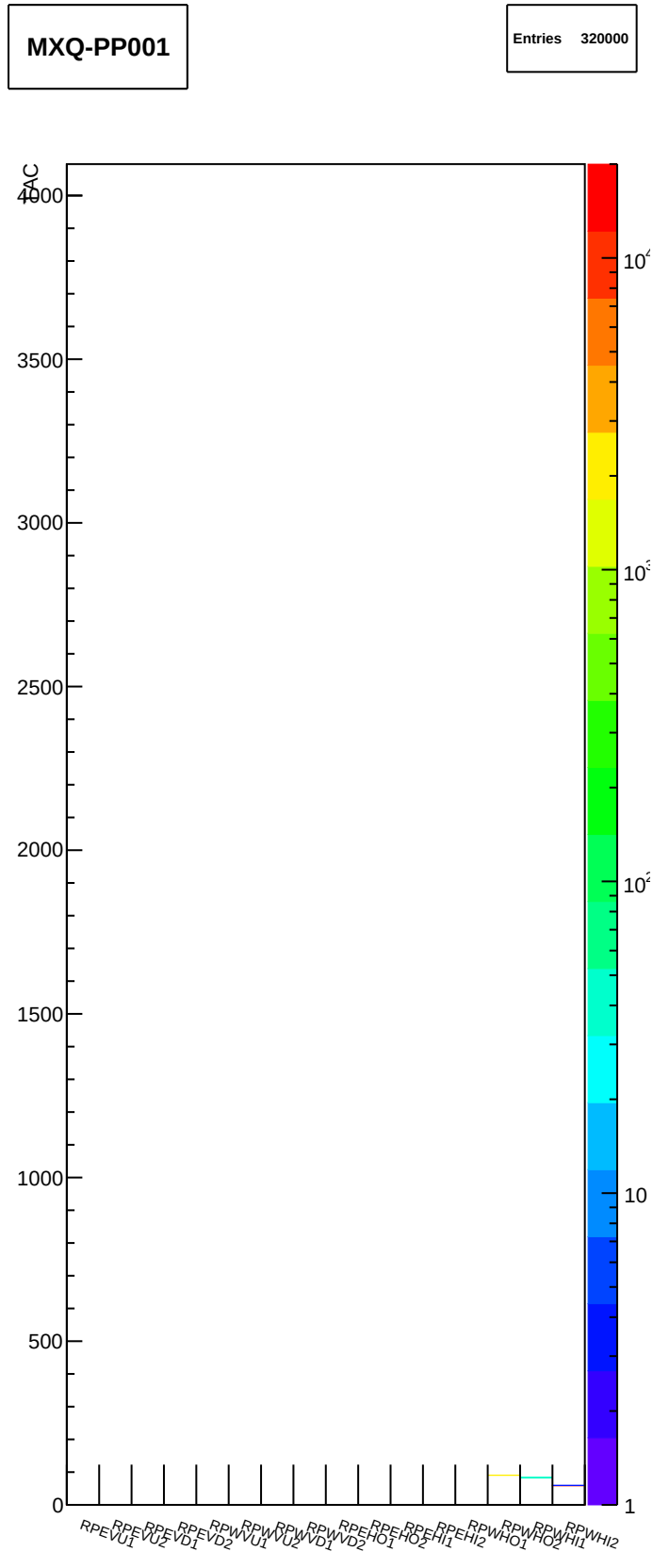

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 40000
TAC sum
1st-Best
2nd-Best
ID

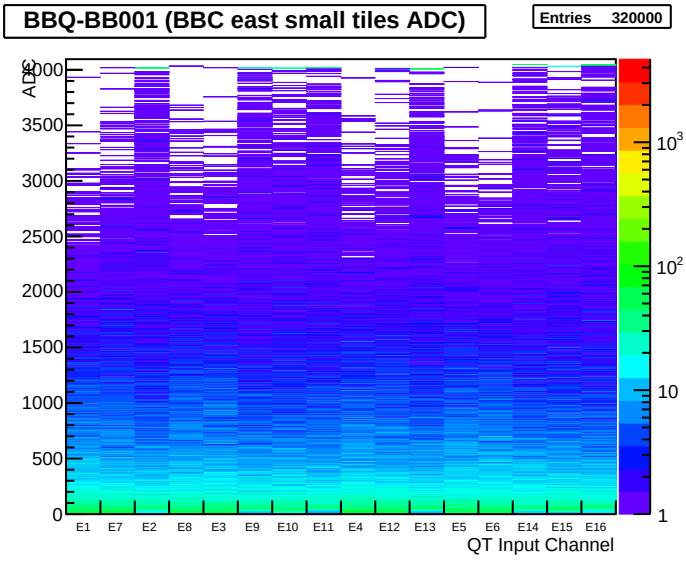

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 2014
TAC sum
1st-Best
2nd-Best
ID

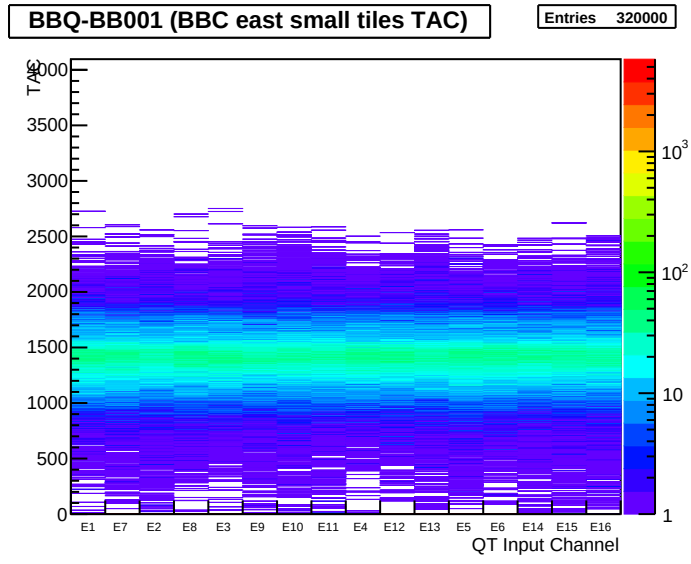

MIX-MT101 Input: VP003 Max TAC
Entries 20000

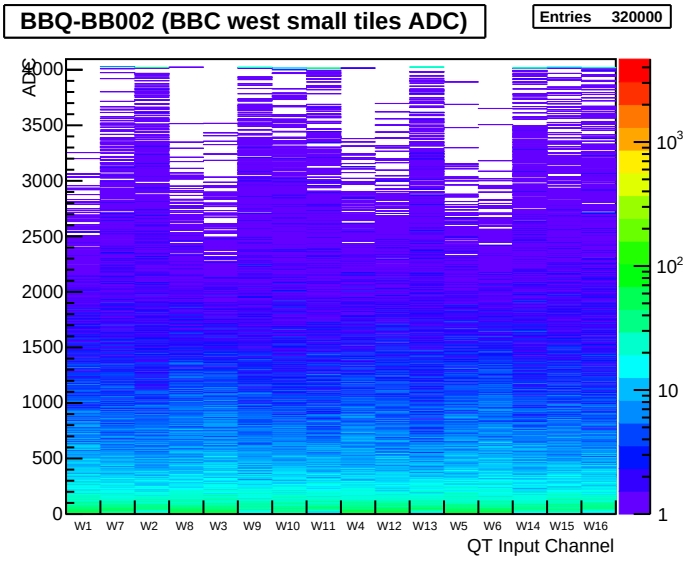

MIX-MT101 Input: VP004 Max TAC
Entries 20000

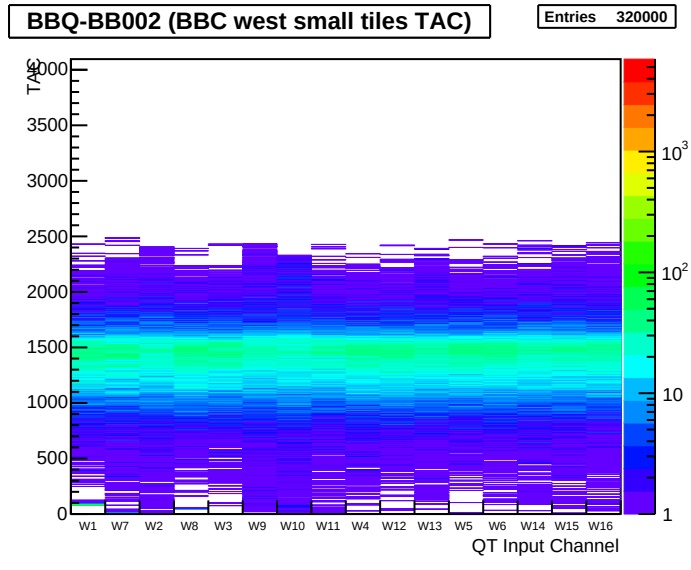

MXQ-PP001
Entries 320000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

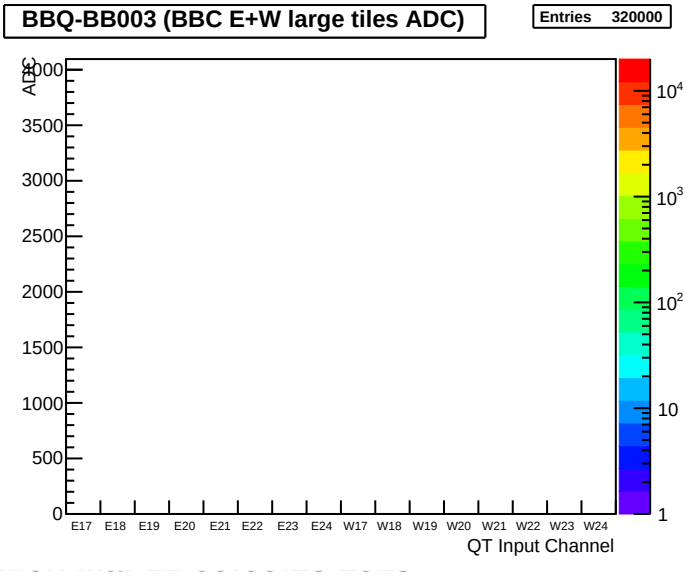

MXQ-PP001
Entries 320000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

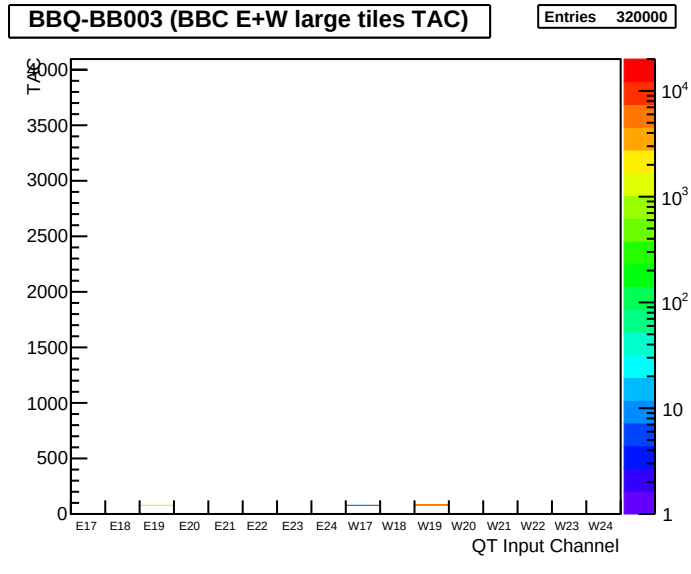

BBQ-BB001 (BBC east small tiles ADC)
Entries 320000
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

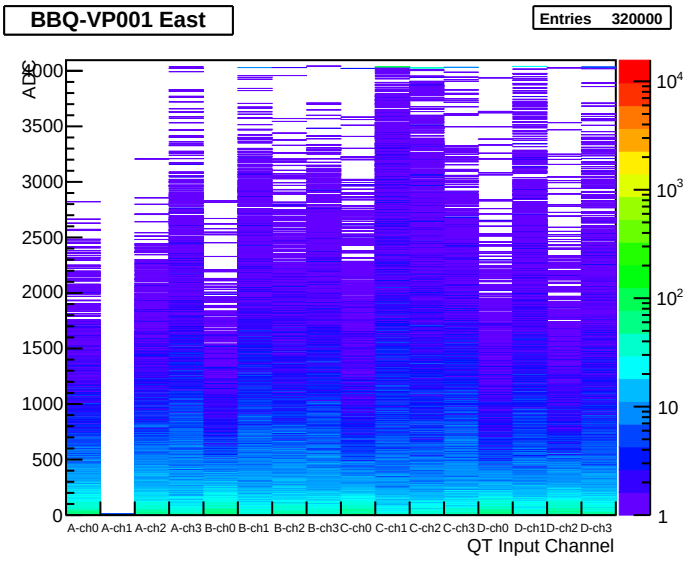

BBQ-BB001 (BBC east small tiles TAC)
Entries 320000
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

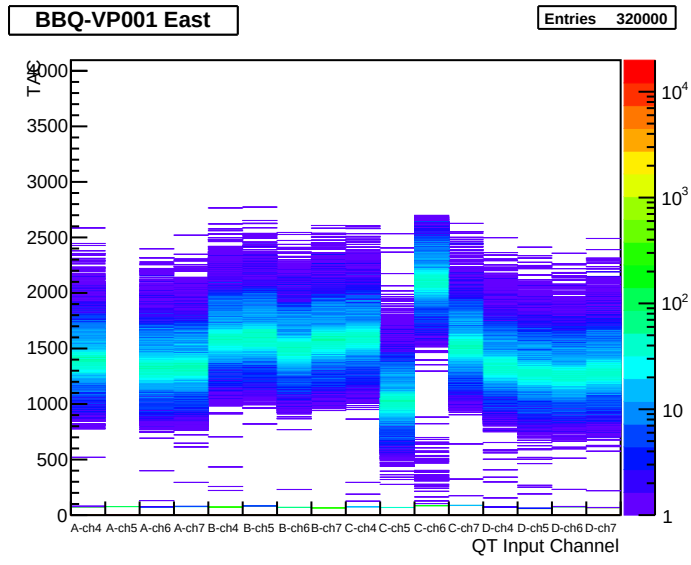

BBQ-BB002 (BBC west small tiles ADC)
Entries 320000
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

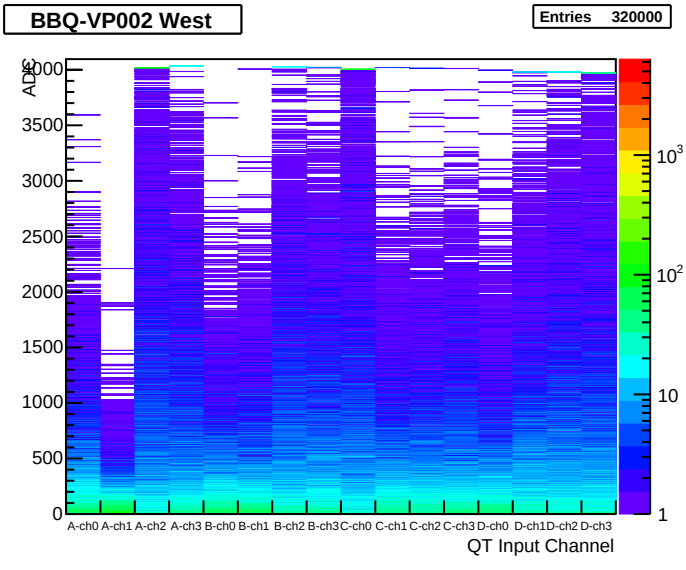

BBQ-BB002 (BBC west small tiles TAC)
Entries 320000
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

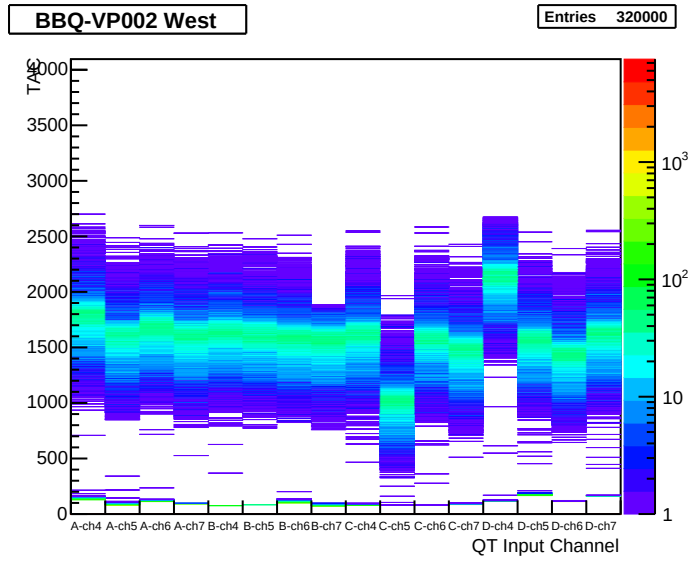

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 320000
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

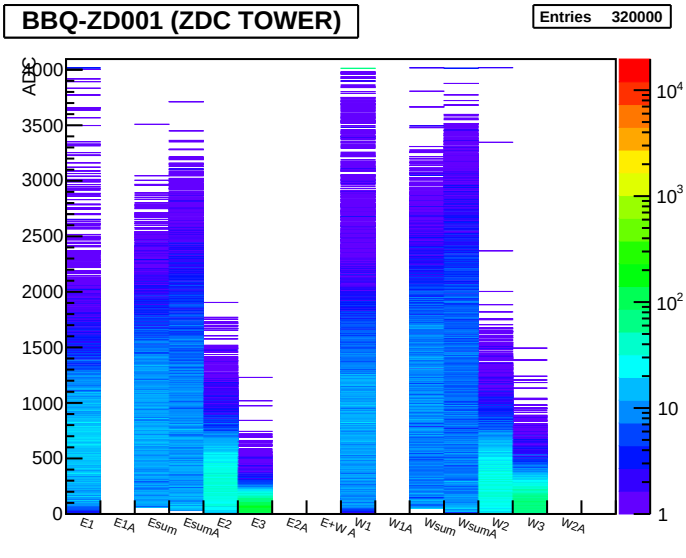

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 320000
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

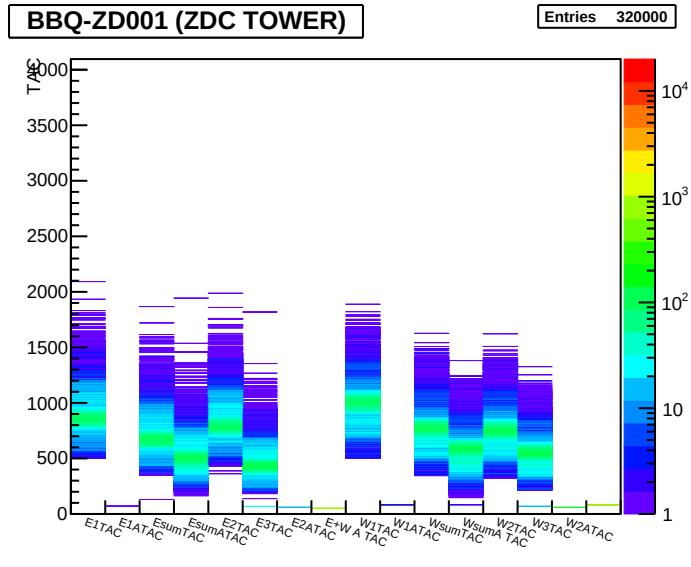

BBQ-VP001 East
Entries 320000
ADC
QT Input Channel

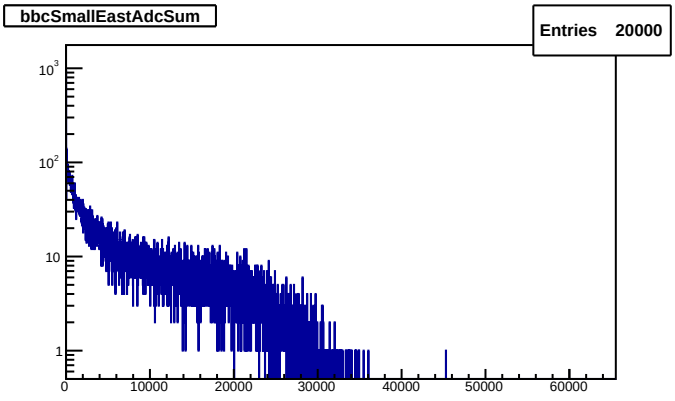

BBQ-VP001 East
Entries 320000
TAC
QT Input Channel

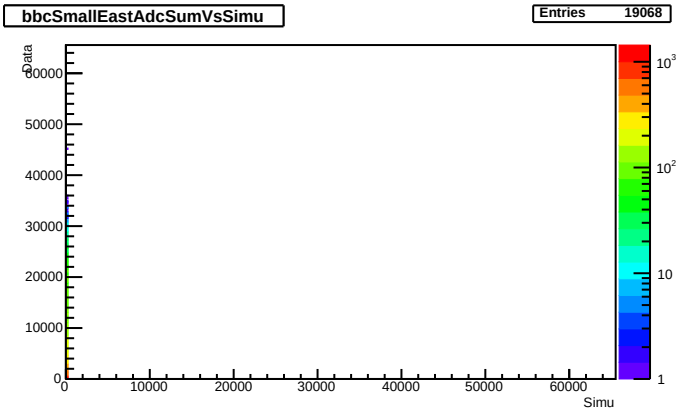

BBQ-VP002 West
Entries 320000
ADC
QT Input Channel

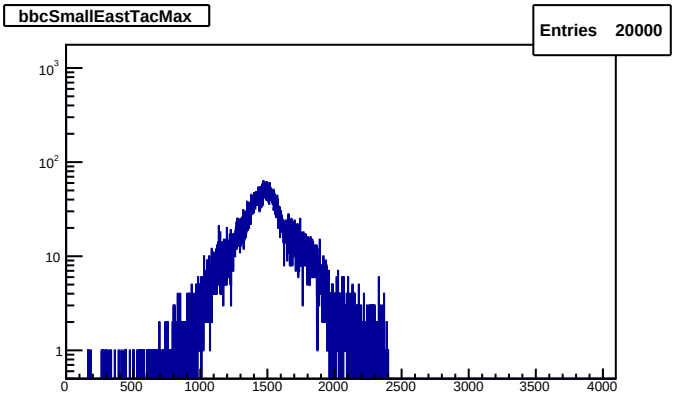

BBQ-VP002 West
Entries 320000
TAC
QT Input Channel

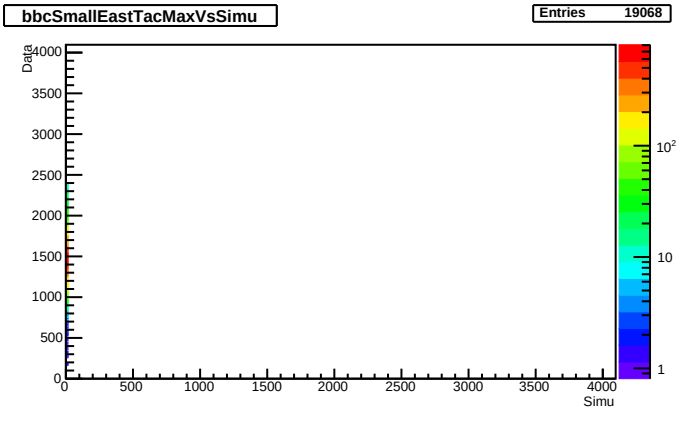

BBQ-ZD001 (ZDC TOWER)
Entries 320000
ADC

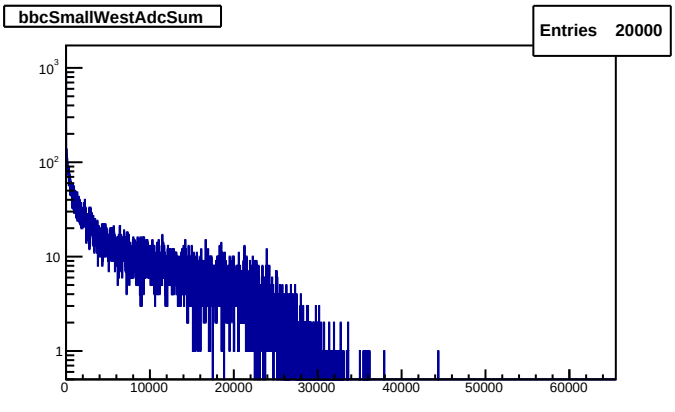

BBQ-ZD001 (ZDC TOWER)
Entries 320000
TAC

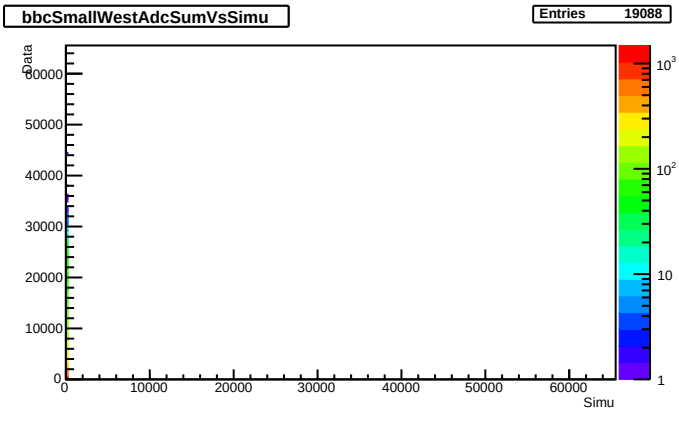

bbcSmallEastAdcSum
Entries 20000

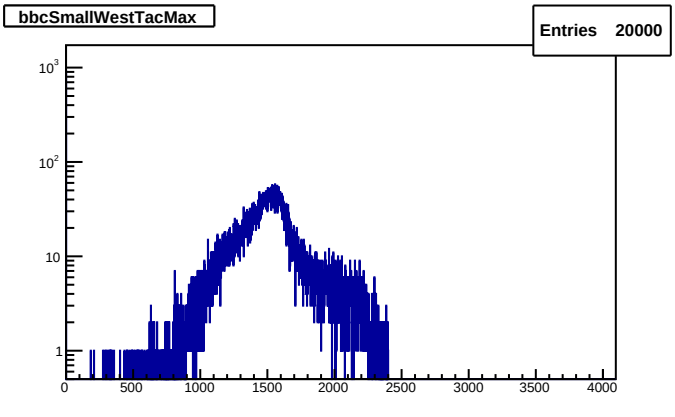

bbcSmallEastAdcSumVsSimu
Entries 19068
Data
Simu

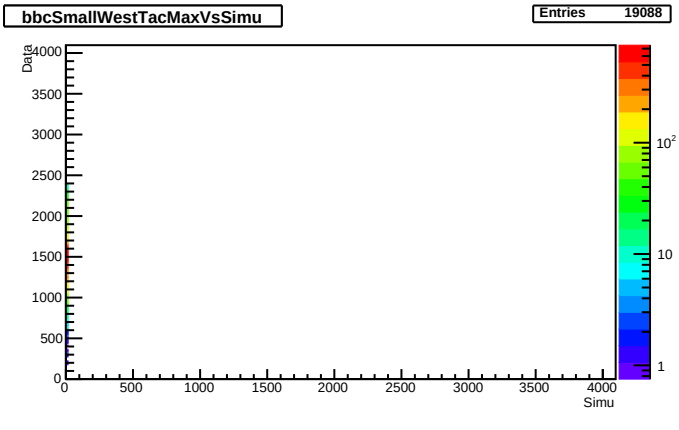

bbcSmallEastTacMax
Entries 20000

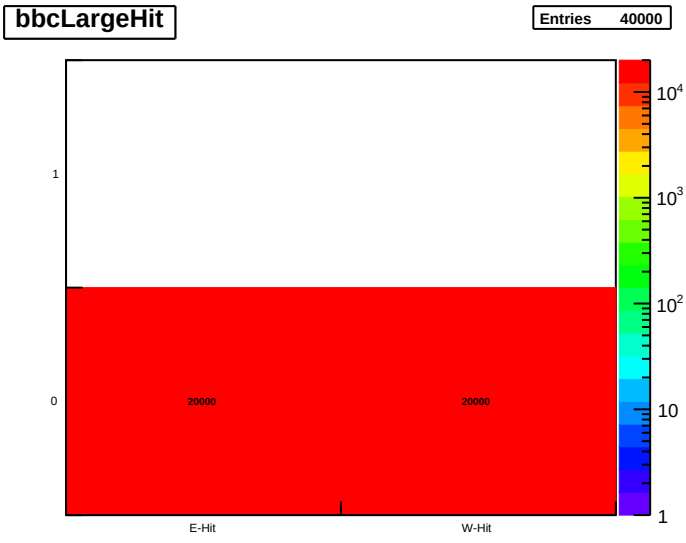

bbcSmallEastTacMaxVsSimu
Entries 19068
Data
Simu

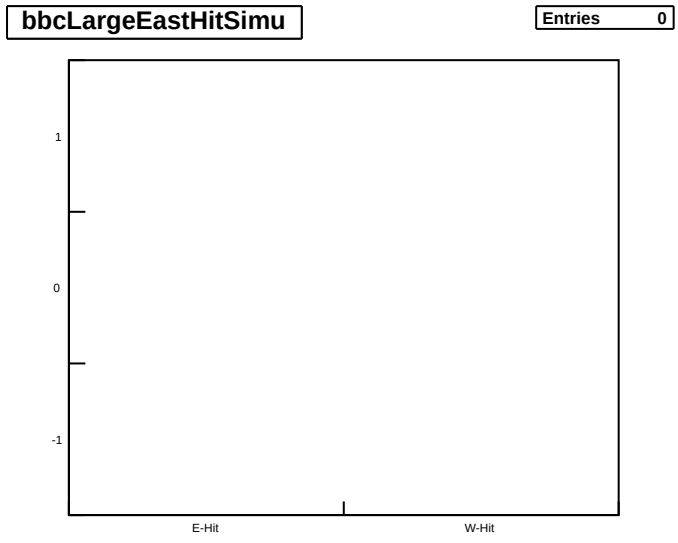

bbcSmallWestAdcSum
Entries 20000

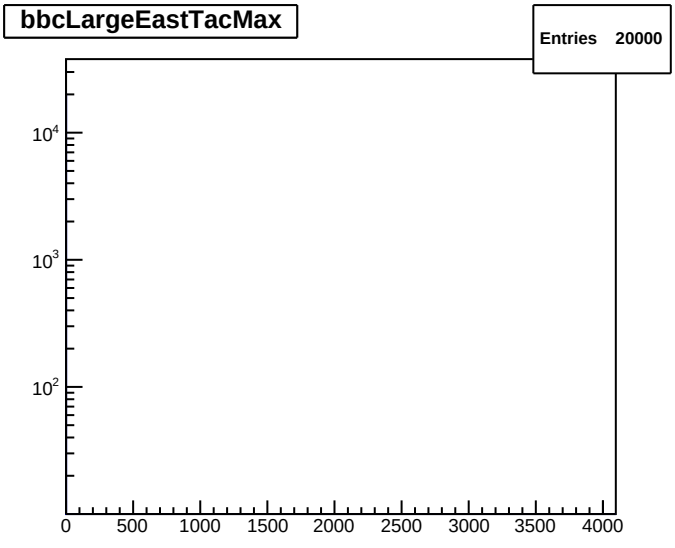

bbcSmallWestAdcSumVsSimu
Entries 19088
Data
Simu

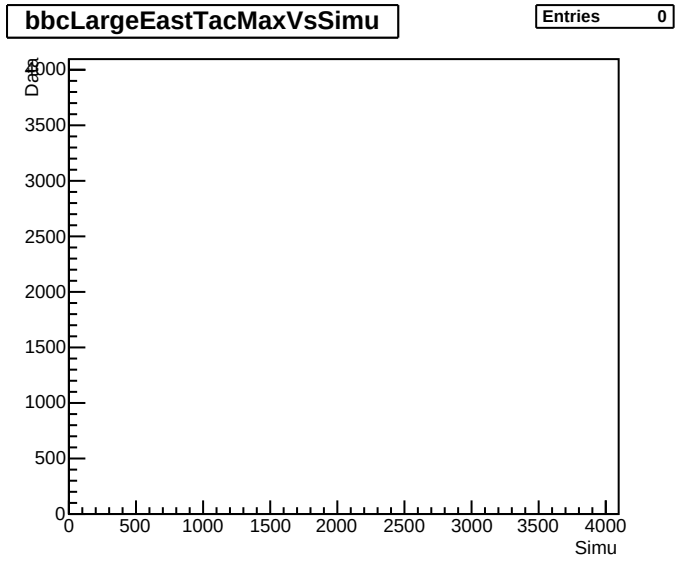

bbcSmallWestTacMax
Entries 20000

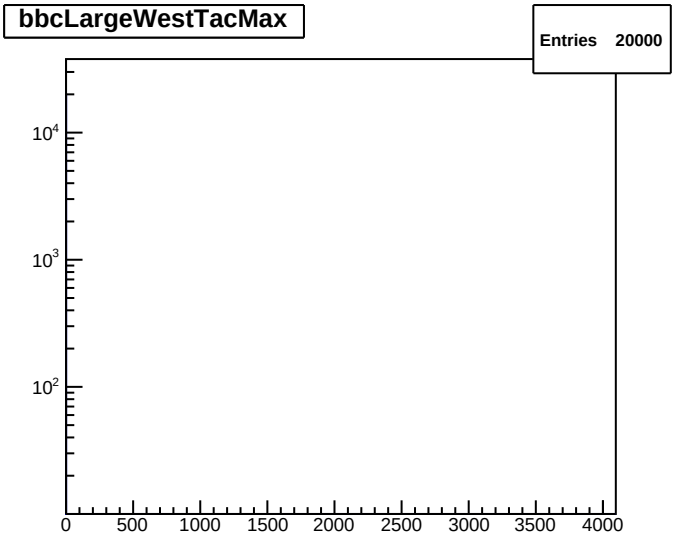

bbcSmallWestTacMaxVsSimu
Entries 19088
Data
Simu

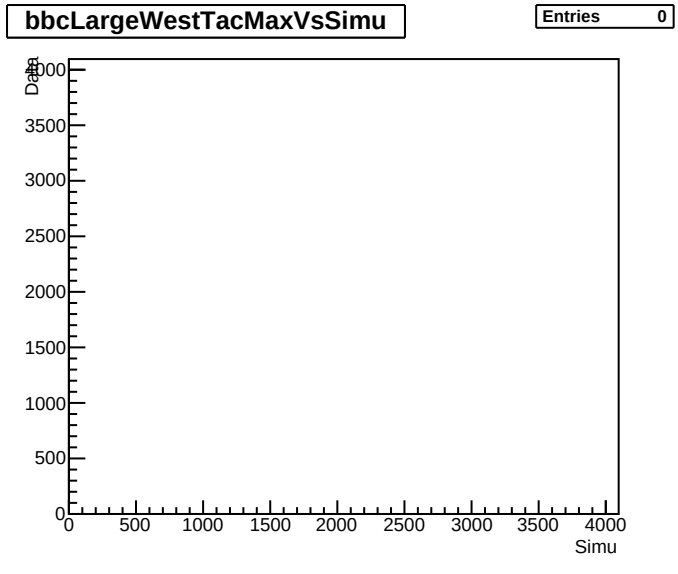

bbcLargeHit
Entries 40000
1
0
20000
20000
E-Hit
W-Hit
10^4
10^3
10^2
10
1

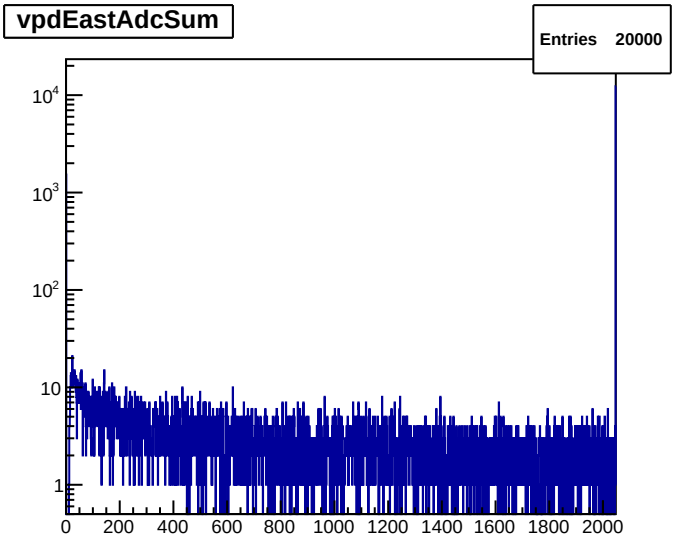

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

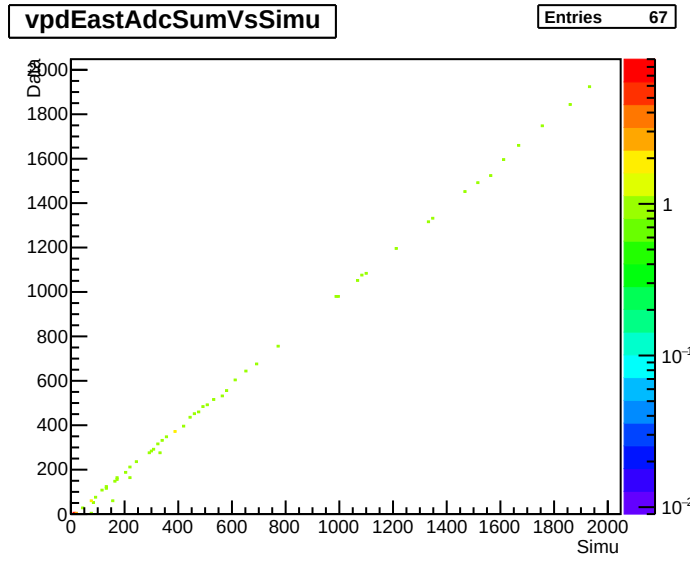

bbcLargeEastTacMax
Entries 20000
10^4
10^3
10^2
0
500
1000
1500
2000
2500
3000
3500
4000

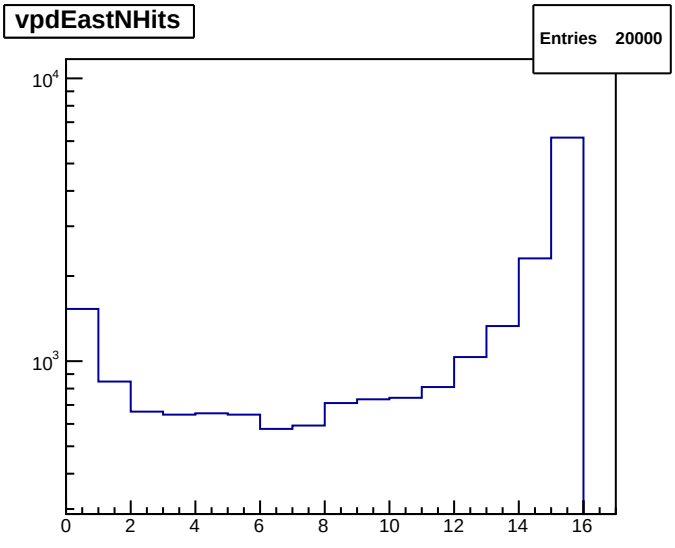

bbcLargeEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

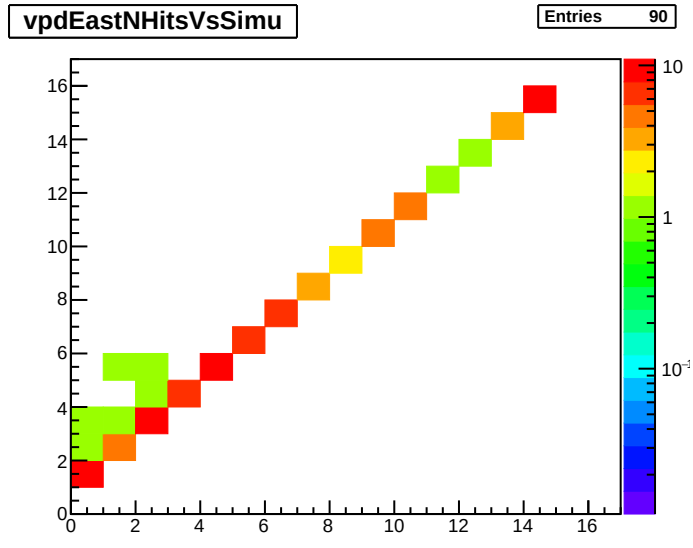

bbcLargeWestTacMax
Entries 20000
10^4
10^3
10^2
0
500
1000
1500
2000
2500
3000
3500
4000

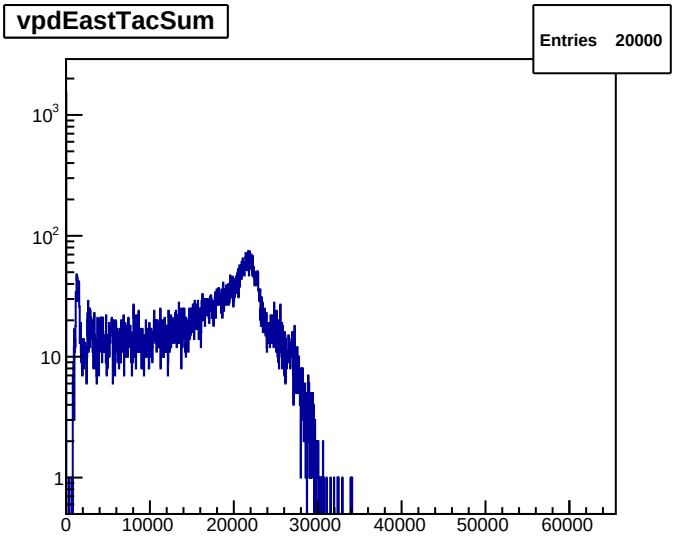

bbcLargeWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

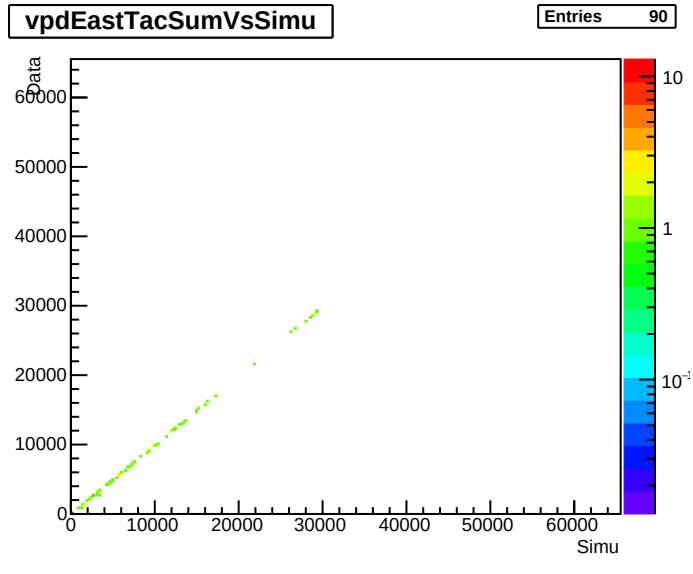

vpdEastAdcSum
Entries 20000

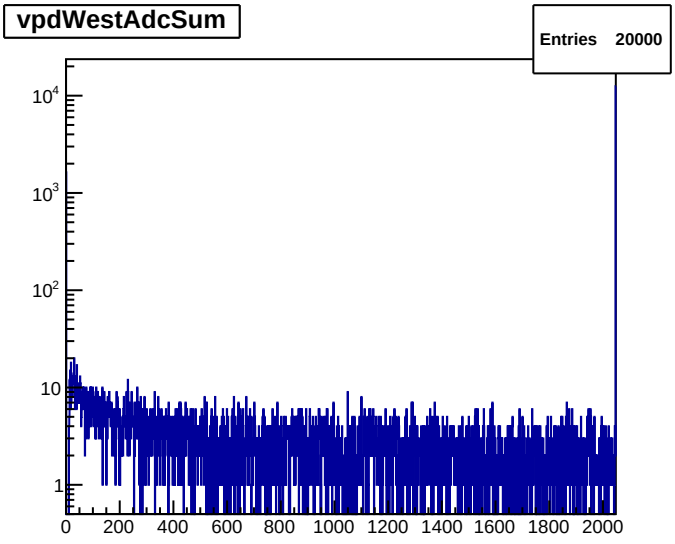

vpdEastAdcSumVsSimu
Entries 67
Data
Simu

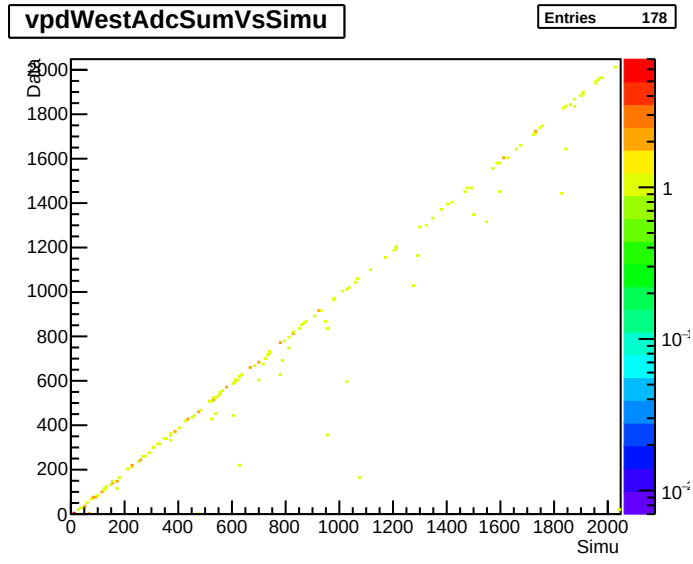

vpdEastNHits
Entries 20000

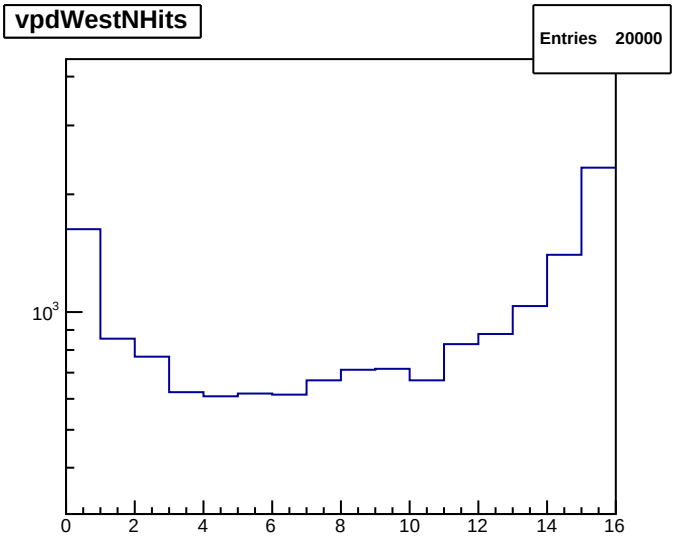

vpdEastNHitsVsSimu
Entries 90

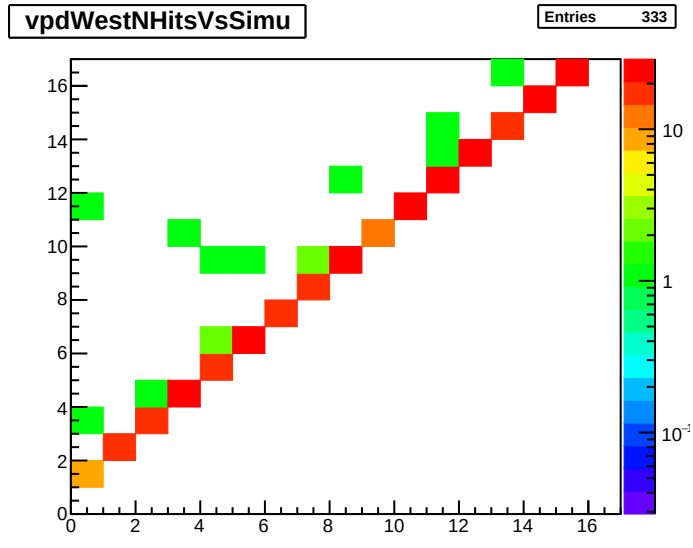

vpdEastTacSum
Entries 20000

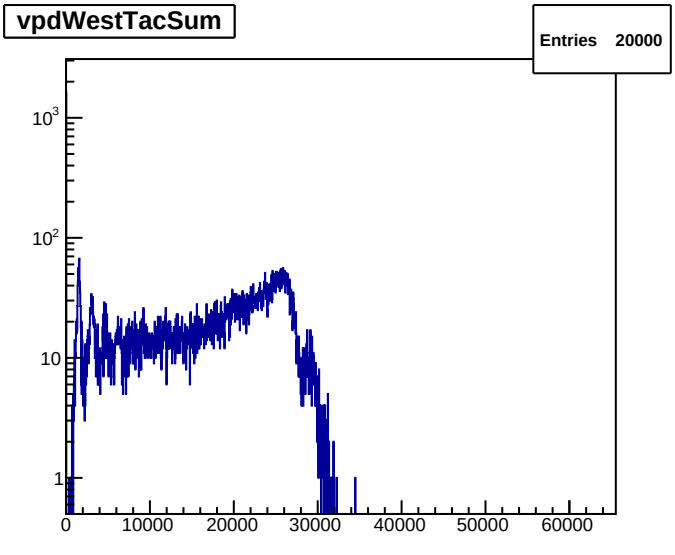

vpdEastTacSumVsSimu
Entries 90
Data
Simu

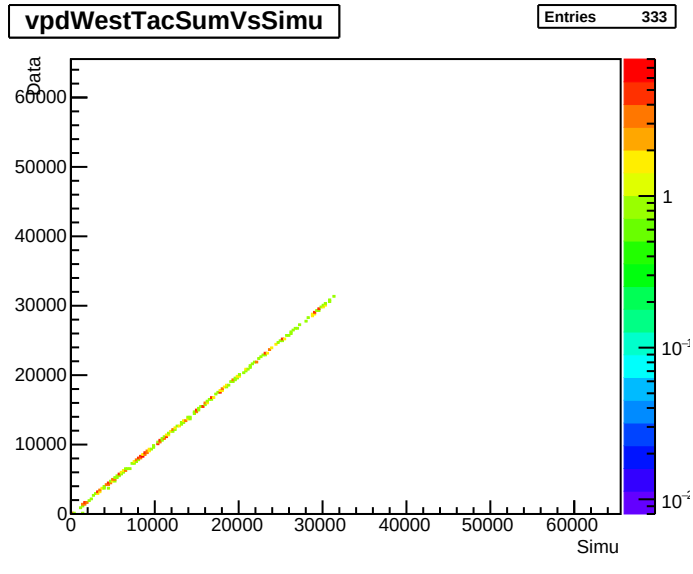

vpdWestAdcSum
Entries 20000

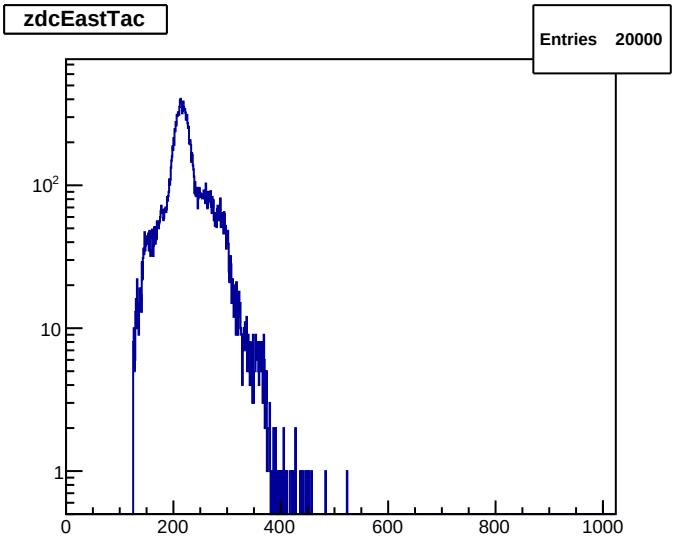

vpdWestAdcSumVsSimu
Entries 178
Data
Simu

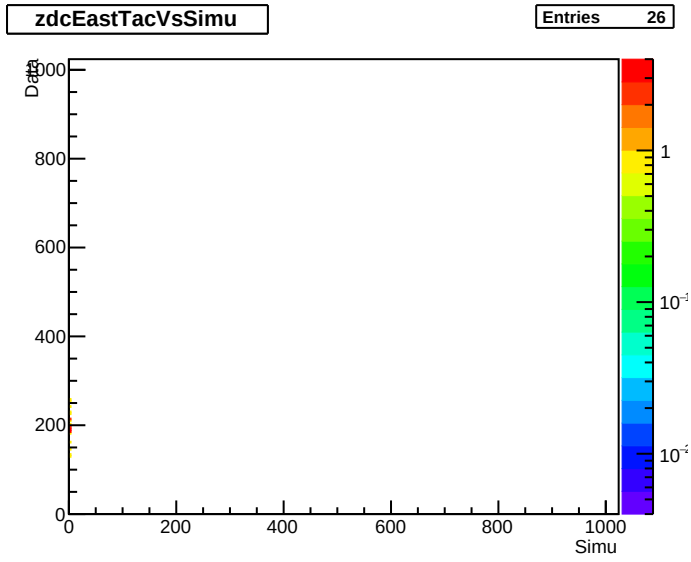

vpdWestNHits
Entries 20000

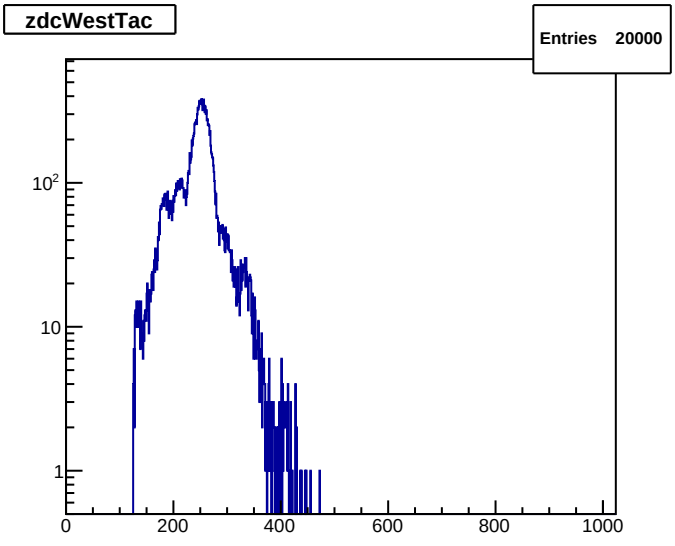

vpdWestNHitsVsSimu
Entries 333

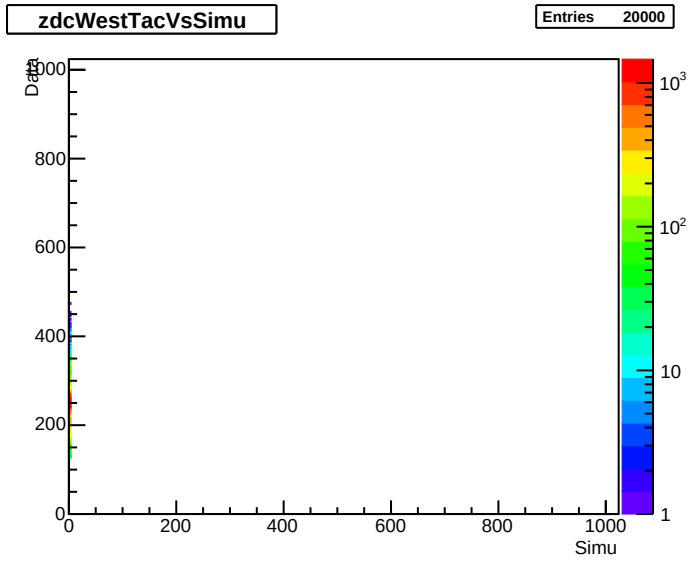

vpdWestTacSum
Entries 20000

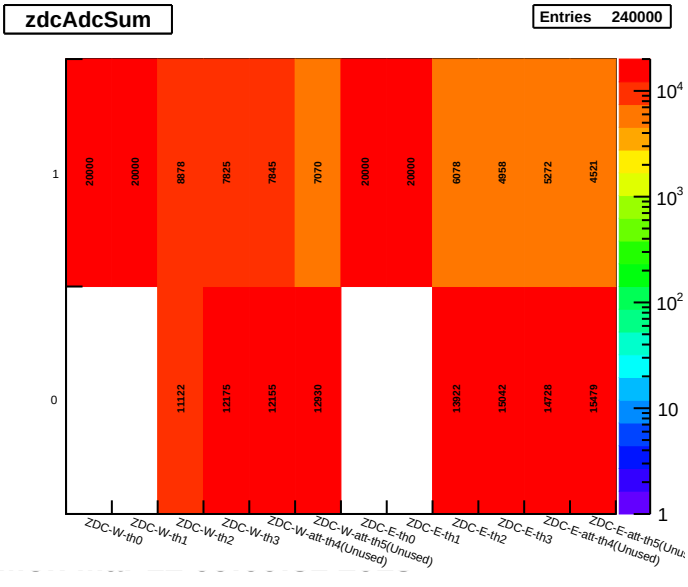

vpdWestTacSumVsSimu
Entries 333
Data
Simu

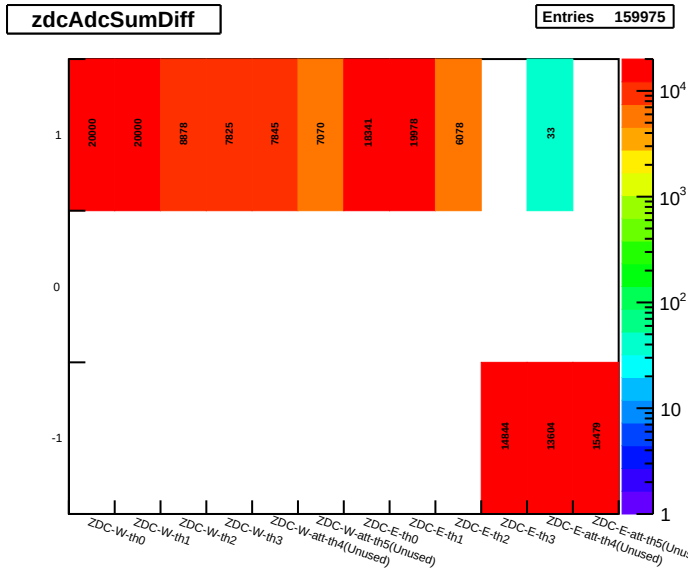

zdcEastTac
Entries 20000

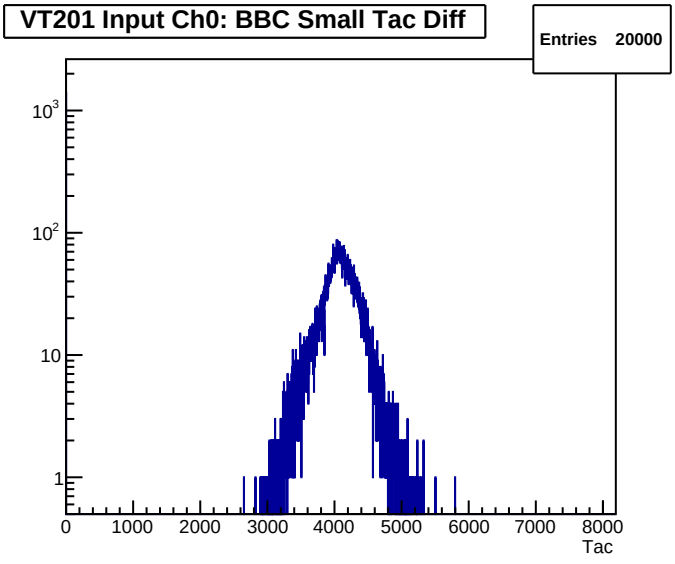

zdcEastTacVsSimu
Entries 26
Data
Simu

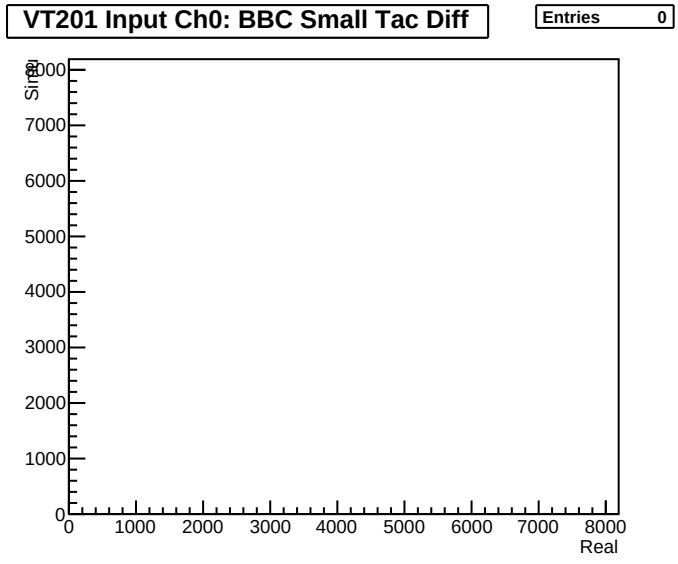

zdcWestTac
Entries 20000

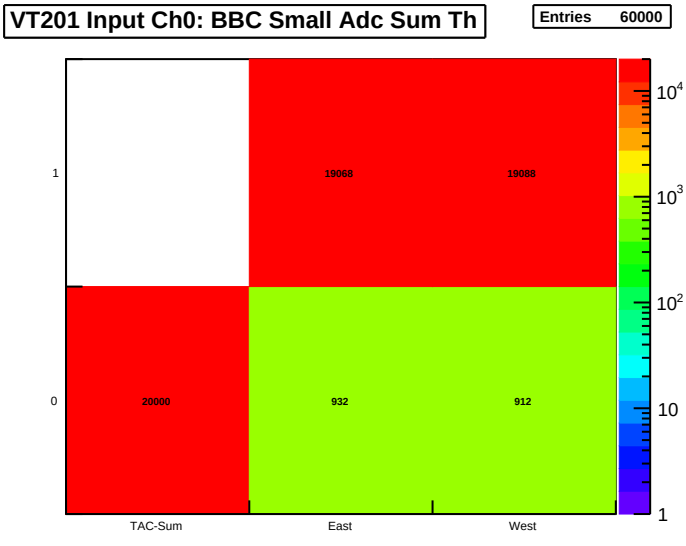

zdcWestTacVsSimu
Entries 20000
Data
Simu

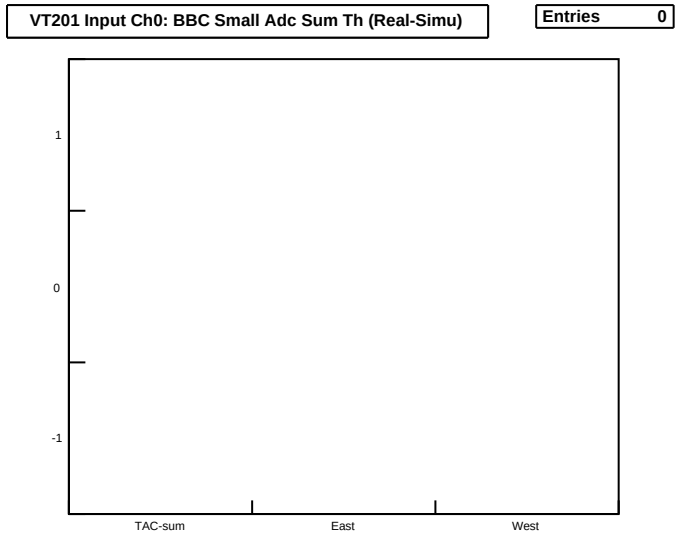

zdcAdcSum
Entries 240000
20000
20000
8878
7825
7845
7070
20000
20000
6078
4958
5272
4521
11122
12175
12155
12930
13922
15042
14728
15479
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

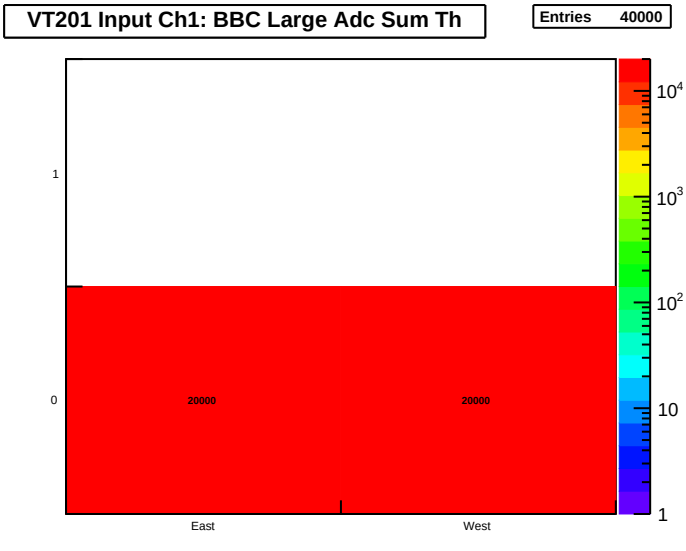

zdcAdcSumDiff
Entries 159975
20000
20000
8878
7825
7845
7070
18341
19978
6078
33
14844
13604
15479
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

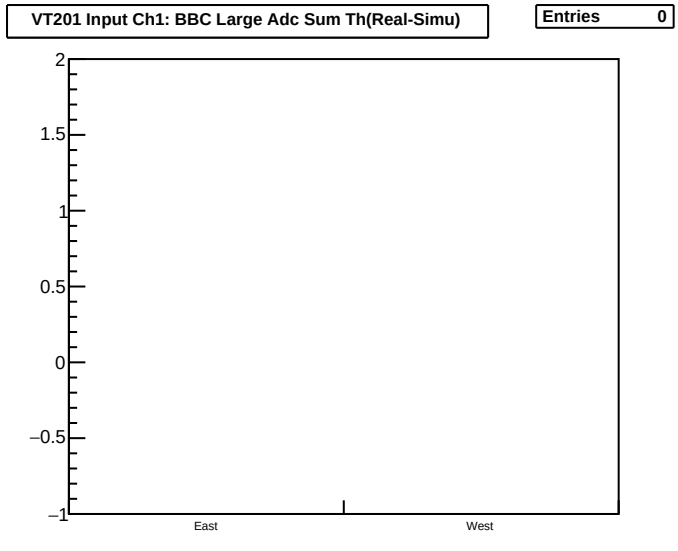

VT201 Input Ch0: BBC Small Tac Diff
Entries 20000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

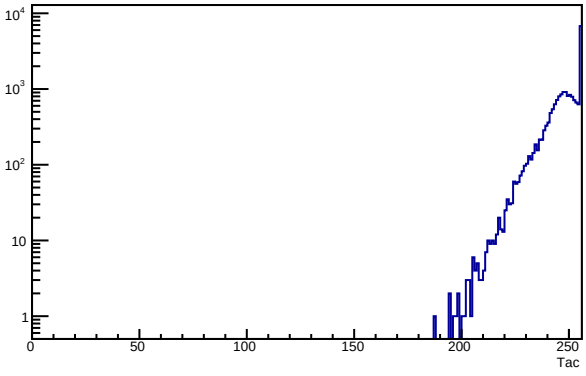

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 60000
19068
19088
20000
932
912
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

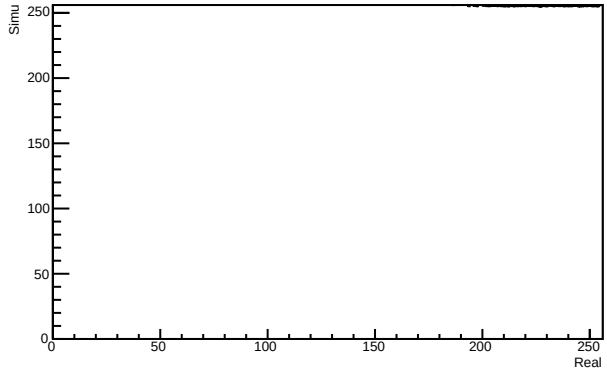

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 40000
20000
20000
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

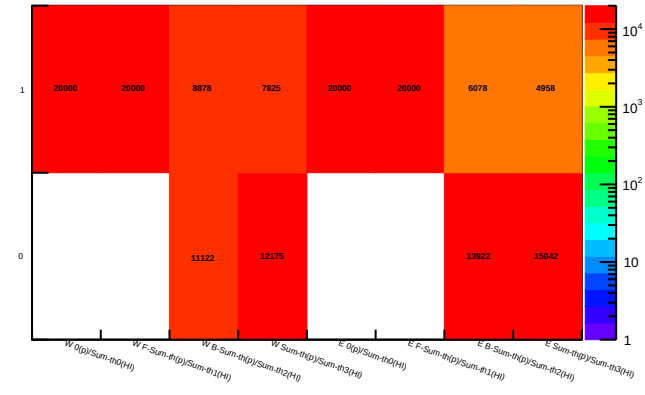
VT201 Input Ch2: ZDC Tac Diff



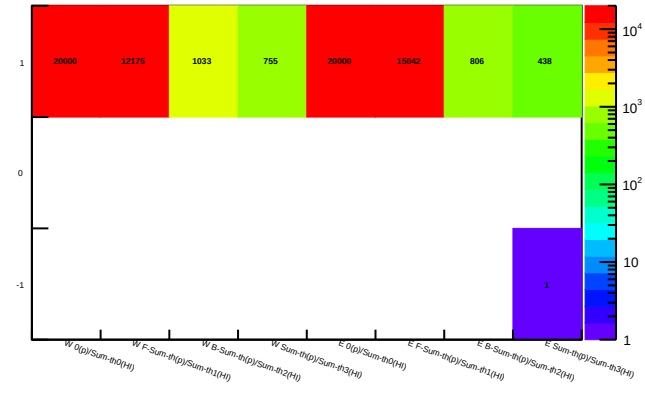
VT201 Input Ch2: ZDC Tac Diff



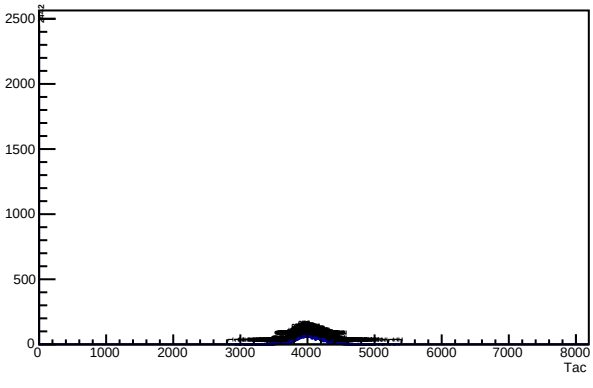
VT201 Input Ch2: ZDC Adc Sum Th



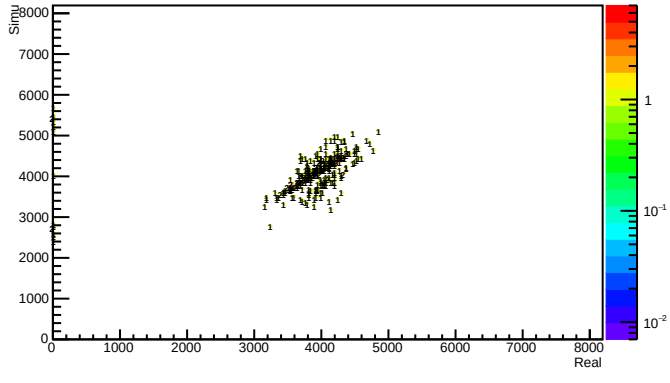
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



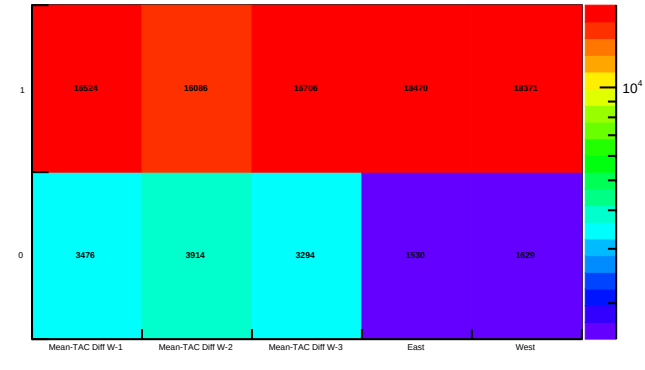
VT201 Input Ch4: VPD Tac Diff



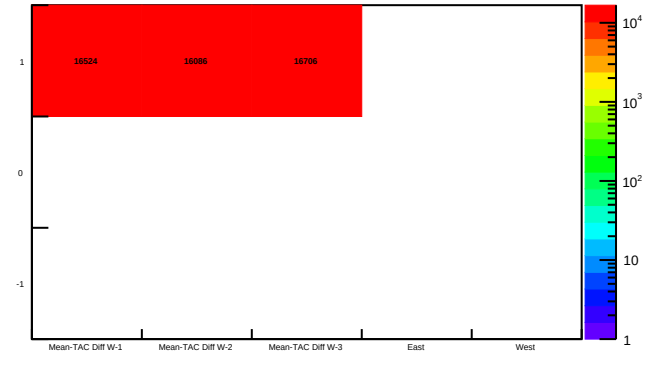
VT201 Input Ch4: VPD Tac Diff

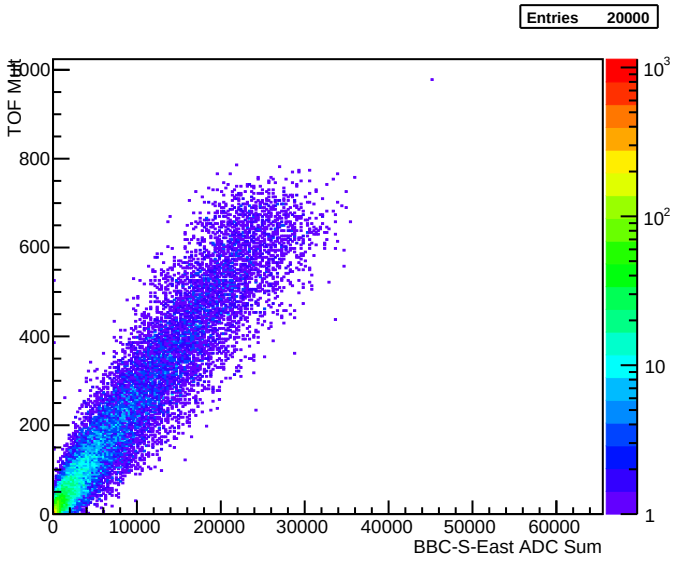


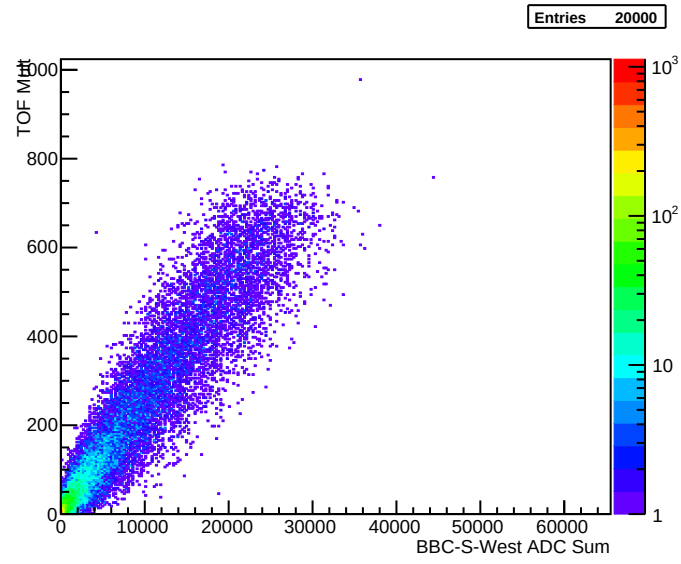
VT201 Input Ch4: VPD Adc Sum Th

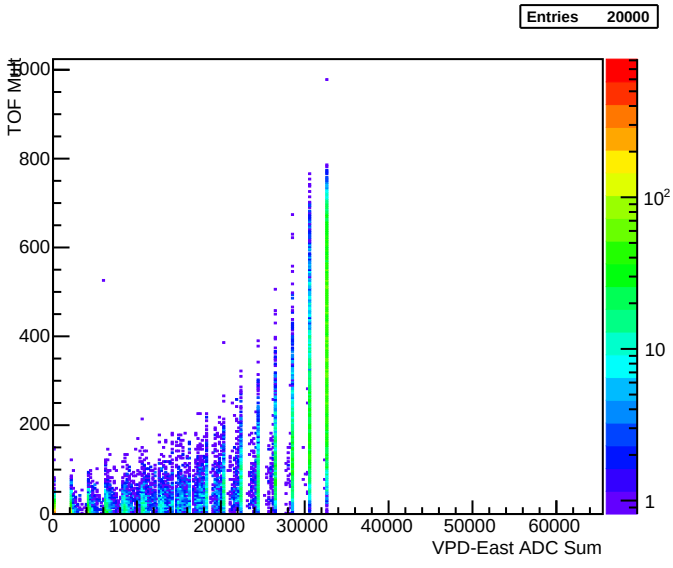


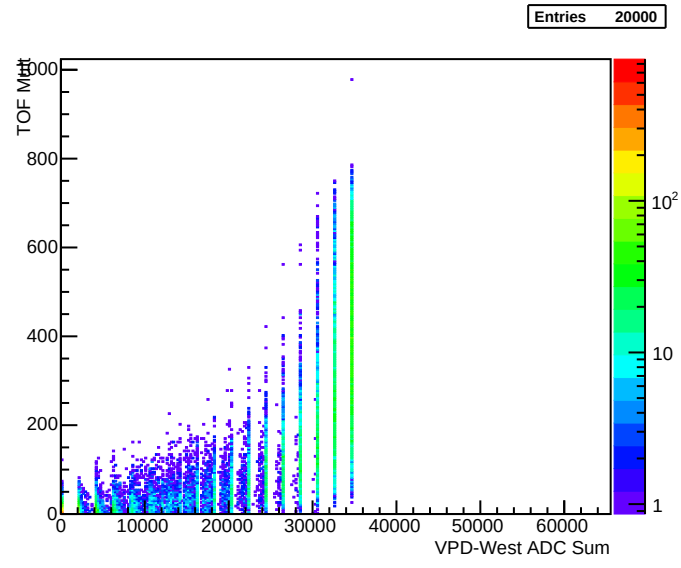
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

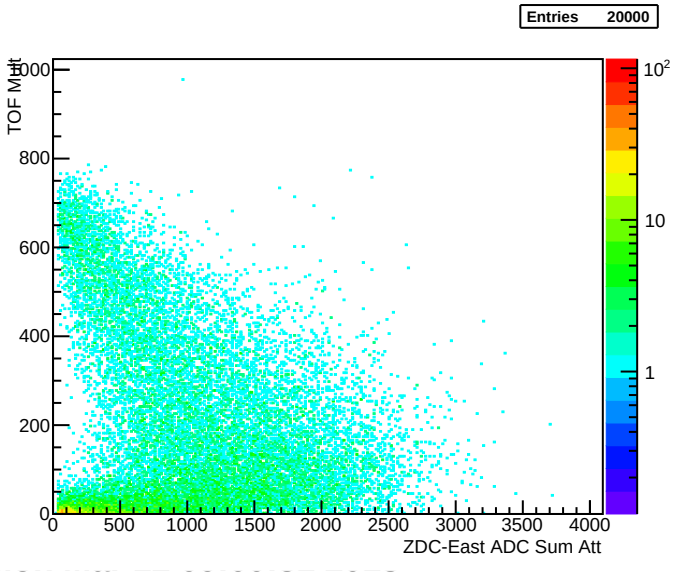


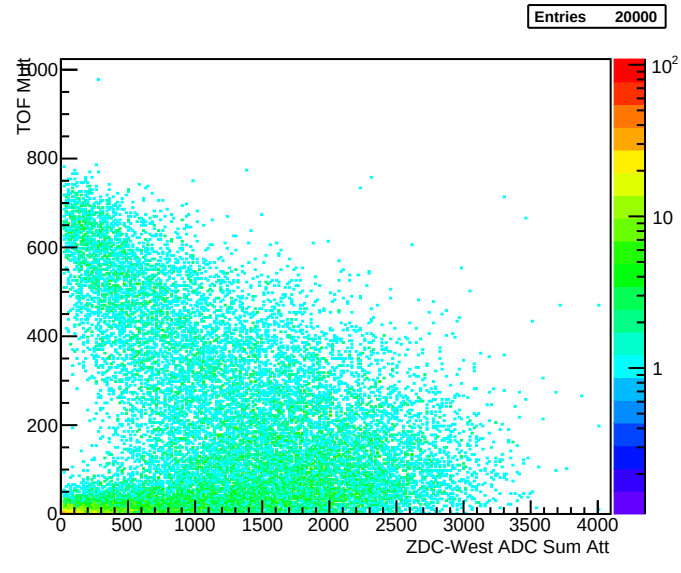

Entries 20000
TOF Mult
BBC-S-East ADC Sum

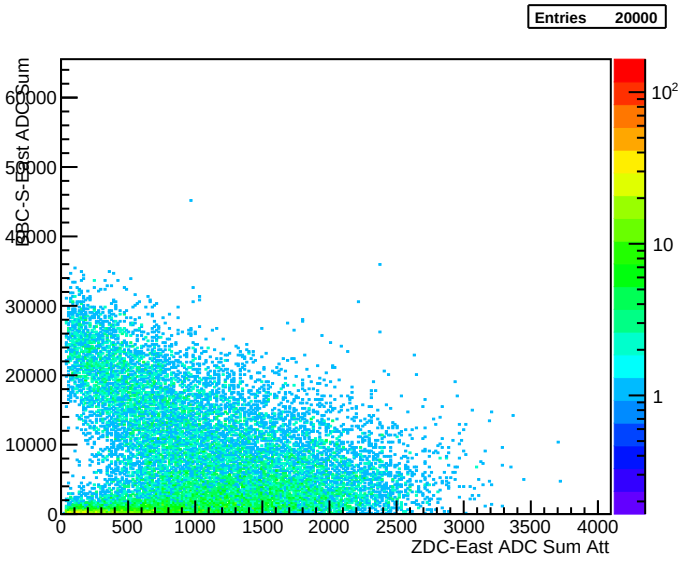

Entries 20000
TOF Mult
BBC-S-West ADC Sum

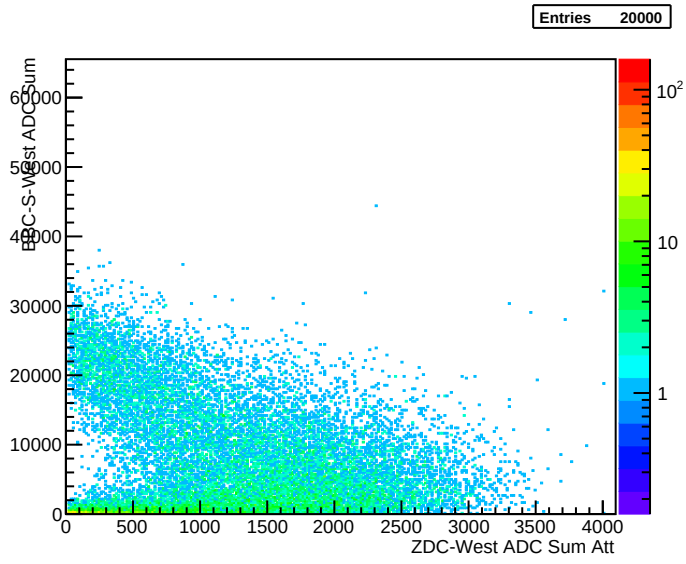

Entries 20000
TOF Mult
VPD-East ADC Sum

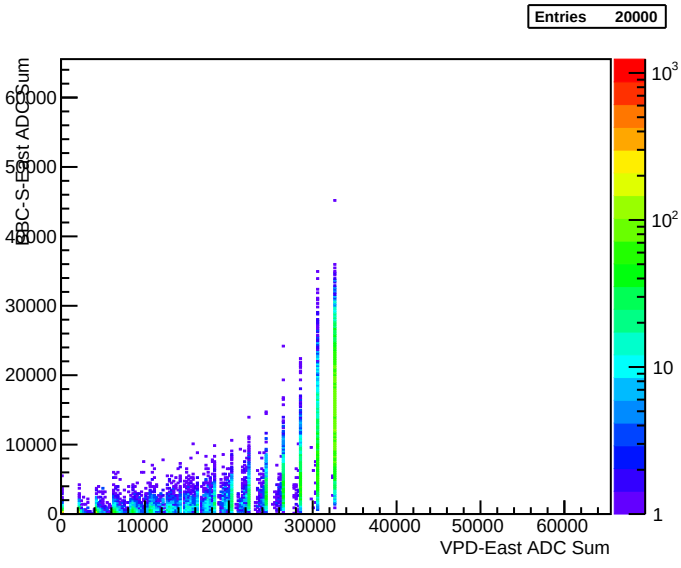

Entries 20000
TOF Mult
VPD-West ADC Sum

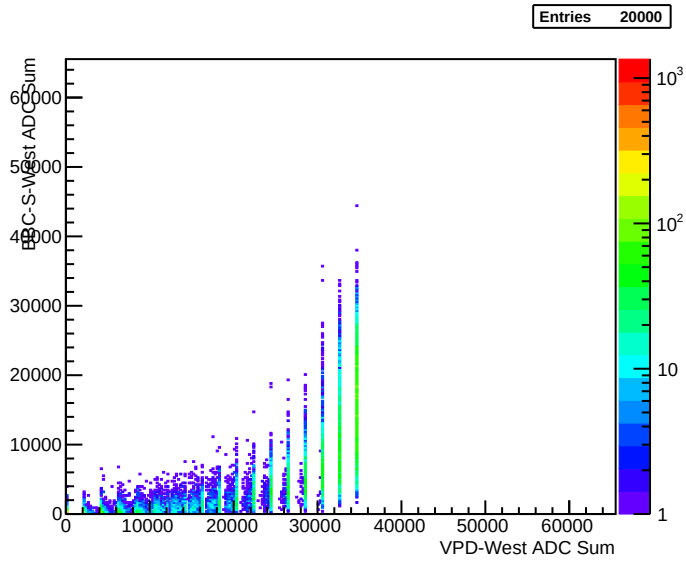

Entries 20000
TOF Mult
ZDC-East ADC Sum Att

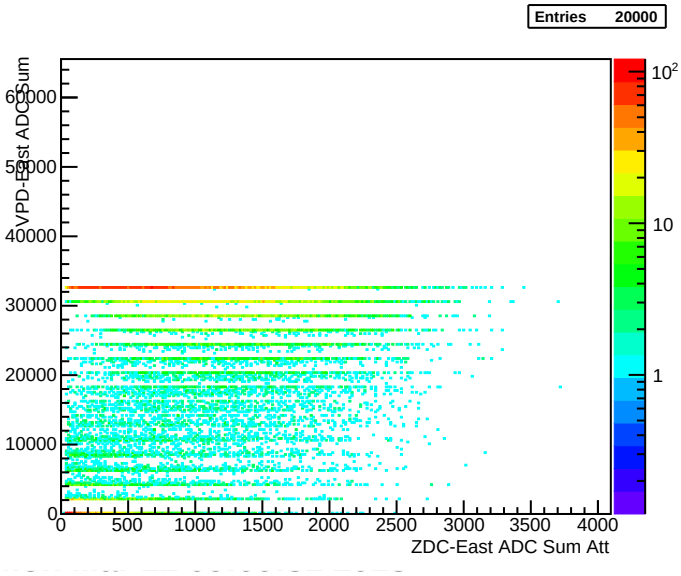

Entries 20000
TOF Mult
ZDC-West ADC Sum Att

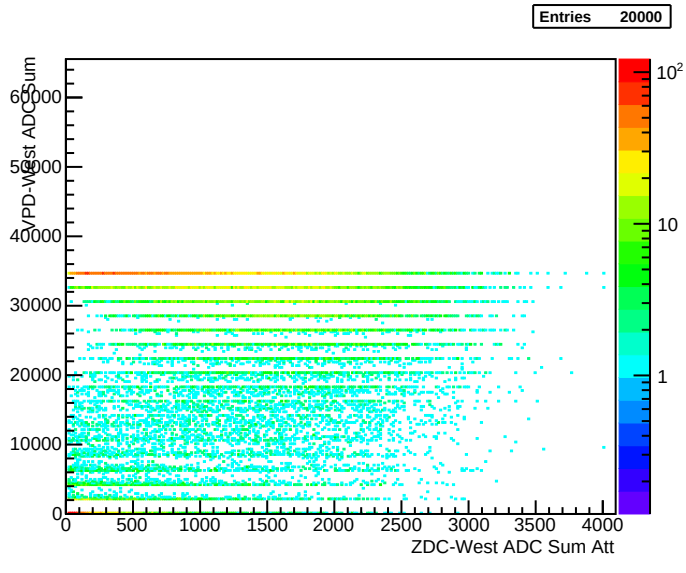

Entries 20000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

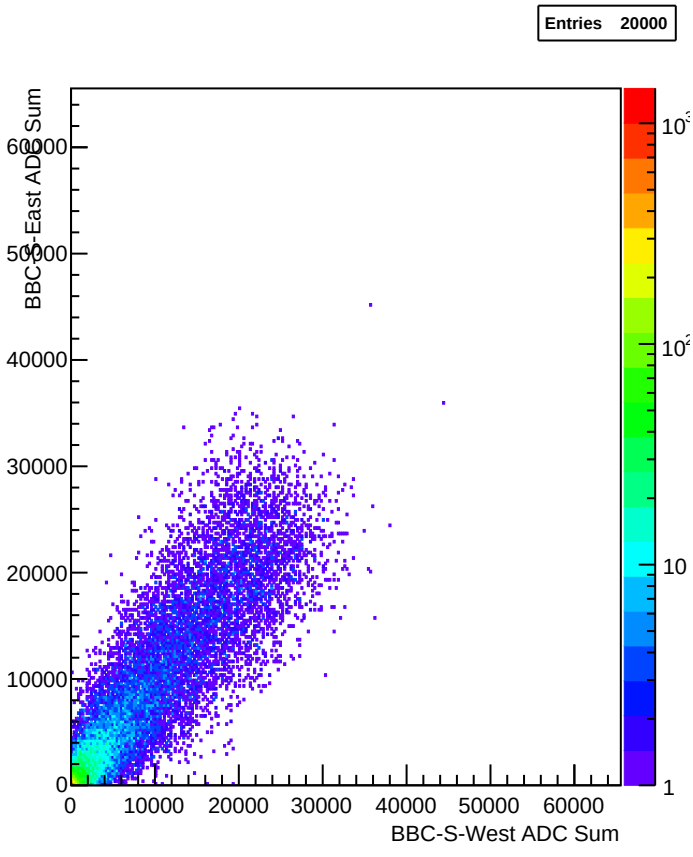

Entries 20000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

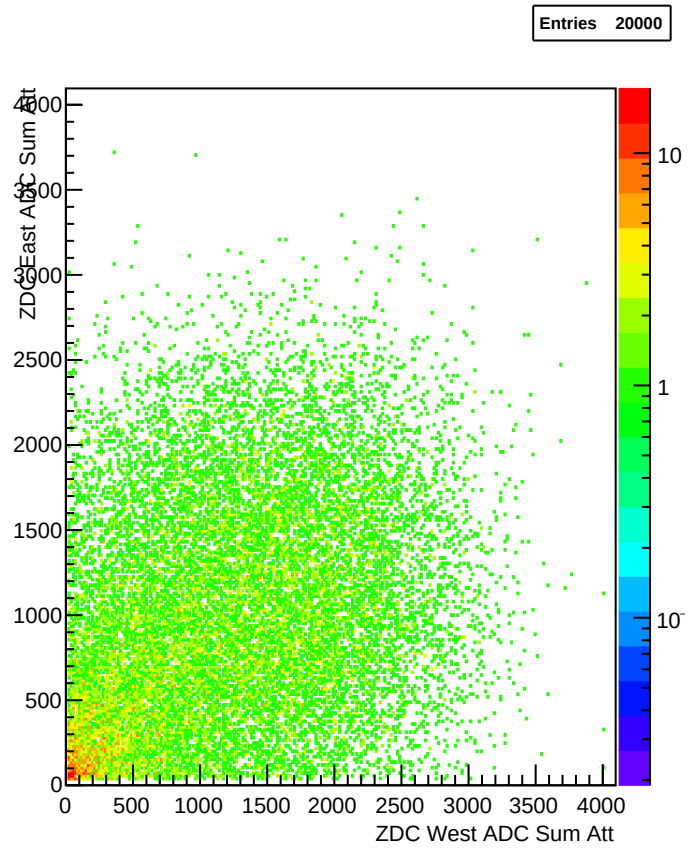

Entries 20000
BBC-S-East ADC Sum
VPD-East ADC Sum

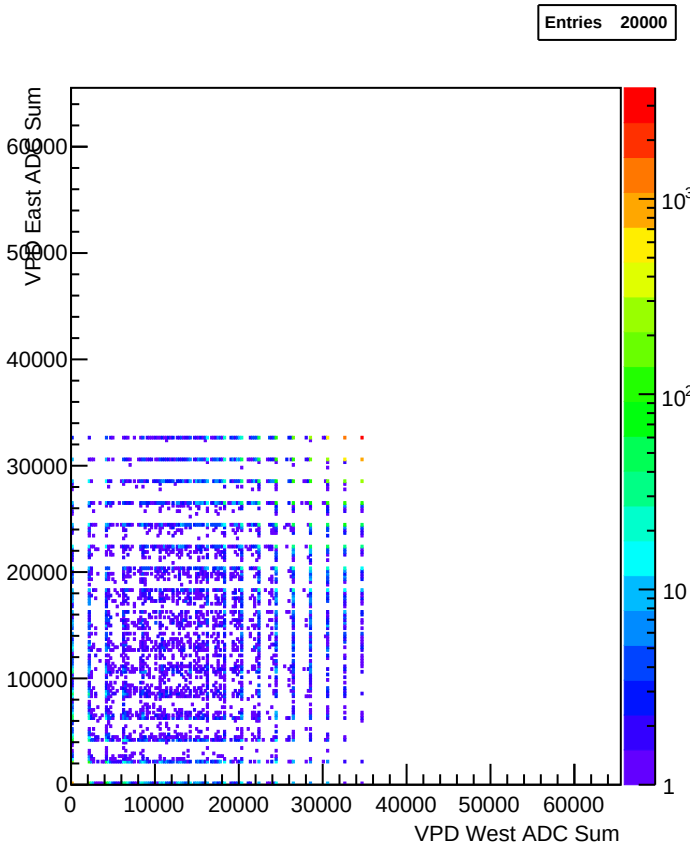

Entries 20000
BBC-S-West ADC Sum
VPD-West ADC Sum

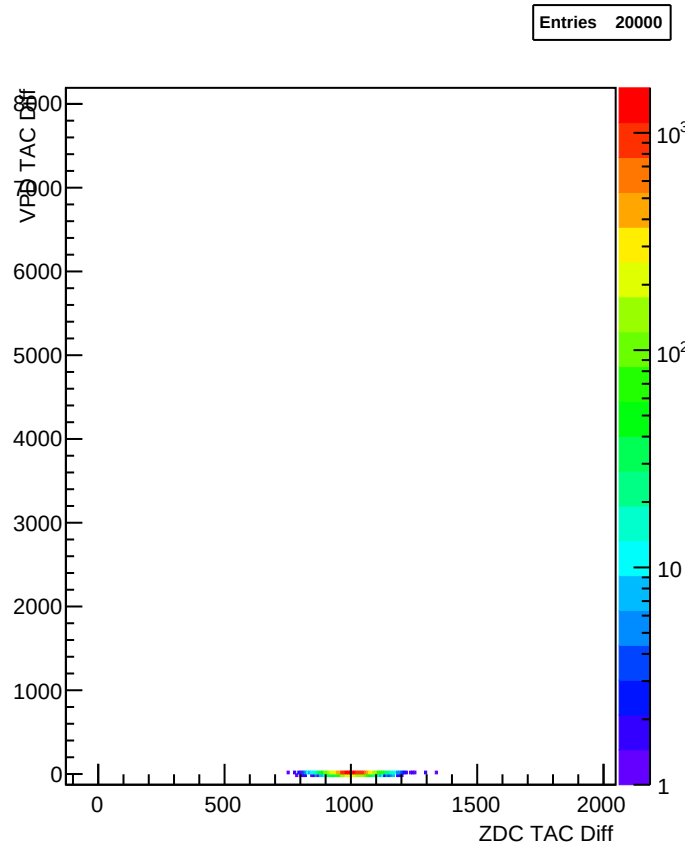

Entries 20000
VPD-East ADC Sum
ZDC-East ADC Sum Att

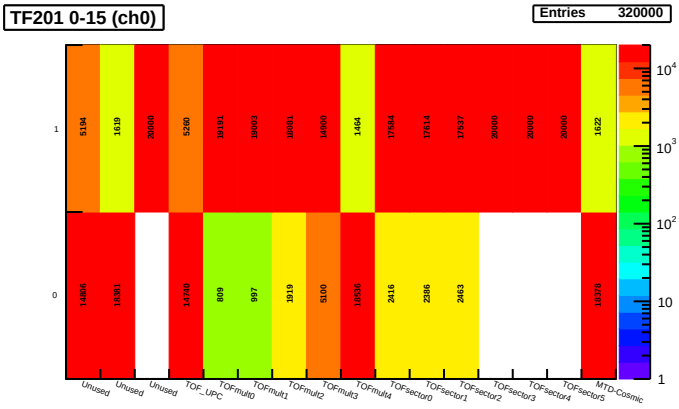

Entries 20000
VPD-West ADC Sum
ZDC-West ADC Sum Att

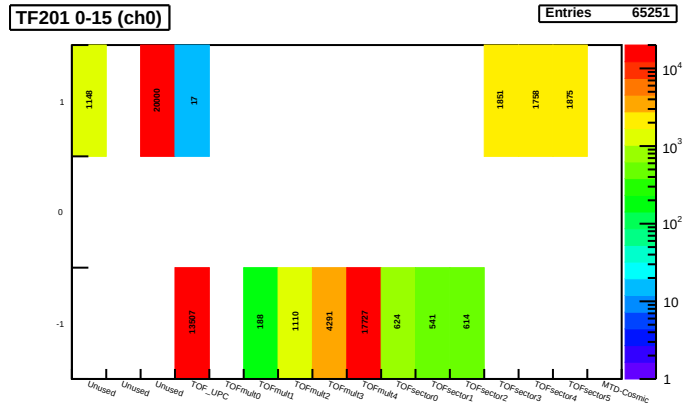

Entries 20000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

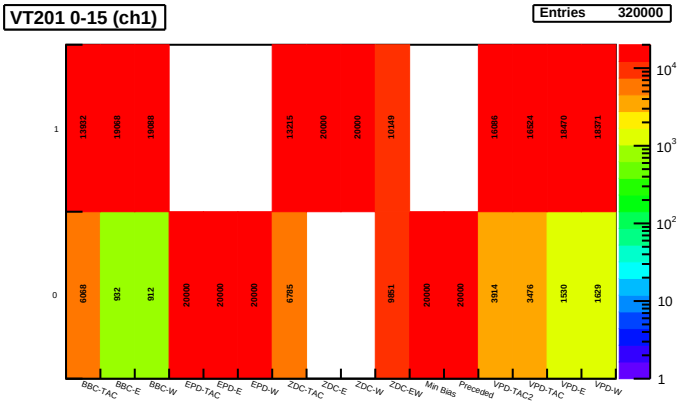

Entries 20000
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 20000
VPD East ADC Sum
VPD West ADC Sum


Entries 20000
VPD TAC Diff
ZDC TAC Diff


TF201 0-15 (ch0)
Entries 320000
5194 1619 20000 5260 19191 19903 19681 14900 1464 17584 17614 17537 20000 20000 20000 1622
14806 18331 14740 809 997 1919 5100 18536 2416 2386 2463 18378
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 65251
1148 20000 17 1851 1758 1875
13987 188 1110 4291 17727 624 541 614
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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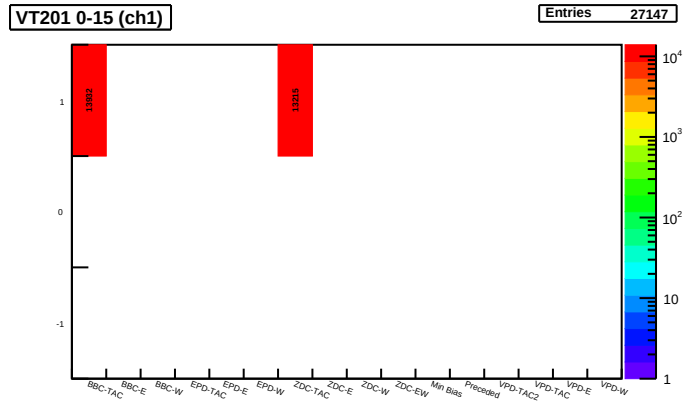
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W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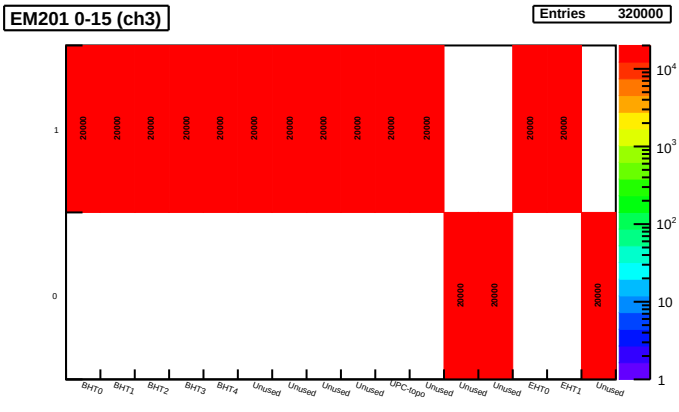
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W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Entries 320000
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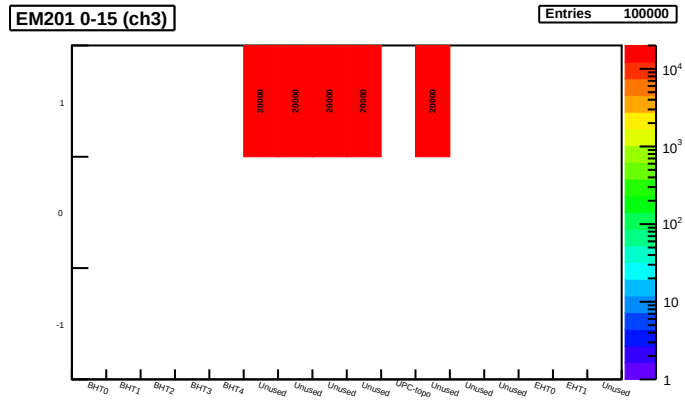
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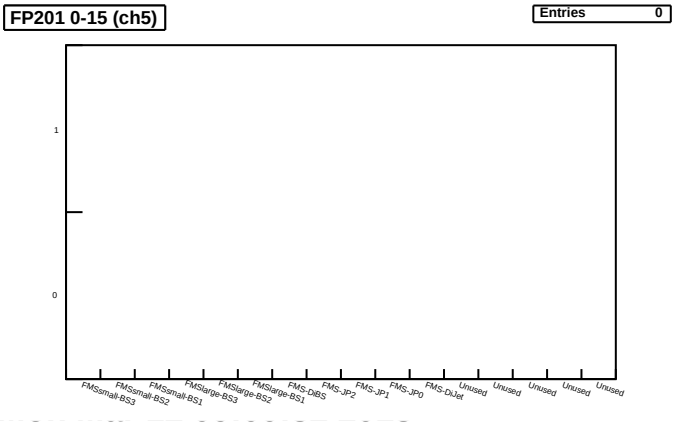
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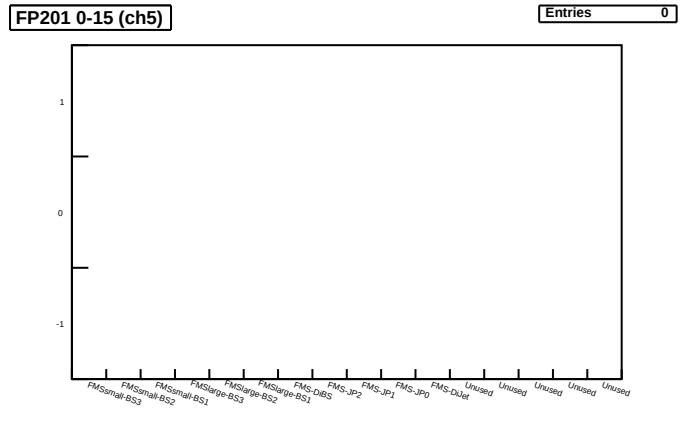
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