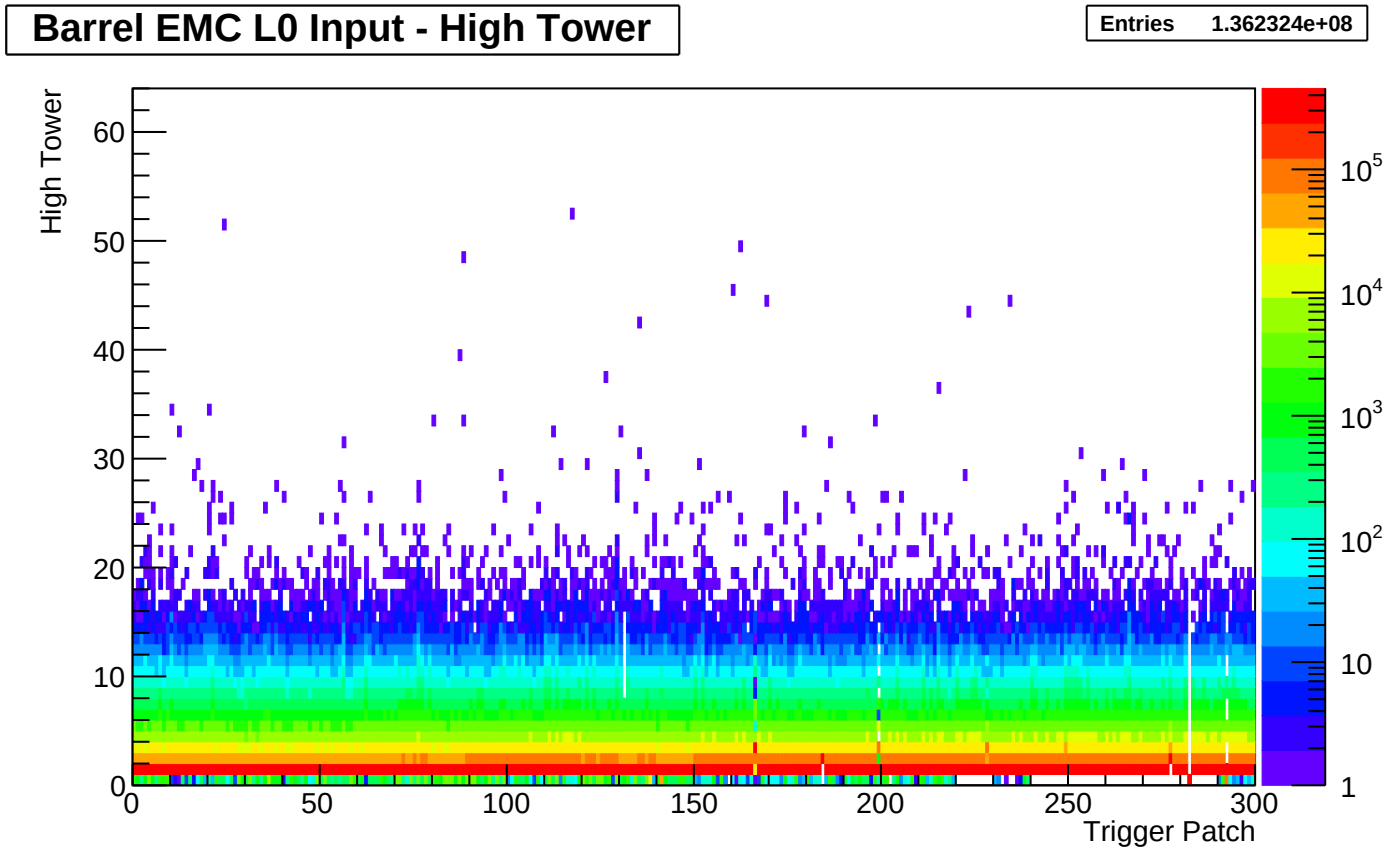
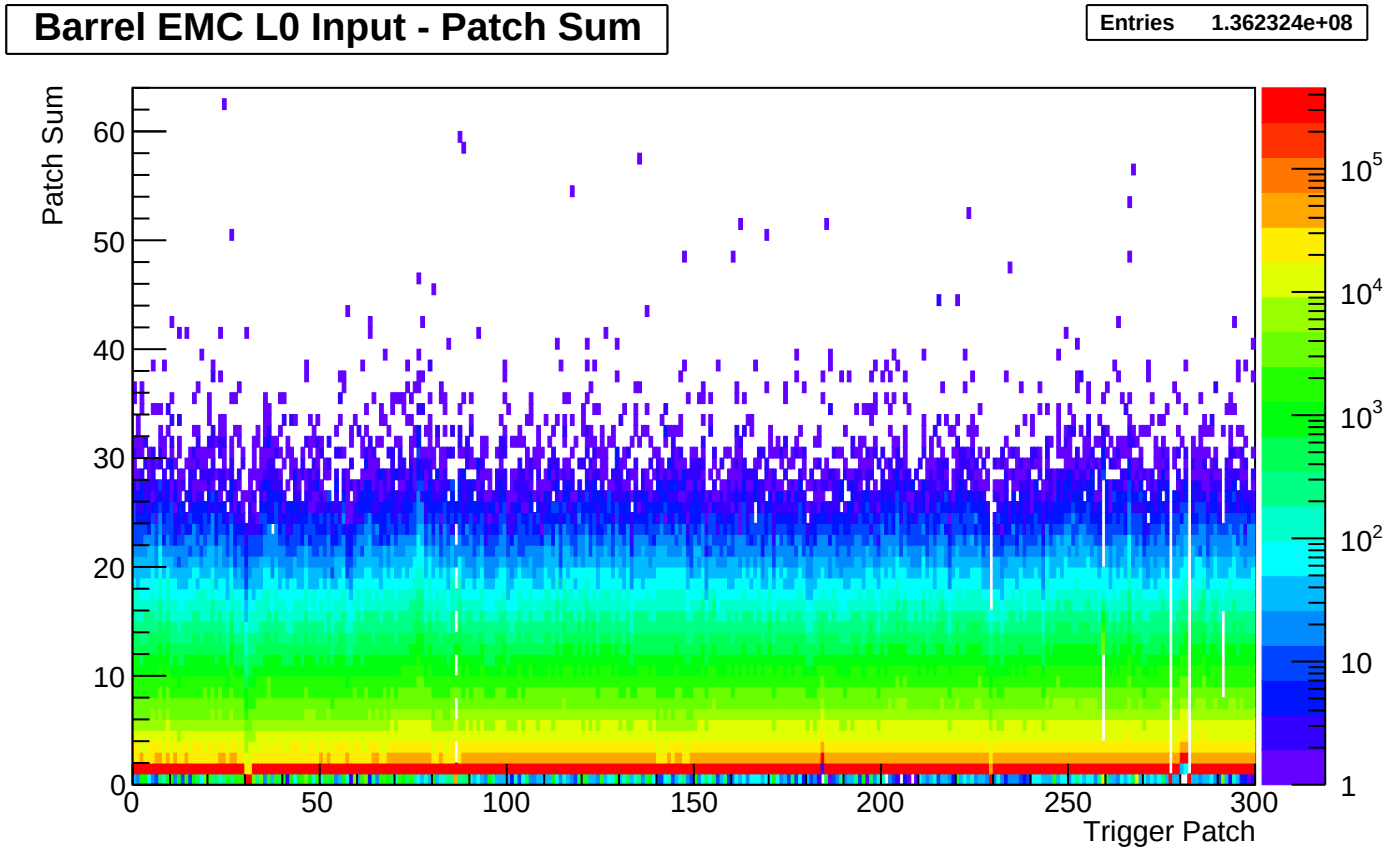
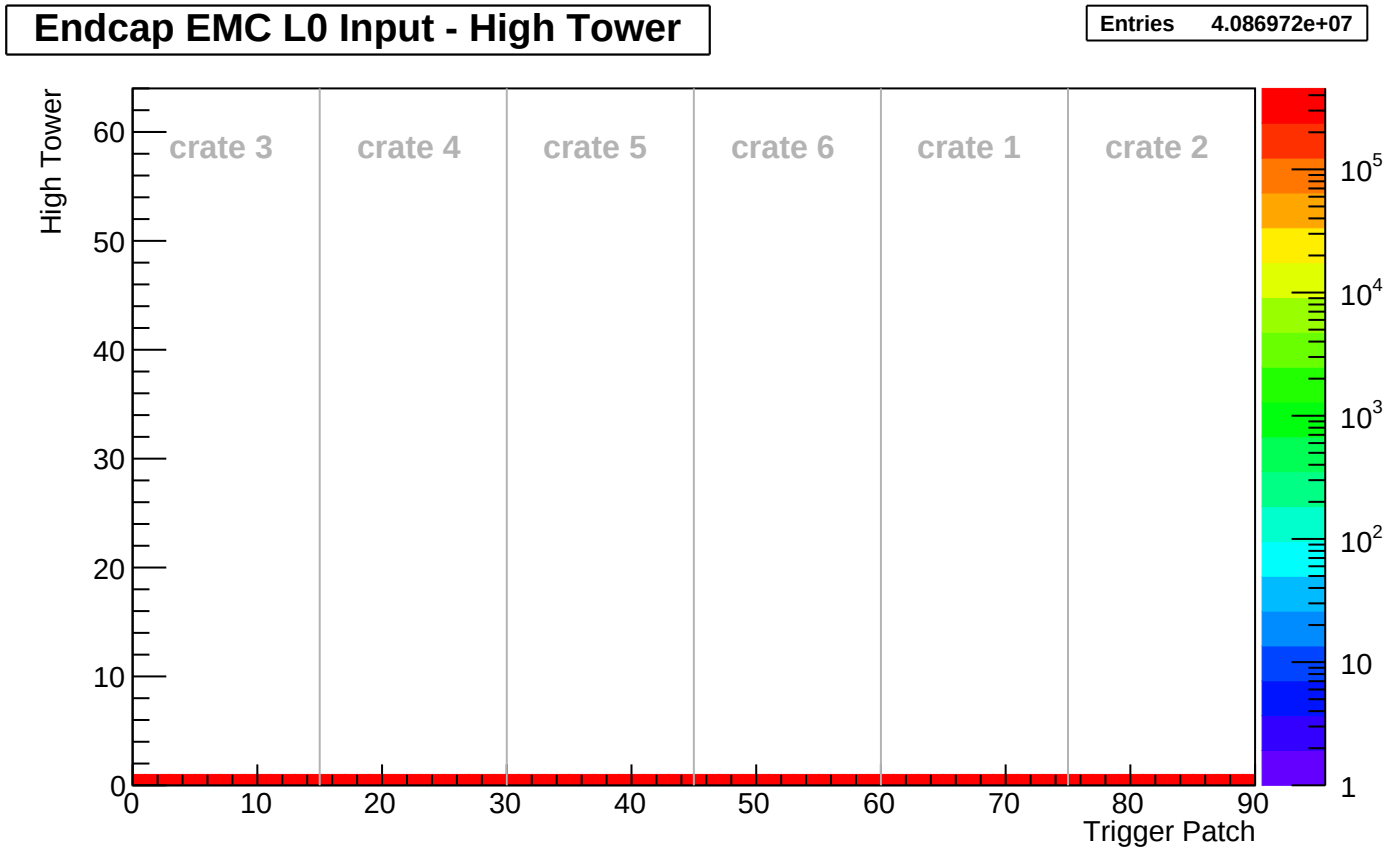
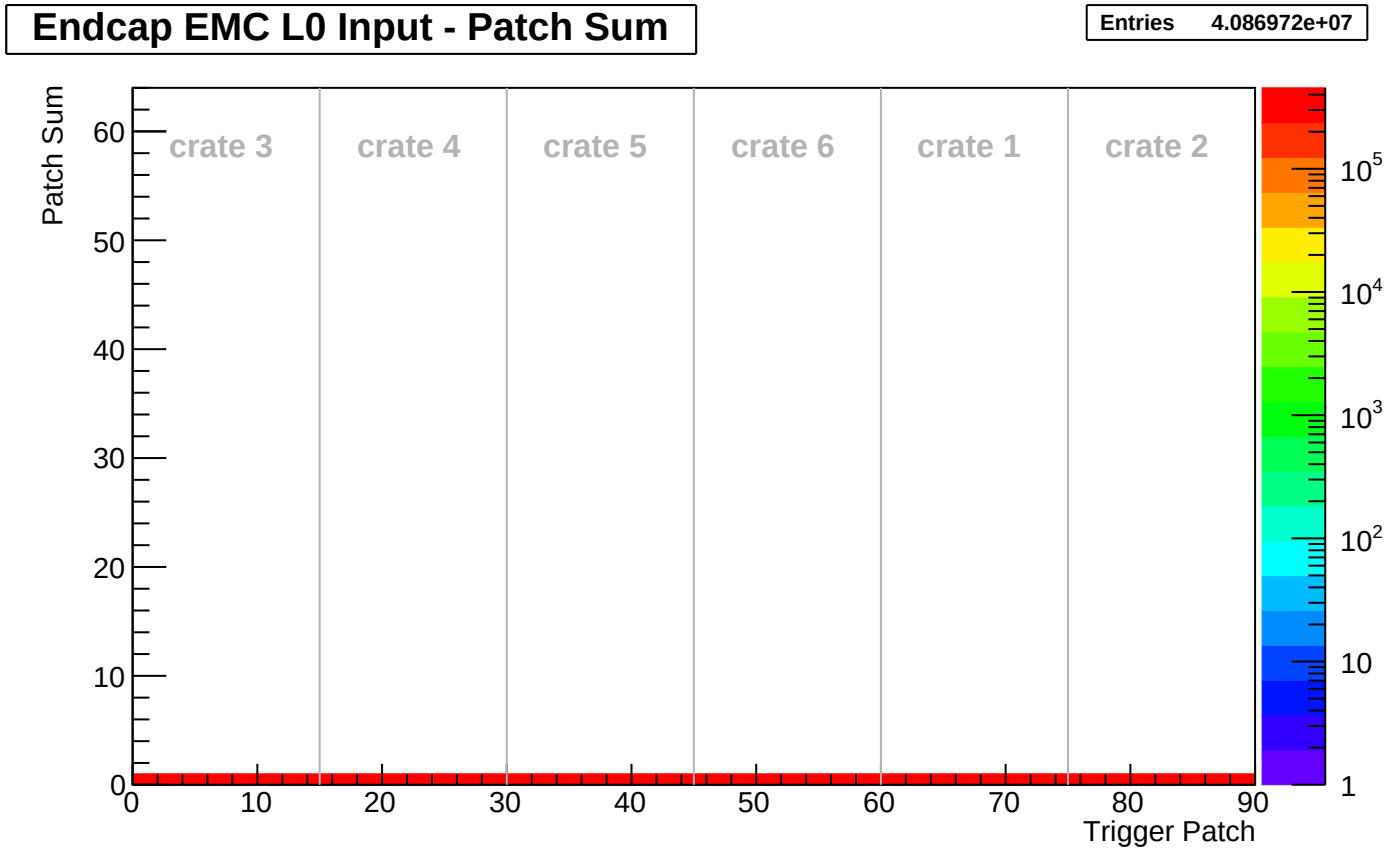
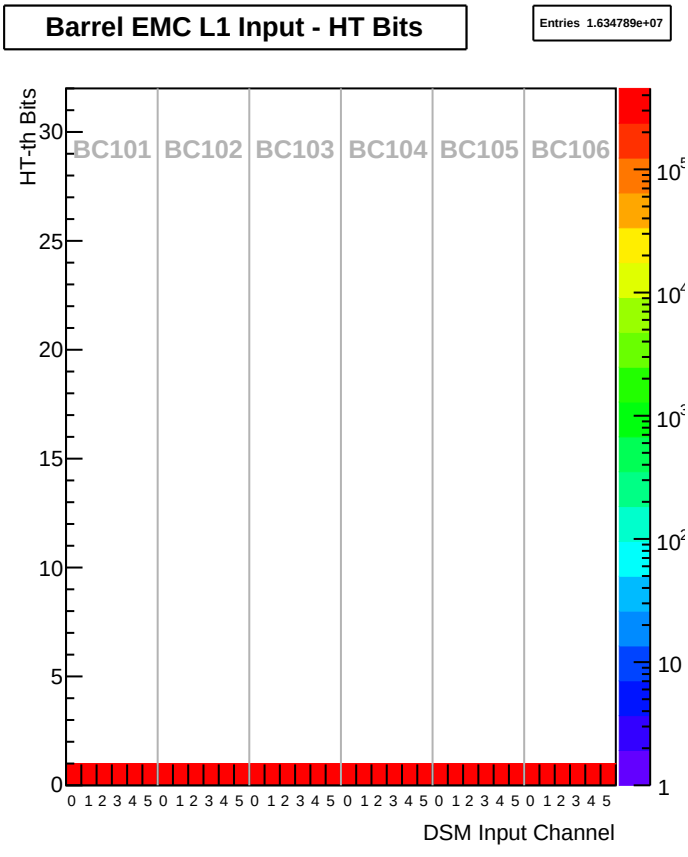
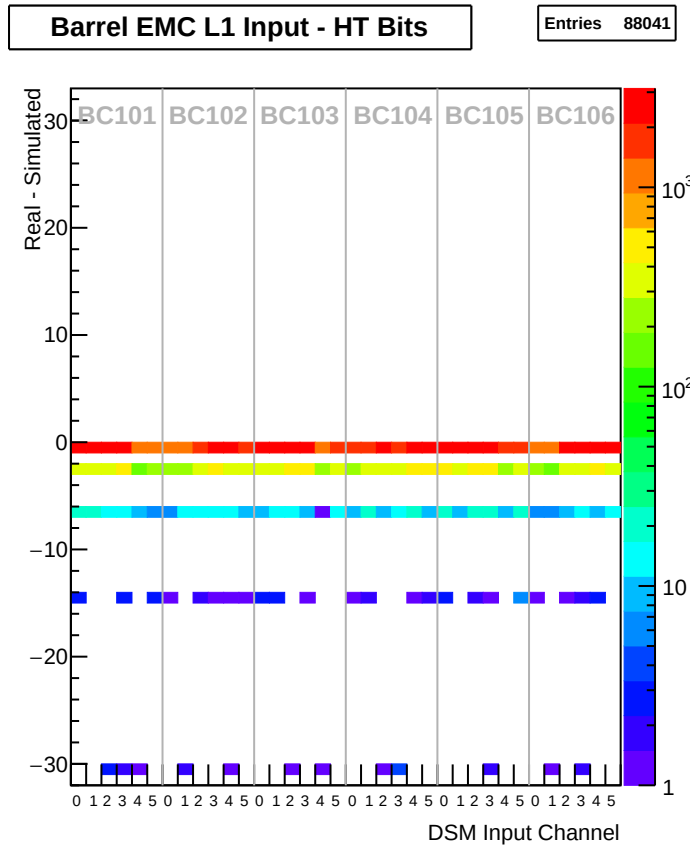

Barrel EMC L0 Input - High Tower
Entries 1.362324e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

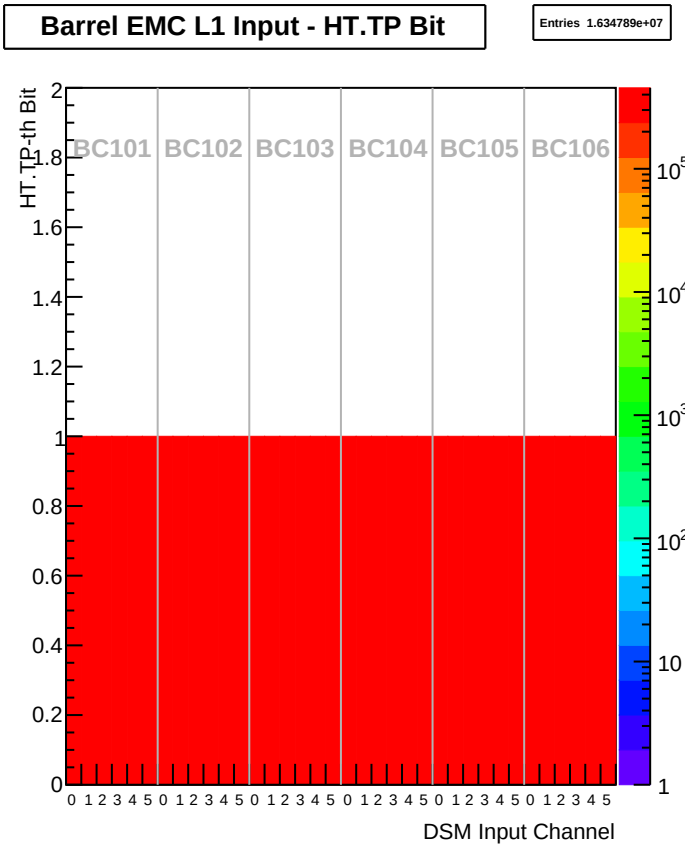

Barrel EMC L0 Input - Patch Sum
Entries 1.362324e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

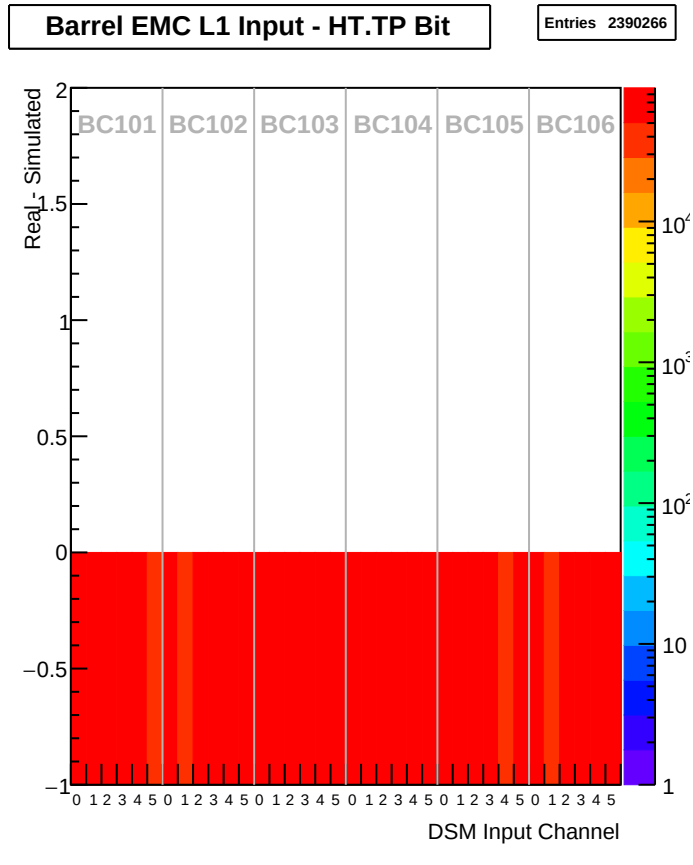

Endcap EMC L0 Input - High Tower
Entries 4.086972e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

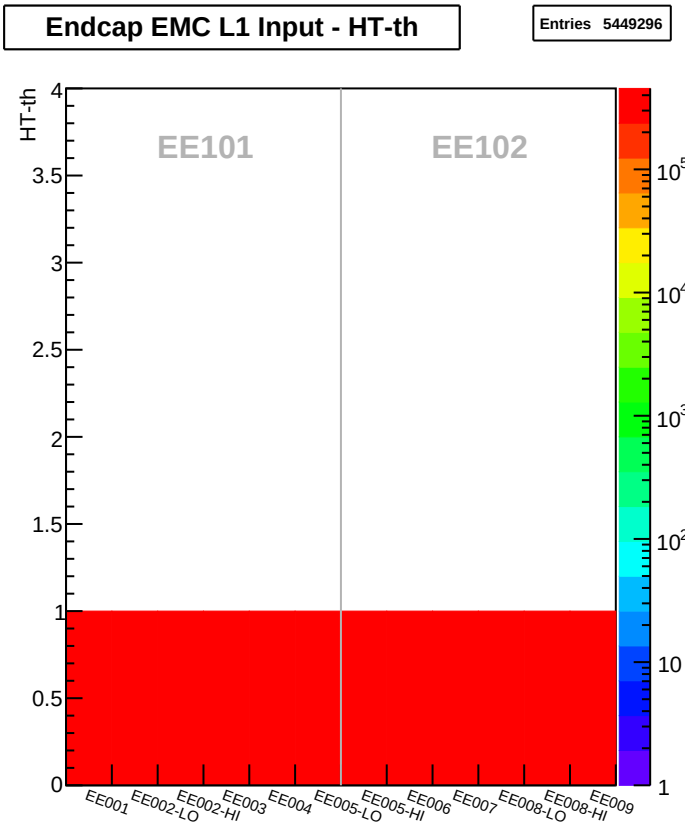

Endcap EMC L0 Input - Patch Sum
Entries 4.086972e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

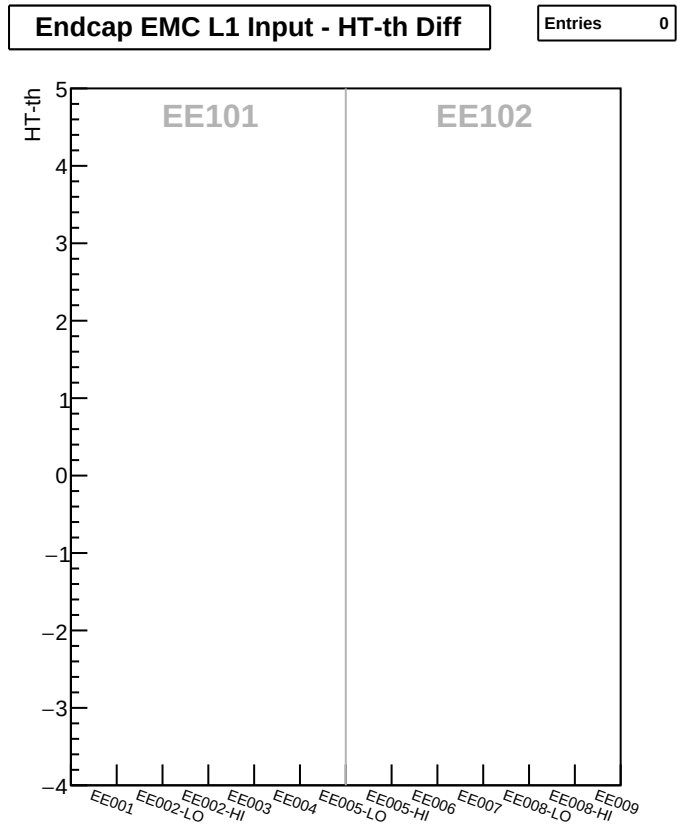

Barrel EMC L1 Input - HT Bits
Entries 1.634789e+07
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

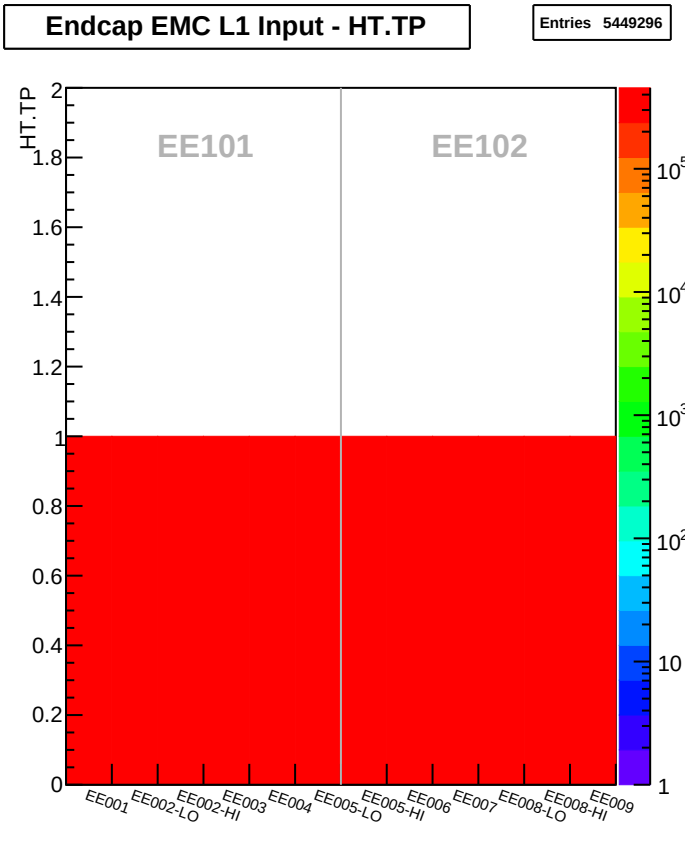

Barrel EMC L1 Input - HT Bits
Entries 88041
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

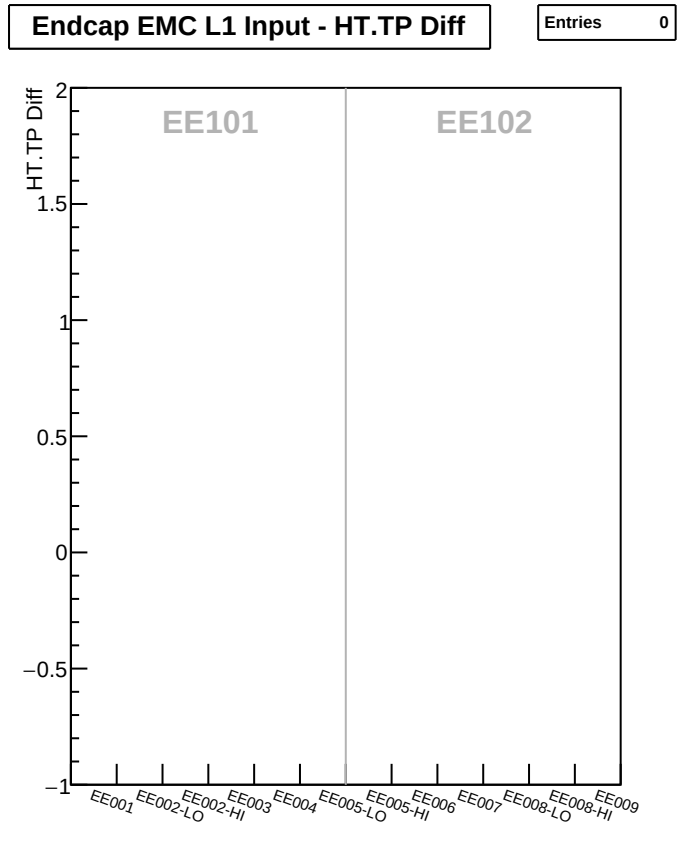

Barrel EMC L1 Input - HT.TP Bit
Entries 1.634789e+07
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

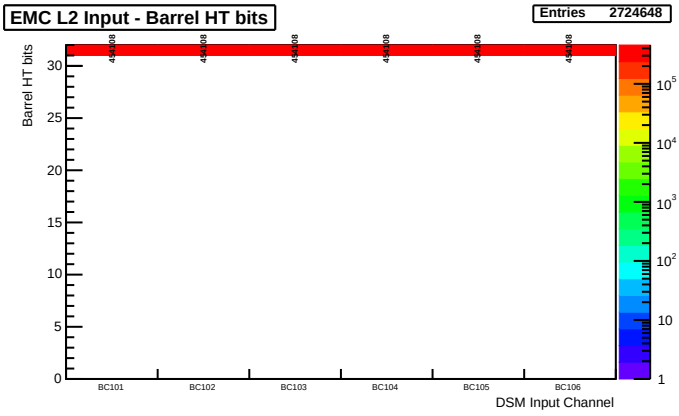

Barrel EMC L1 Input - HT.TP Bit
Entries 2390266
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

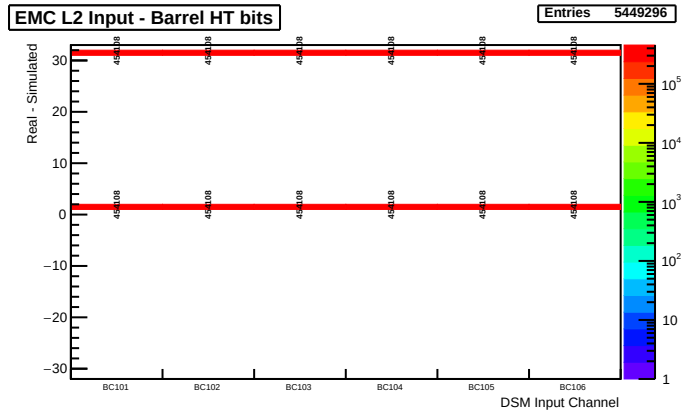

Endcap EMC L1 Input - HT-th
Entries 5449296
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

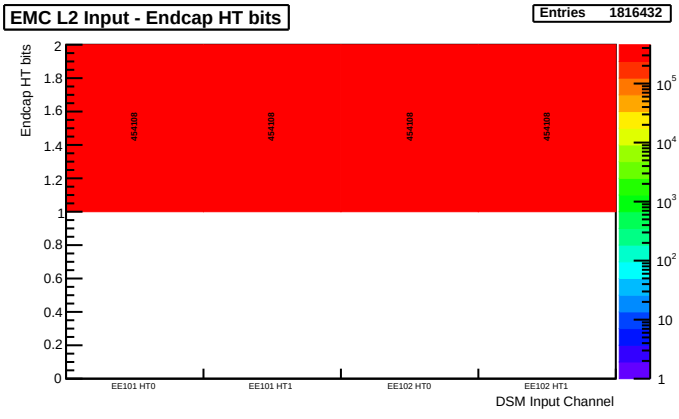

Endcap EMC L1 Input - HT-th Diff
Entries 0
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

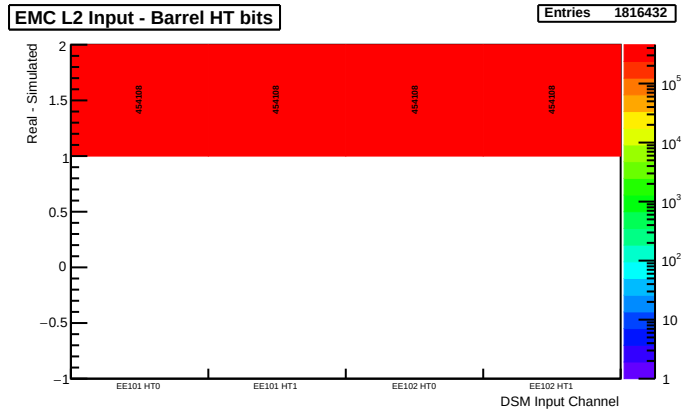

Endcap EMC L1 Input - HT.TP
Entries 5449296
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

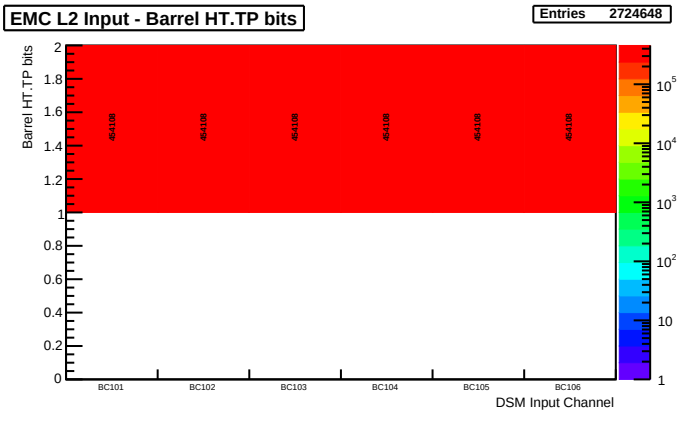

Endcap EMC L1 Input - HT.TP Diff
Entries 0
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

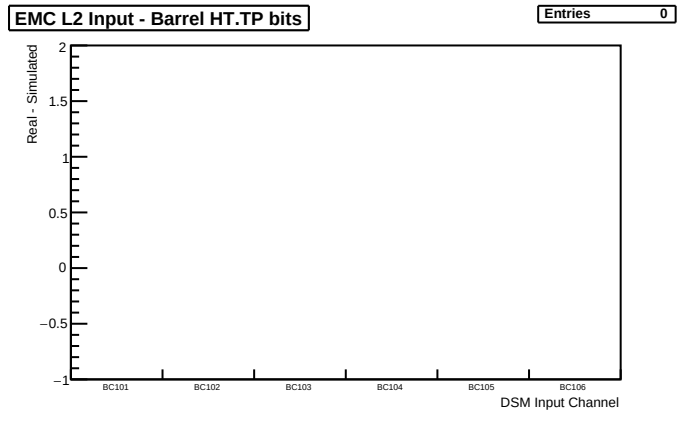

EMC L2 Input - Barrel HT bits
Entries 2724648
Barrel HT bits
454108
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

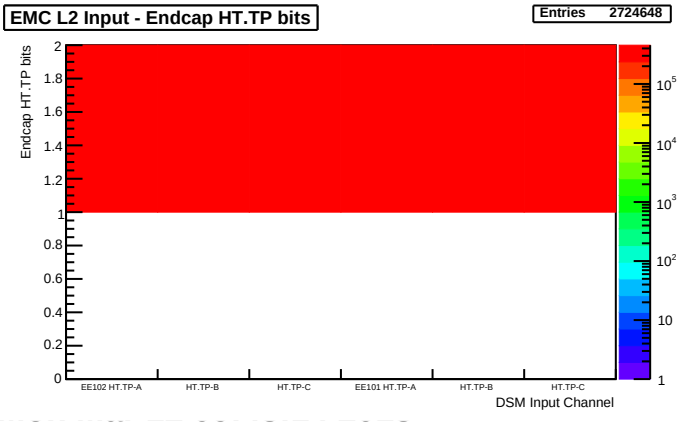

EMC L2 Input - Barrel HT bits
Entries 5449296
Real - Simulated
454108
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

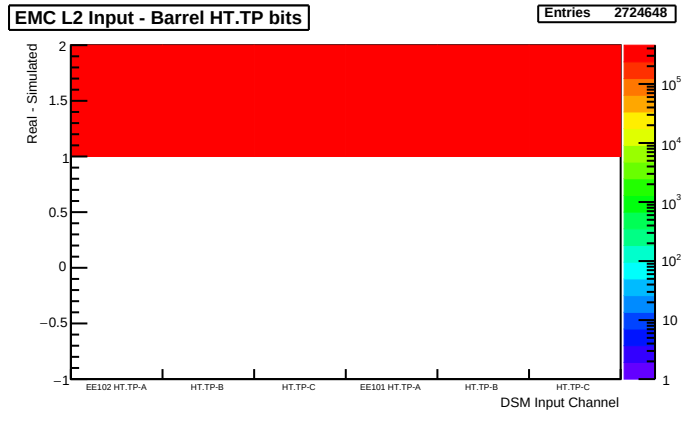

EMC L2 Input - Endcap HT bits
Entries 1816432
Endcap HT bits
454108
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1
DSM Input Channel

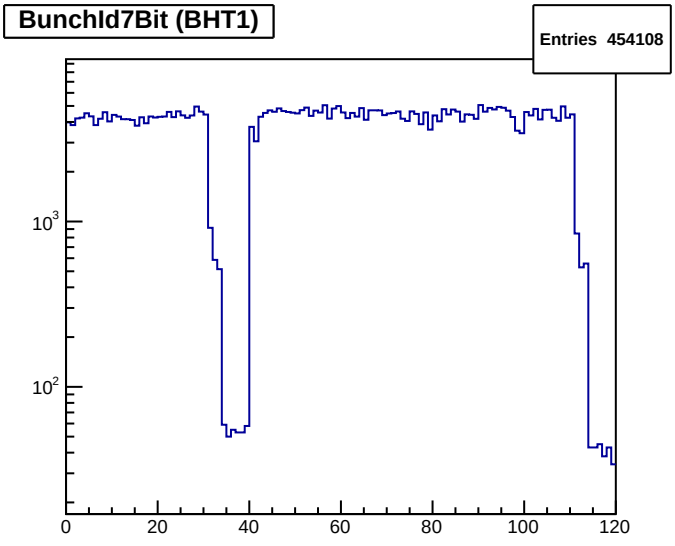

EMC L2 Input - Barrel HT bits
Entries 1816432
Real - Simulated
454108
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1
DSM Input Channel

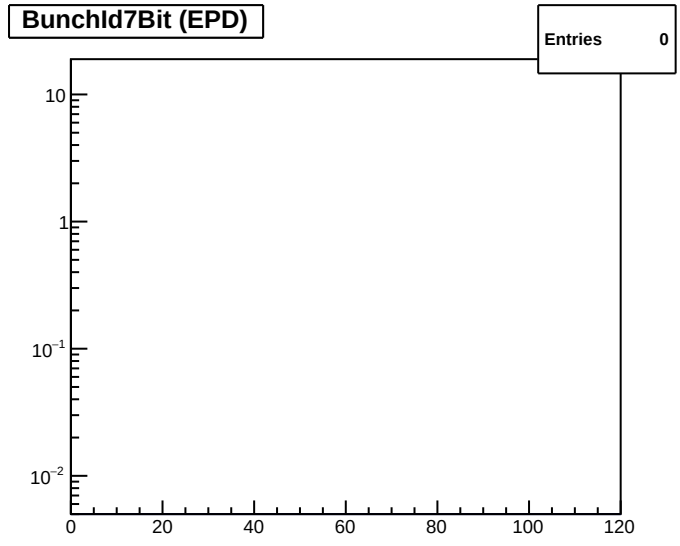

EMC L2 Input - Barrel HT.TP bits
Entries 2724648
Barrel HT.TP bits
454108
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

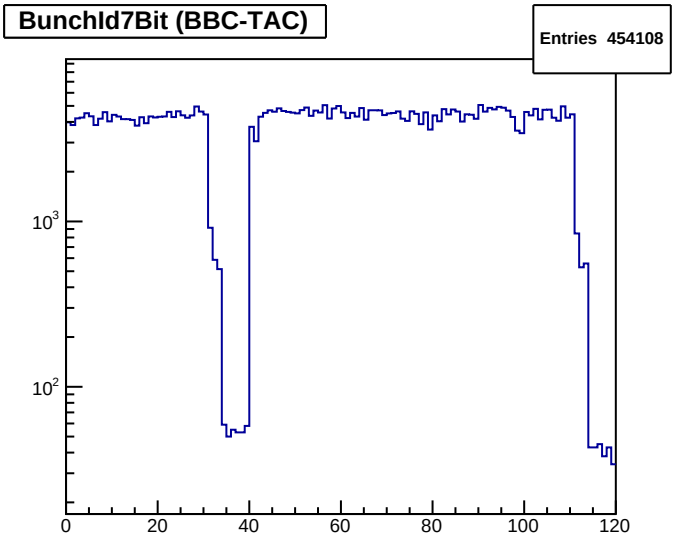

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

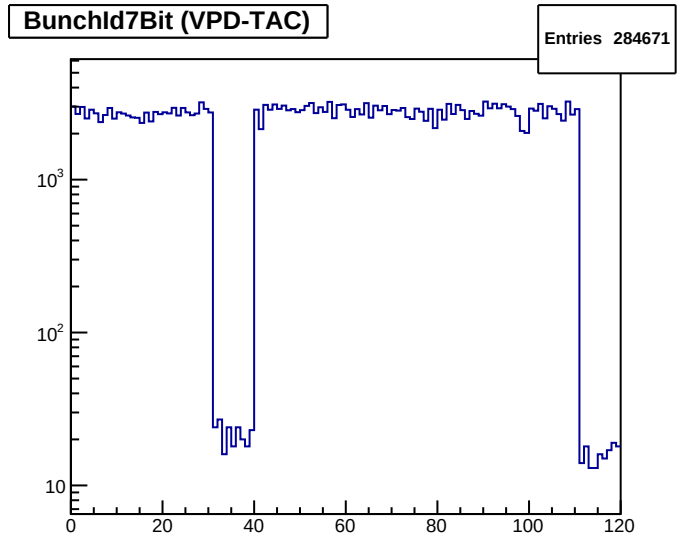

EMC L2 Input - Endcap HT.TP bits
Entries 2724648
Endcap HT.TP bits
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C
DSM Input Channel

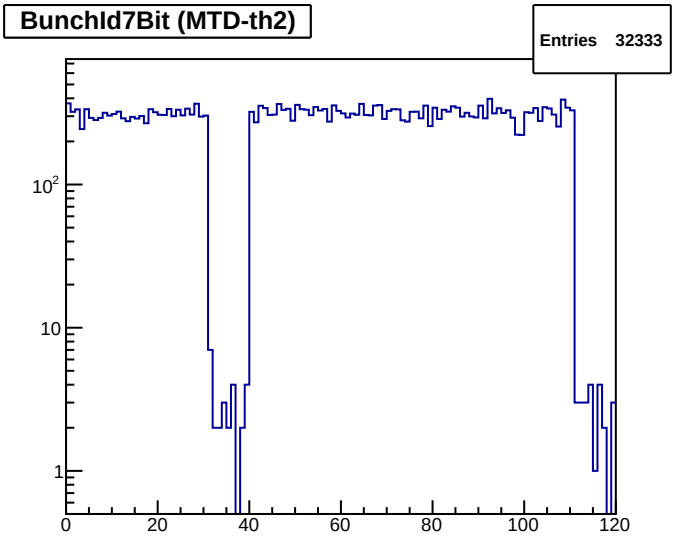

EMC L2 Input - Barrel HT.TP bits
Entries 2724648
Real - Simulated
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C
DSM Input Channel

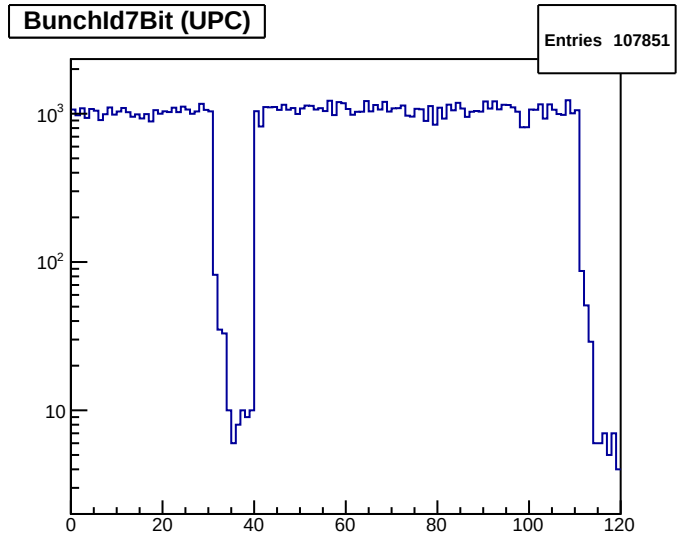

BunchId7Bit (BHT1)
Entries 454108
10^3
10^2
0 20 40 60 80 100 120

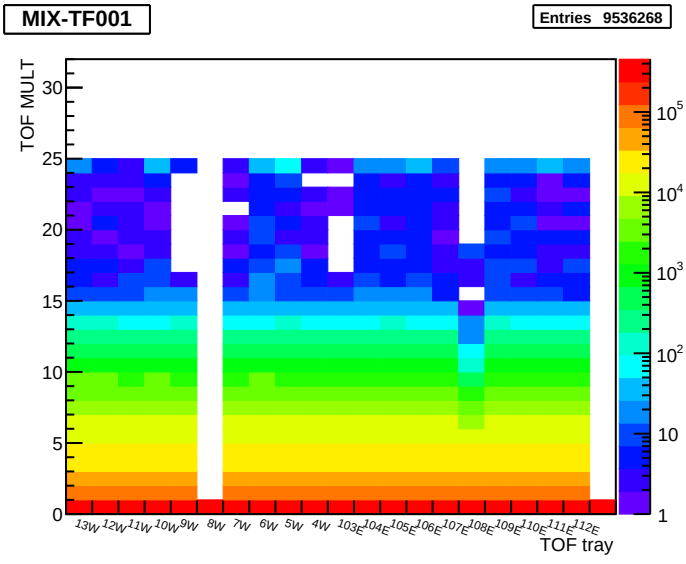

BunchId7Bit (EPD)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

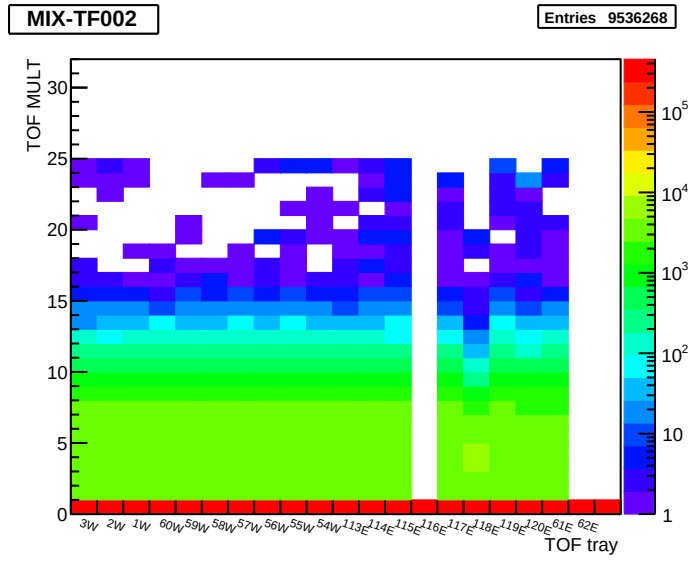

BunchId7Bit (BBC-TAC)
Entries 454108
10^3
10^2
0 20 40 60 80 100 120

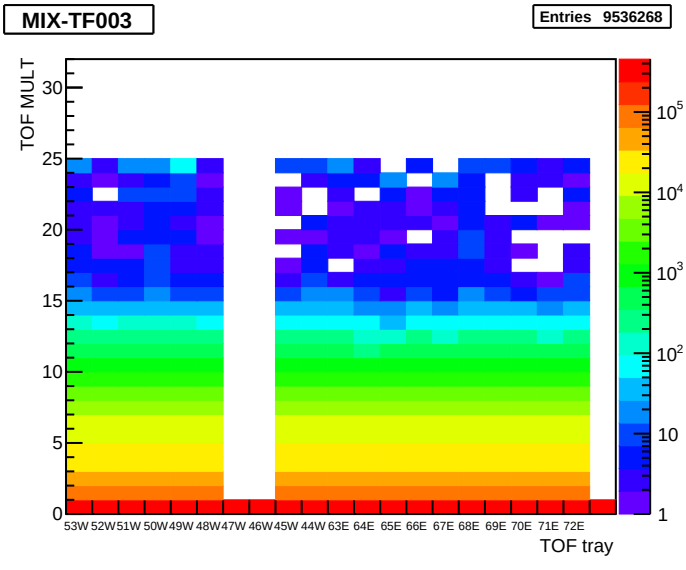

BunchId7Bit (VPD-TAC)
Entries 284671
10^3
10^2
10
0 20 40 60 80 100 120

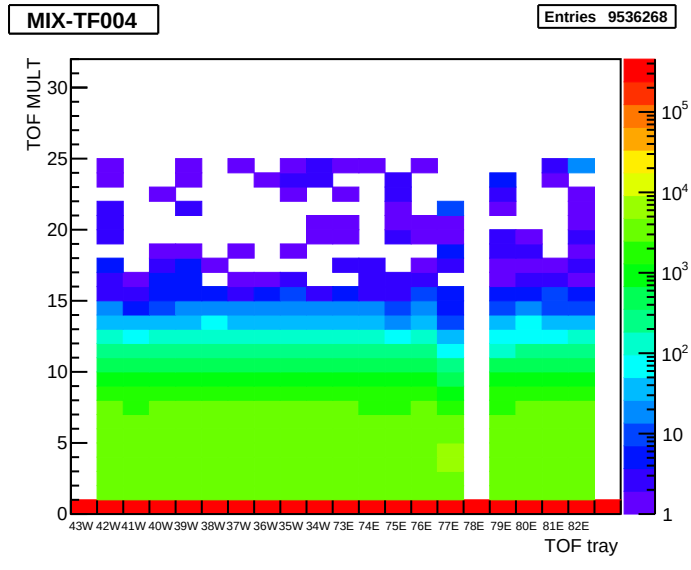

BunchId7Bit (MTD-th2)
Entries 32333
10^2
10
1
0 20 40 60 80 100 120

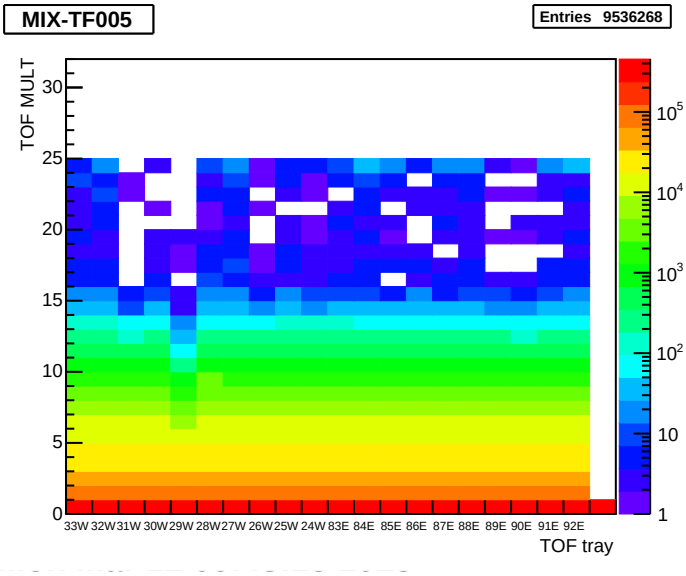

BunchId7Bit (UPC)
Entries 107851
10^3
10^2
10
0 20 40 60 80 100 120

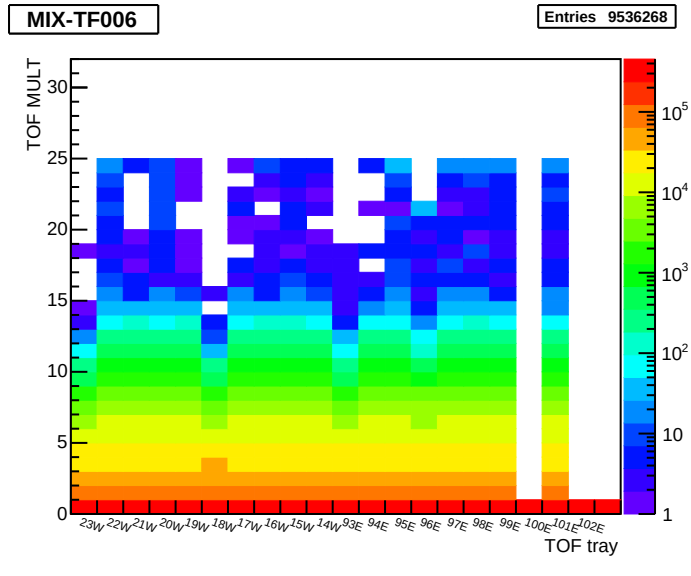

MIX-TF001
Entries 9536268
TOF MULT
TOF tray

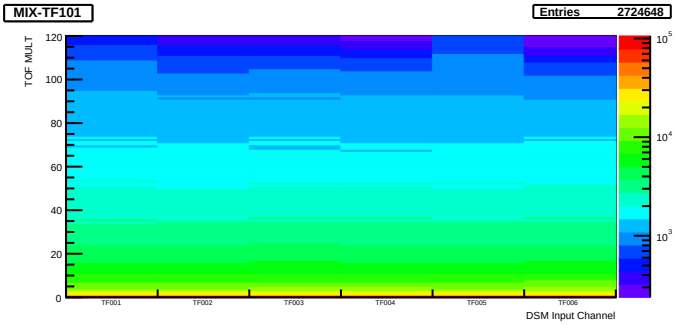

MIX-TF002
Entries 9536268
TOF MULT
TOF tray

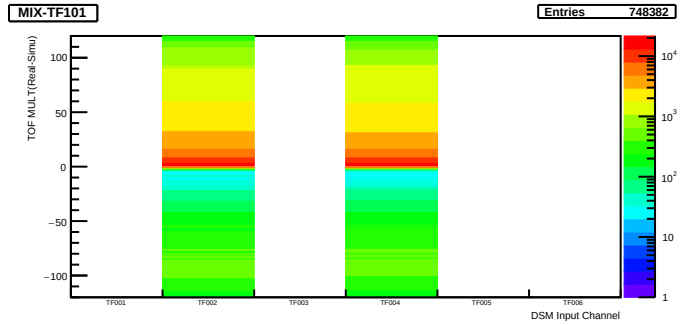

MIX-TF003
Entries 9536268
TOF MULT
TOF tray

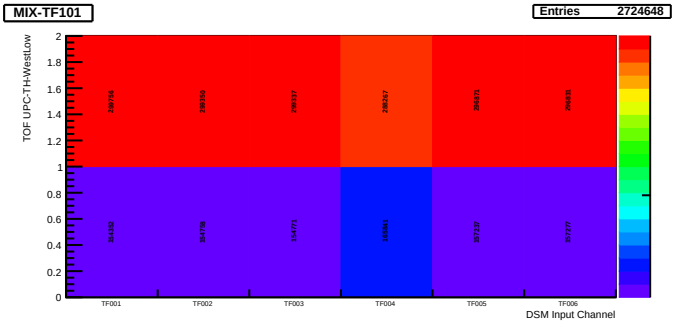

MIX-TF004
Entries 9536268
TOF MULT
TOF tray

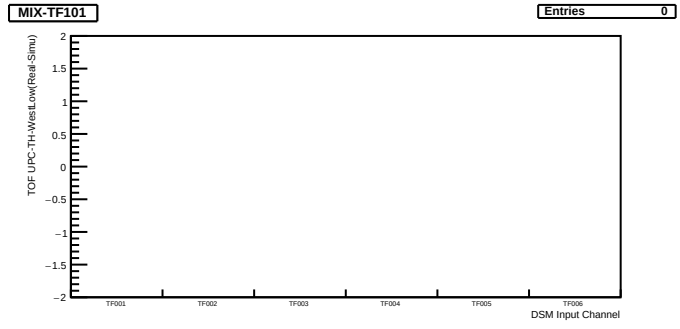

MIX-TF005
Entries 9536268
TOF MULT
TOF tray

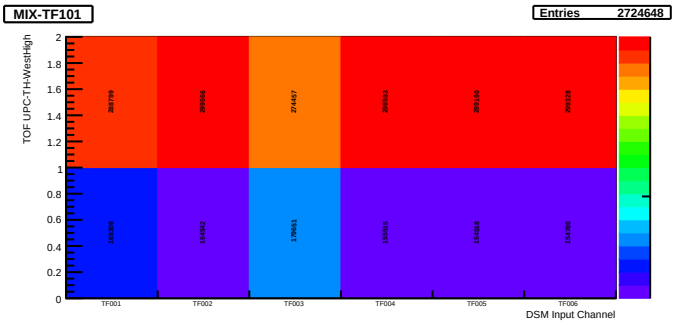

MIX-TF006
Entries 9536268
TOF MULT
TOF tray

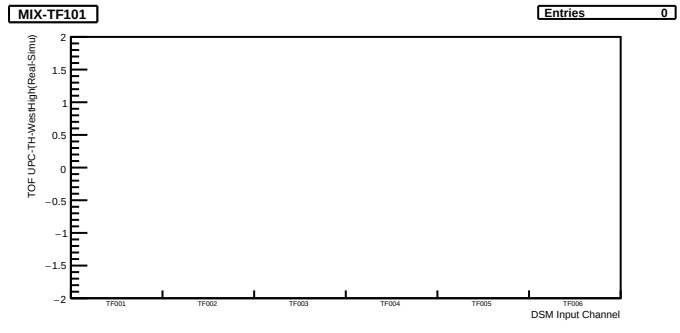

MIX-TF101
Entries 2724648
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

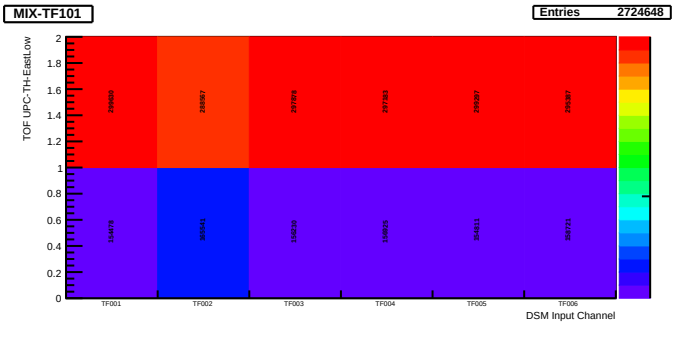

MIX-TF101
Entries 748382
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

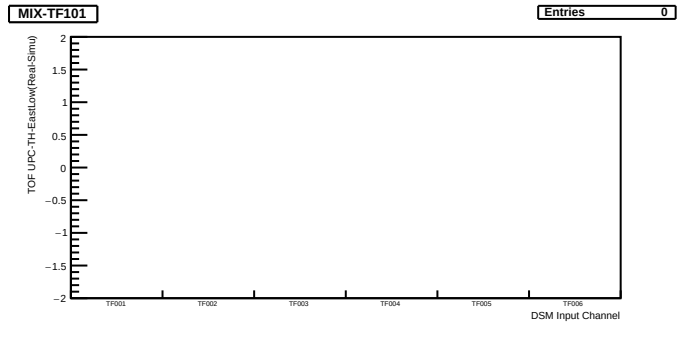

MIX-TF101
Entries 2724648
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

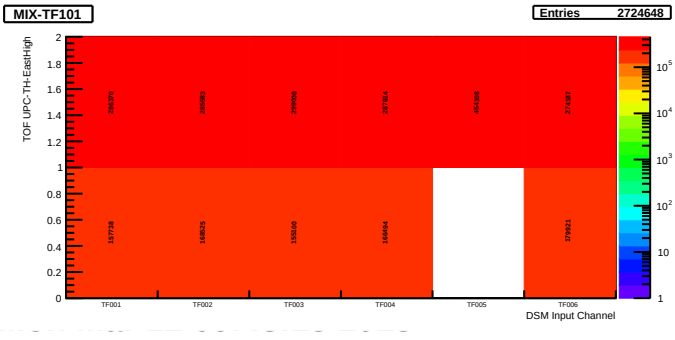

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

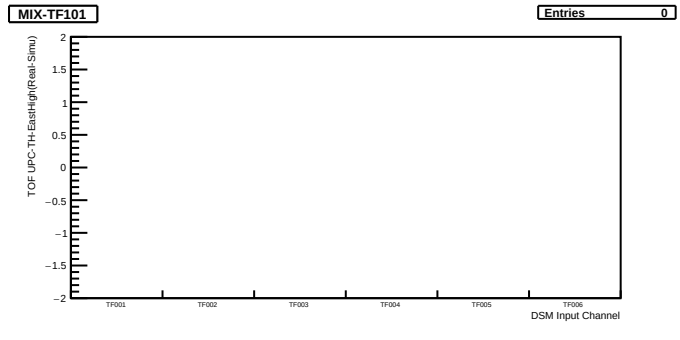

MIX-TF101
Entries 2724648
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

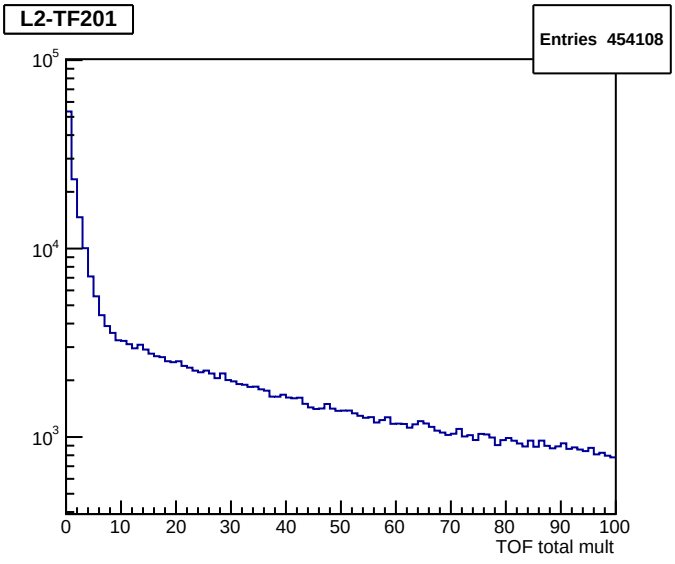

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

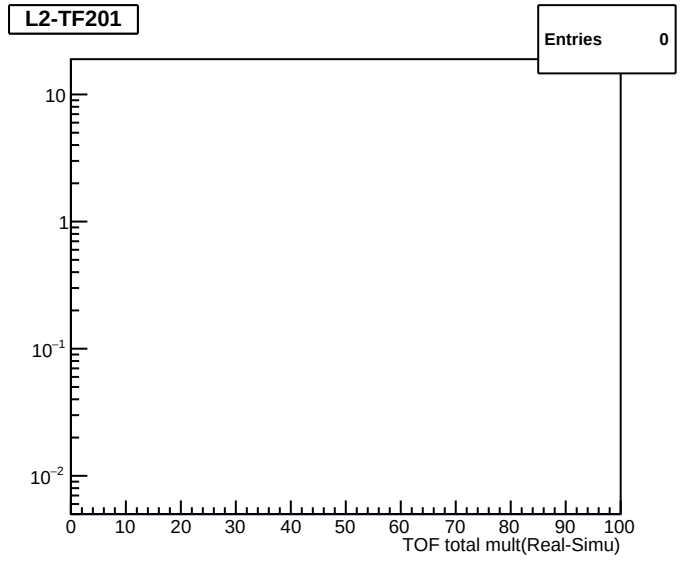

MIX-TF101
Entries 2724648
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

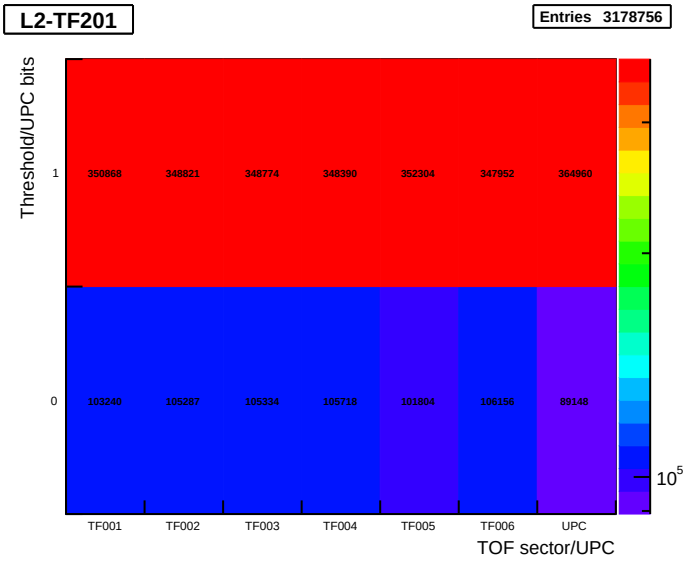

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

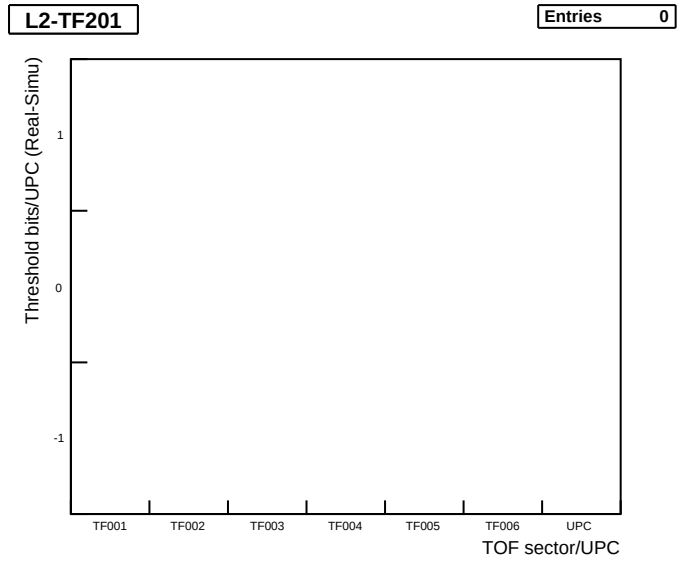

MIX-TF101
Entries 2724648
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

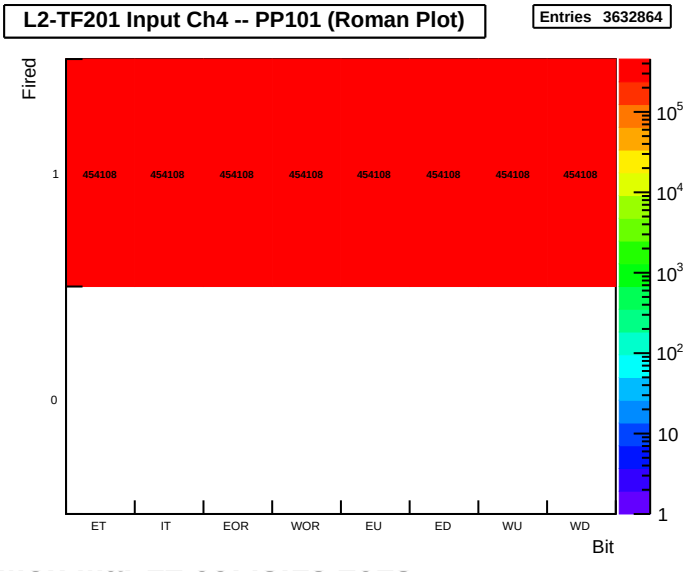

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

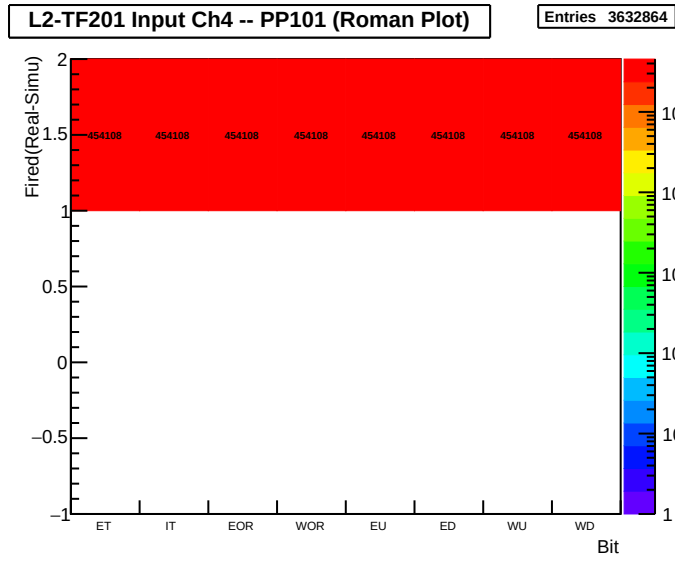

L2-TF201
Entries 454108
TOF total mult

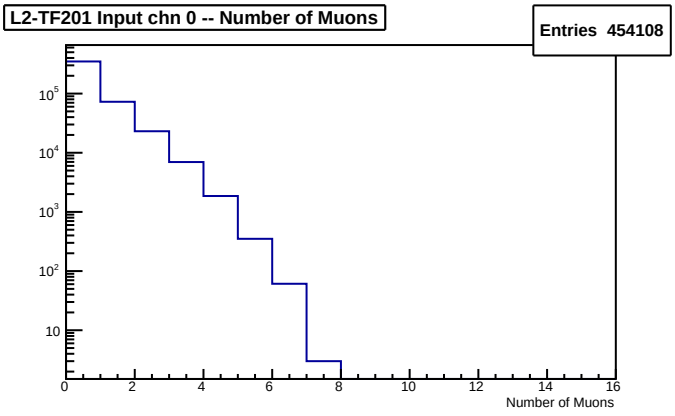

L2-TF201
Entries 0
TOF total mult(Real-Simu)

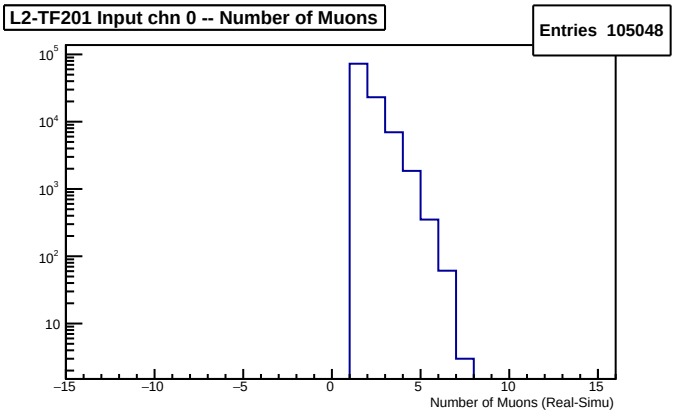

L2-TF201
Entries 3178756
Threshold/UPC bits
350868 348821 348774 348390 352304 347952 364960
103240 105287 105334 105718 101804 106156 89148
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

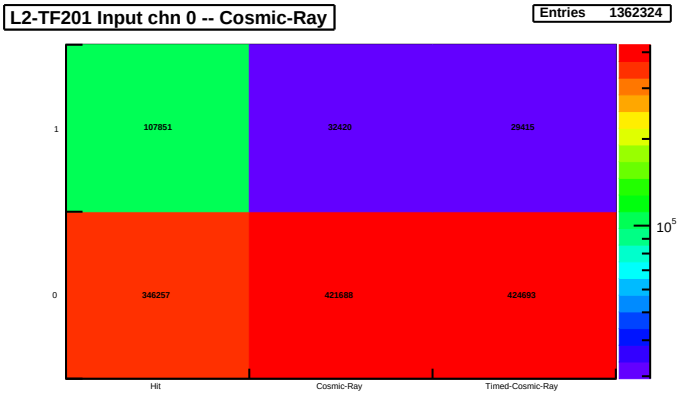

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

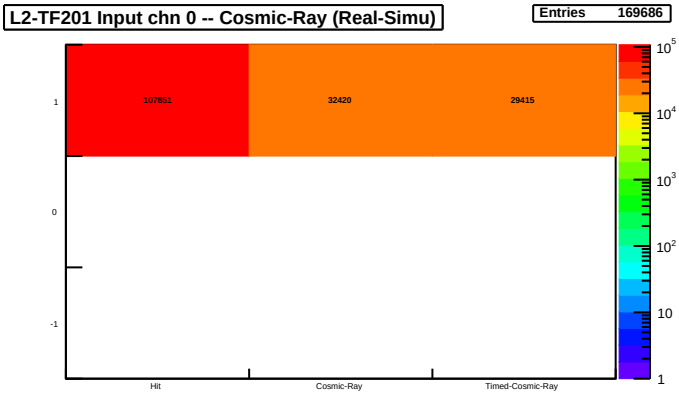

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 3632864
Fired
454108 454108 454108 454108 454108 454108 454108 454108
ET IT EOR WOR EU ED WU WD
Bit

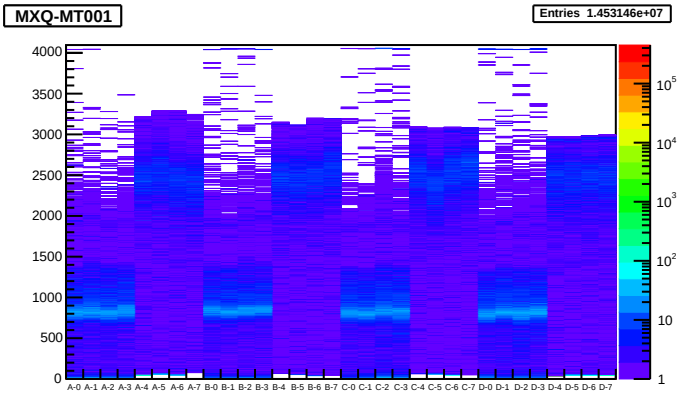

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 3632864
Fired(Real-Simu)
454108 454108 454108 454108 454108 454108 454108 454108
ET IT EOR WOR EU ED WU WD
Bit

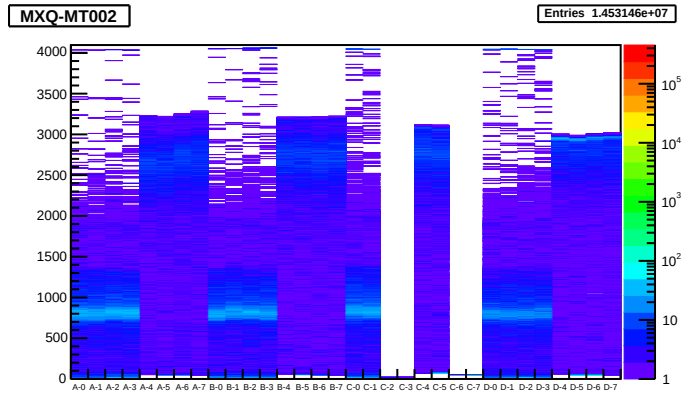

L2-TF201 Input chn 0 -- Number of Muons
Entries 454108
Number of Muons

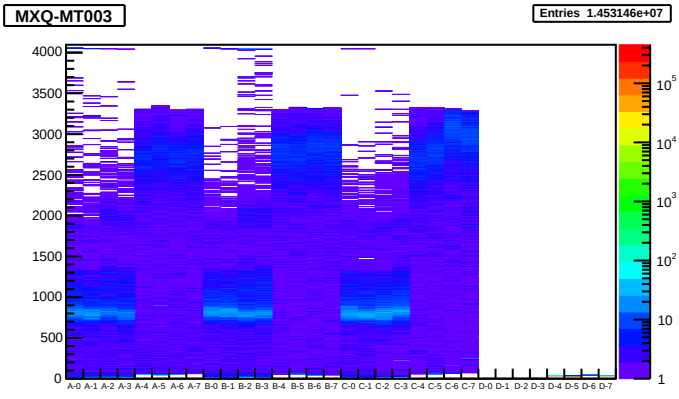

L2-TF201 Input chn 0 -- Number of Muons
Entries 105048
Number of Muons (Real-Simu)

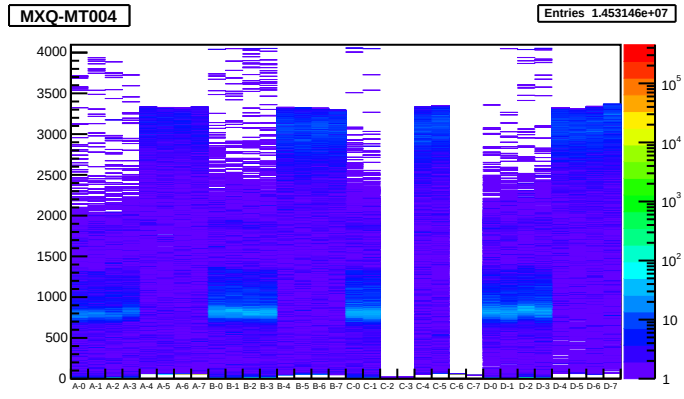

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 1362324
107851
32420
29415
346257
421688
424693
Hit
Cosmic-Ray
Timed-Cosmic-Ray

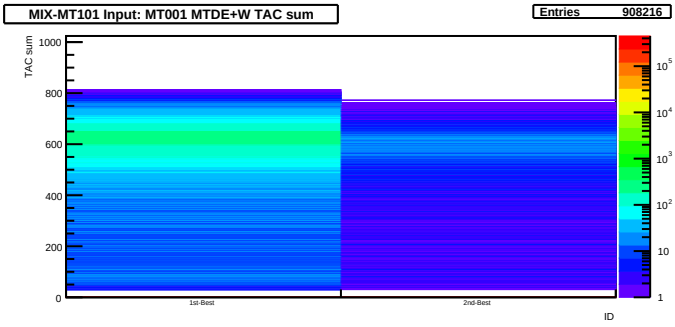

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 169686
107851
32420
29415
Hit
Cosmic-Ray
Timed-Cosmic-Ray

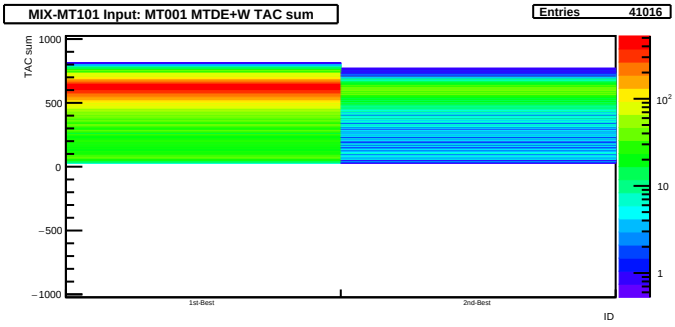

MXQ-MT001
Entries 1.453146e+07

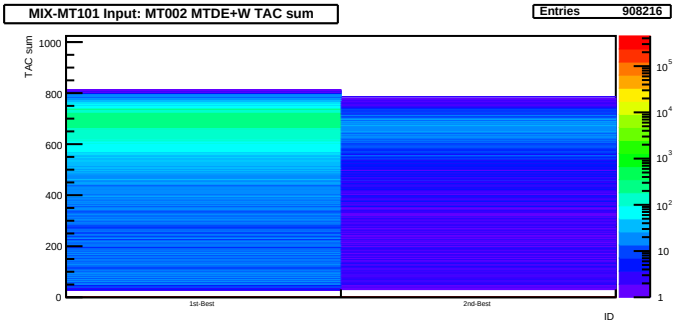

MXQ-MT002
Entries 1.453146e+07

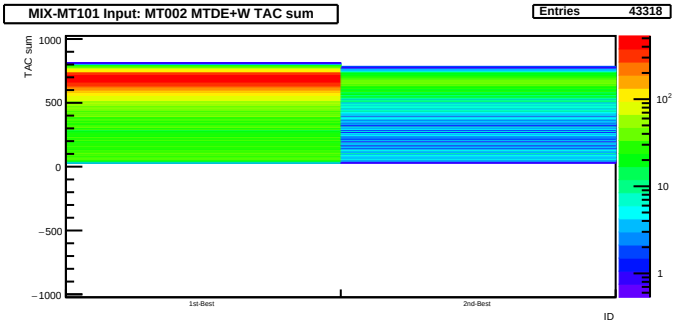

MXQ-MT003
Entries 1.453146e+07

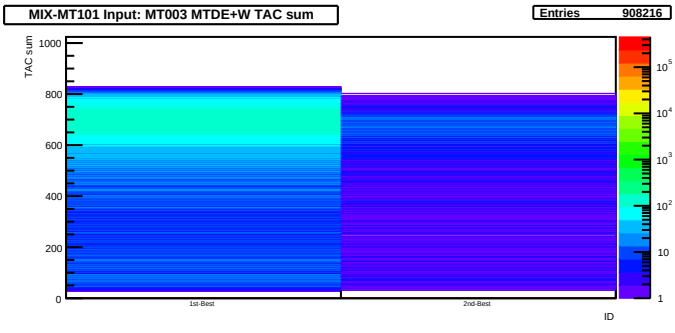

MXQ-MT004
Entries 1.453146e+07

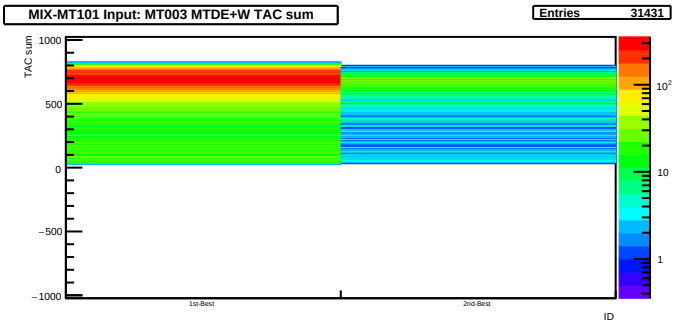

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 908216
TAC sum
1st-Best
2nd-Best
ID

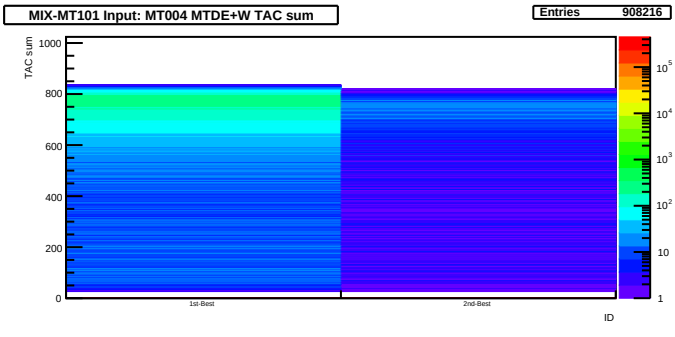

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 41016
TAC sum
1st-Best
2nd-Best
ID

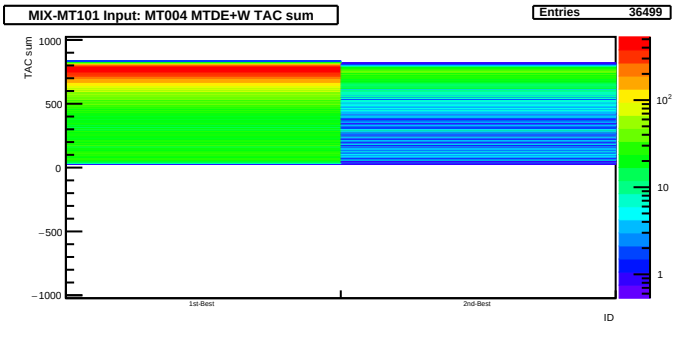

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 908216
TAC sum
1st-Best
2nd-Best
ID

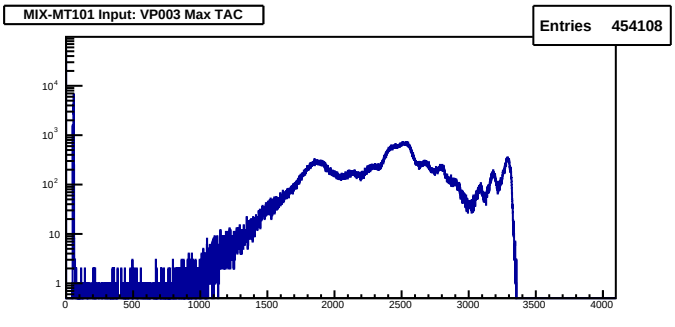

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 43318
TAC sum
1st-Best
2nd-Best
ID

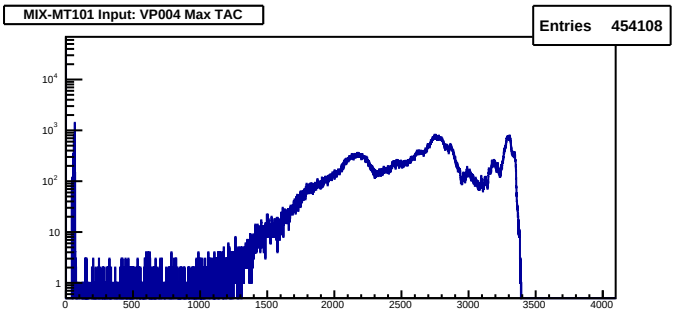

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 908216
TAC sum
1st-Best
2nd-Best
ID

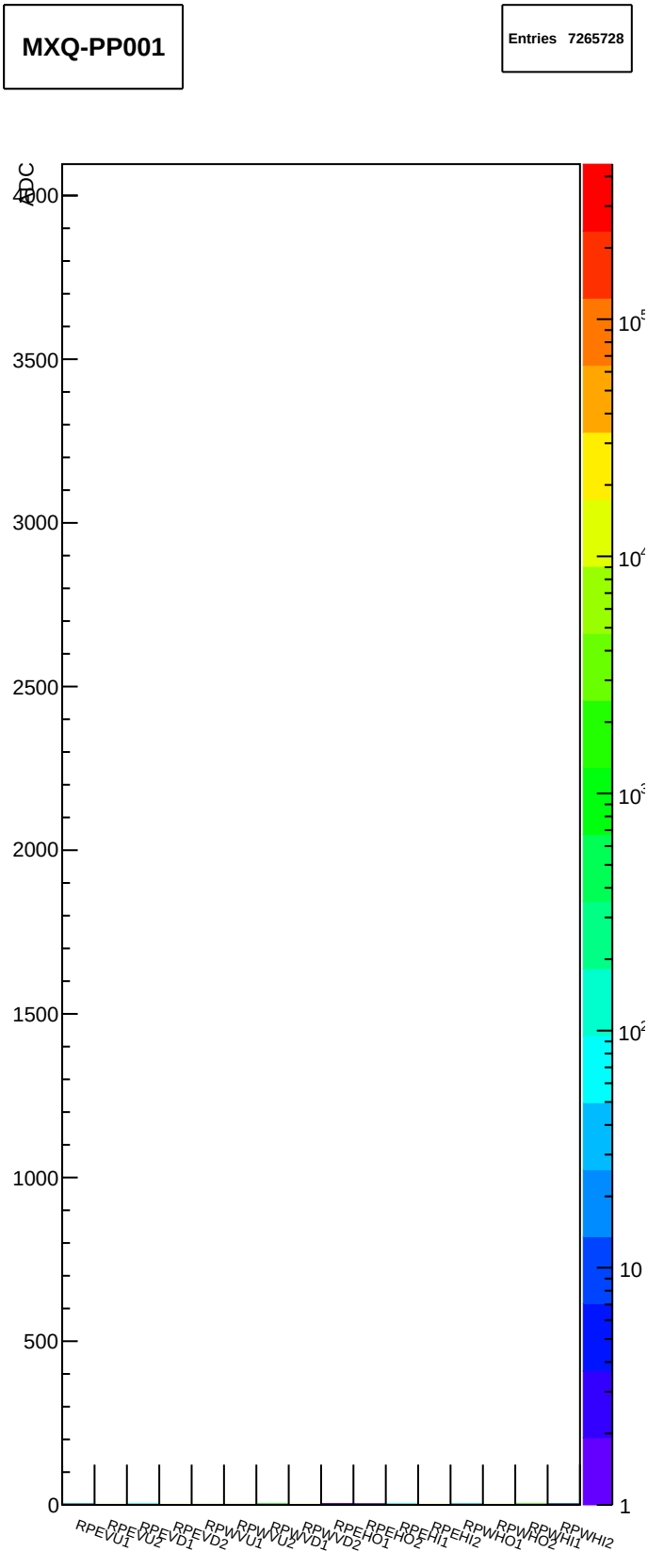

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 31431
TAC sum
1st-Best
2nd-Best
ID

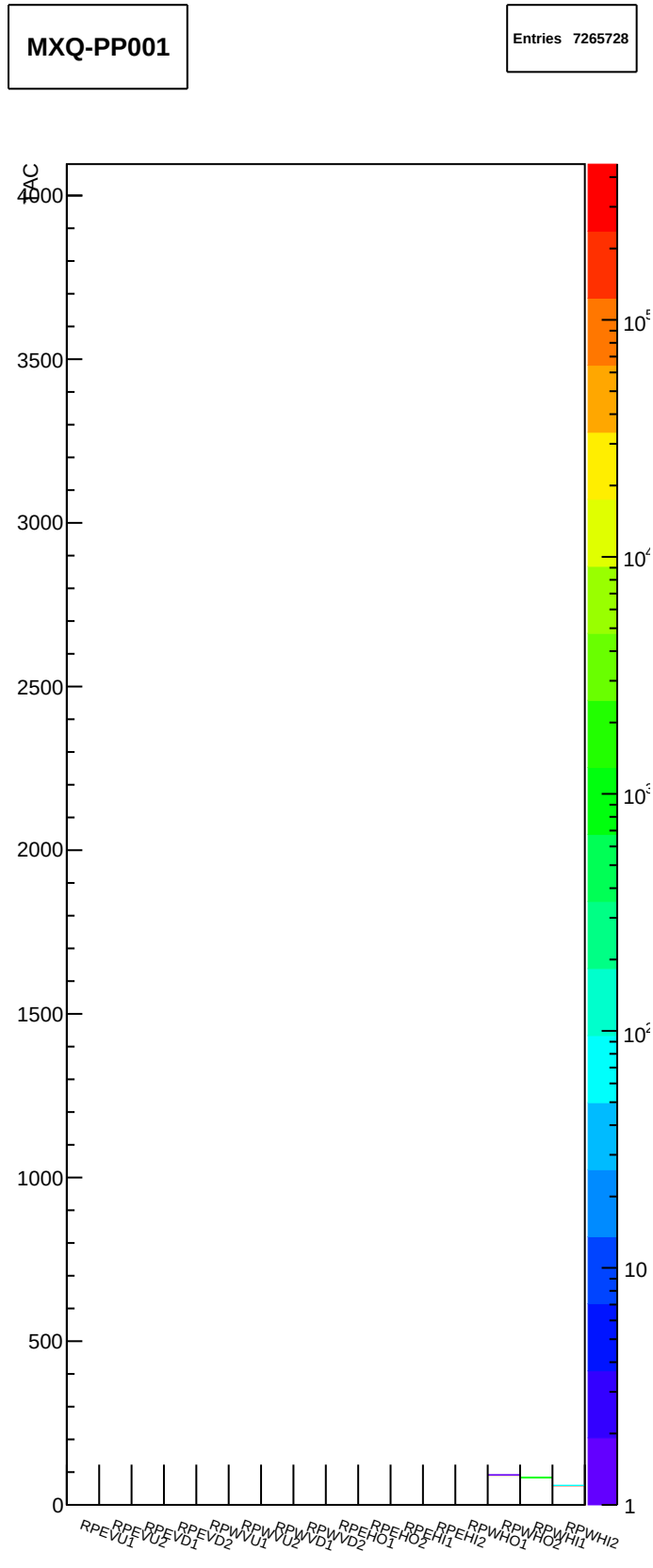

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 908216
TAC sum
1st-Best
2nd-Best
ID

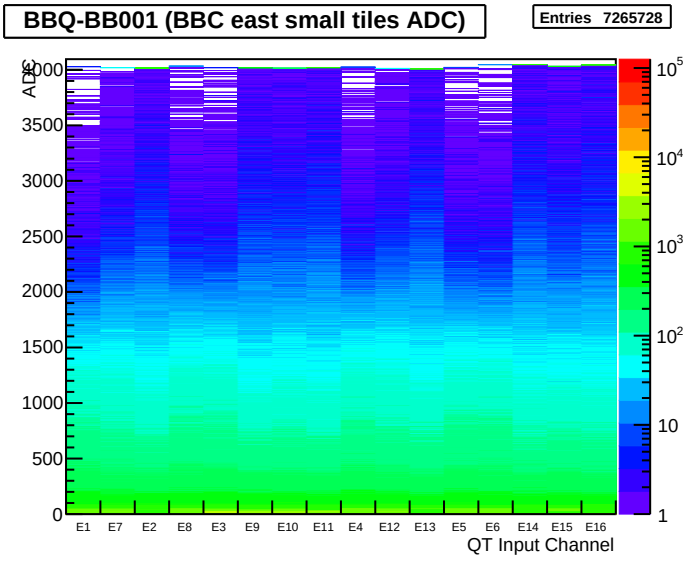

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 36499
TAC sum
1st-Best
2nd-Best
ID

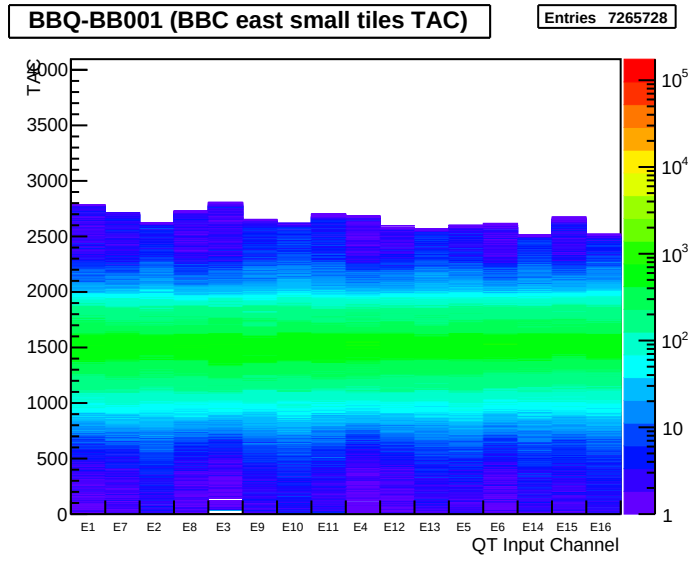

MIX-MT101 Input: VP003 Max TAC
Entries 454108

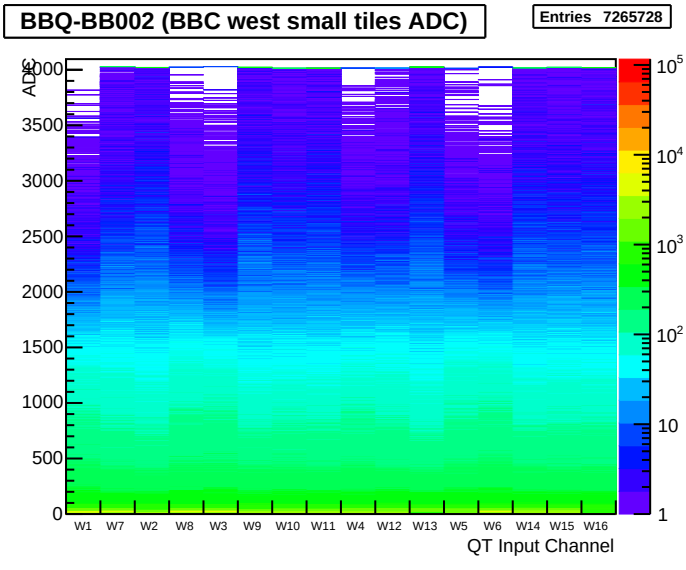

MIX-MT101 Input: VP004 Max TAC
Entries 454108

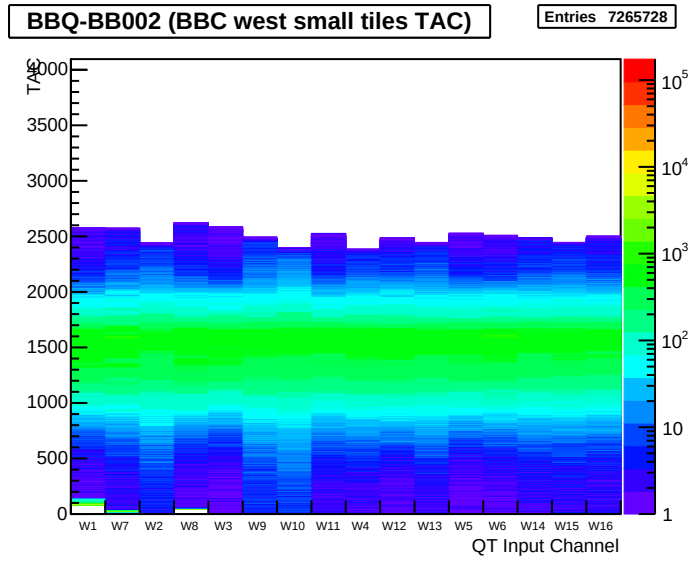

MXQ-PP001
Entries 7265728
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

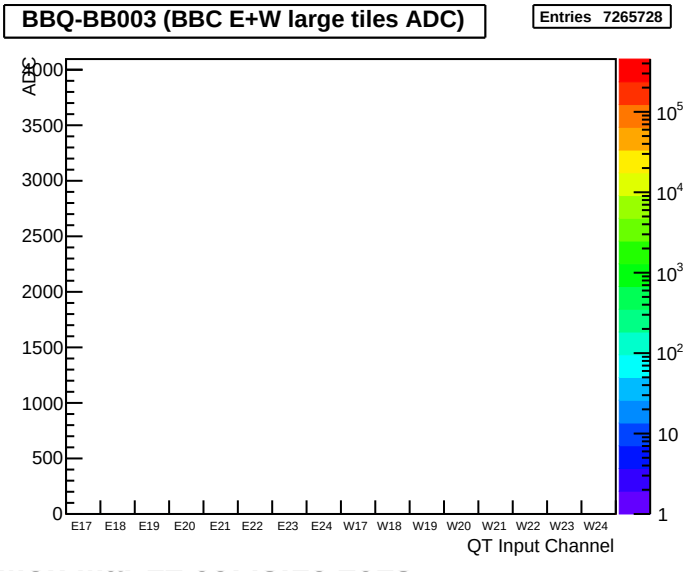

MXQ-PP001
Entries 7265728
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

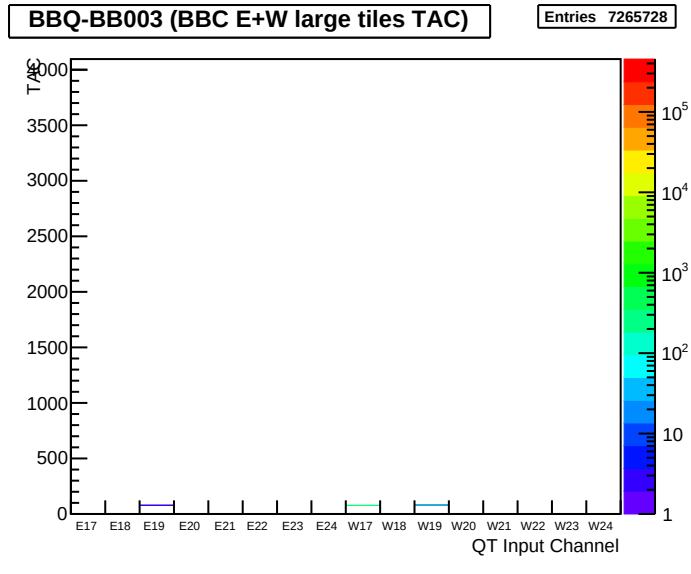

BBQ-BB001 (BBC east small tiles ADC)
Entries 7265728
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

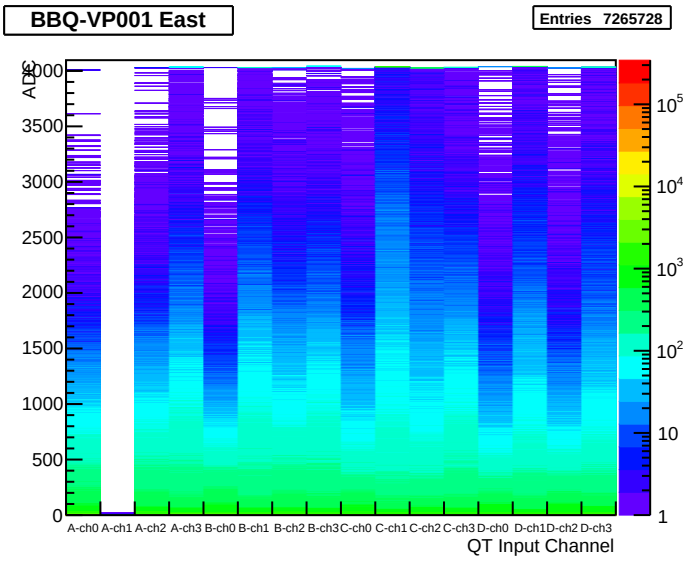

BBQ-BB001 (BBC east small tiles TAC)
Entries 7265728
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

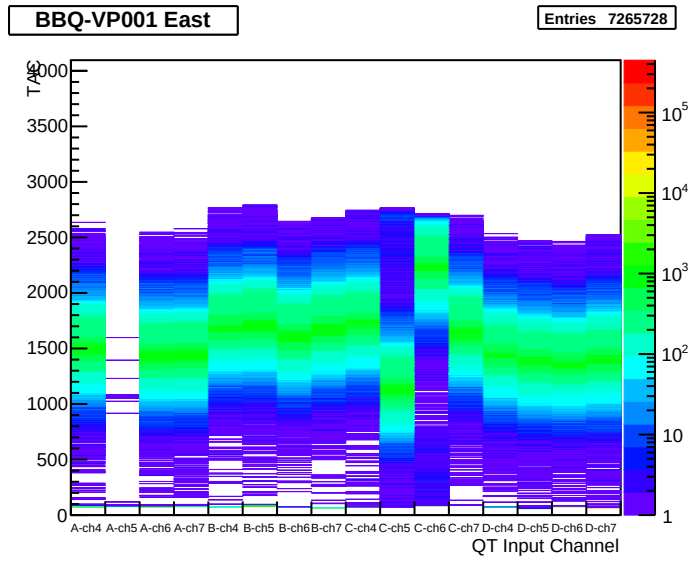

BBQ-BB002 (BBC west small tiles ADC)
Entries 7265728
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

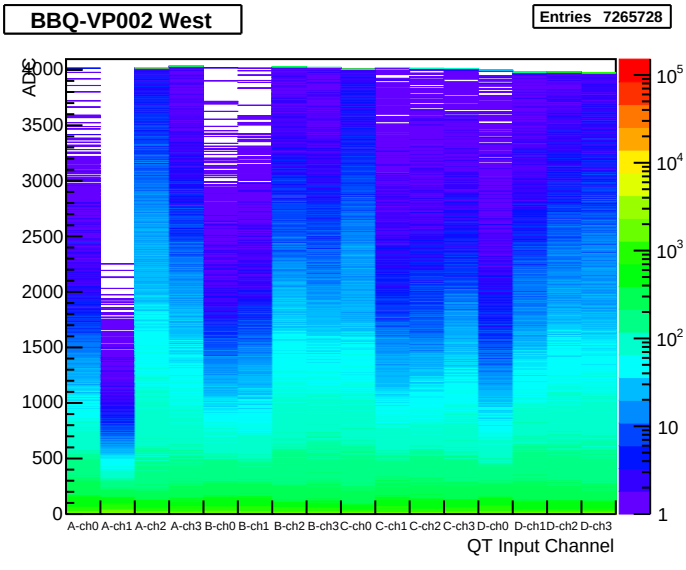

BBQ-BB002 (BBC west small tiles TAC)
Entries 7265728
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

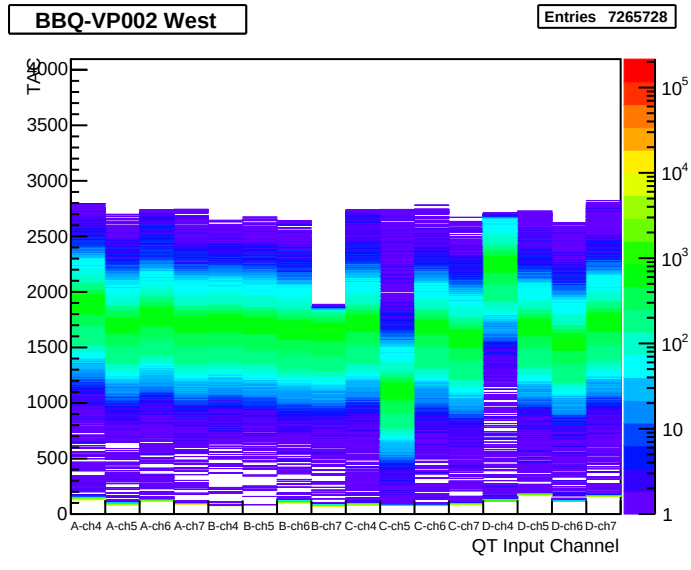

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7265728
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

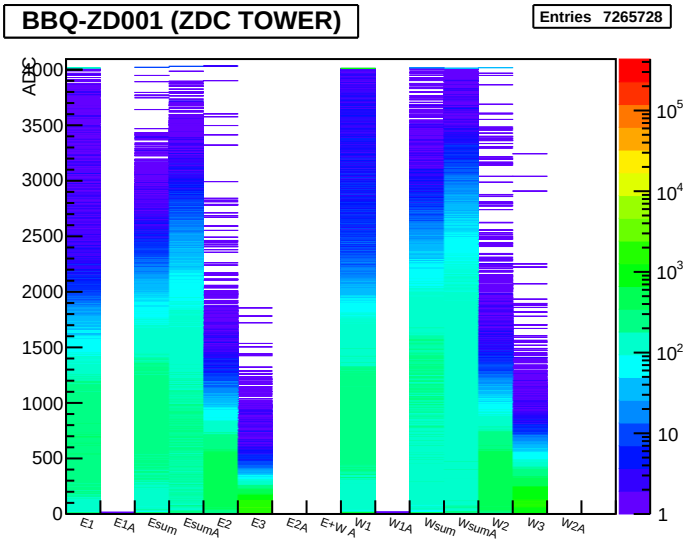

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7265728
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

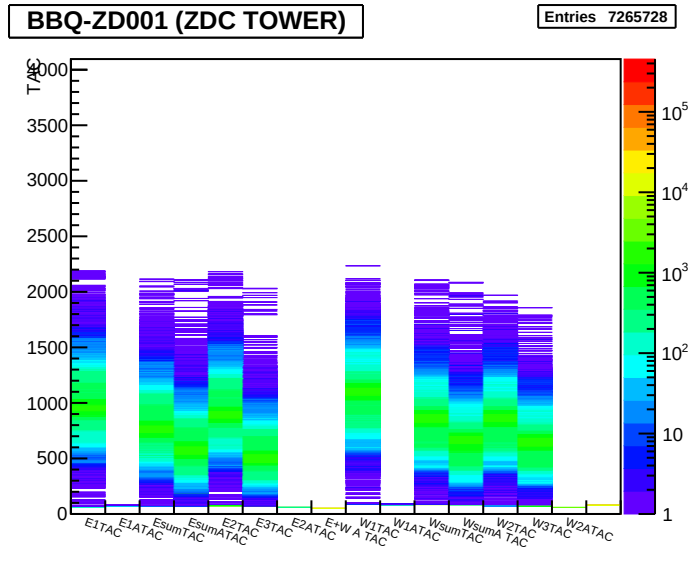

BBQ-VP001 East
Entries 7265728
ADC
QT Input Channel

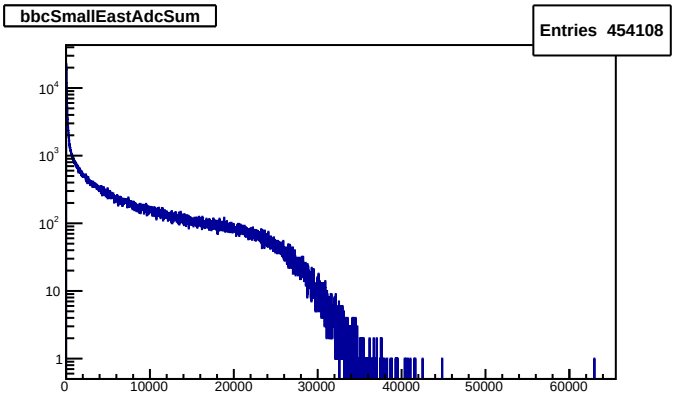

BBQ-VP001 East
Entries 7265728
TAC
QT Input Channel

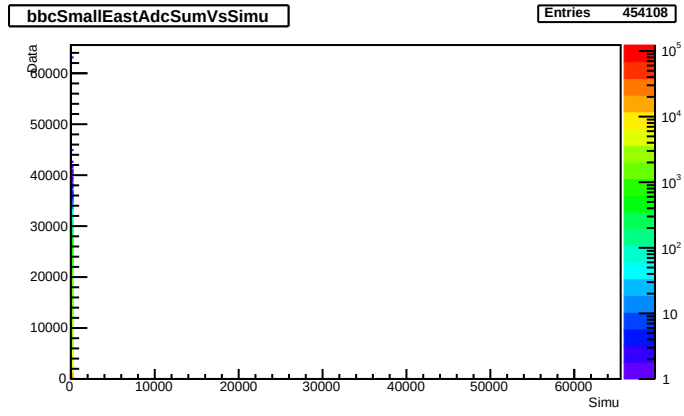

BBQ-VP002 West
Entries 7265728
ADC
QT Input Channel

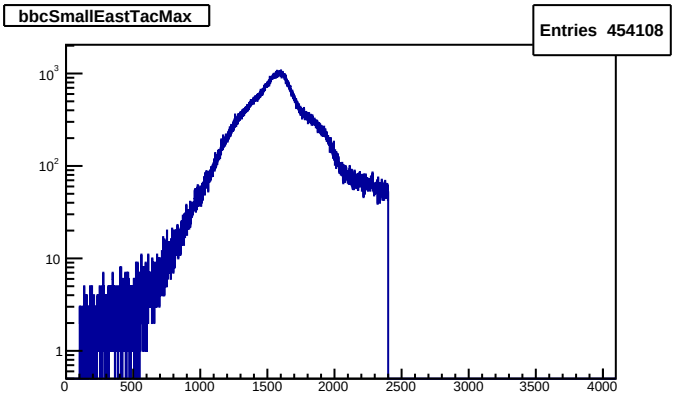

BBQ-VP002 West
Entries 7265728
TAC
QT Input Channel

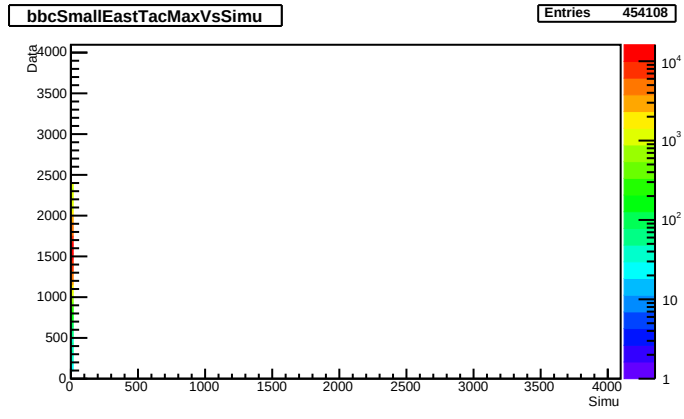

BBQ-ZD001 (ZDC TOWER)
Entries 7265728
ADC

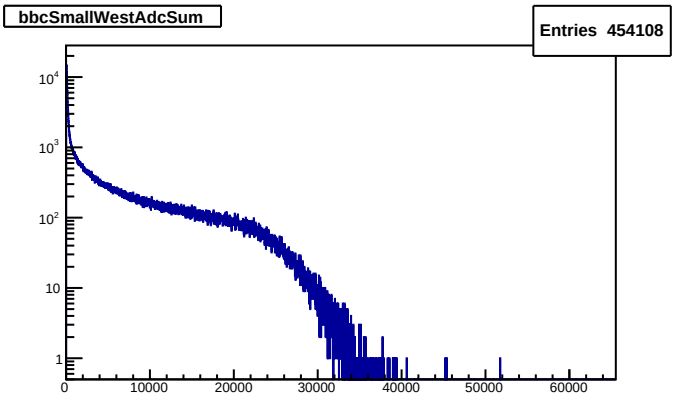

BBQ-ZD001 (ZDC TOWER)
Entries 7265728
TAC

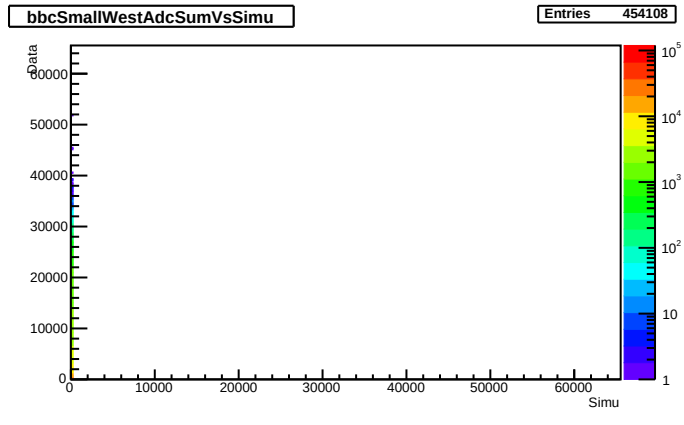

bbcSmallEastAdcSum
Entries 454108

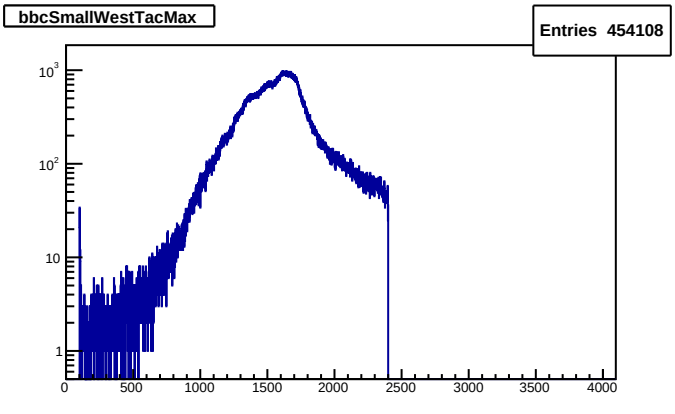

bbcSmallEastAdcSumVsSimu
Entries 454108
Data
Simu

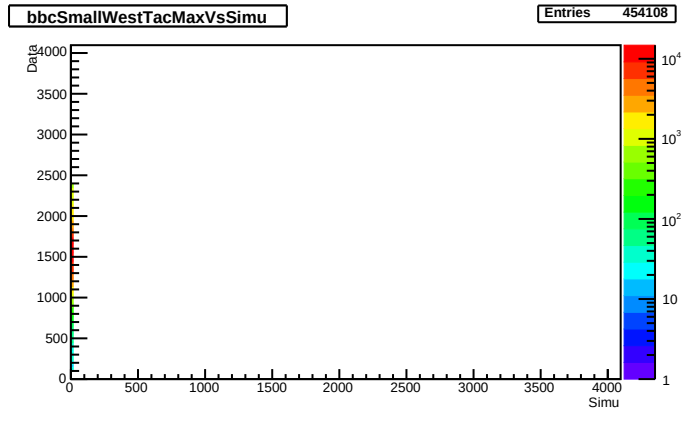

bbcSmallEastTacMax
Entries 454108

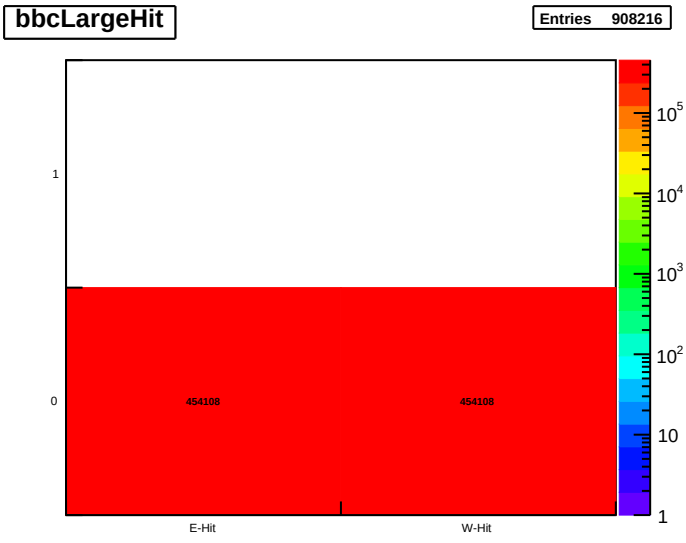

bbcSmallEastTacMaxVsSimu
Entries 454108
Data
Simu

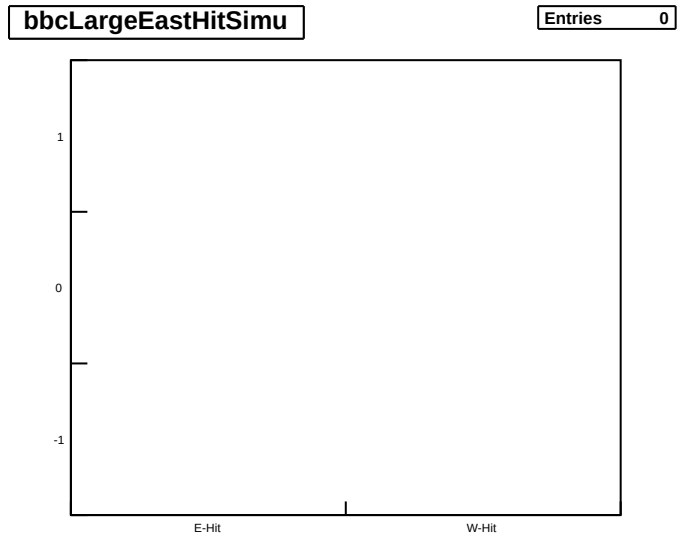

bbcSmallWestAdcSum
Entries 454108

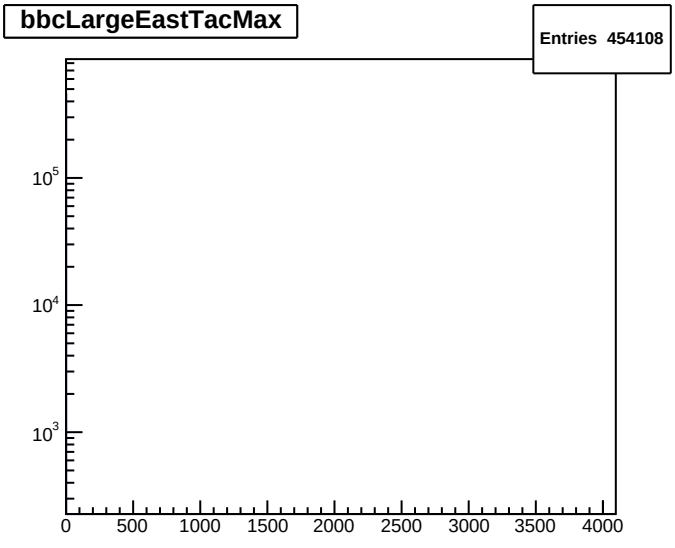

bbcSmallWestAdcSumVsSimu
Entries 454108
Data
Simu

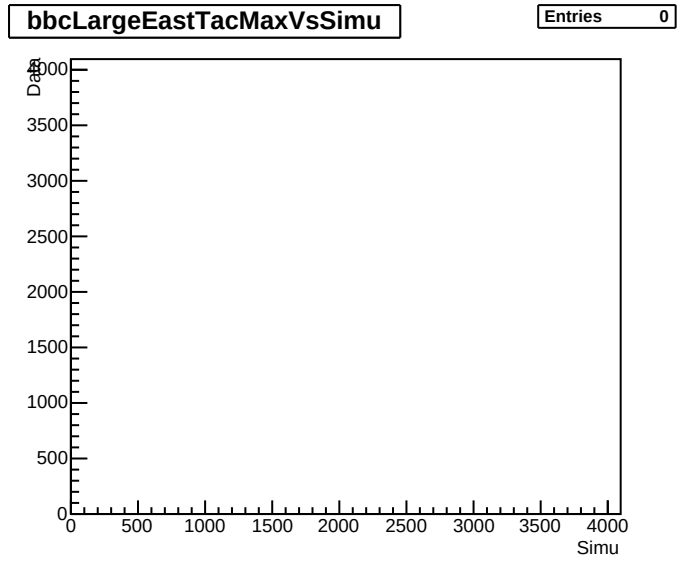

bbcSmallWestTacMax
Entries 454108

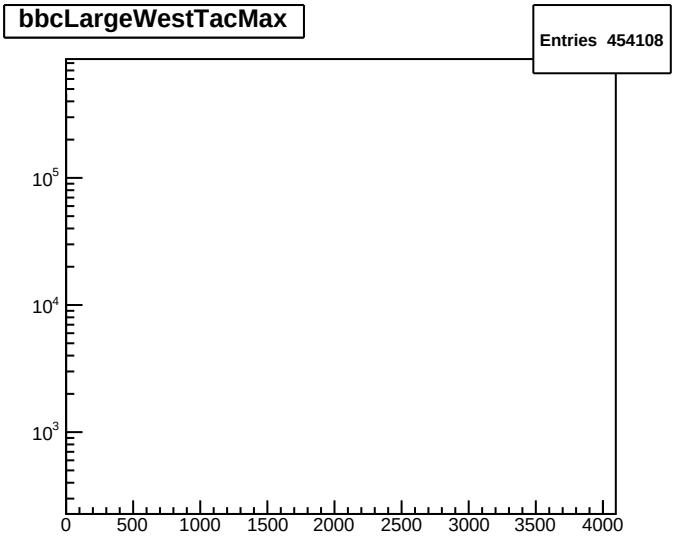

bbcSmallWestTacMaxVsSimu
Entries 454108
Data
Simu

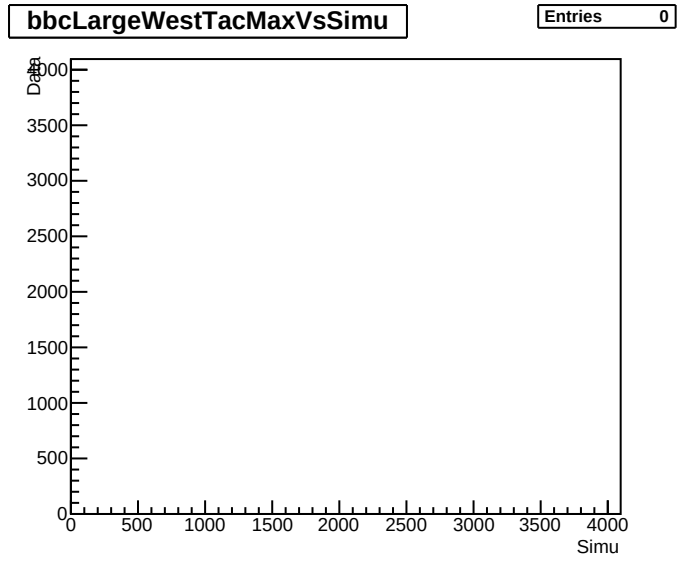

bbcLargeHit
Entries 908216
454108
454108
E-Hit
W-Hit

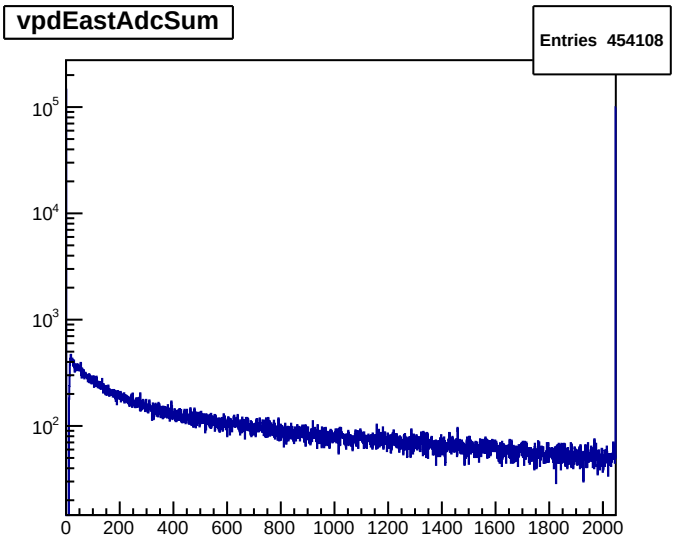

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

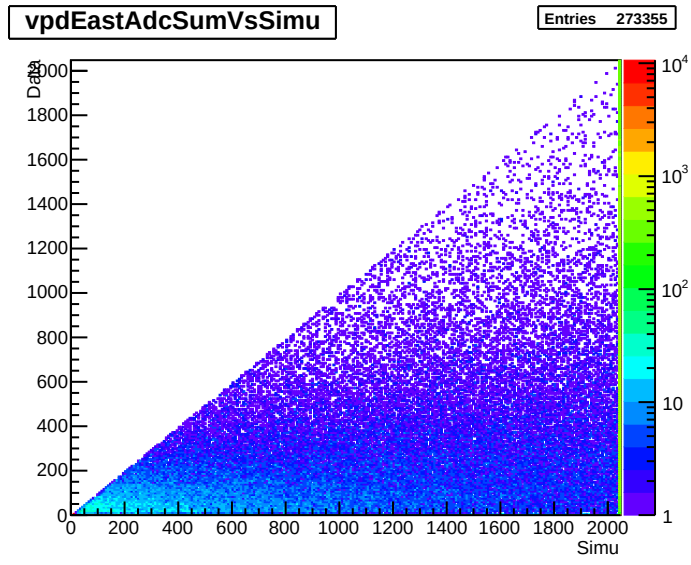

bbcLargeEastTacMax
Entries 454108

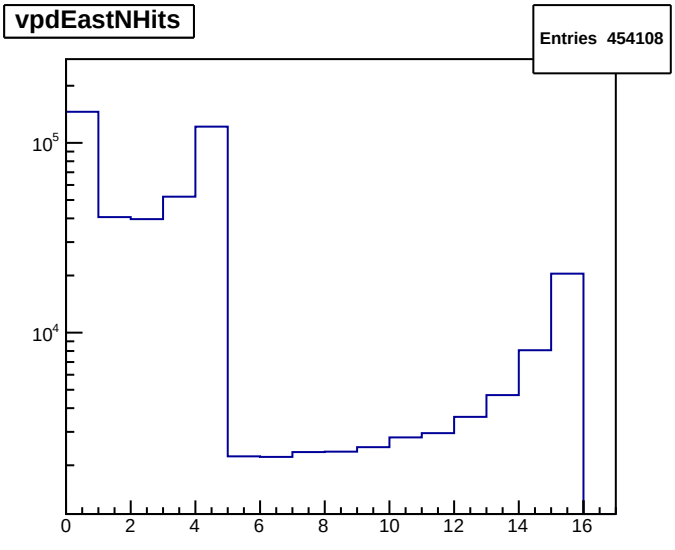

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

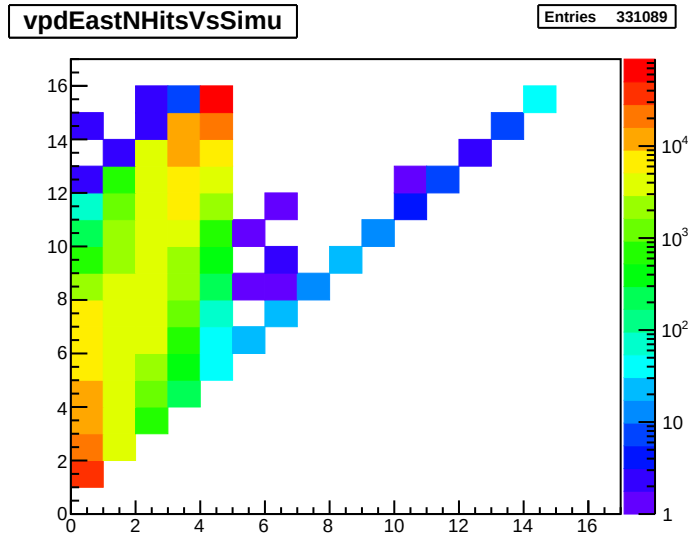

bbcLargeWestTacMax
Entries 454108

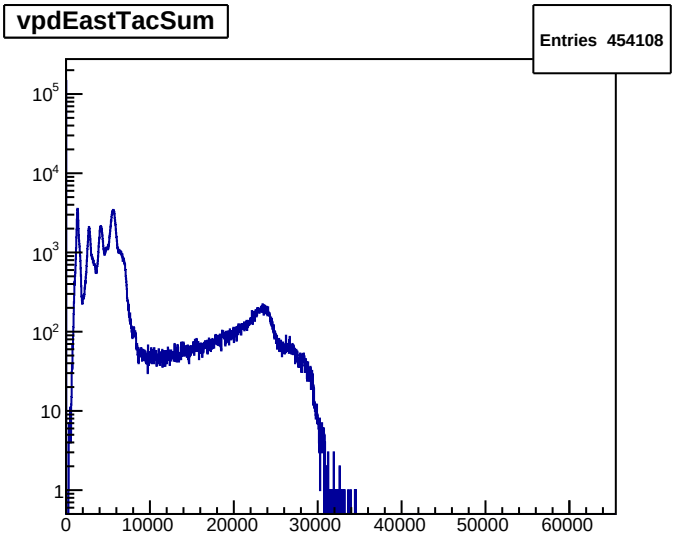

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

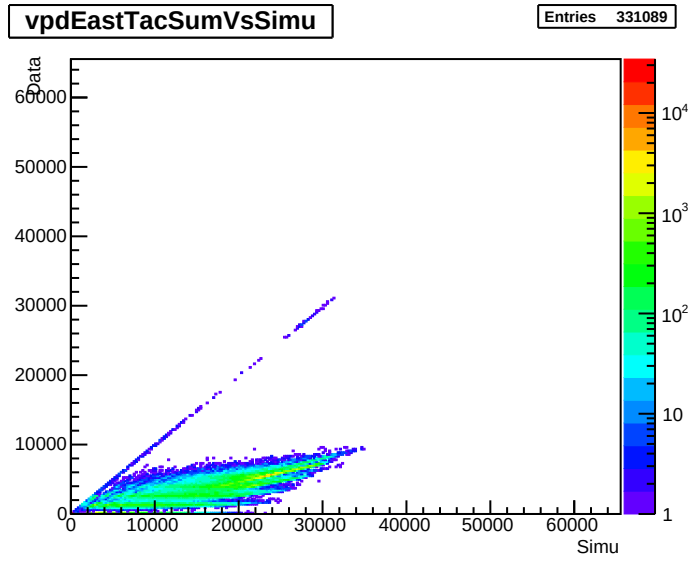

vpdEastAdcSum
Entries 454108

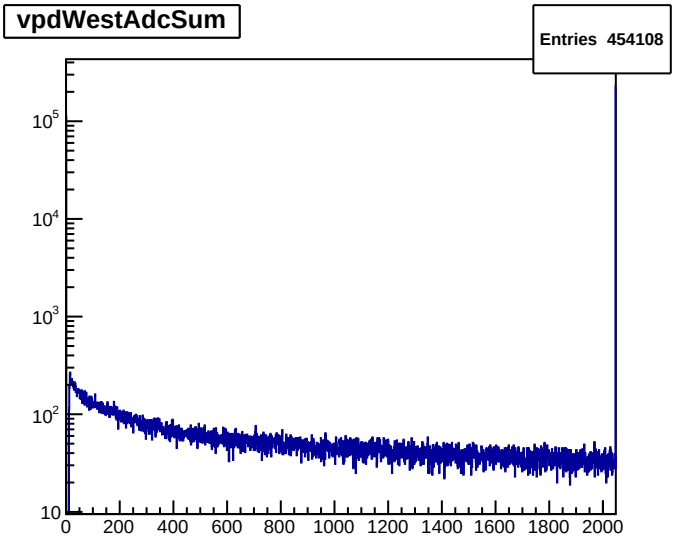

vpdEastAdcSumVsSimu
Entries 273355
Data
Simu

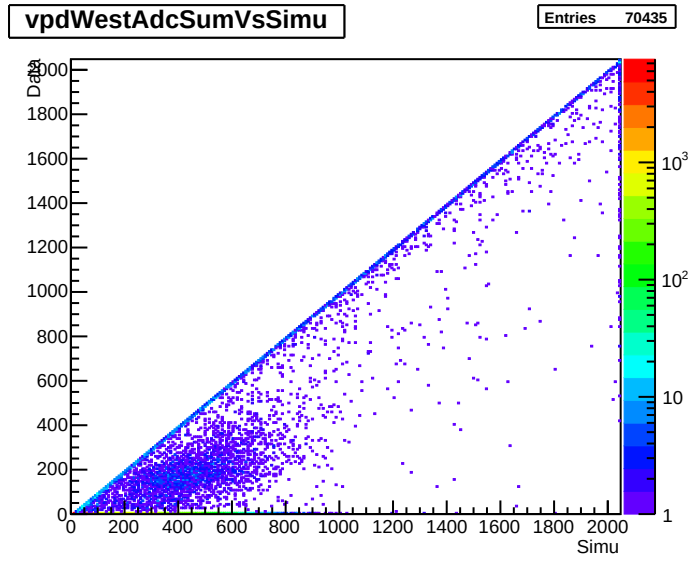

vpdEastNHits
Entries 454108

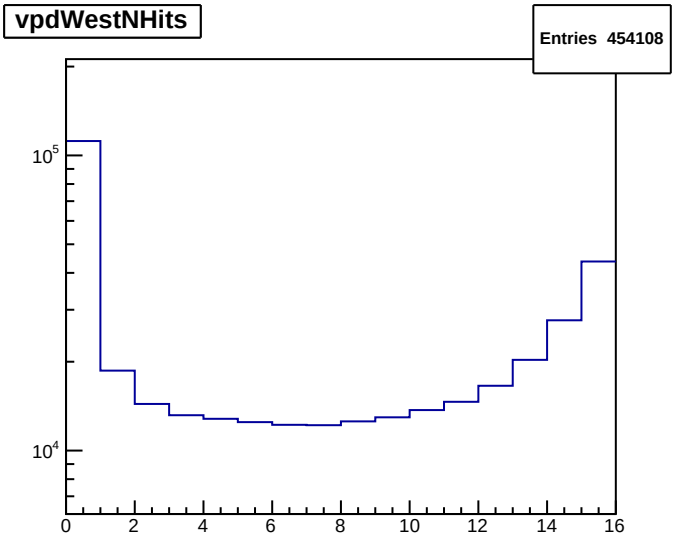

vpdEastNHitsVsSimu
Entries 331089

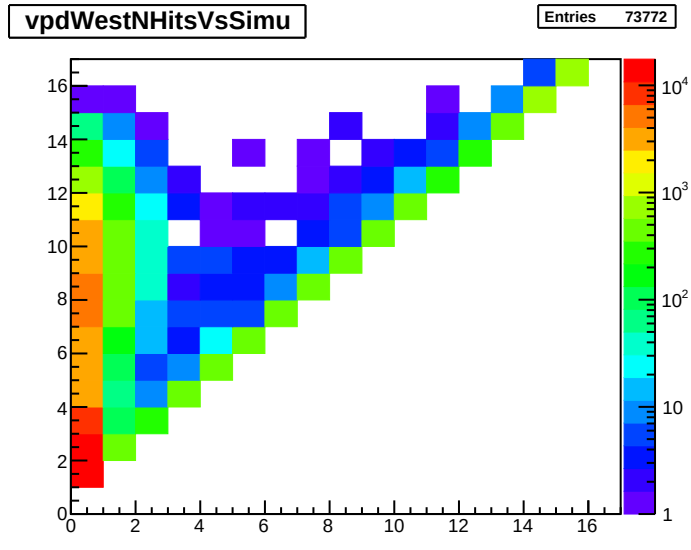

vpdEastTacSum
Entries 454108

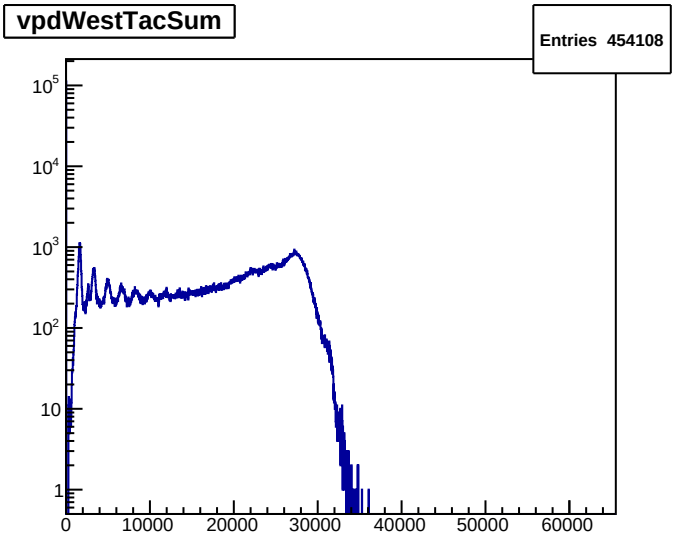

vpdEastTacSumVsSimu
Entries 331089
Data
Simu

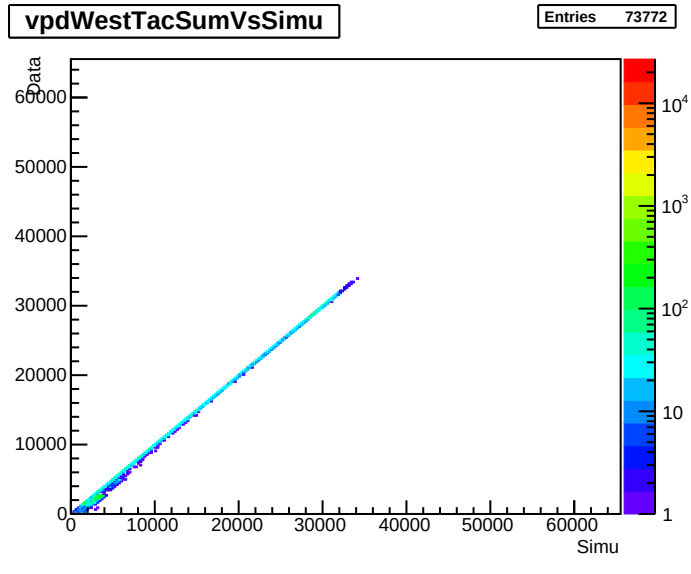

vpdWestAdcSum
Entries 454108

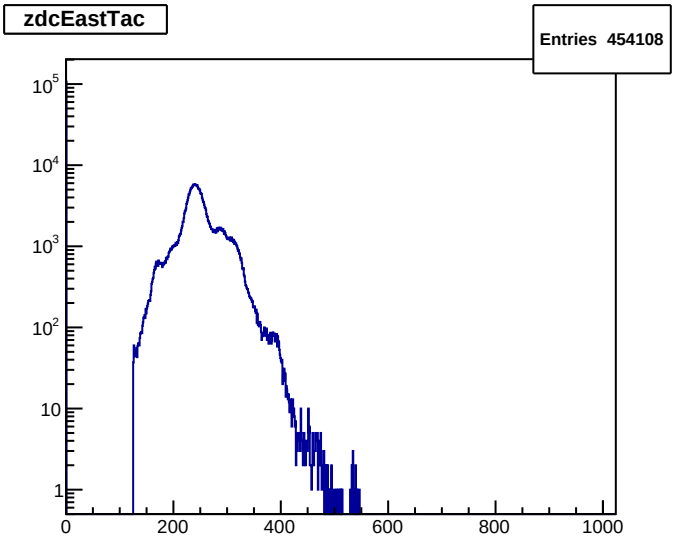

vpdWestAdcSumVsSimu
Entries 70435
Data
Simu

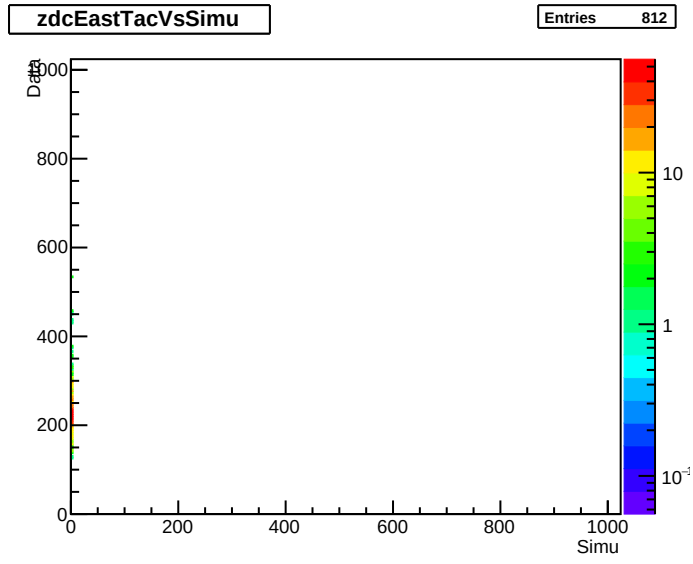

vpdWestNHits
Entries 454108

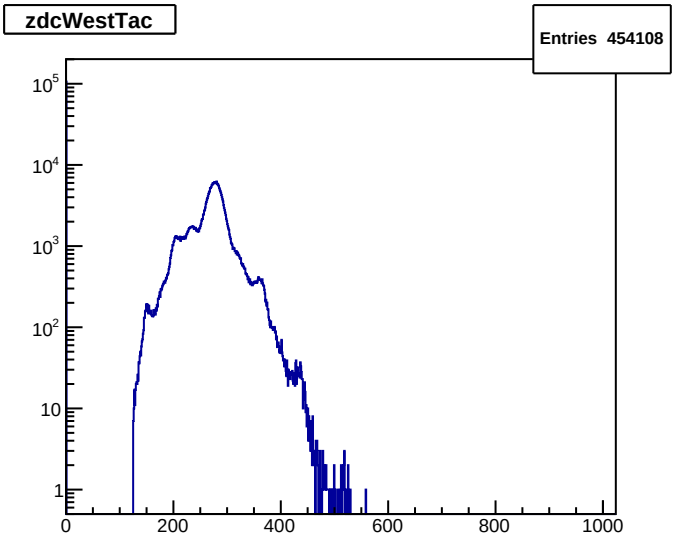

vpdWestNHitsVsSimu
Entries 73772

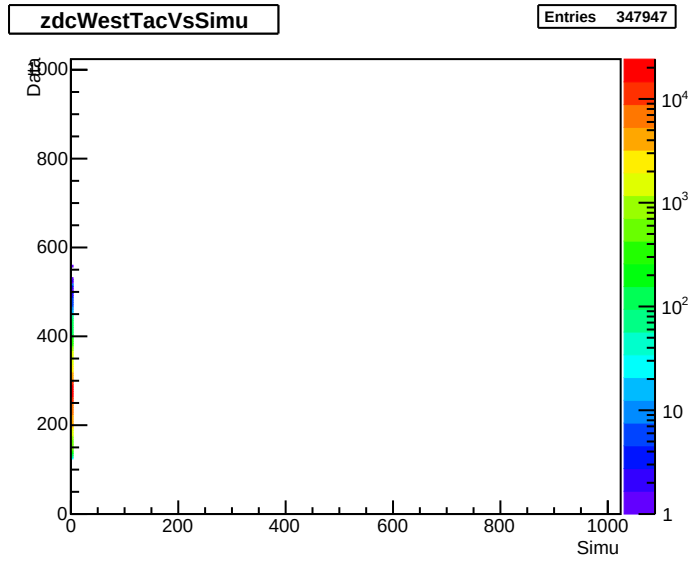

vpdWestTacSum
Entries 454108

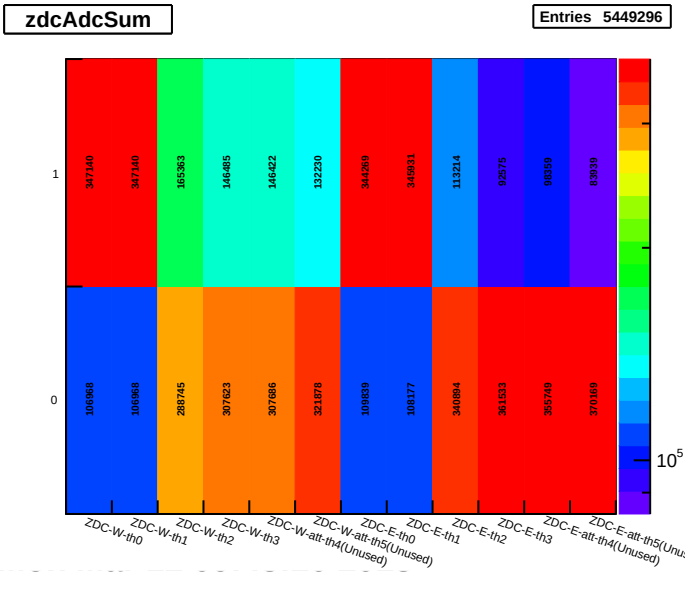

vpdWestTacSumVsSimu
Entries 73772
Data
Simu

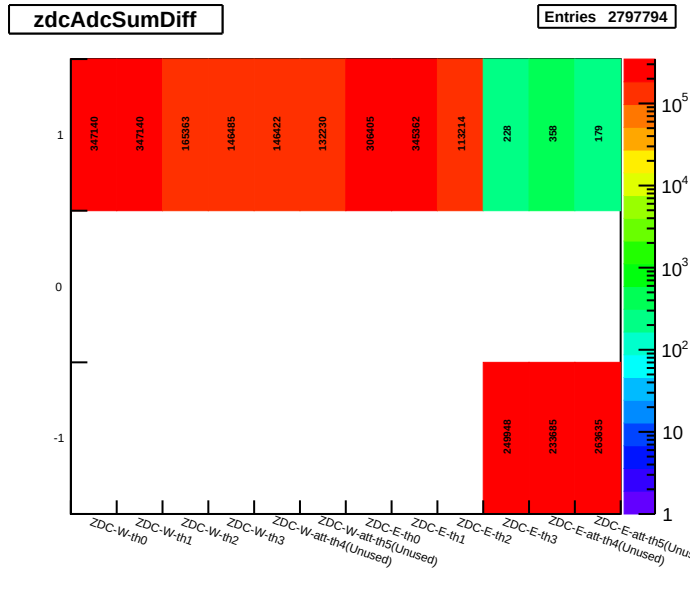

zdcEastTac
Entries 454108

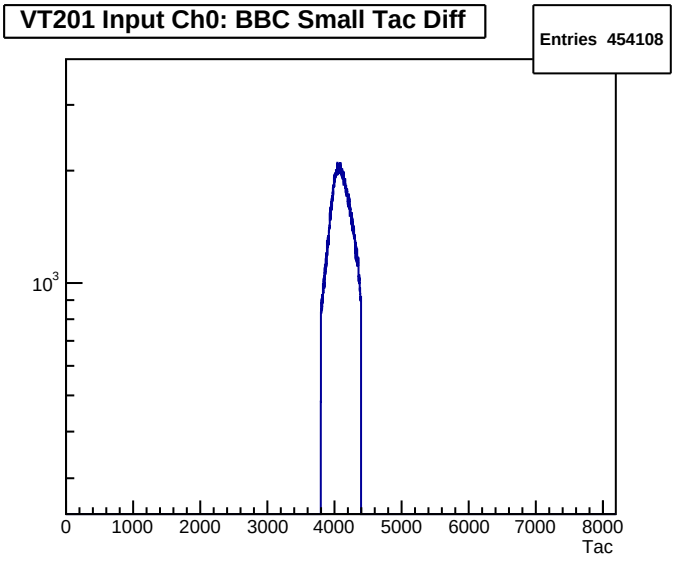

zdcEastTacVsSimu
Entries 812
Data
Simu

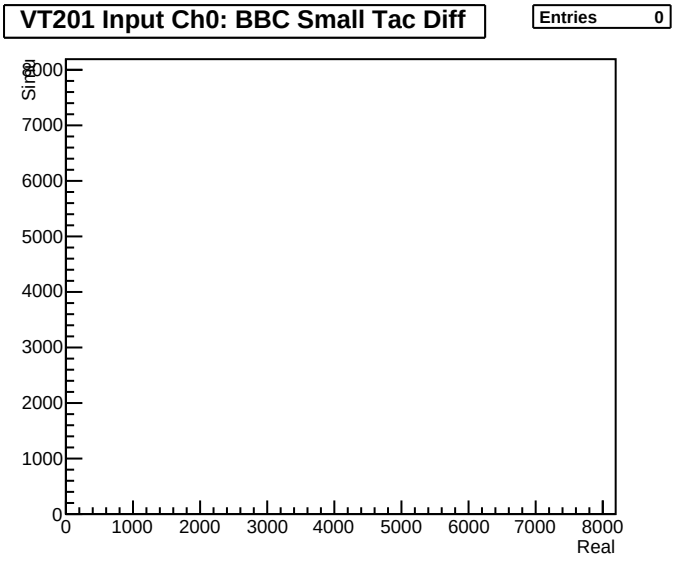

zdcWestTac
Entries 454108

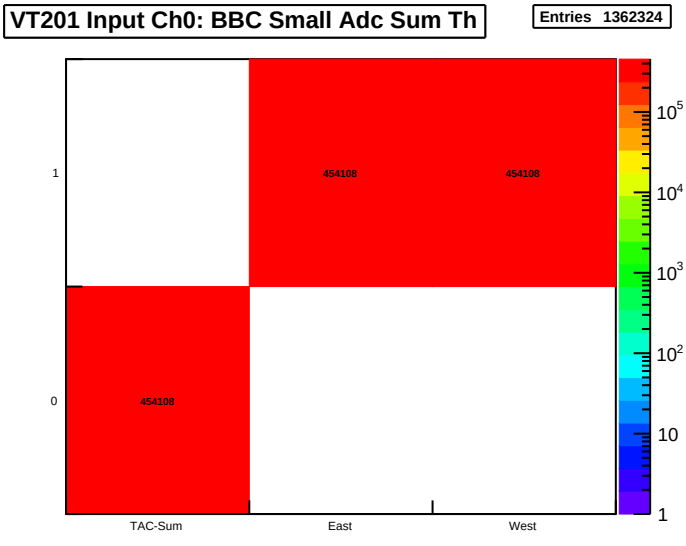

zdcWestTacVsSimu
Entries 347947
Data
Simu

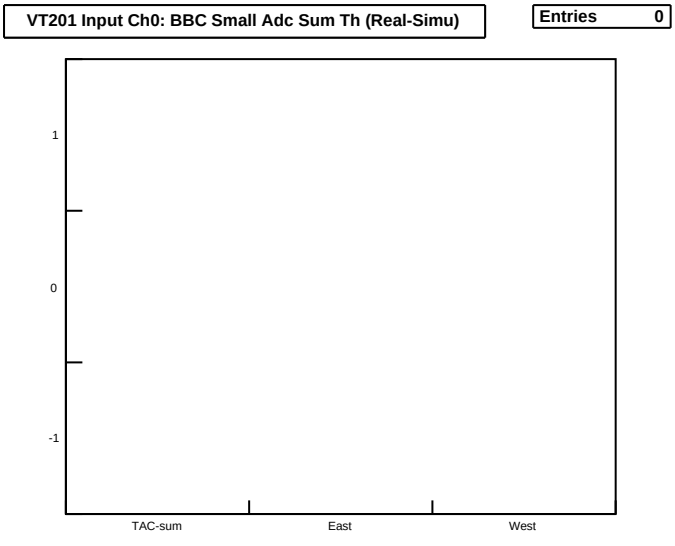

zdcAdcSum
Entries 5449296
347140
347140
165363
146485
146422
132230
344269
346931
113214
92575
98359
83939
106968
106968
288745
307623
307686
321878
109839
108177
340894
361533
355749
370169
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

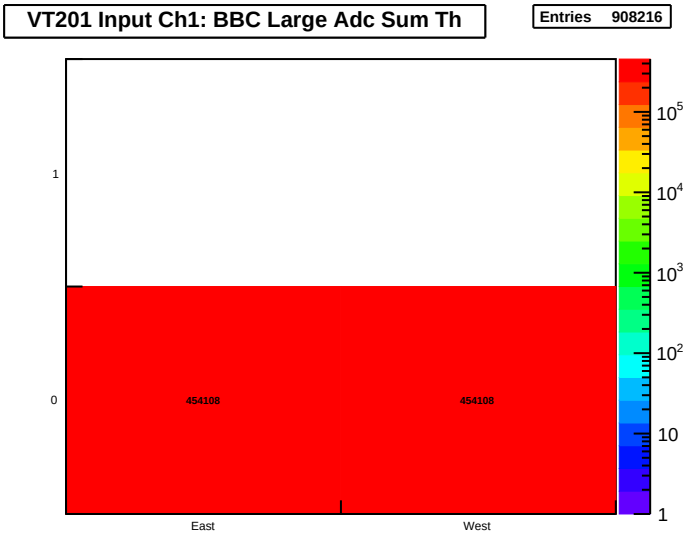

zdcAdcSumDiff
Entries 2797794
347140
347140
165363
146485
146422
132230
306405
345362
113214
228
358
179
249948
233685
265635
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

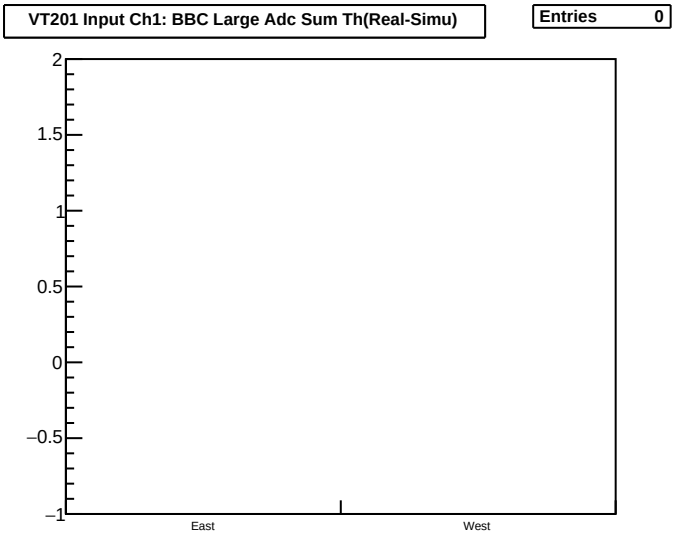

VT201 Input Ch0: BBC Small Tac Diff
Entries 454108
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

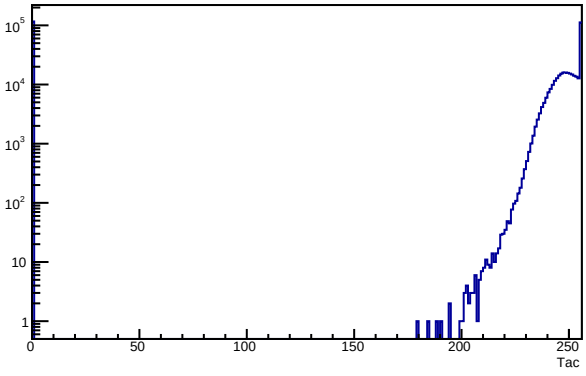

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 1362324
454108
454108
454108
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

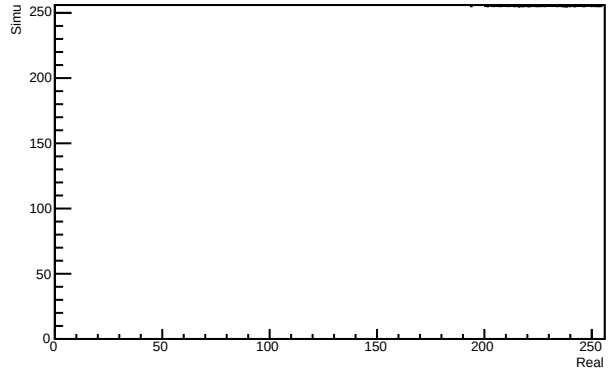

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 908216
454108
454108
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

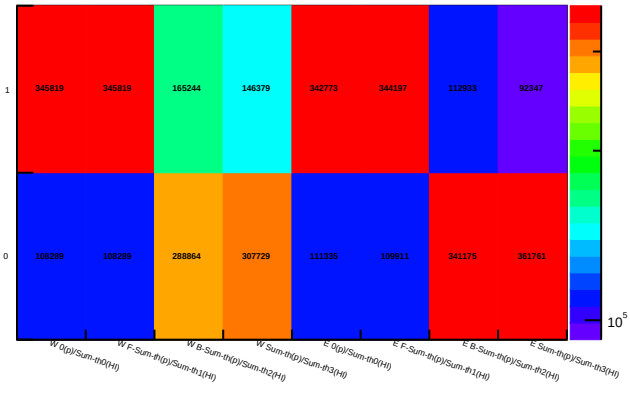
VT201 Input Ch2: ZDC Tac Diff



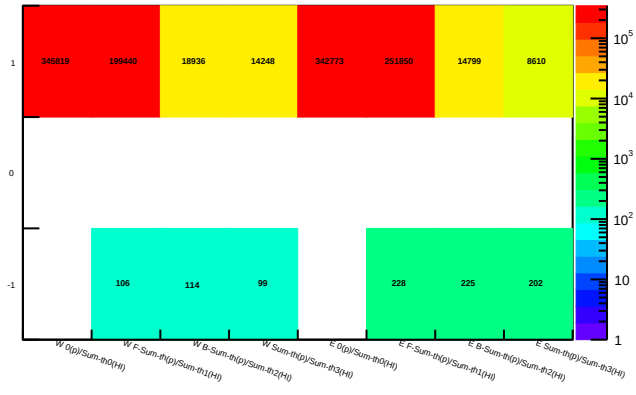
VT201 Input Ch2: ZDC Tac Diff



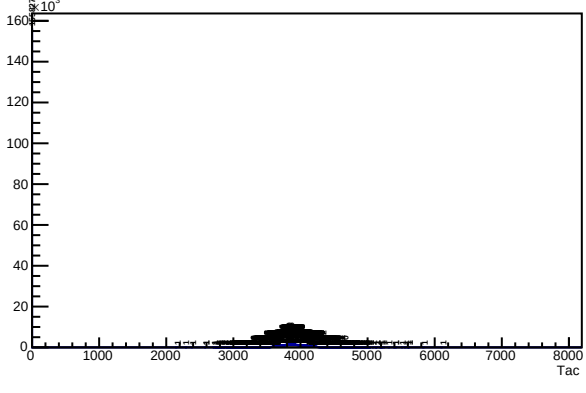
VT201 Input Ch2: ZDC Adc Sum Th



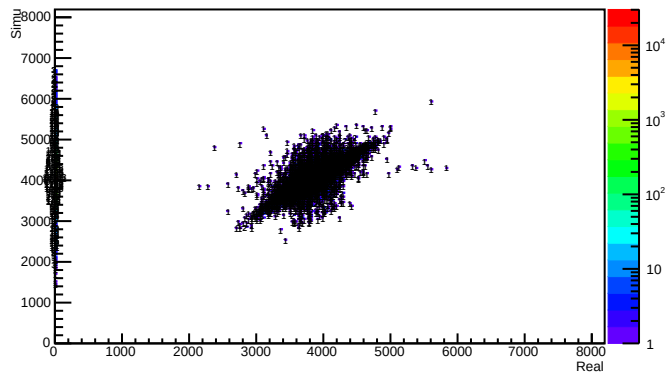
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



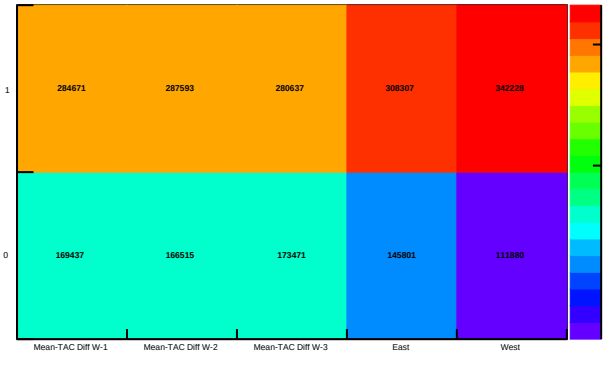
VT201 Input Ch4: VPD Tac Diff



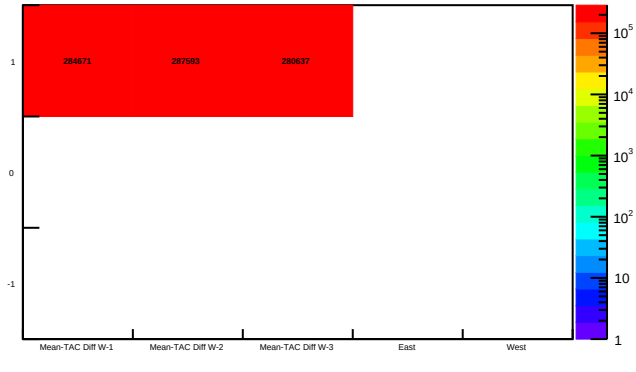
VT201 Input Ch4: VPD Tac Diff

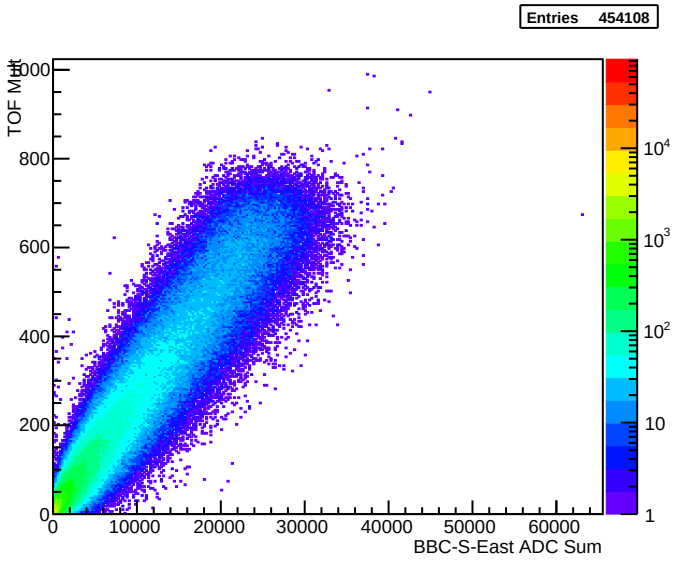


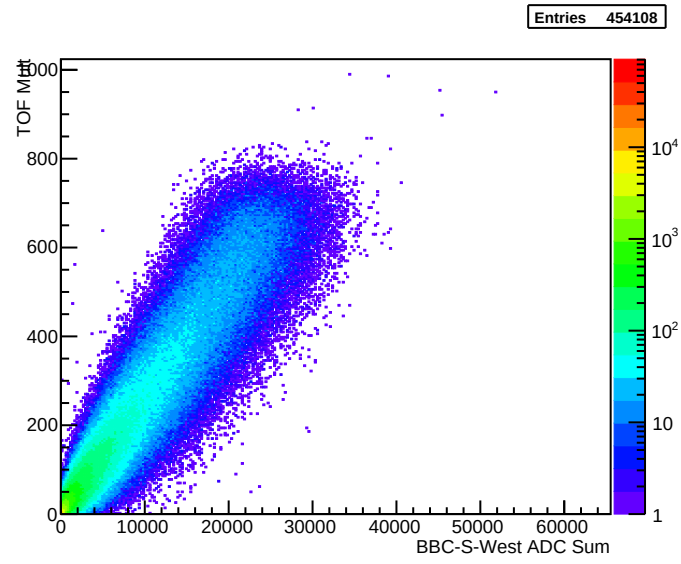
VT201 Input Ch4: VPD Adc Sum Th

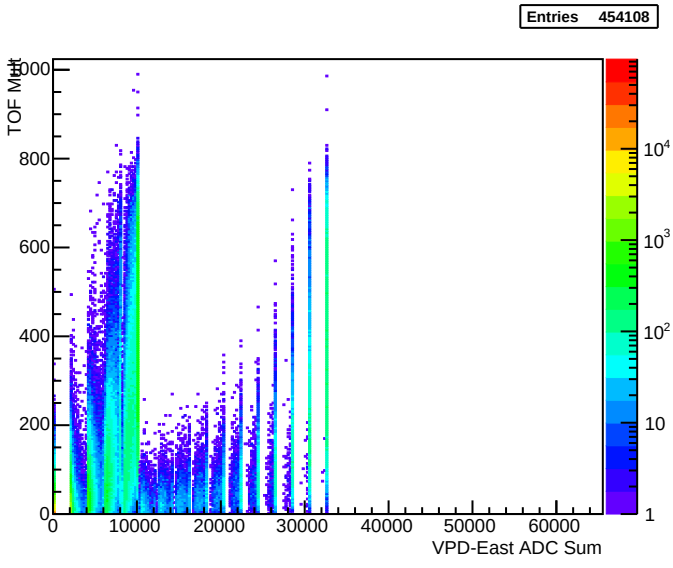


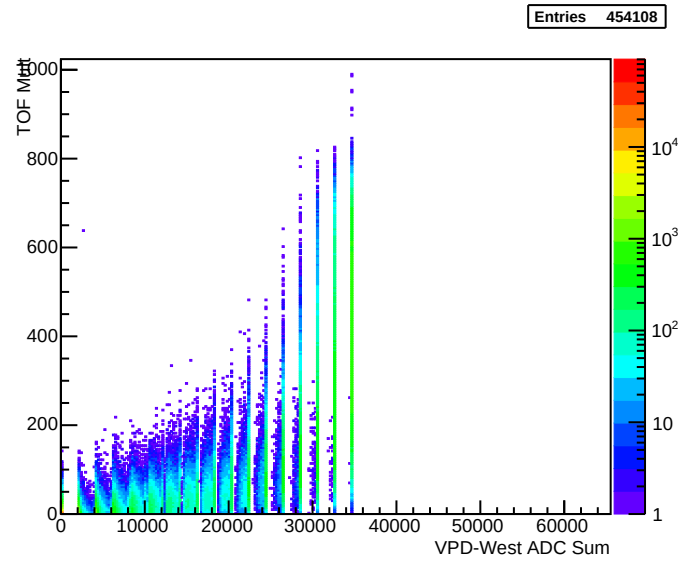
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

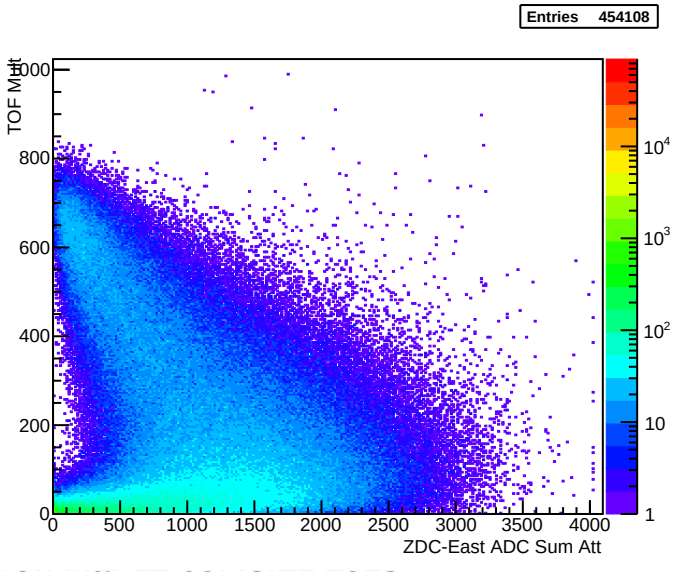


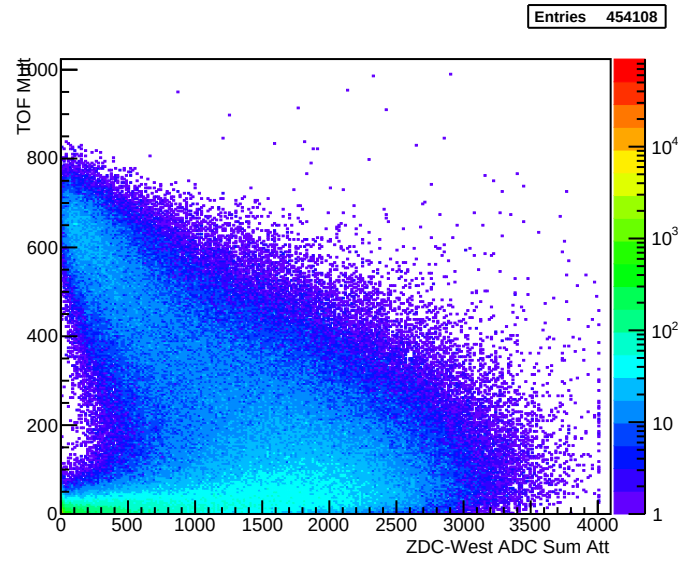

Entries 454108
TOF Mult
BBC-S-East ADC Sum

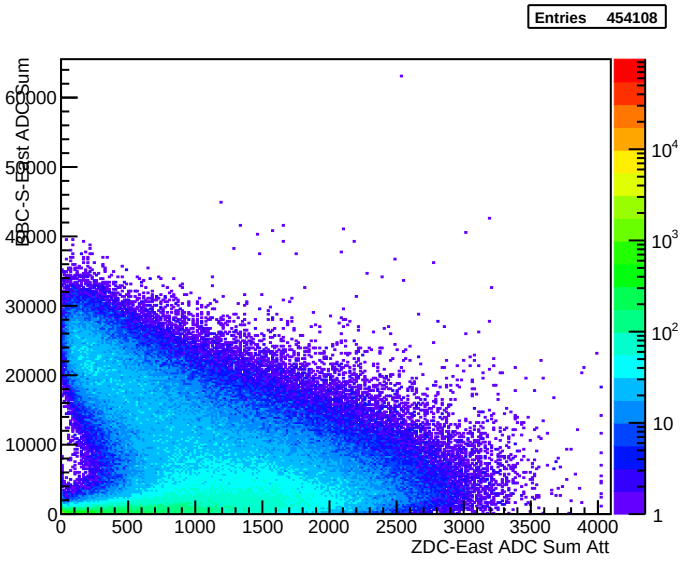

Entries 454108
TOF Mult
BBC-S-West ADC Sum

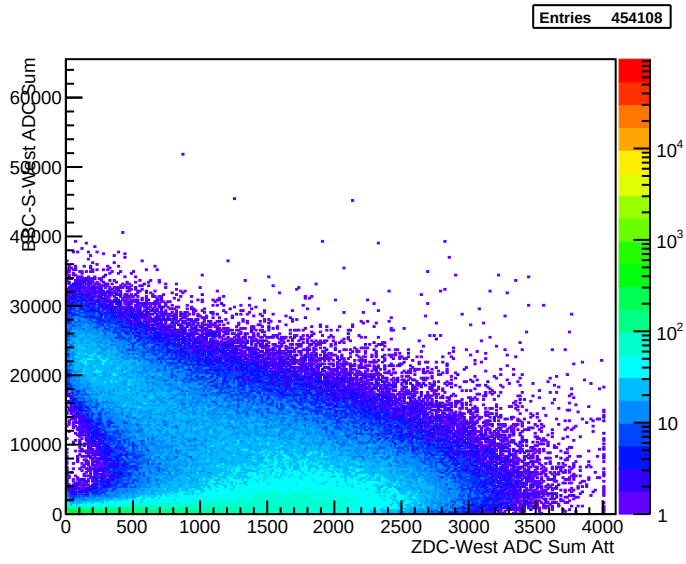

Entries 454108
TOF Mult
VPD-East ADC Sum

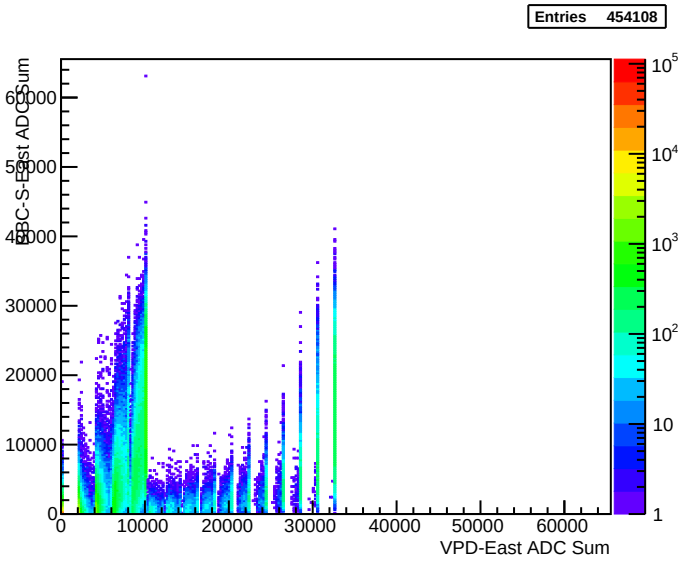

Entries 454108
TOF Mult
VPD-West ADC Sum

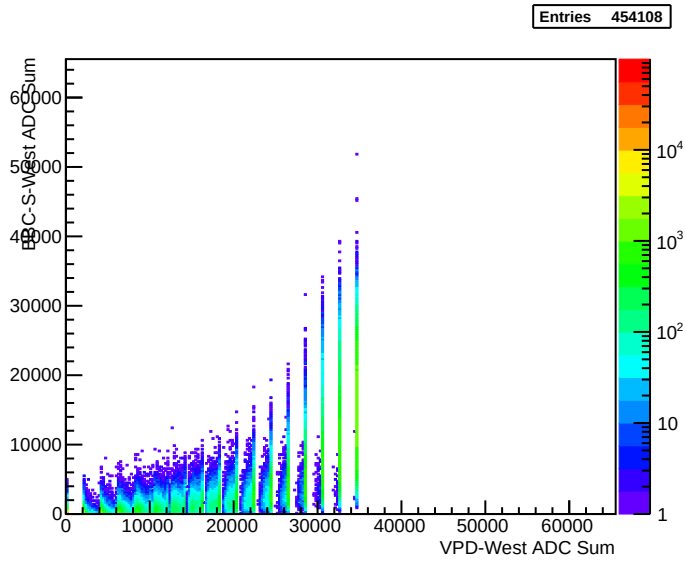

Entries 454108
TOF Mult
ZDC-East ADC Sum Att

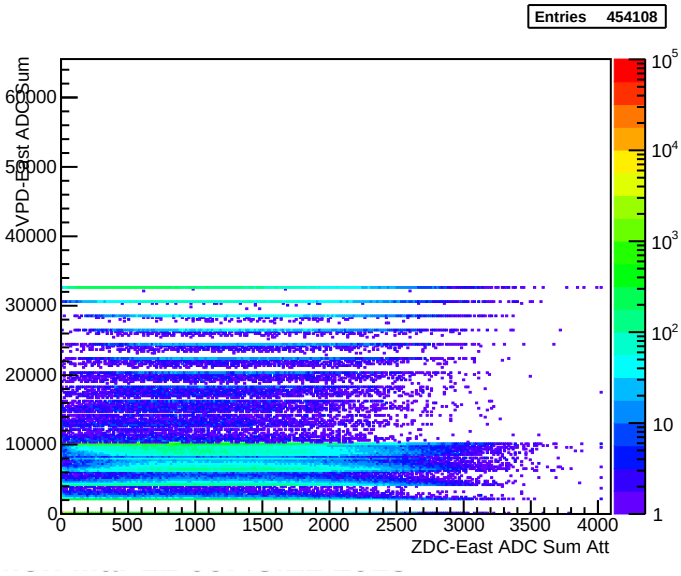

Entries 454108
TOF Mult
ZDC-West ADC Sum Att

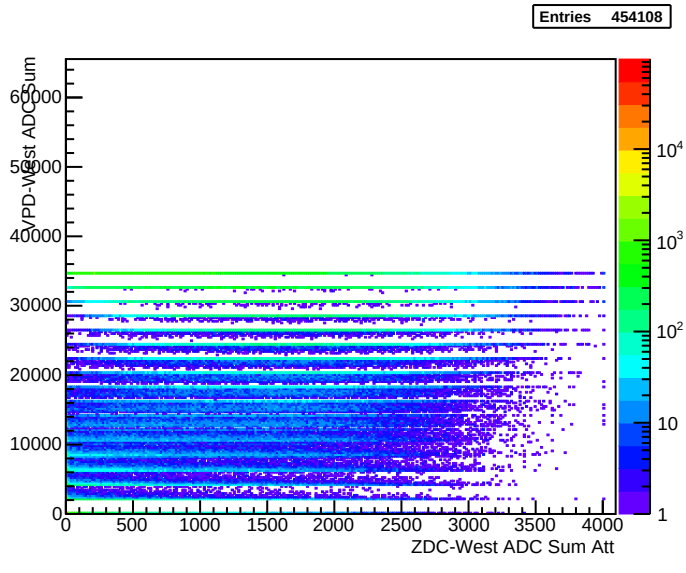

Entries 454108
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

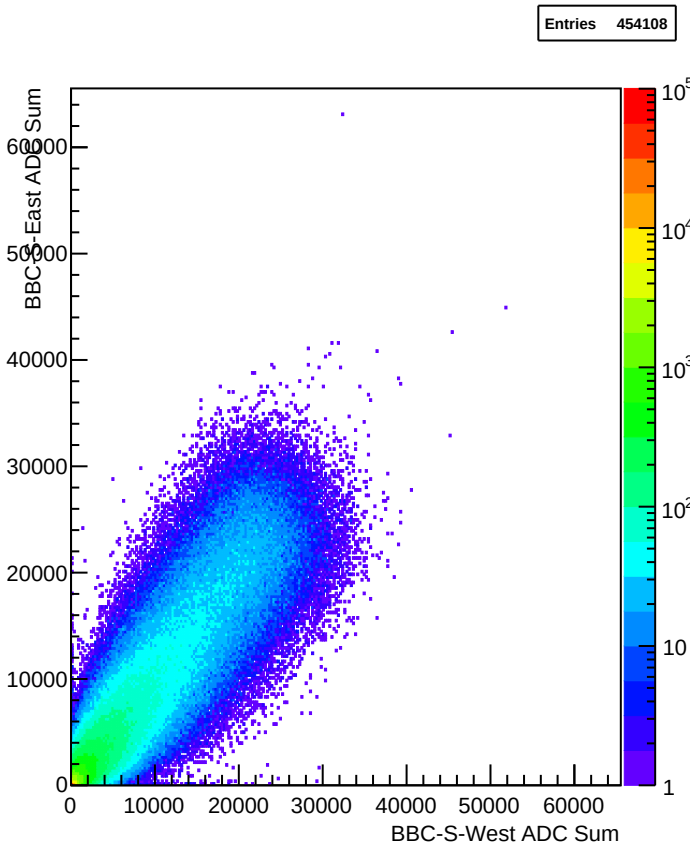

Entries 454108
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

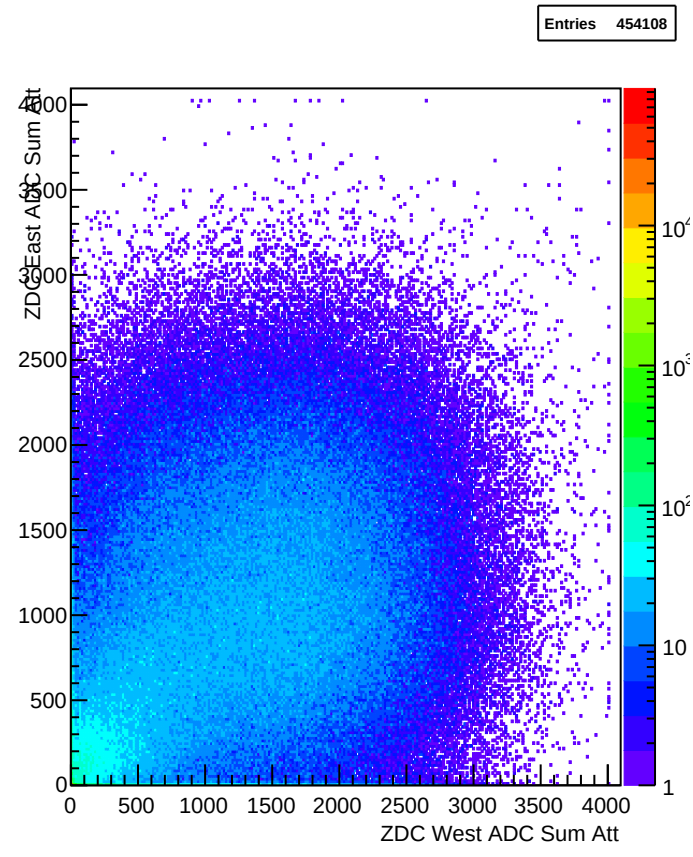

Entries 454108
BBC-S-East ADC Sum
VPD-East ADC Sum

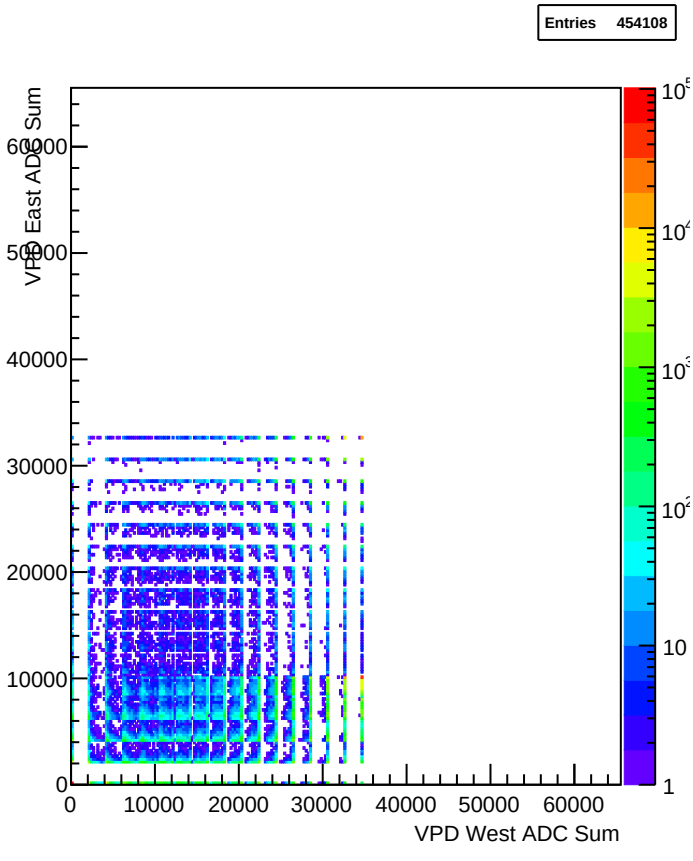

Entries 454108
BBC-S-West ADC Sum
VPD-West ADC Sum

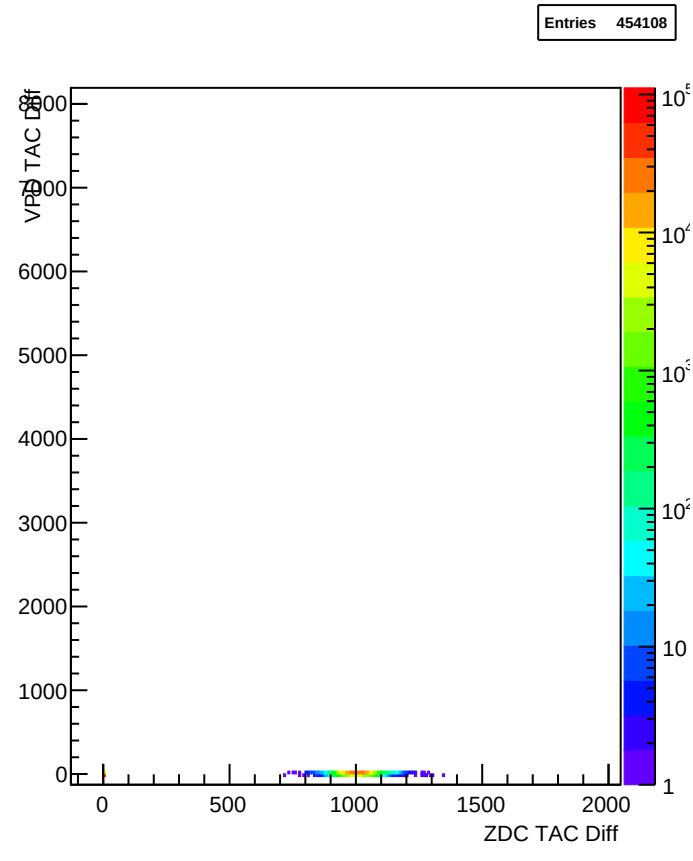

Entries 454108
VPD-East ADC Sum
ZDC-East ADC Sum Att

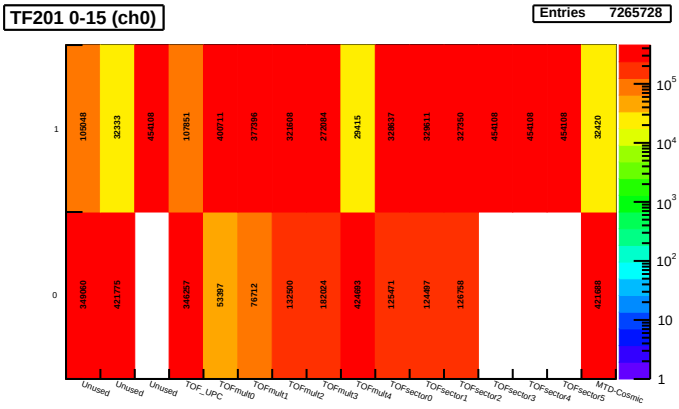

Entries 454108
VPD-West ADC Sum
ZDC-West ADC Sum Att

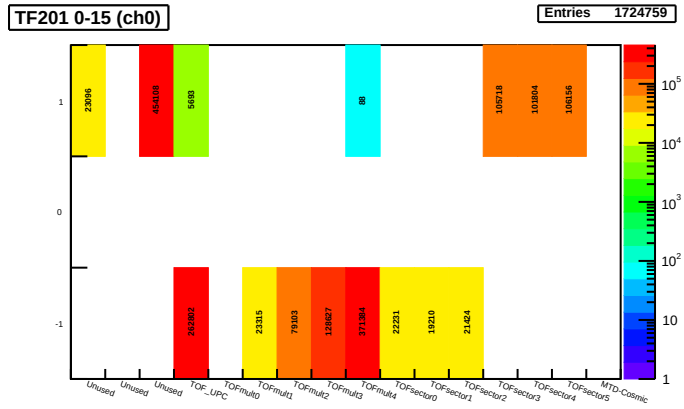

Entries 454108
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 454108
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 454108
VPD East ADC Sum
VPD West ADC Sum


Entries 454108
VPD TAC Diff
ZDC TAC Diff


TF201 0-15 (ch0)
Entries 7265728
105948 32333 454108 107951 400711 377096 323608 272884 29415 328637 329611 327350 454108 454108 454108 32420
349660 421775 346257 53397 76712 132500 182024 424693 125471 124497 126758 421888
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 1724759
23096 454108 5693 88 105718 101804 106156
262802 23315 79103 128627 371384 22231 19210 21424
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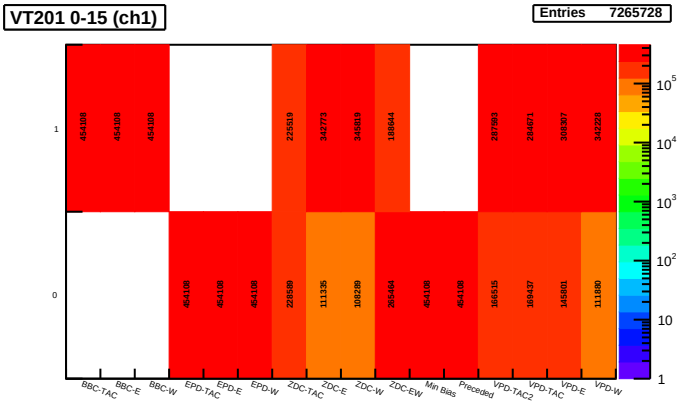
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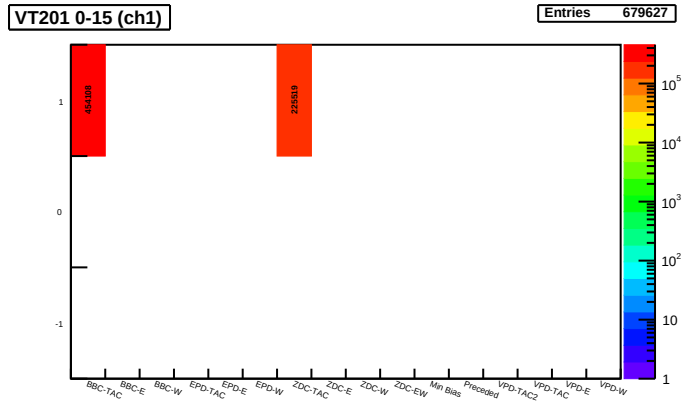
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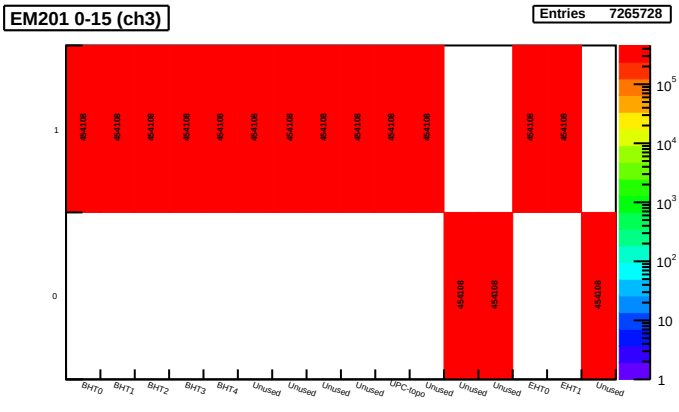
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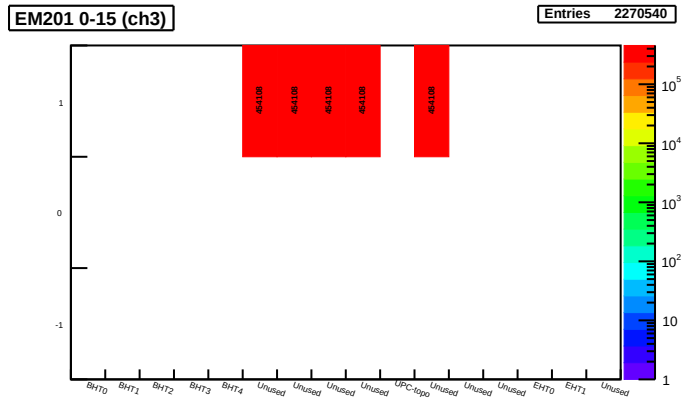
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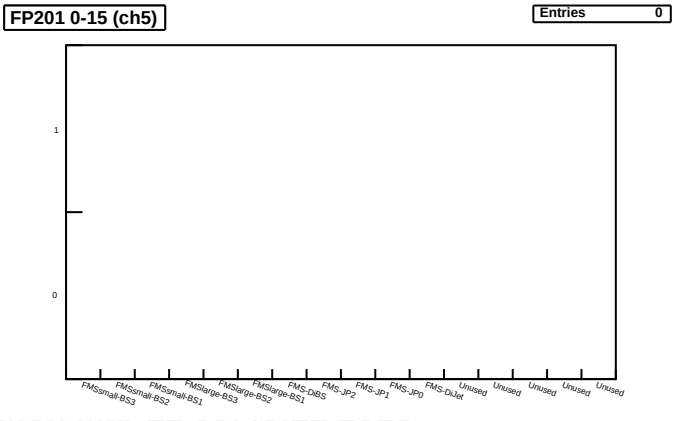
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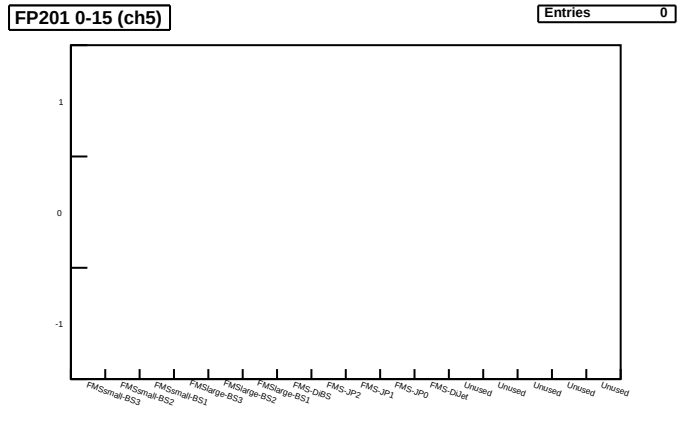
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