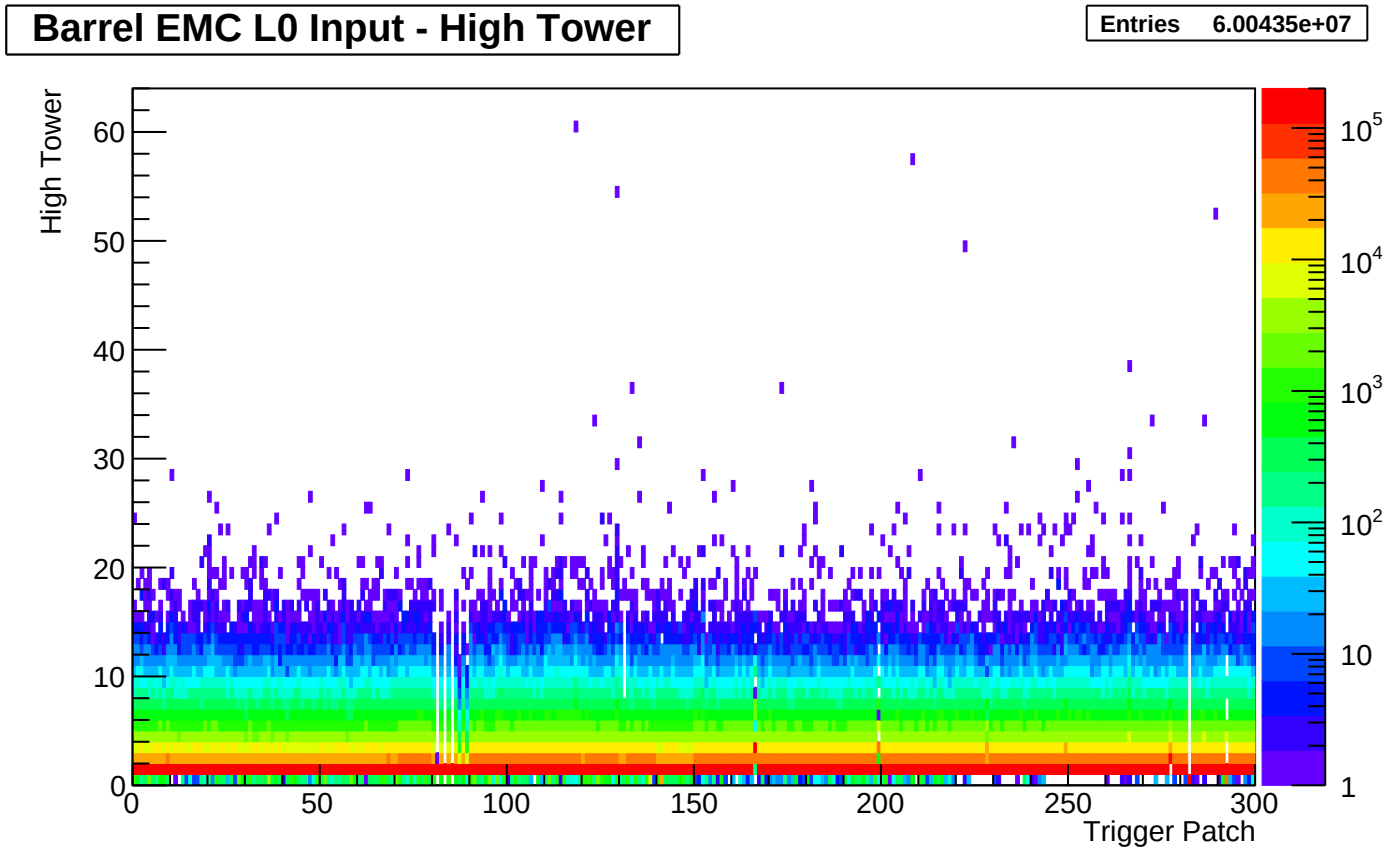
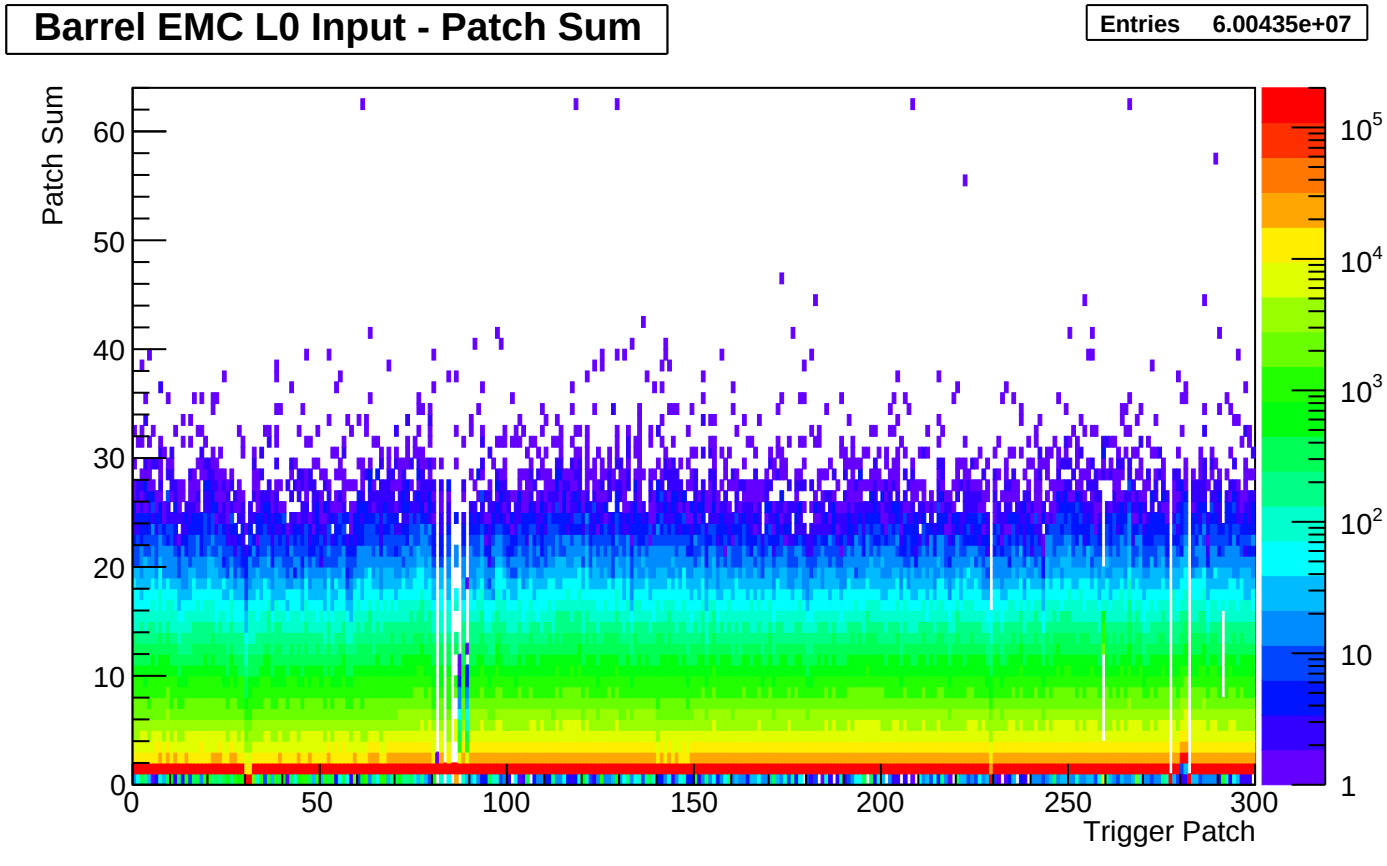
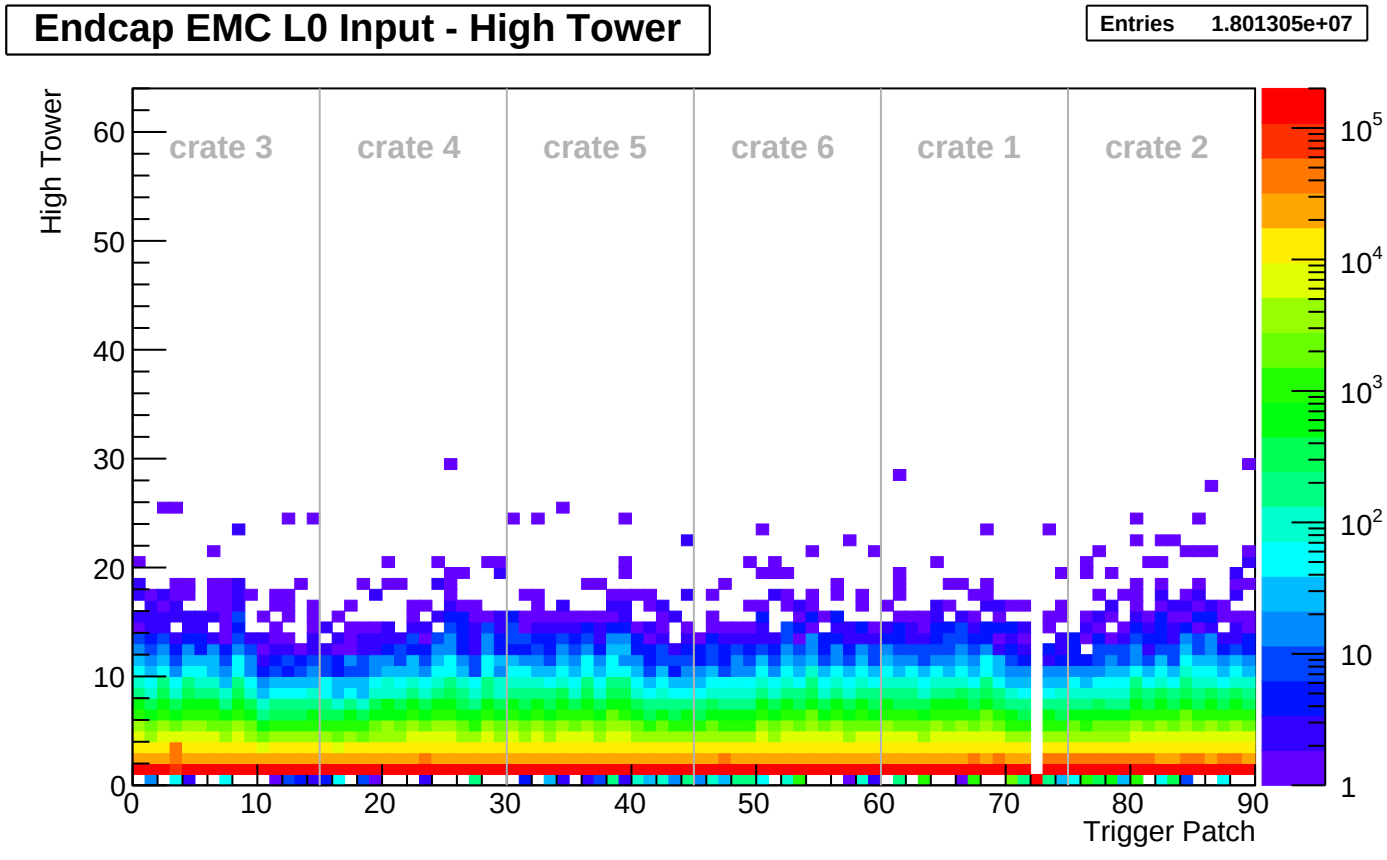
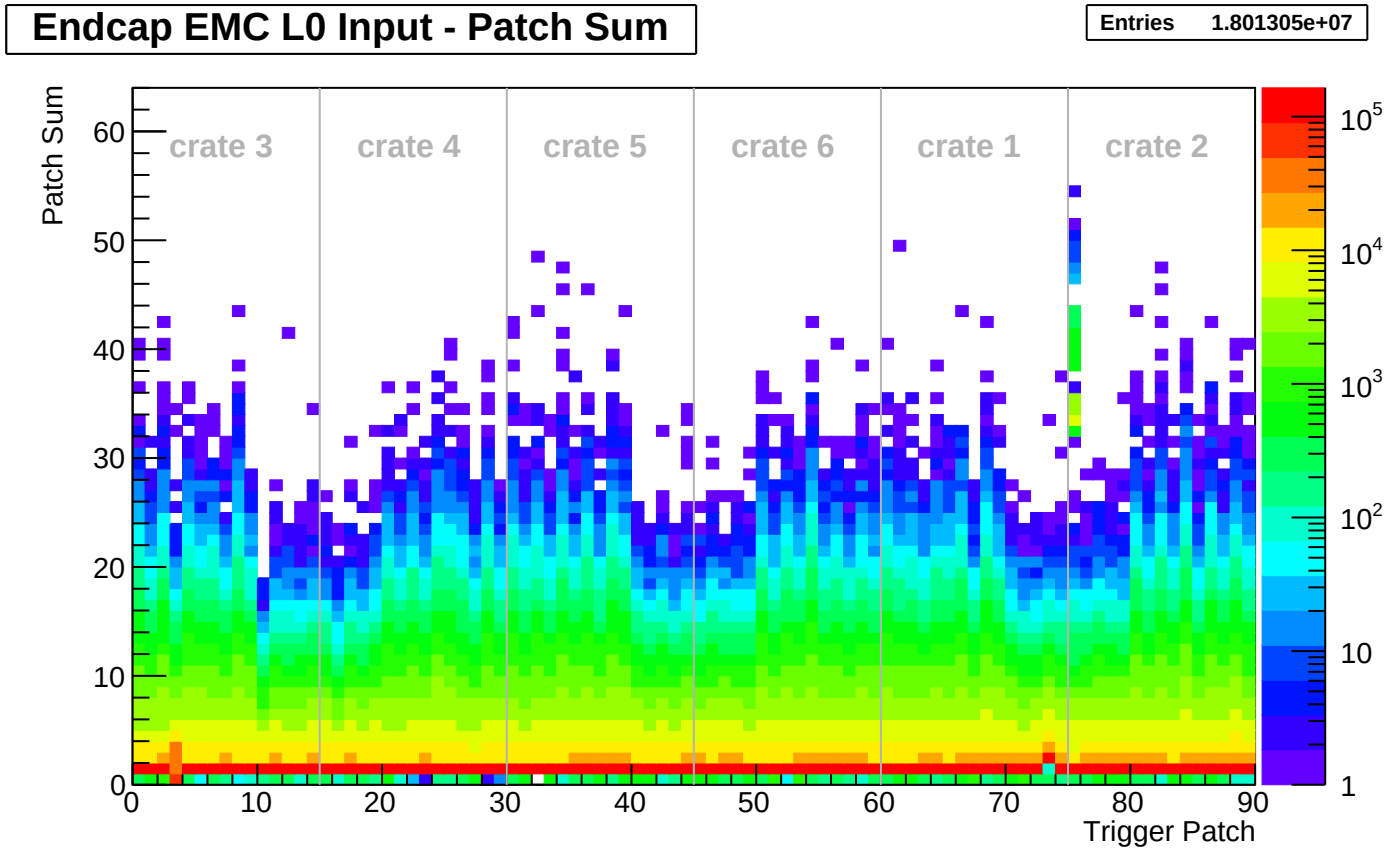
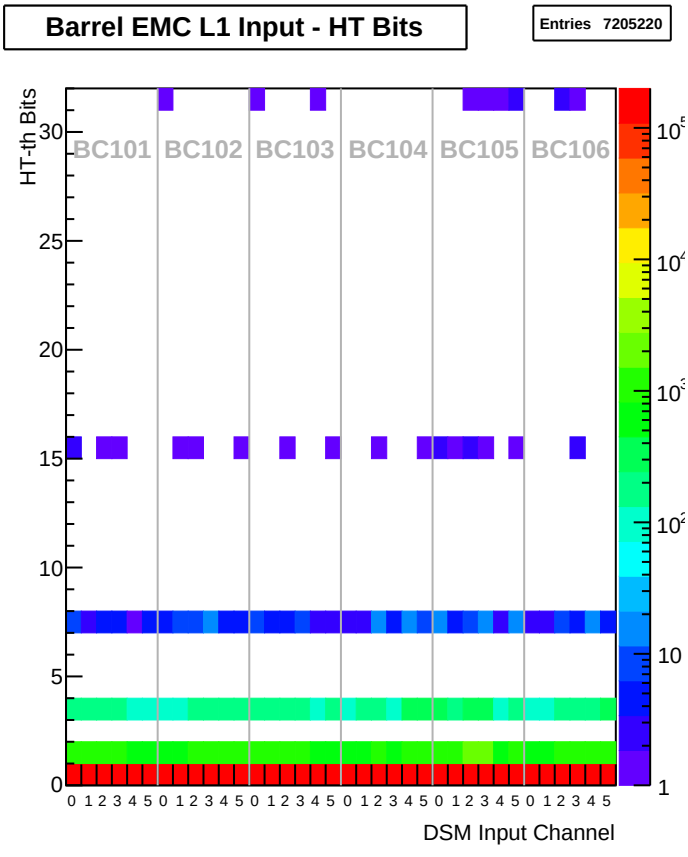
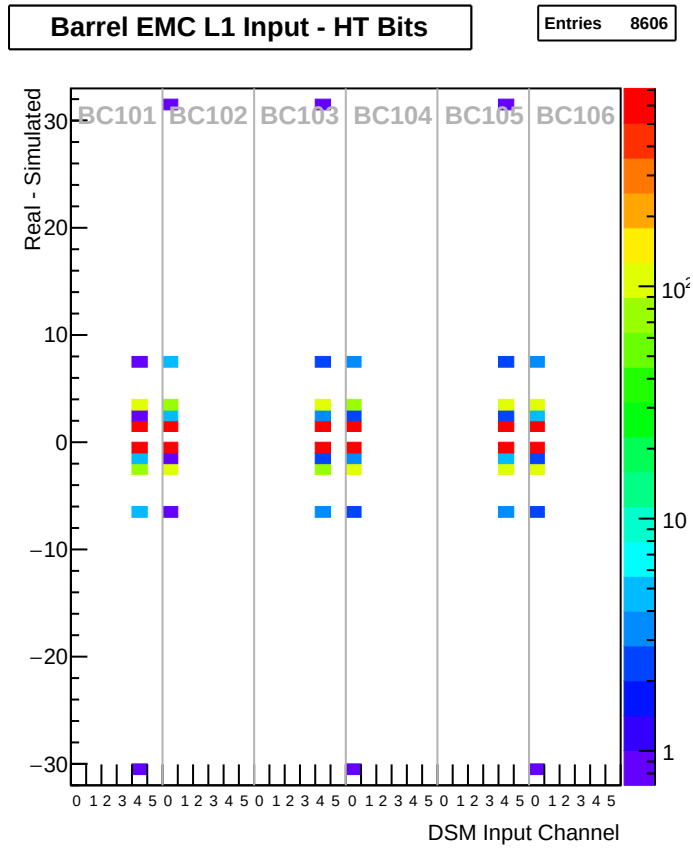

Barrel EMC L0 Input - High Tower
Entries 6.00435e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

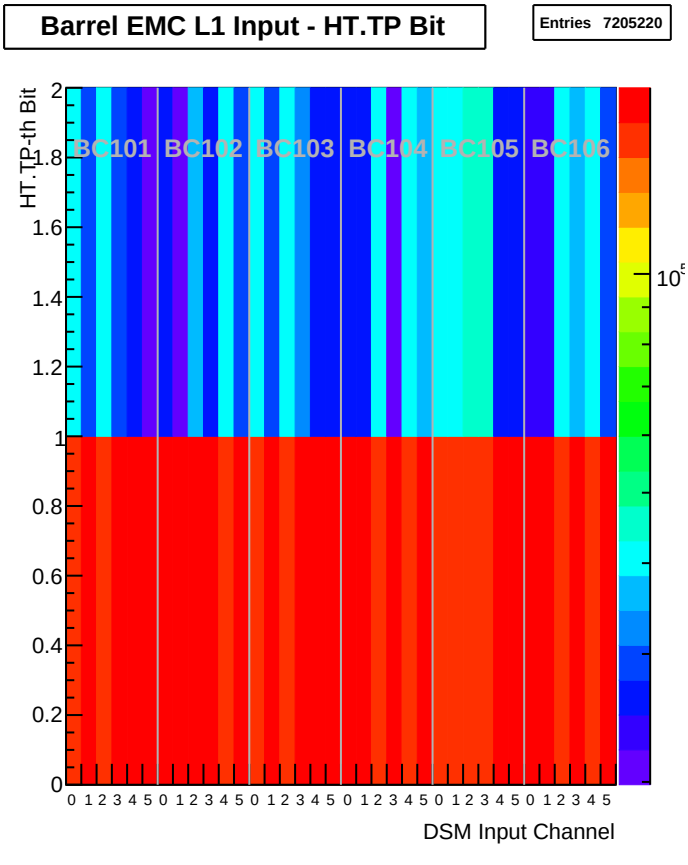

Barrel EMC L0 Input - Patch Sum
Entries 6.00435e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

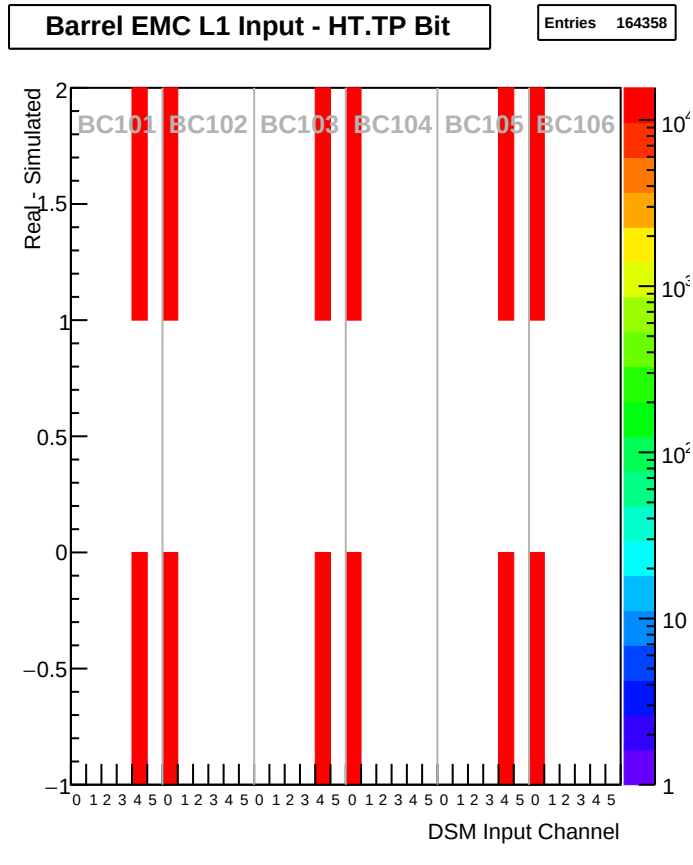

Endcap EMC L0 Input - High Tower
Entries 1.801305e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

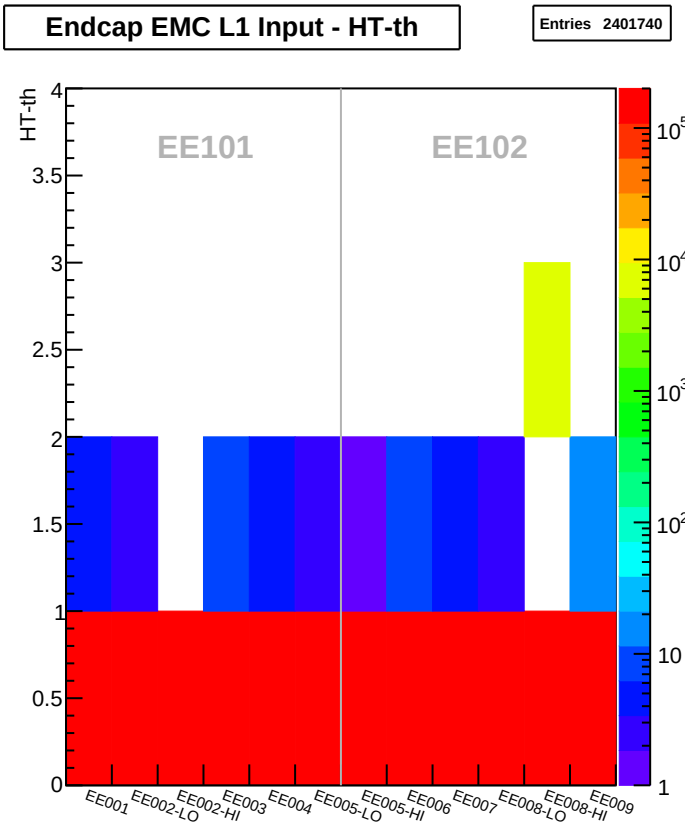

Endcap EMC L0 Input - Patch Sum
Entries 1.801305e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

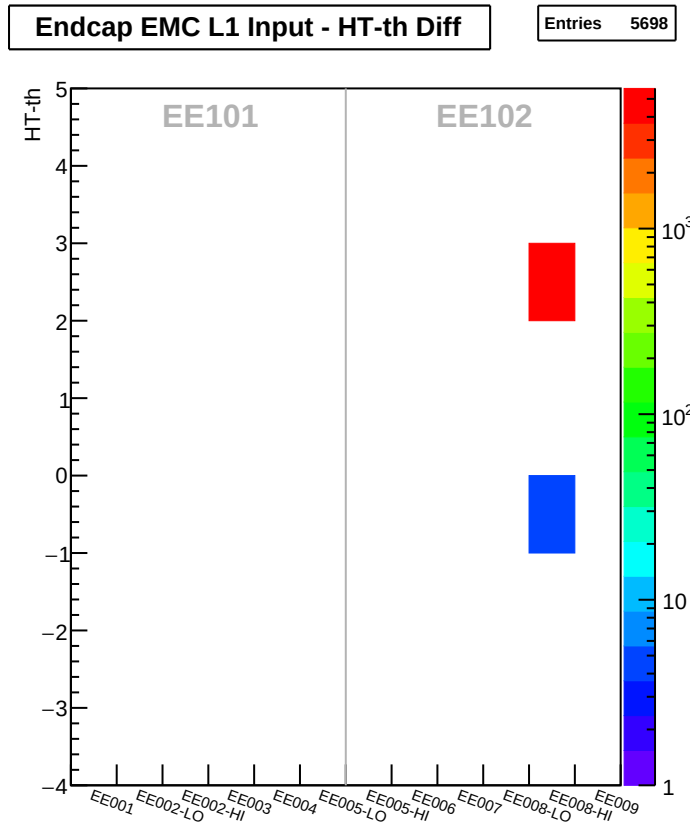

Barrel EMC L1 Input - HT Bits
Entries 7205220
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

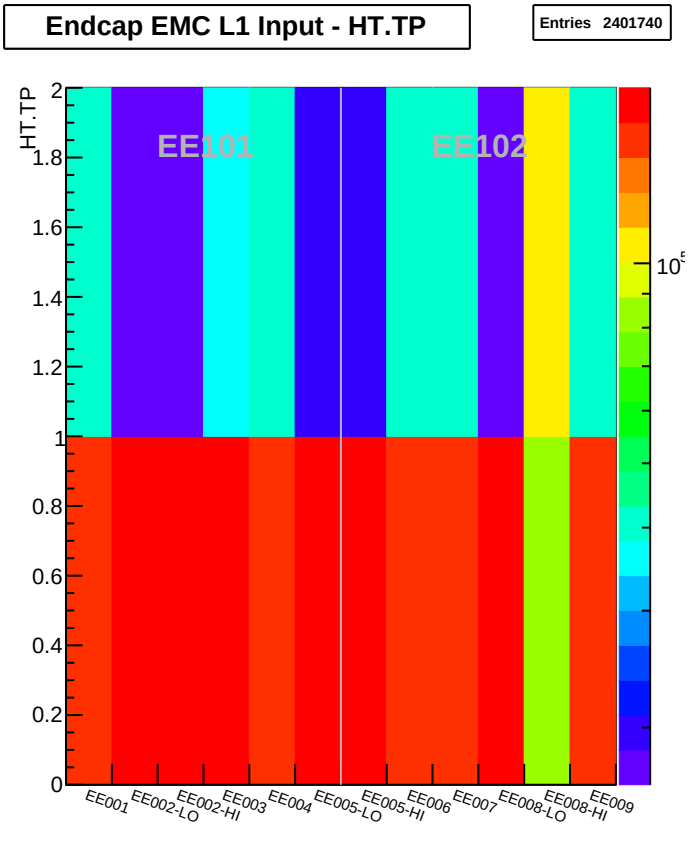

Barrel EMC L1 Input - HT Bits
Entries 8606
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

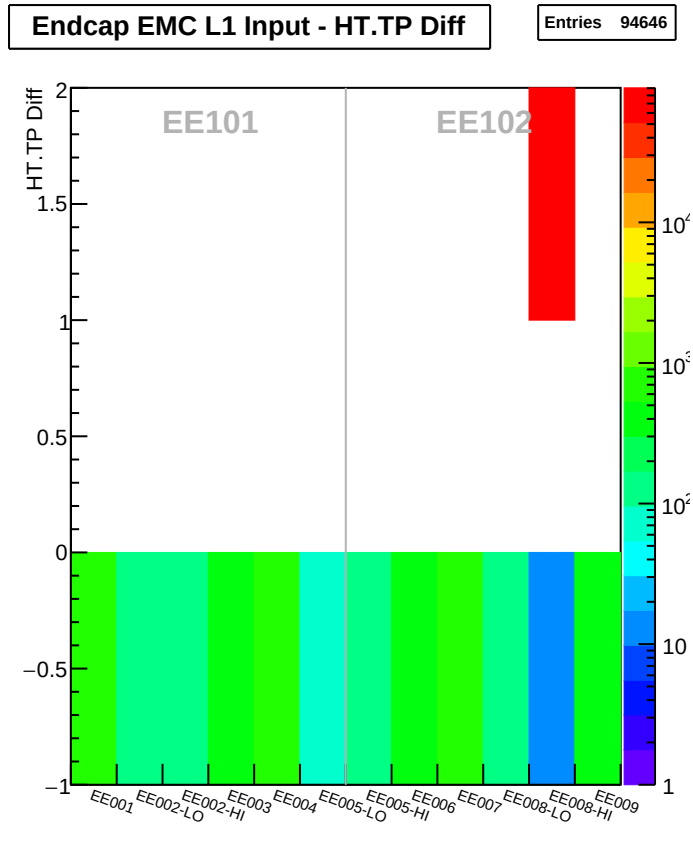

Barrel EMC L1 Input - HT.TP Bit
Entries 7205220
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

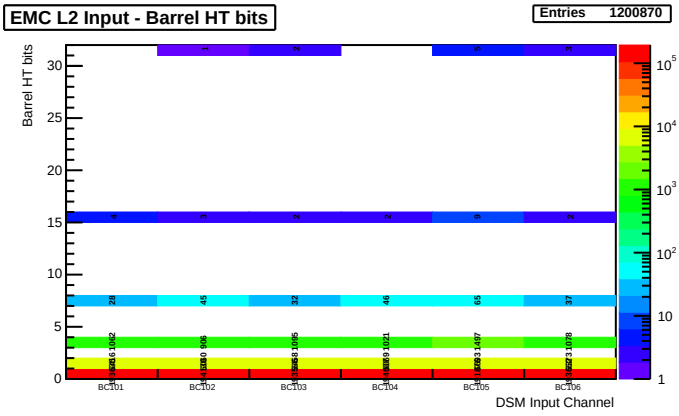

Barrel EMC L1 Input - HT.TP Bit
Entries 164358
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

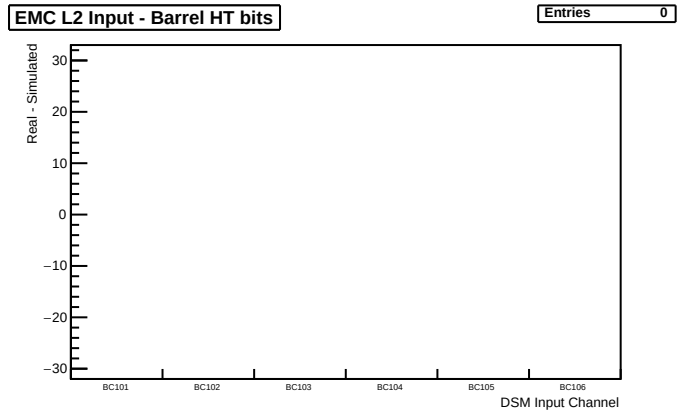

Endcap EMC L1 Input - HT-th
Entries 2401740
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

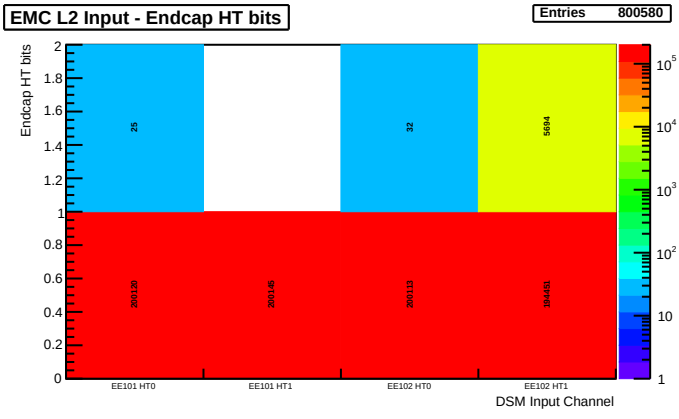

Endcap EMC L1 Input - HT-th Diff
Entries 5698
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

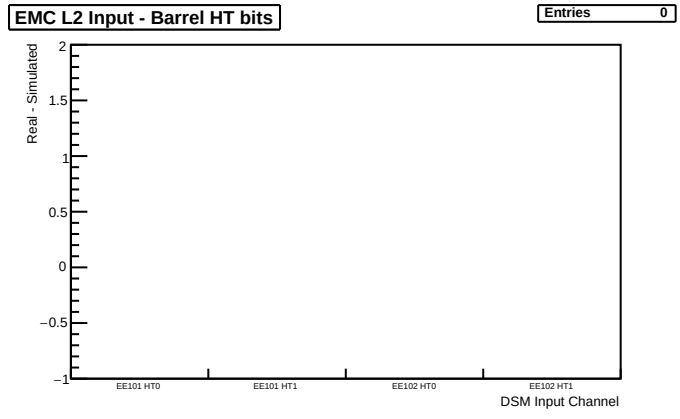

Endcap EMC L1 Input - HT.TP
Entries 2401740
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

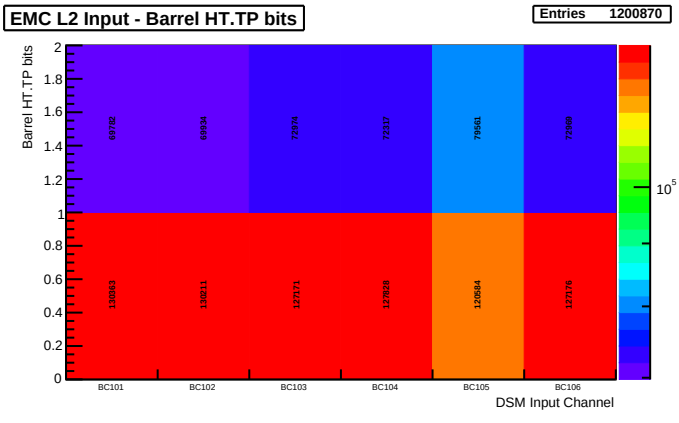

Endcap EMC L1 Input - HT.TP Diff
Entries 94646
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

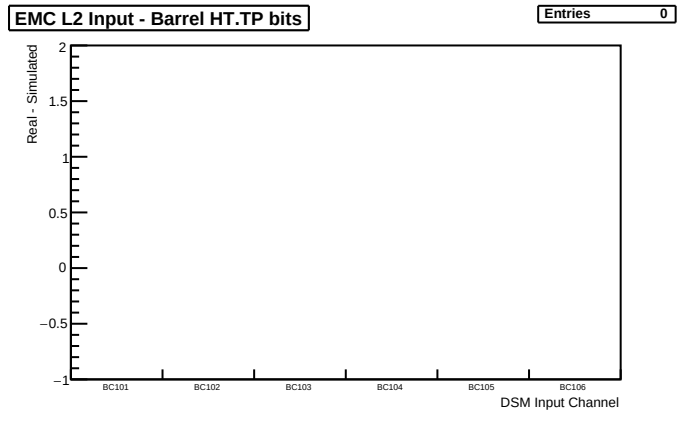

EMC L2 Input - Barrel HT bits
Entries 1200870
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

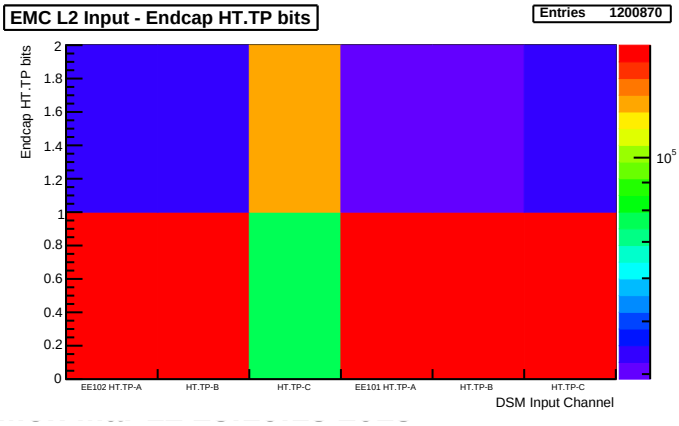

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

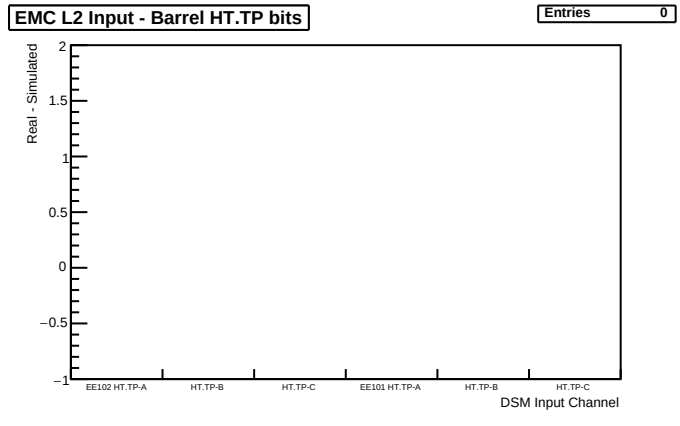

EMC L2 Input - Endcap HT bits
Entries 800580
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

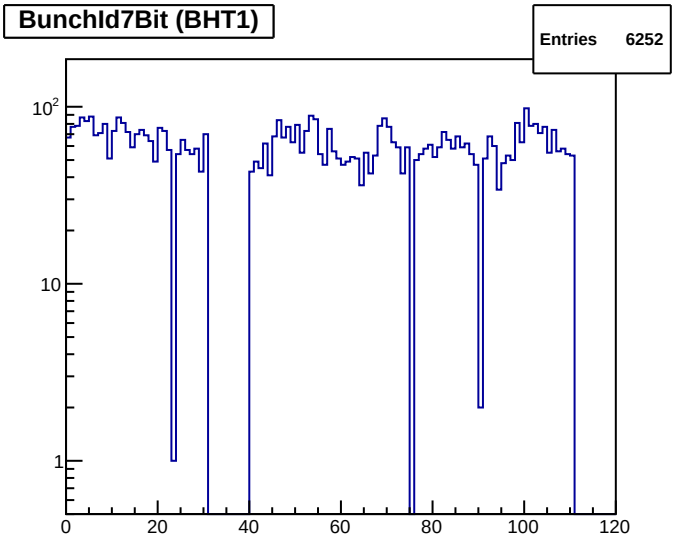

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

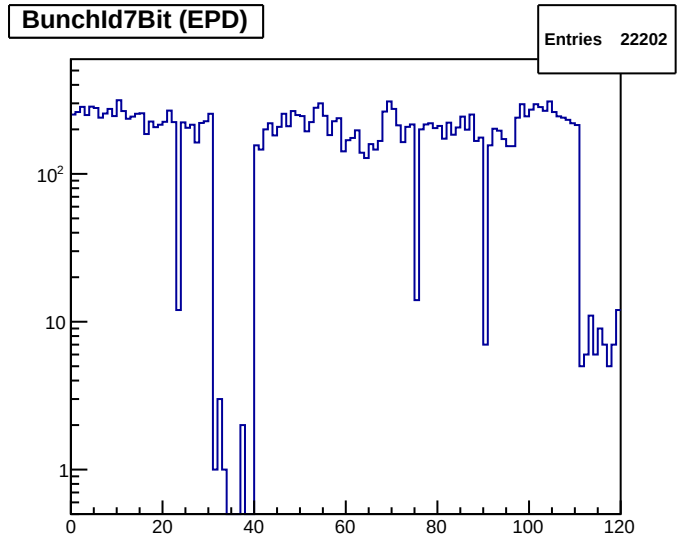

EMC L2 Input - Barrel HT.TP bits
Entries 1200870
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

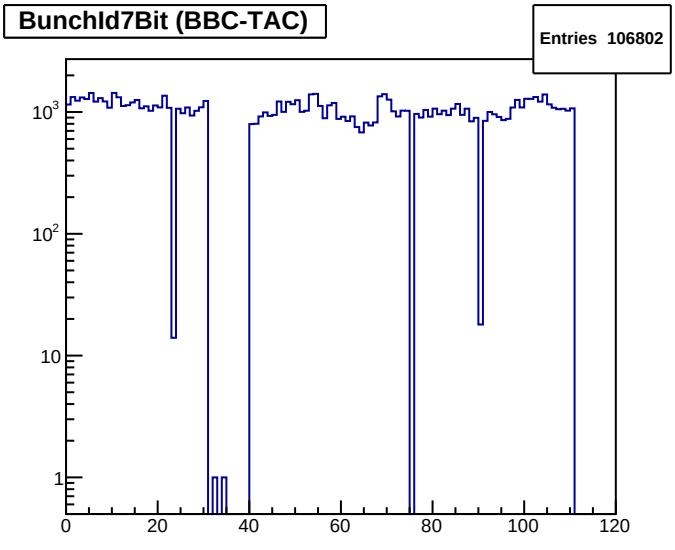

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

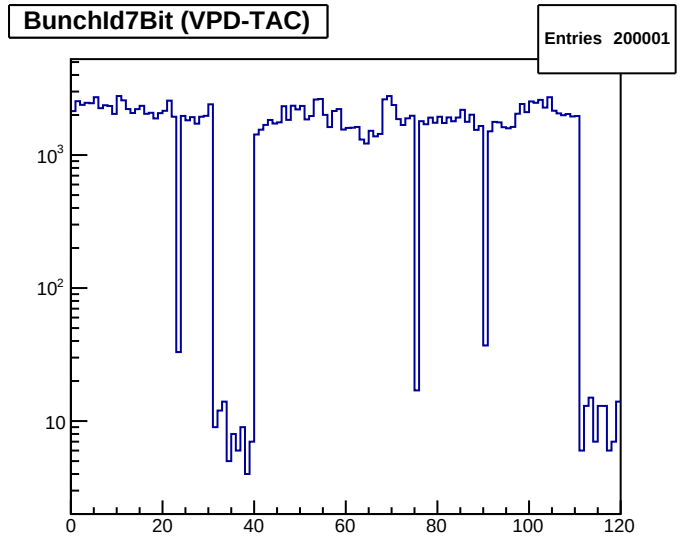

EMC L2 Input - Endcap HT.TP bits
Entries 1200870
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

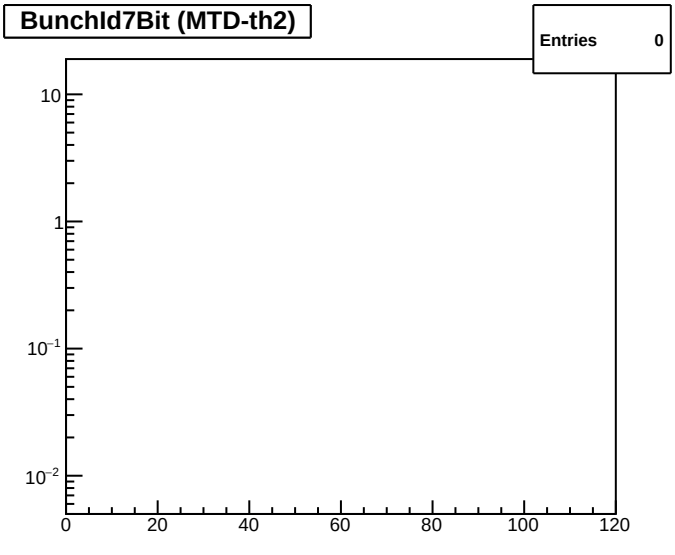

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

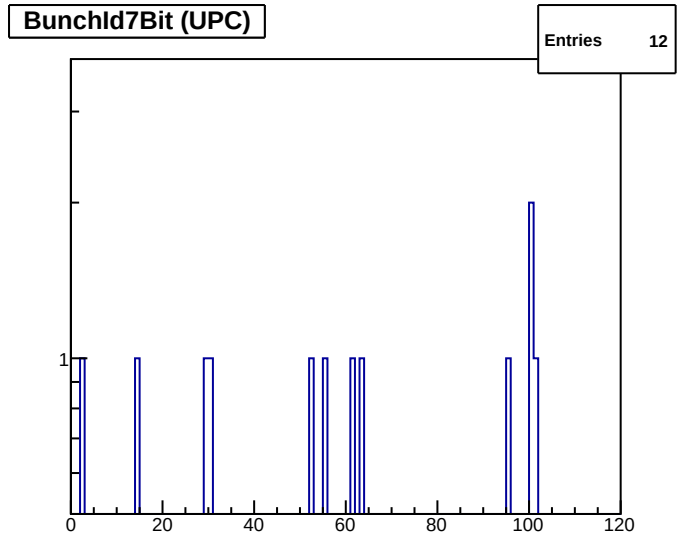

BunchId7Bit (BHT1)
Entries 6252

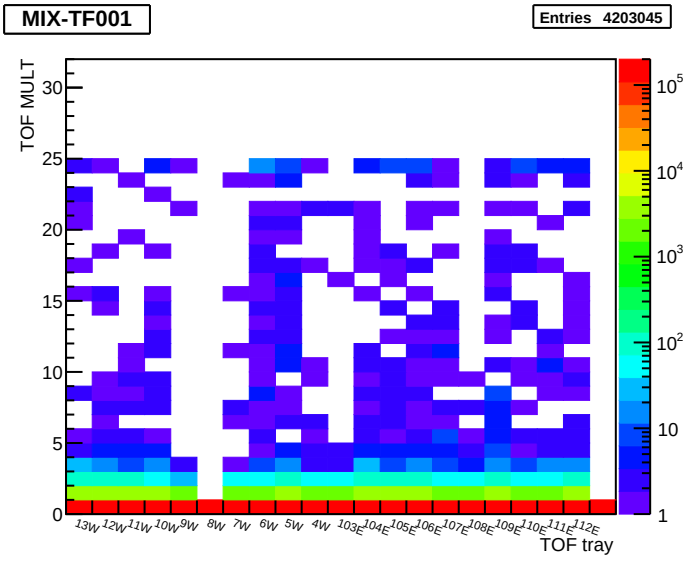

BunchId7Bit (EPD)
Entries 22202

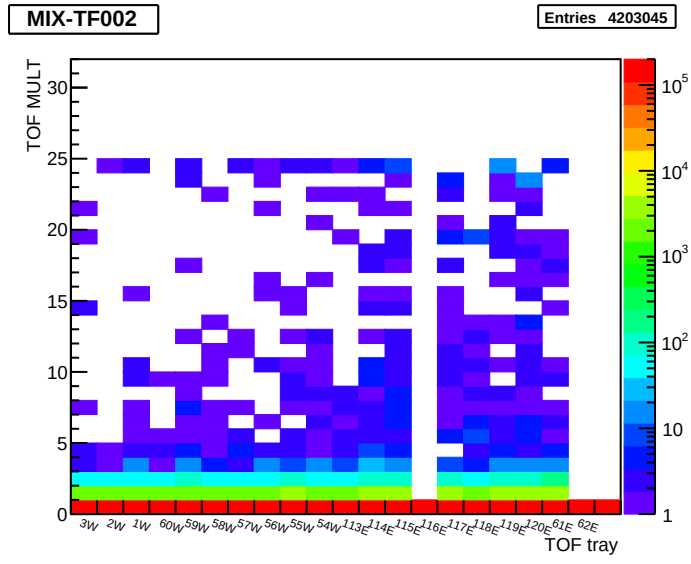

BunchId7Bit (BBC-TAC)
Entries 106802

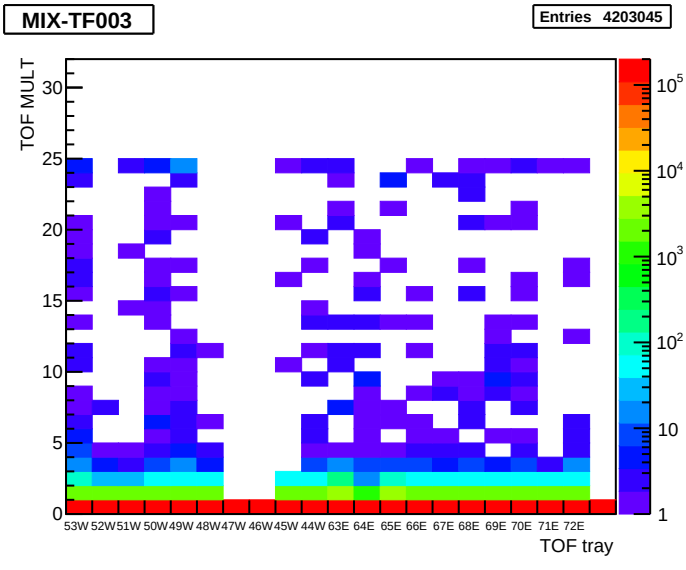

BunchId7Bit (VPD-TAC)
Entries 200001

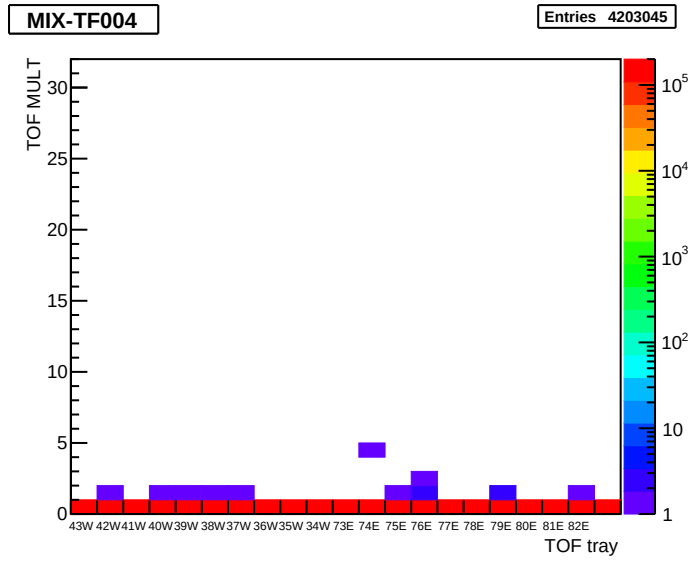

BunchId7Bit (MTD-th2)
Entries 0

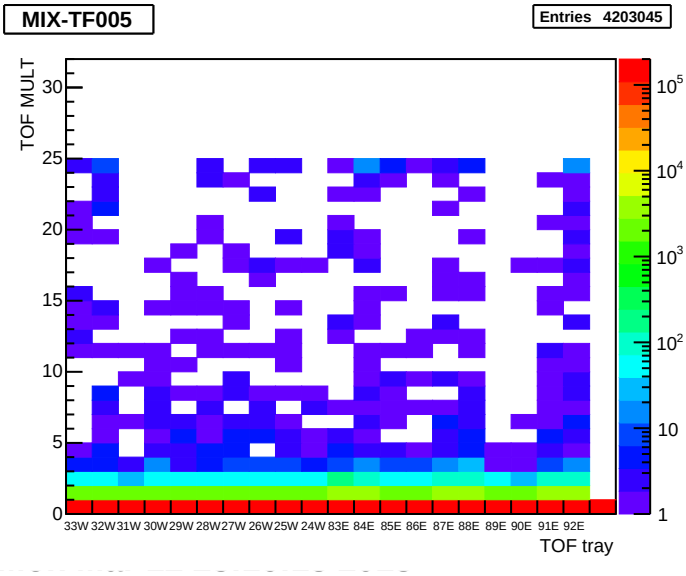

BunchId7Bit (UPC)
Entries 12

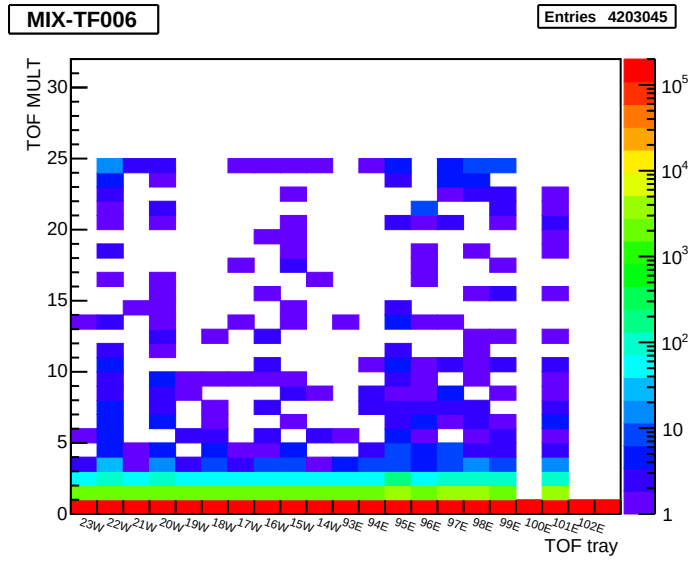

MIX-TF001
Entries 4203045
TOF MULT
TOF tray

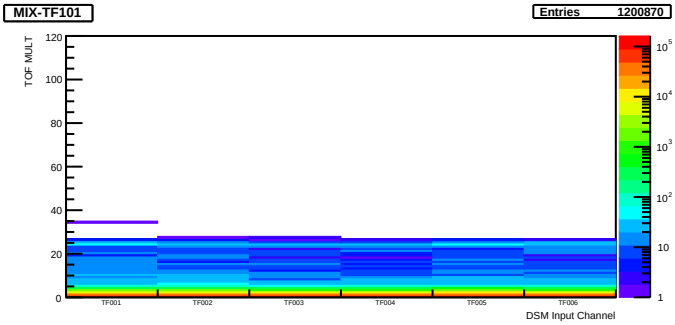

MIX-TF002
Entries 4203045
TOF MULT
TOF tray

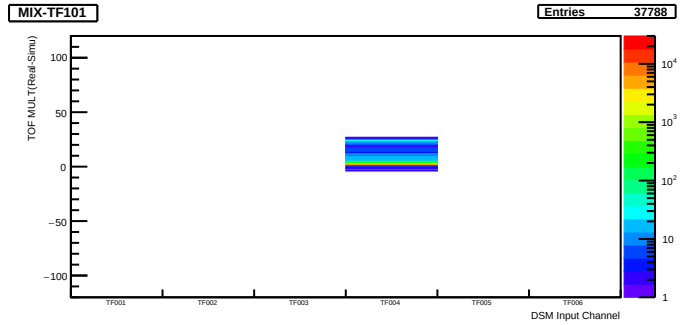

MIX-TF003
Entries 4203045
TOF MULT
TOF tray

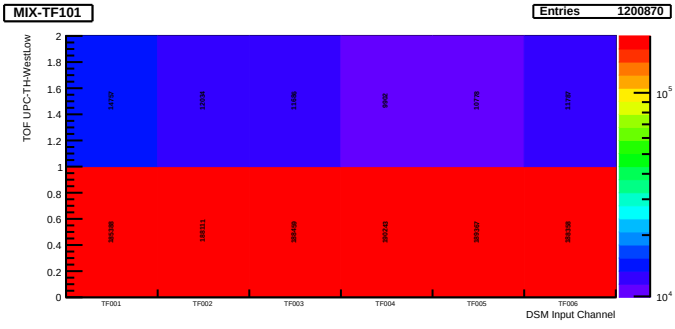

MIX-TF004
Entries 4203045
TOF MULT
TOF tray

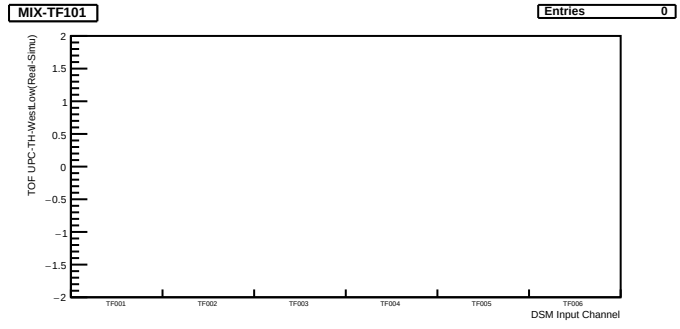

MIX-TF005
Entries 4203045
TOF MULT
TOF tray

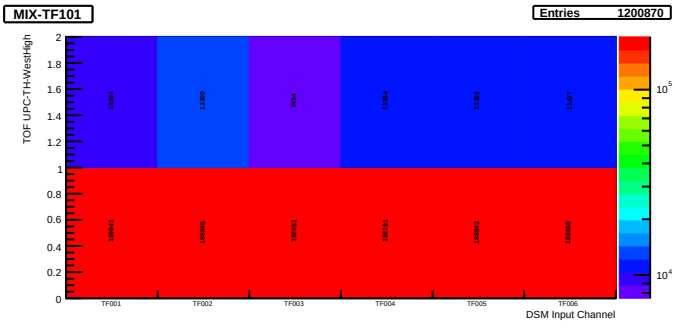

MIX-TF006
Entries 4203045
TOF MULT
TOF tray

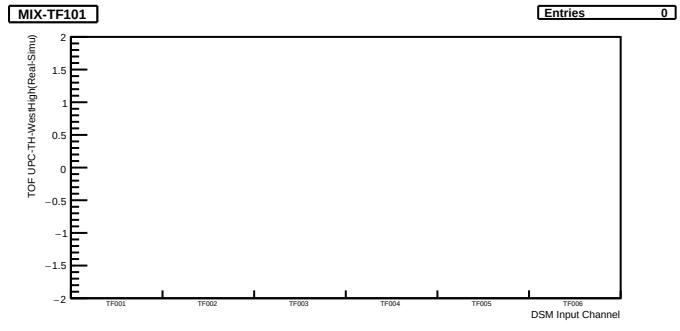

MIX-TF101
Entries 1200870
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

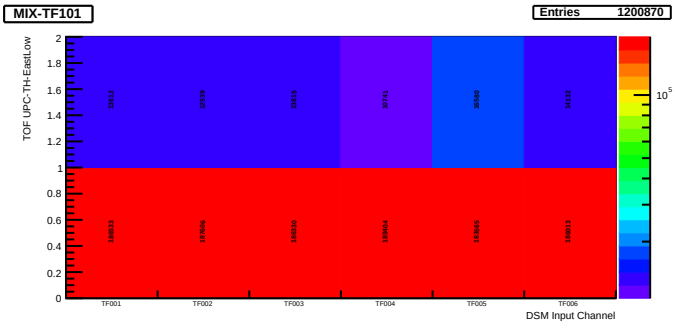

MIX-TF101
Entries 37788
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

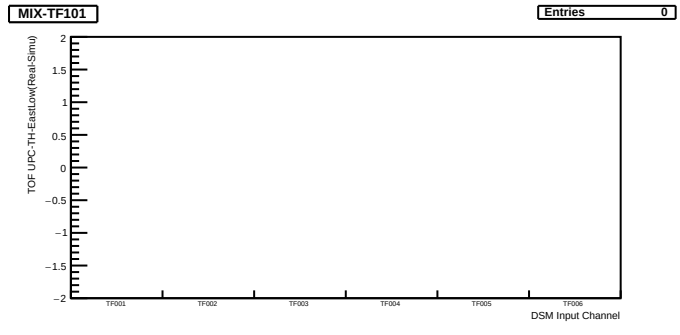

MIX-TF101
Entries 1200870
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

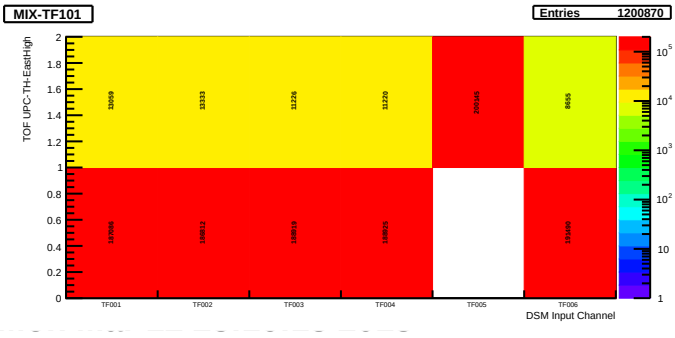

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

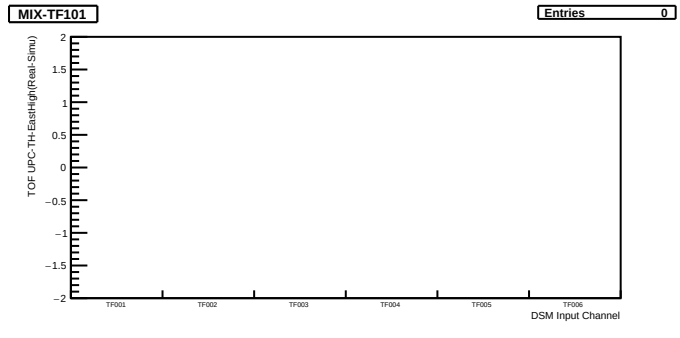

MIX-TF101
Entries 1200870
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

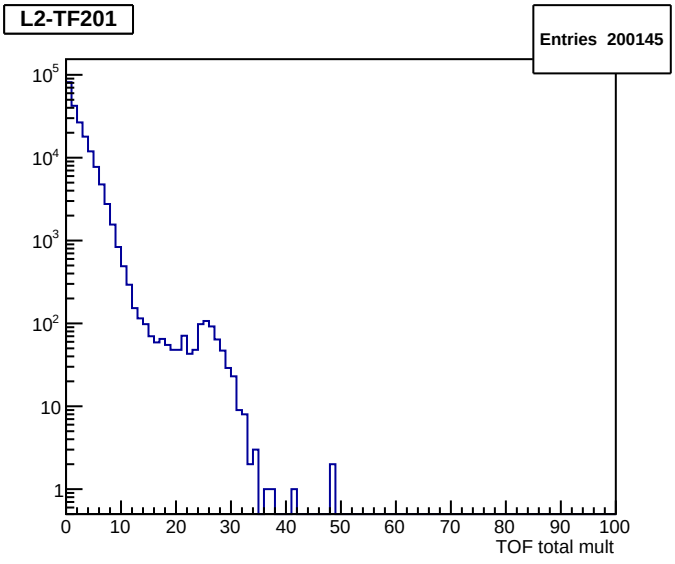

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

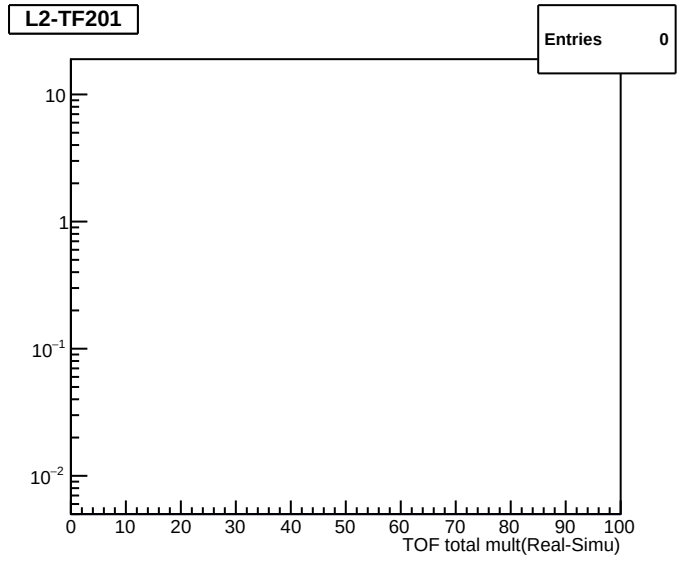

MIX-TF101
Entries 1200870
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

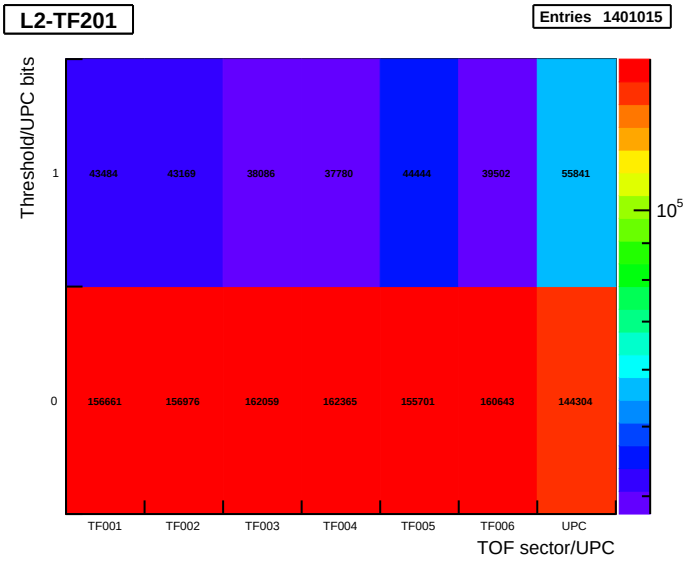

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

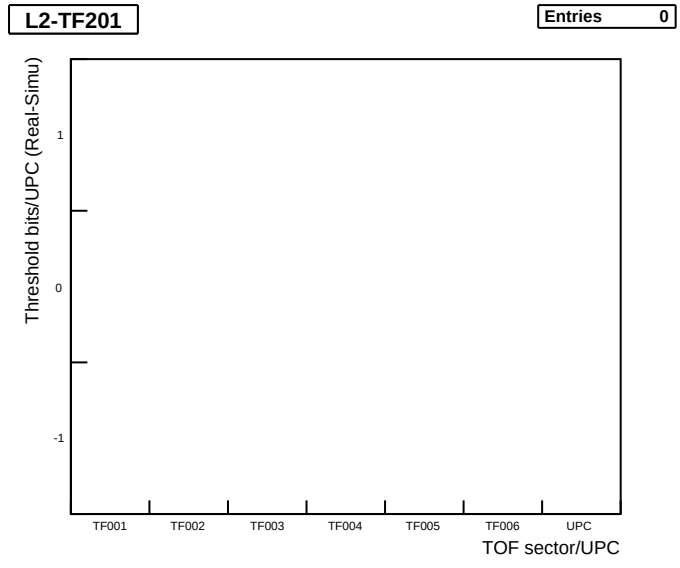

MIX-TF101
Entries 1200870
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

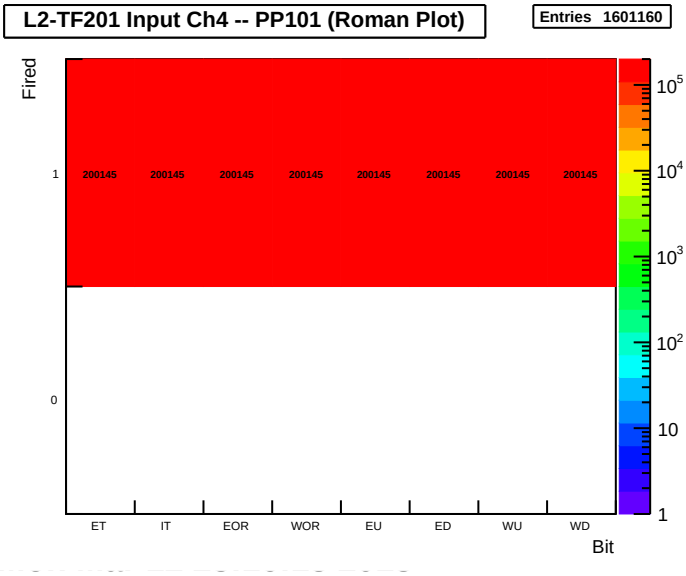

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

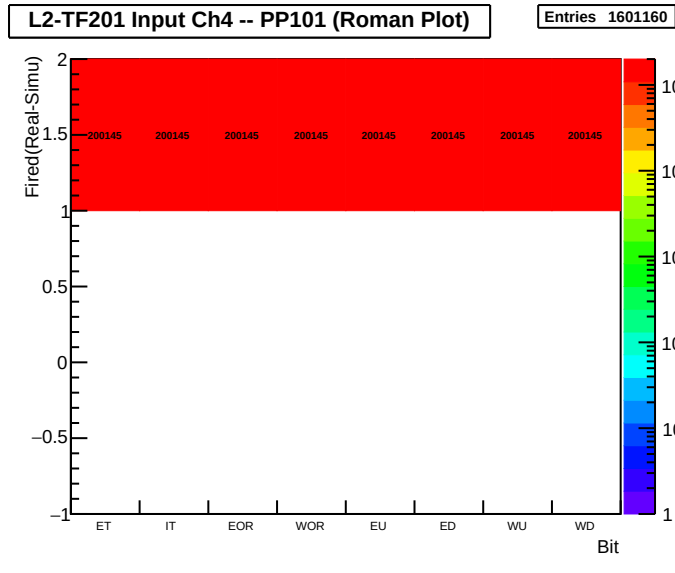

L2-TF201
Entries 200145
TOF total mult

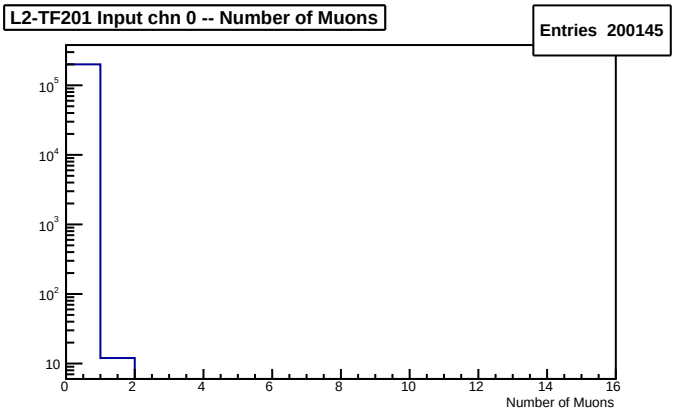

L2-TF201
Entries 0
TOF total mult(Real-Simu)

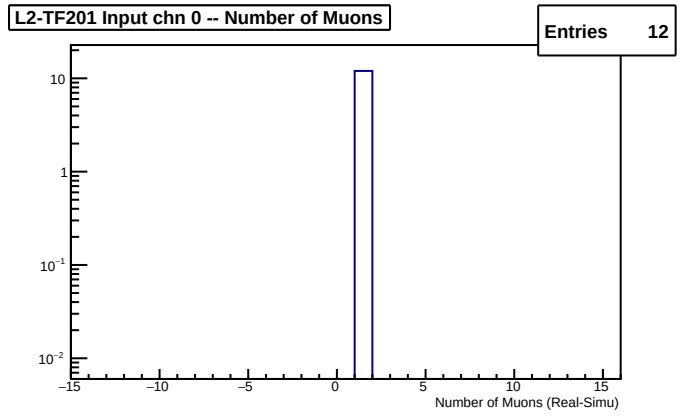

L2-TF201
Entries 1401015
Threshold/UPC bits
43484
43169
38086
37780
44444
39502
55841
156661
156976
162059
162365
155701
160643
144304
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

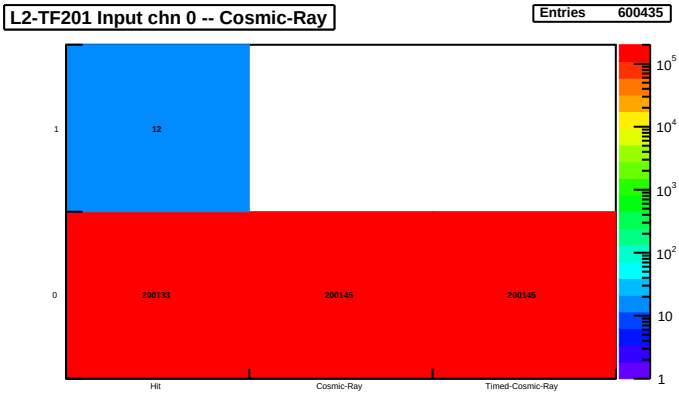

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

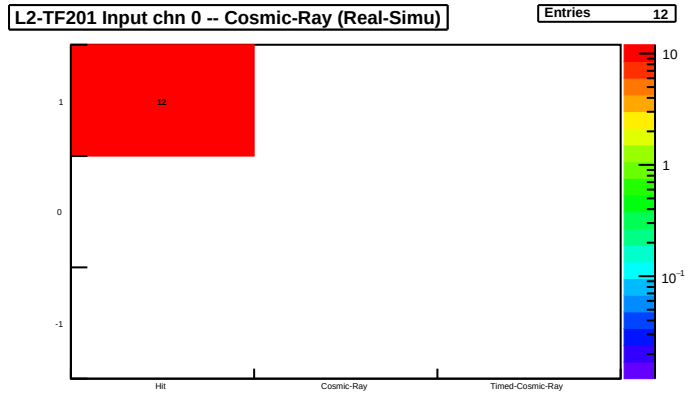

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 1601160
Fired
200145
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

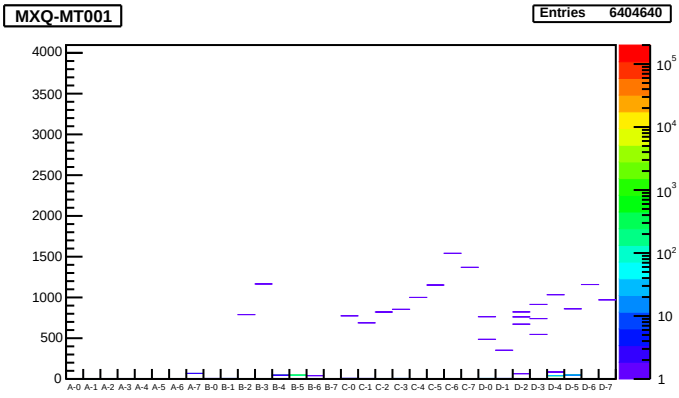

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 1601160
Fired(Real-Simu)
200145
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

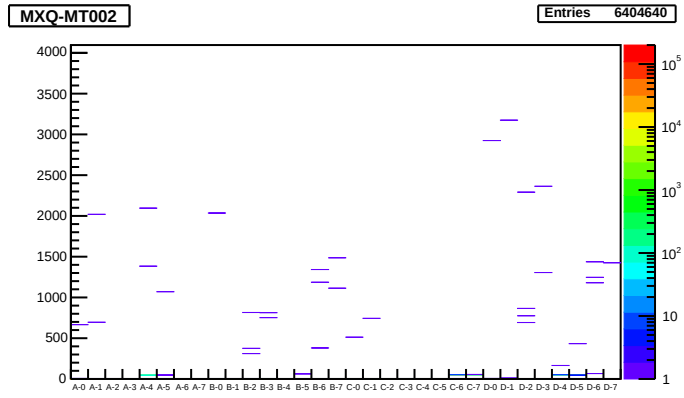

L2-TF201 Input chn 0 -- Number of Muons
Entries 200145
Number of Muons

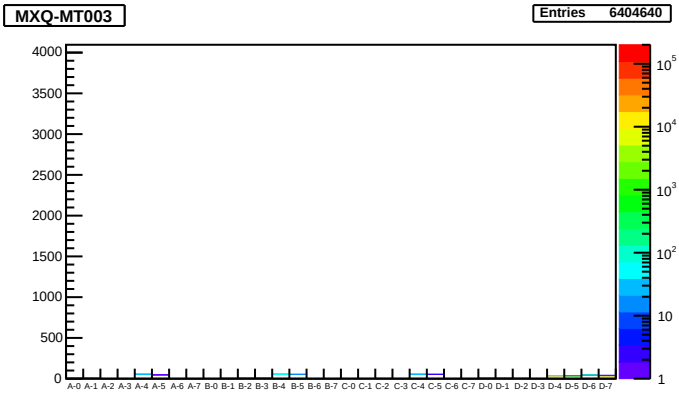

L2-TF201 Input chn 0 -- Number of Muons
Entries 12
Number of Muons (Real-Simu)

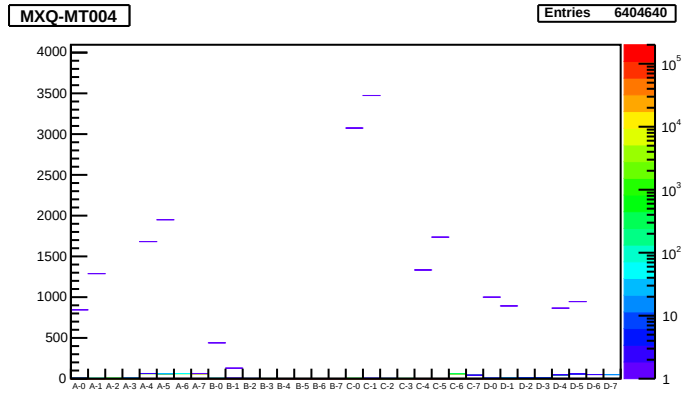

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 600435
12
200133
200145
200145
Hit
Cosmic-Ray
Timed-Cosmic-Ray

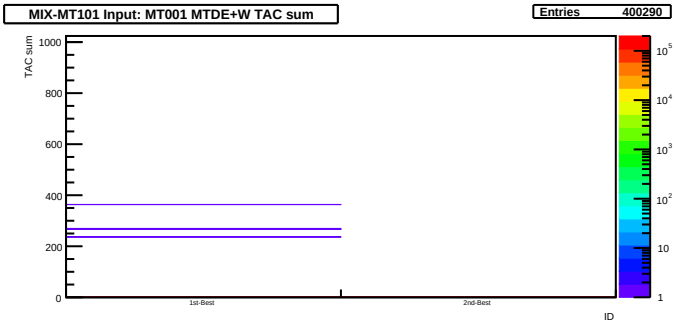

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 12
12
Hit
Cosmic-Ray
Timed-Cosmic-Ray

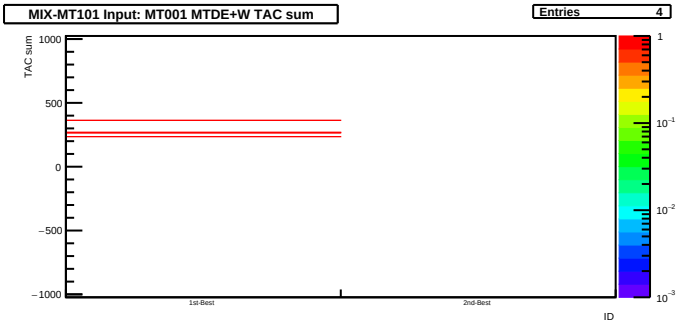

MXQ-MT001
Entries 6404640
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

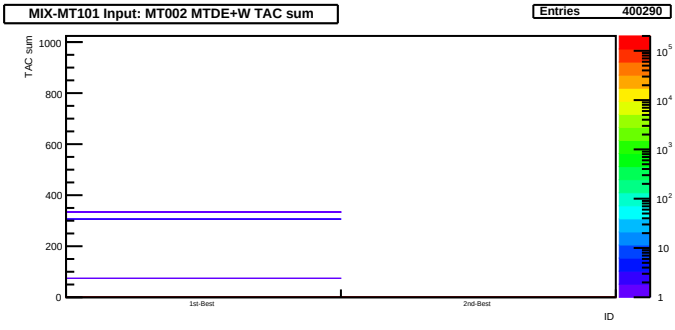

MXQ-MT002
Entries 6404640
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

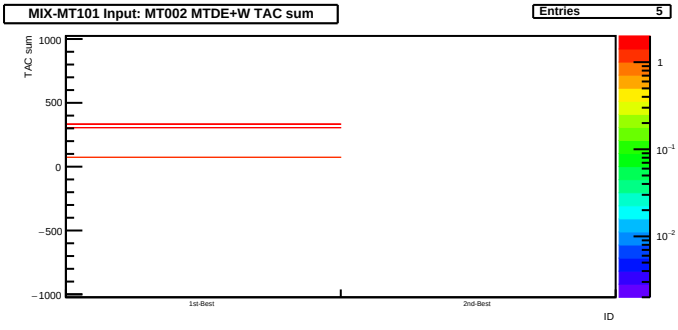

MXQ-MT003
Entries 6404640
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

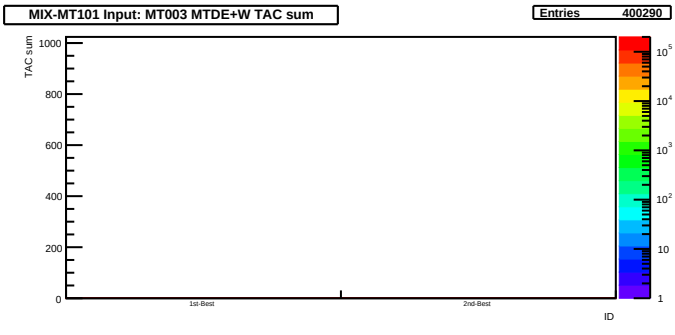

MXQ-MT004
Entries 6404640
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^5
10^4
10^3
10^2
10
1

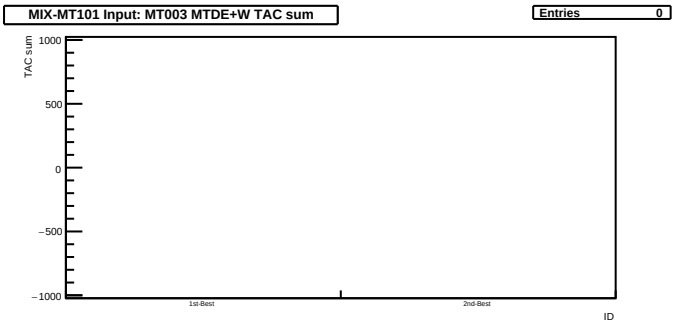

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 400290
TAC sum
ID
1st-Best
2nd-Best

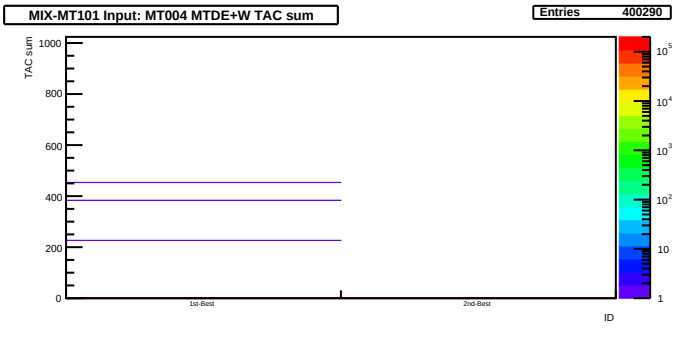

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 4
TAC sum
ID
1st-Best
2nd-Best

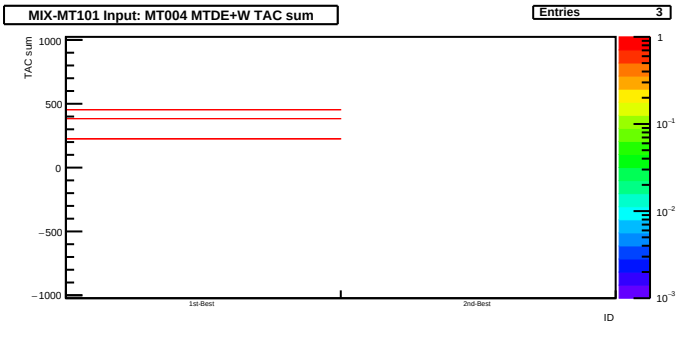

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 400290
TAC sum
ID
1st-Best
2nd-Best

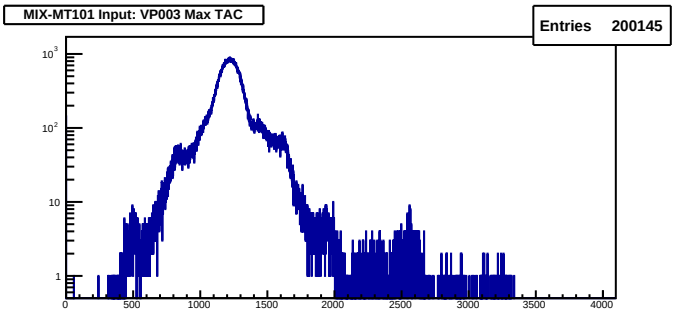

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 5
TAC sum
ID
1st-Best
2nd-Best

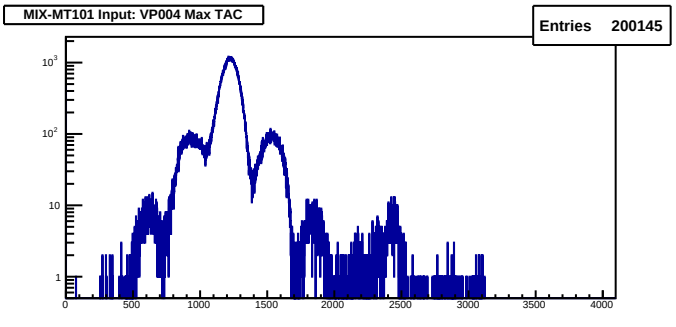

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 400290
TAC sum
ID
1st-Best
2nd-Best

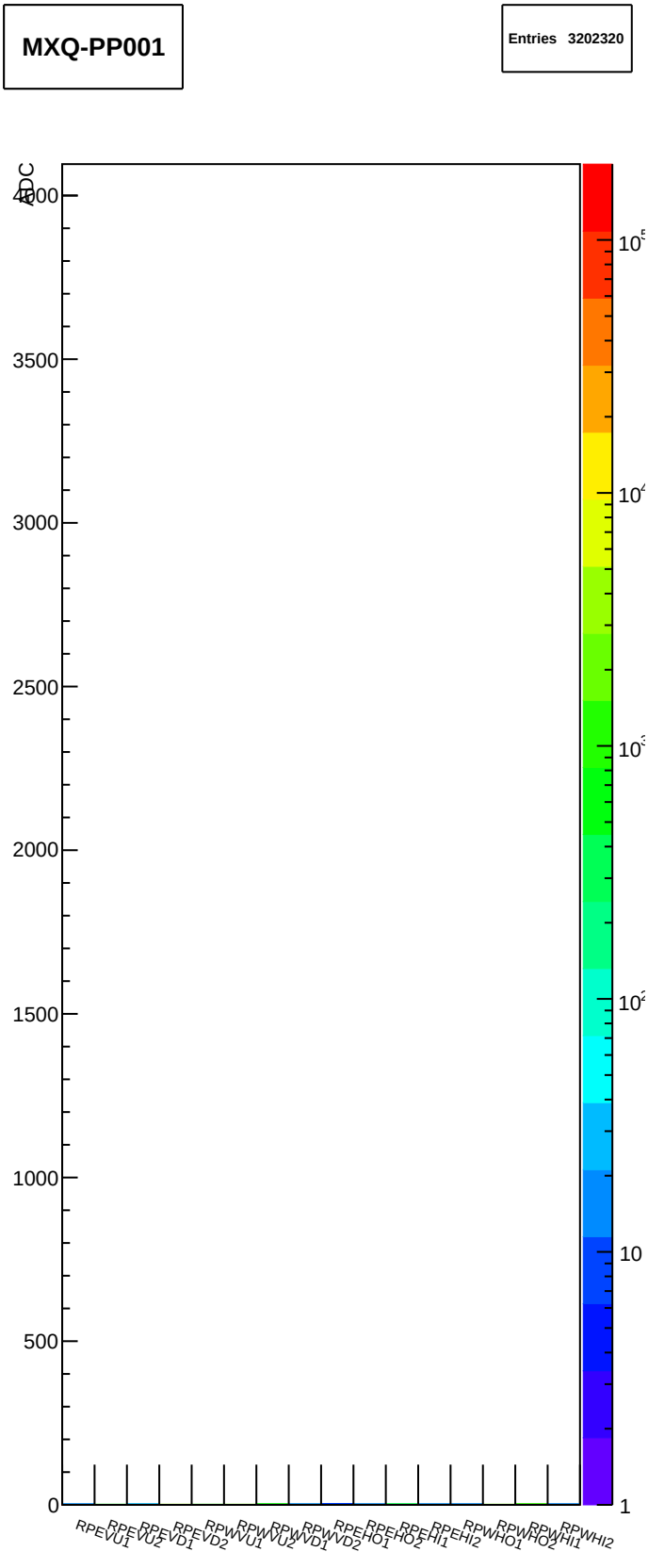

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 0
TAC sum
ID
1st-Best
2nd-Best

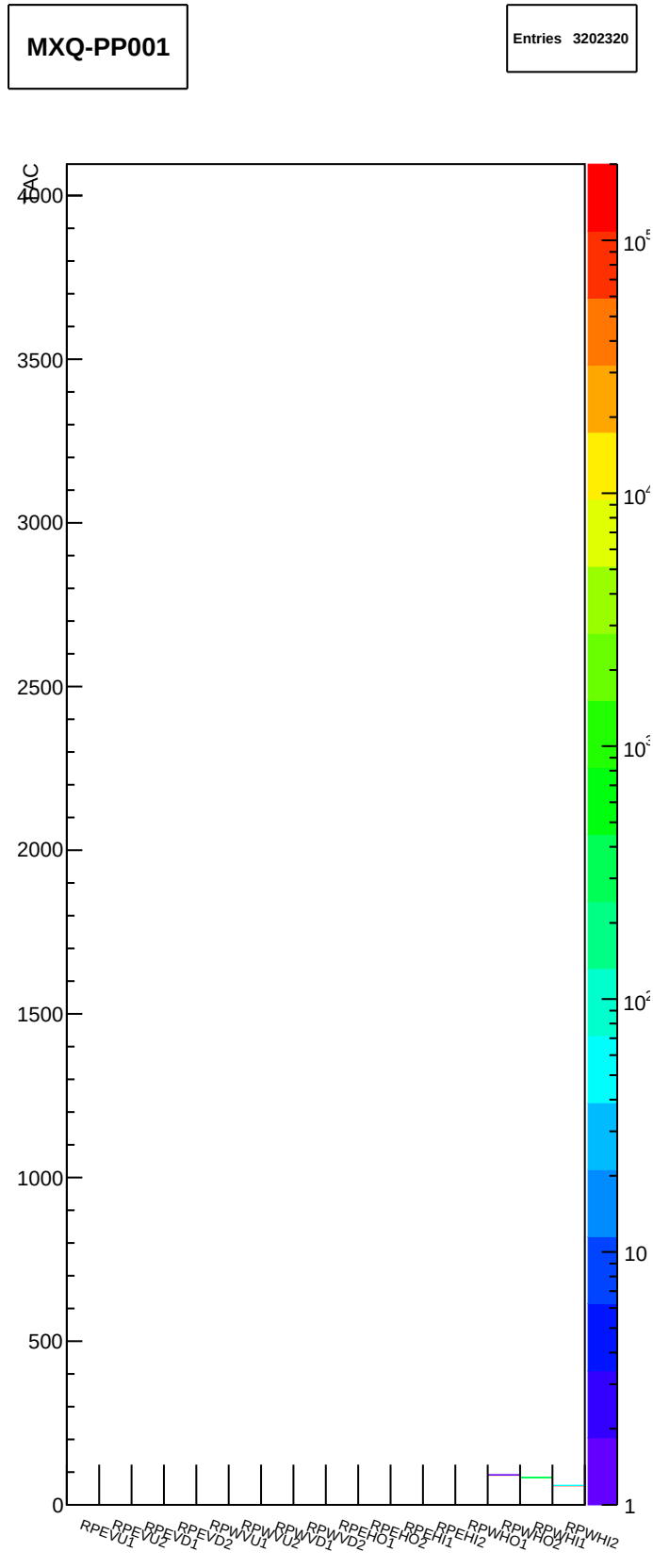

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 400290
TAC sum
ID
1st-Best
2nd-Best

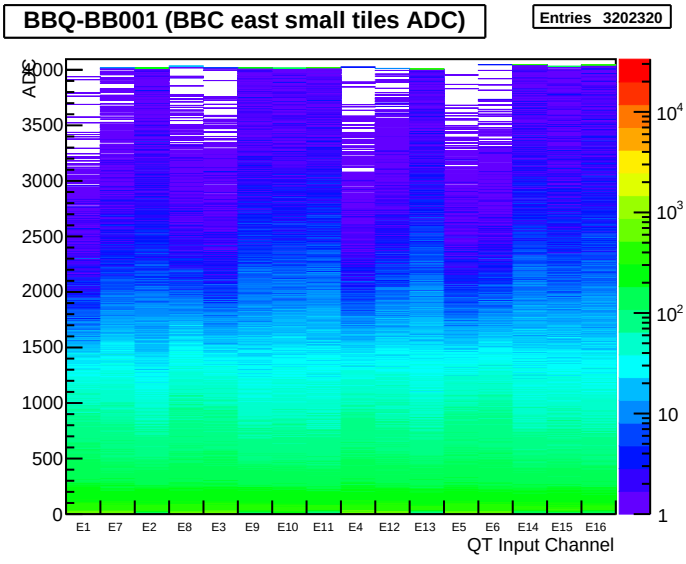

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 3
TAC sum
ID
1st-Best
2nd-Best

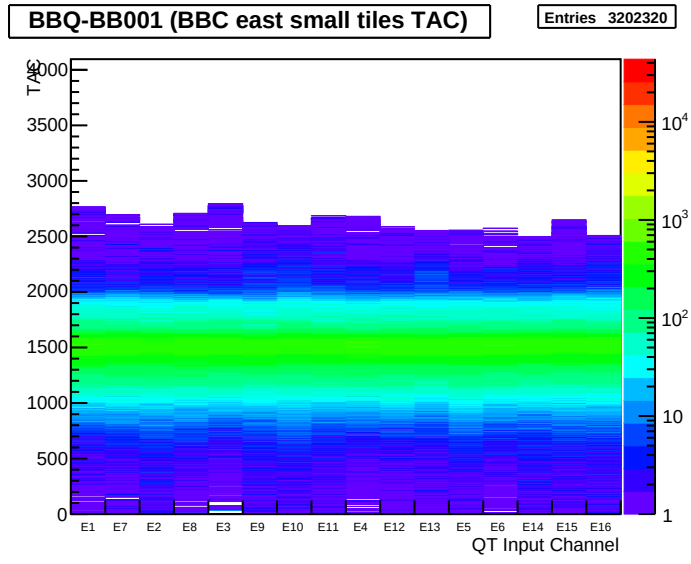

MIX-MT101 Input: VP003 Max TAC
Entries 200145

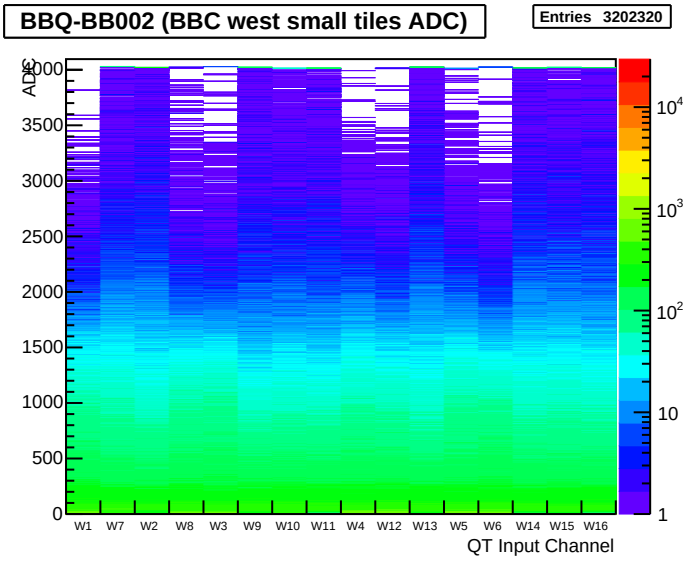

MIX-MT101 Input: VP004 Max TAC
Entries 200145

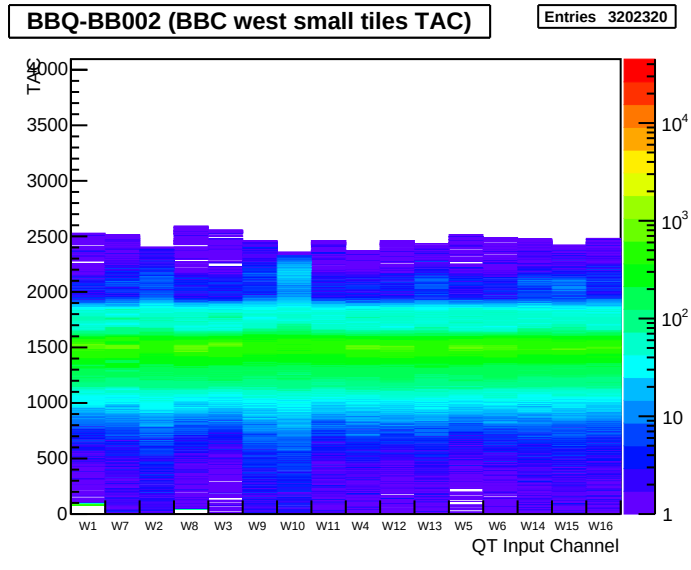

MXQ-PP001
Entries 3202320
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

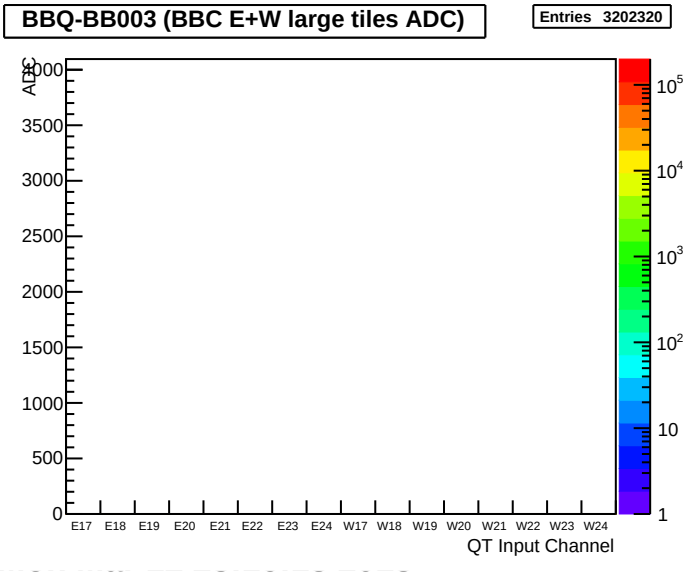

MXQ-PP001
Entries 3202320
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

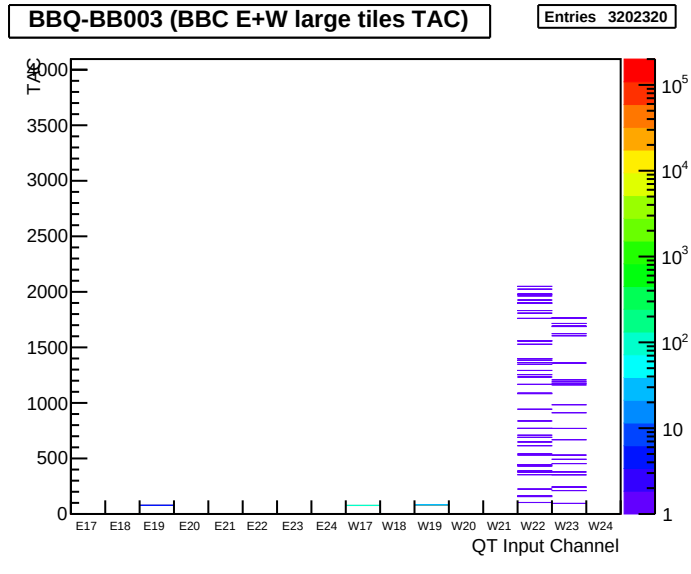

BBQ-BB001 (BBC east small tiles ADC)
Entries 3202320
ADC
QT Input Channel

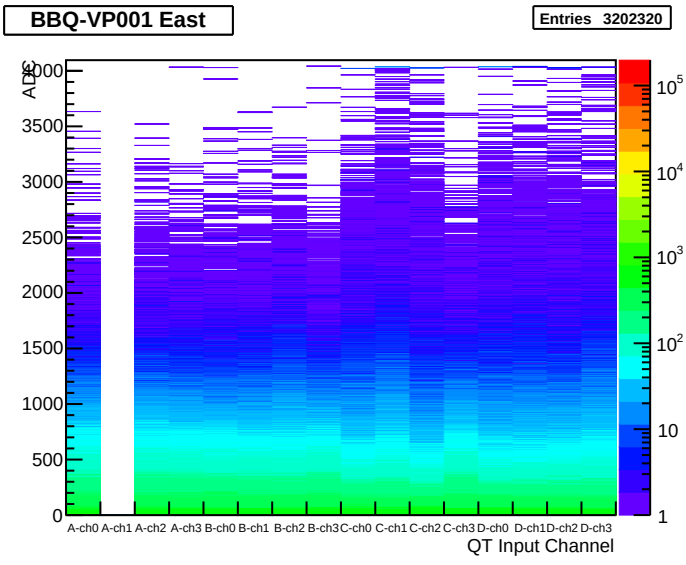

BBQ-BB001 (BBC east small tiles TAC)
Entries 3202320
TAC
QT Input Channel

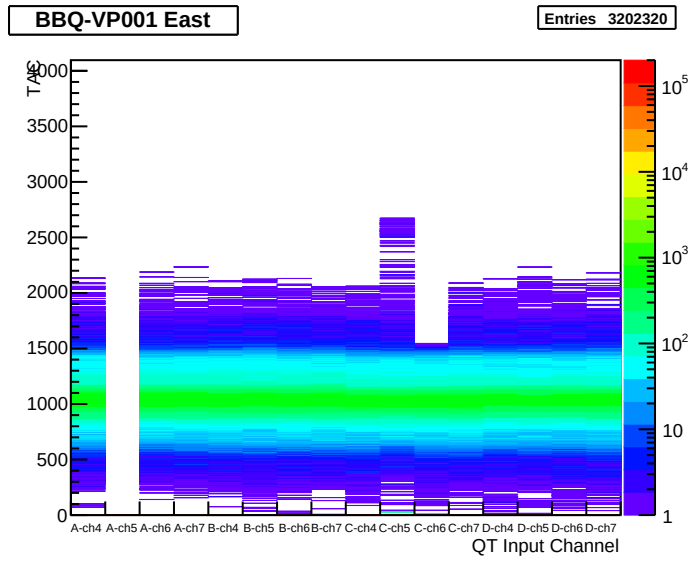

BBQ-BB002 (BBC west small tiles ADC)
Entries 3202320
ADC
QT Input Channel

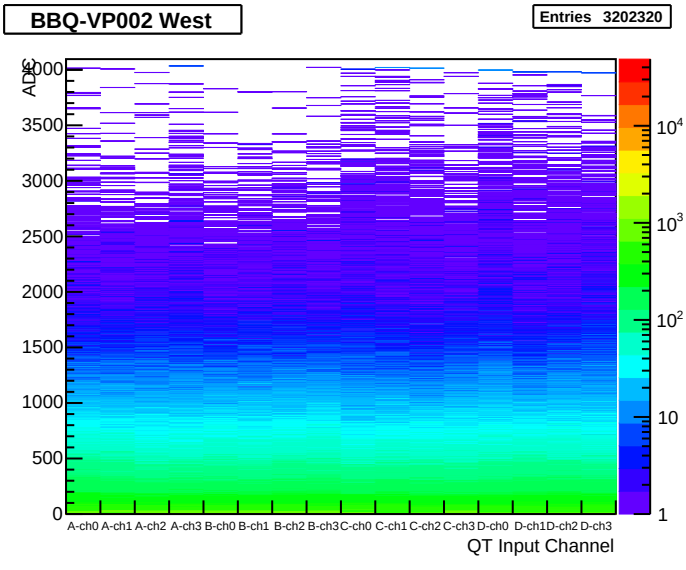

BBQ-BB002 (BBC west small tiles TAC)
Entries 3202320
TAC
QT Input Channel

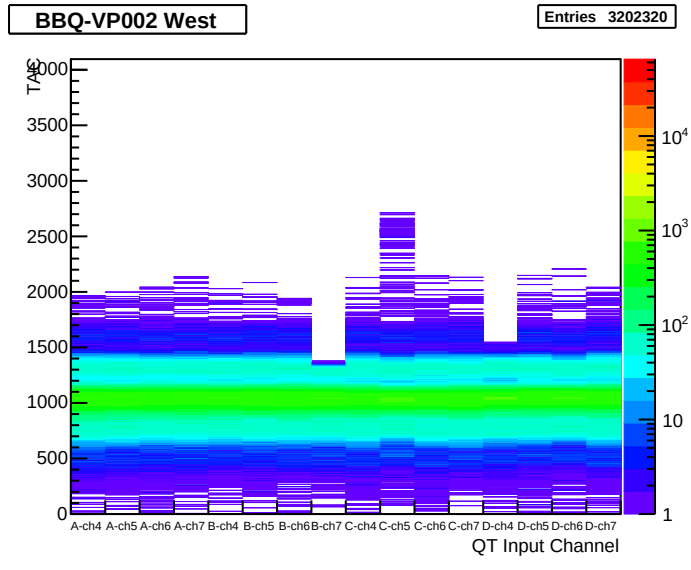

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3202320
ADC
QT Input Channel

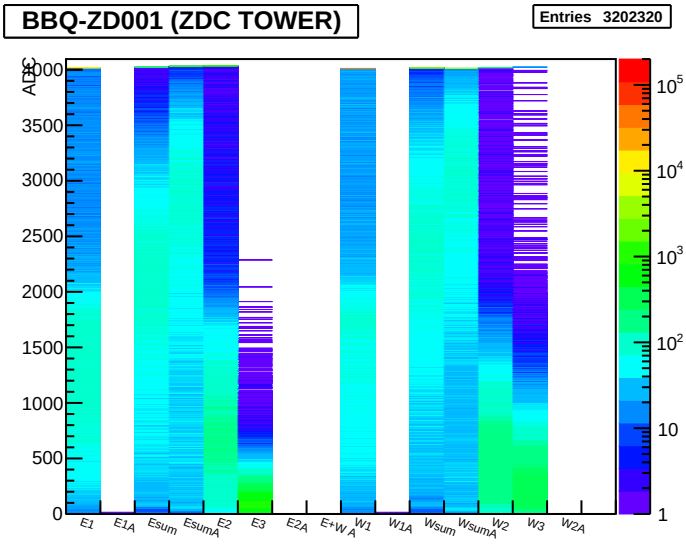

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3202320
TAC
QT Input Channel

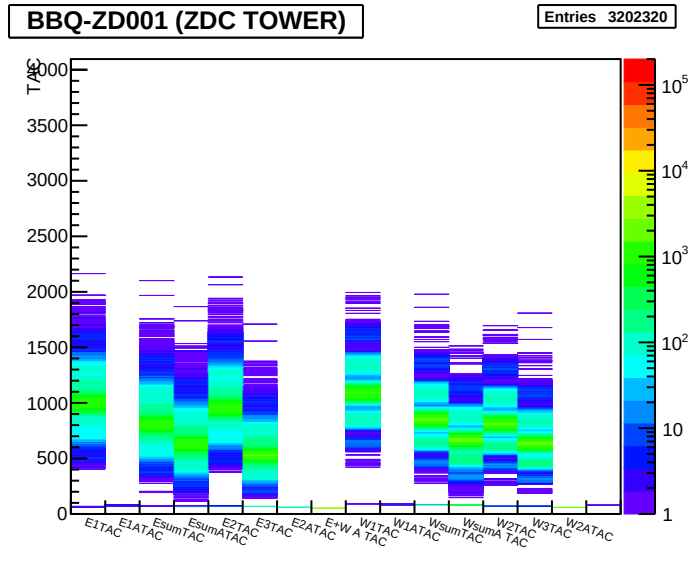

BBQ-VP001 East
Entries 3202320
ADC
QT Input Channel

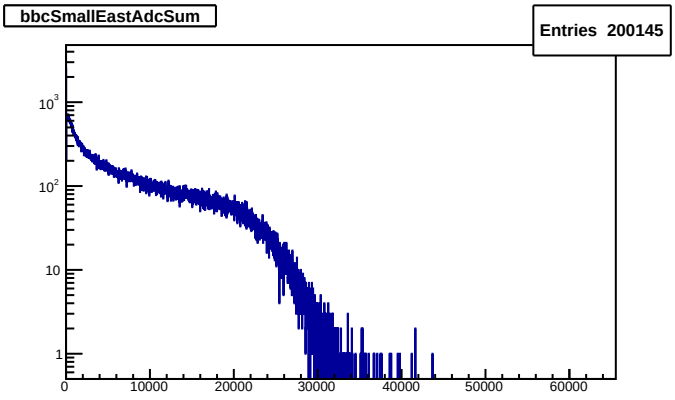

BBQ-VP001 East
Entries 3202320
TAC
QT Input Channel

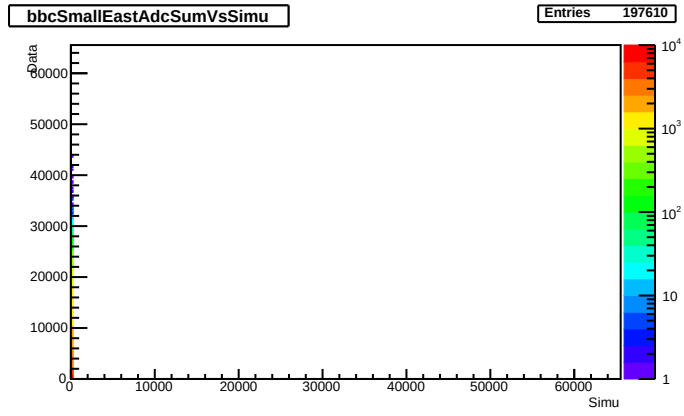

BBQ-VP002 West
Entries 3202320
ADC
QT Input Channel

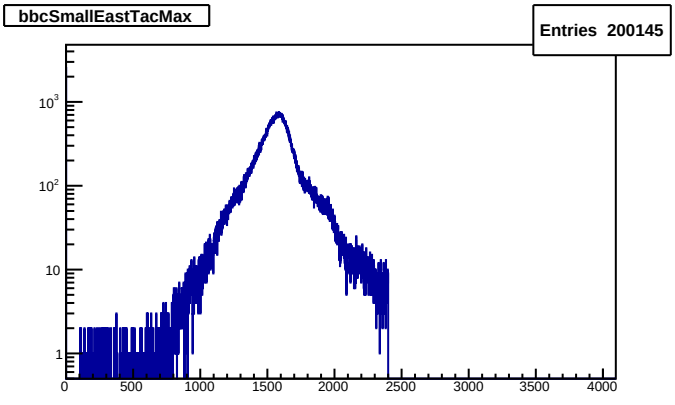

BBQ-VP002 West
Entries 3202320
TAC
QT Input Channel

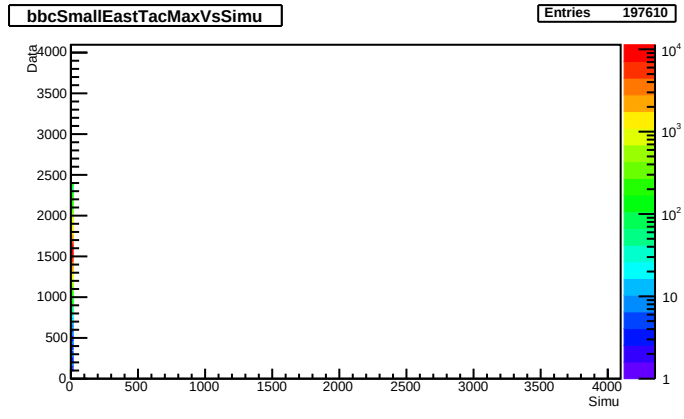

BBQ-ZD001 (ZDC TOWER)
Entries 3202320
ADC

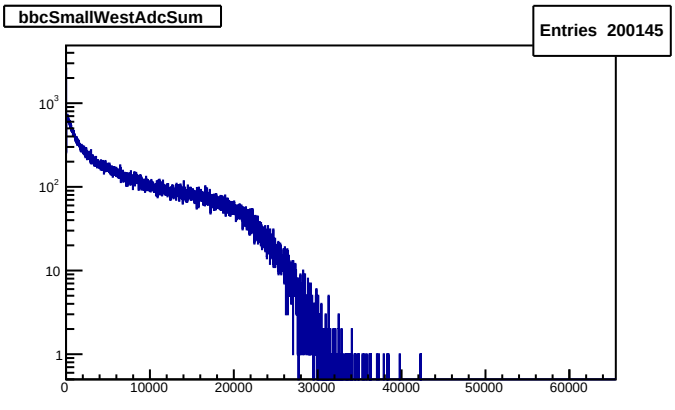

BBQ-ZD001 (ZDC TOWER)
Entries 3202320
TAC

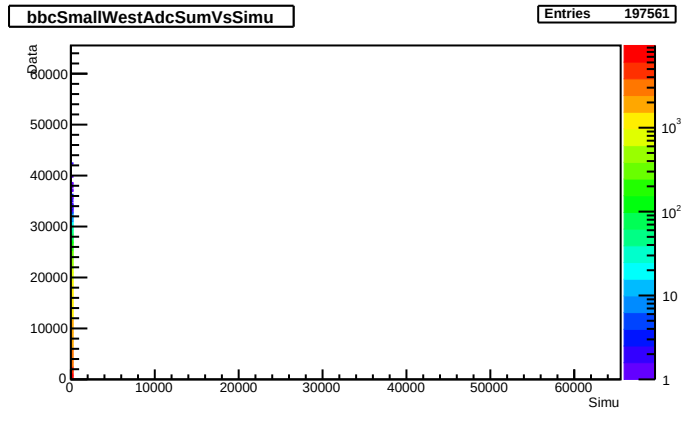

bbcSmallEastAdcSum
Entries 200145

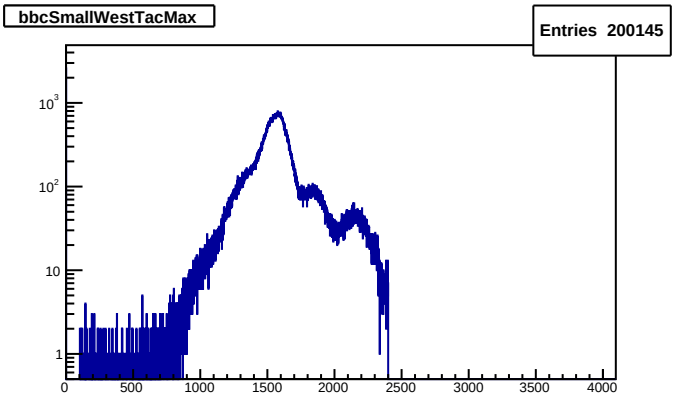

bbcSmallEastAdcSumVsSimu
Entries 197610
Data
Simu

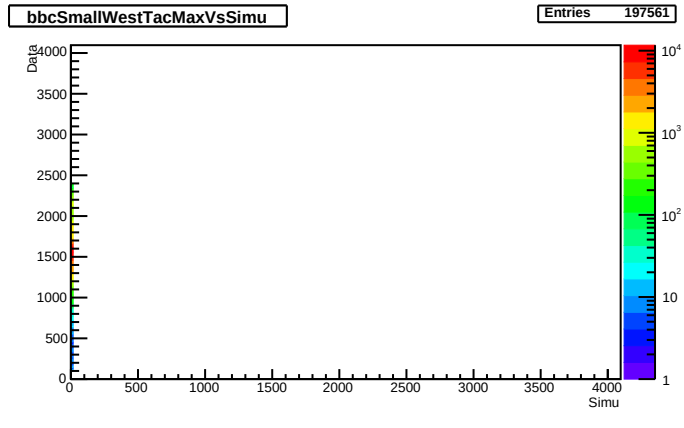

bbcSmallEastTacMax
Entries 200145

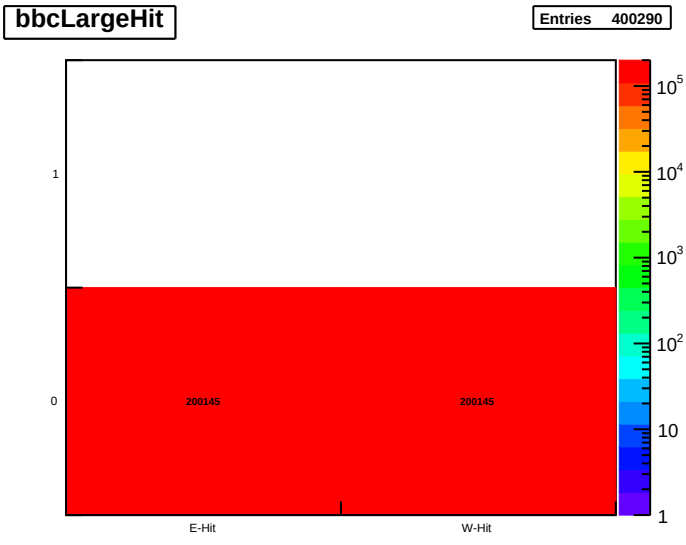

bbcSmallEastTacMaxVsSimu
Entries 197610
Data
Simu

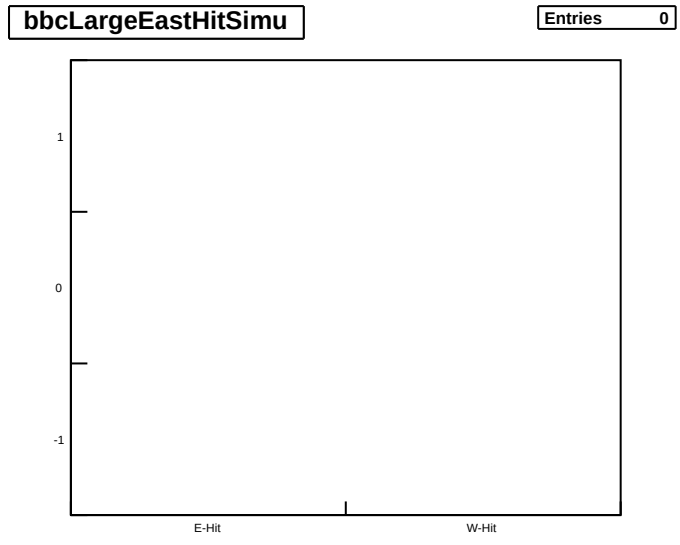

bbcSmallWestAdcSum
Entries 200145

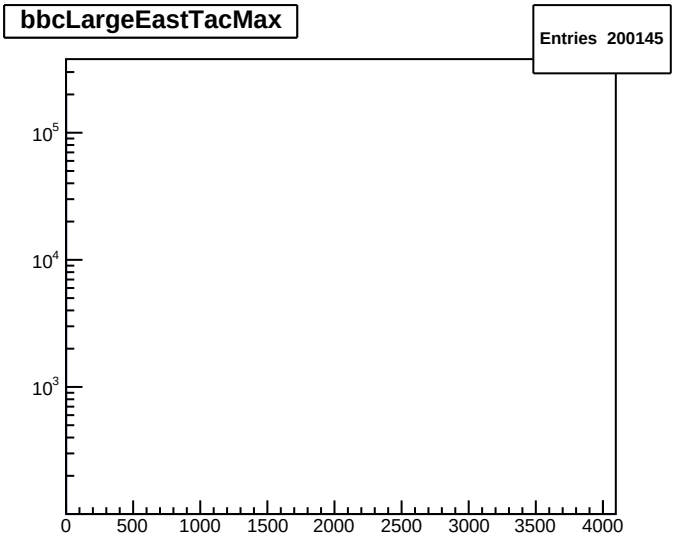

bbcSmallWestAdcSumVsSimu
Entries 197561
Data
Simu

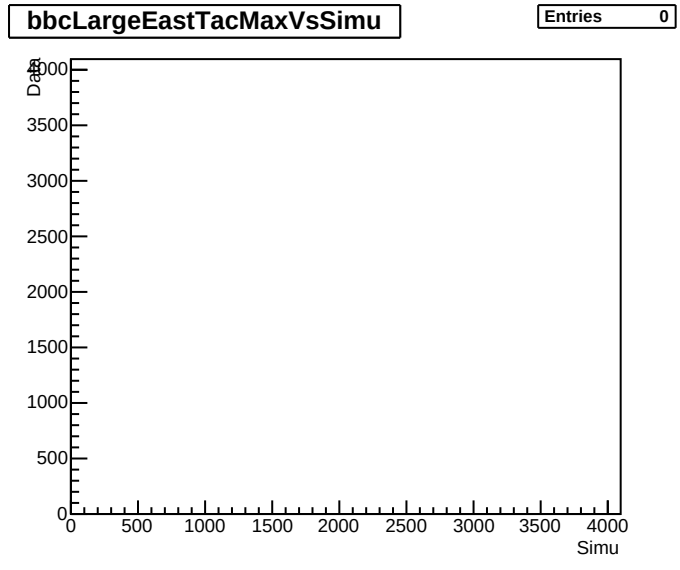

bbcSmallWestTacMax
Entries 200145

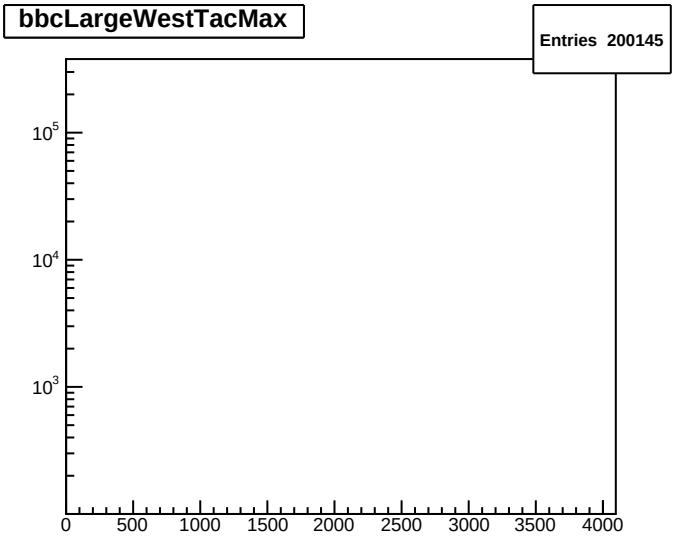

bbcSmallWestTacMaxVsSimu
Entries 197561
Data
Simu

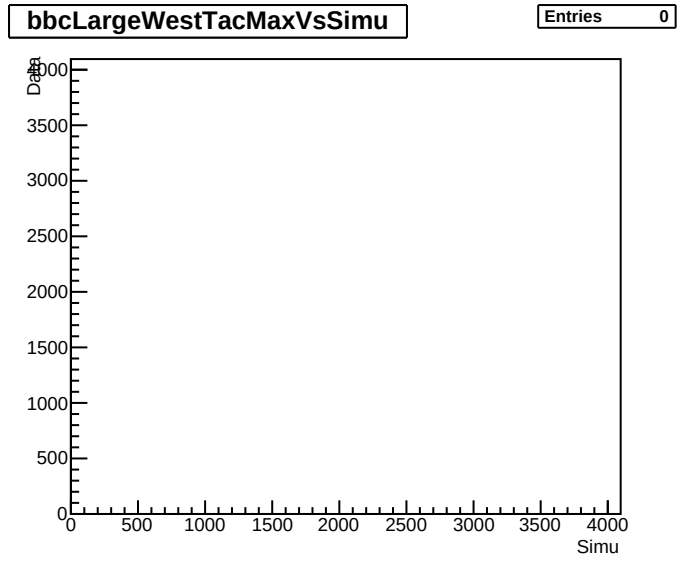

bbcLargeHit
Entries 400290
1
0
200145
200145
E-Hit
W-Hit
10^5
10^4
10^3
10^2
10
1

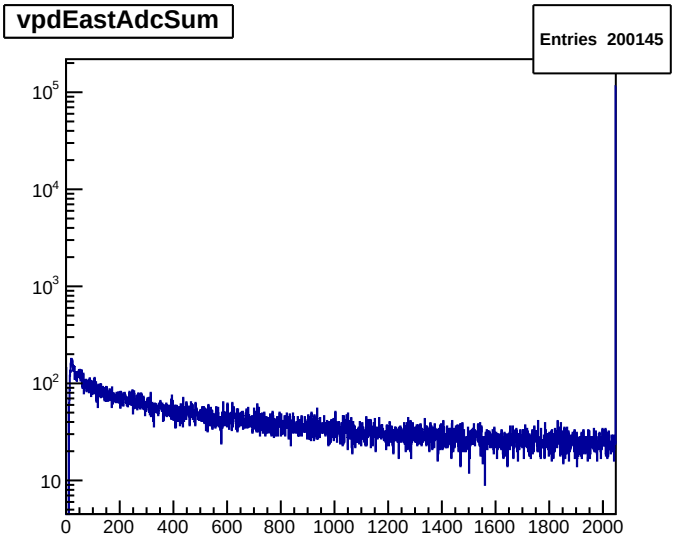

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

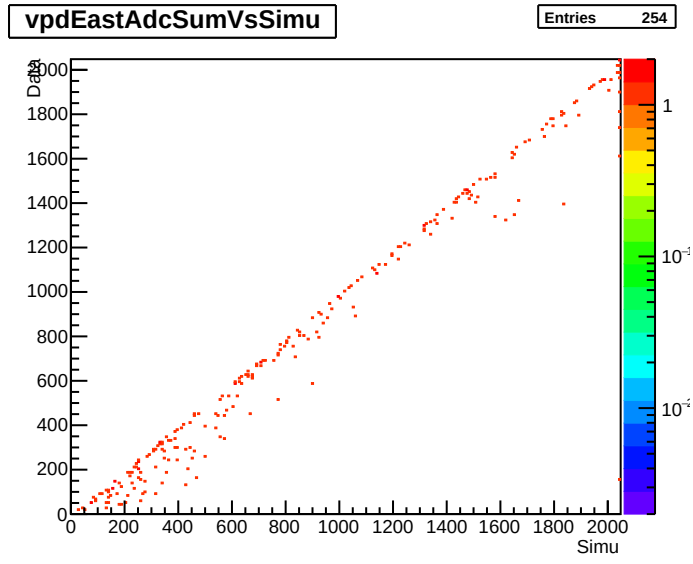

bbcLargeEastTacMax
Entries 200145
10^5
10^4
10^3
0
500
1000
1500
2000
2500
3000
3500
4000

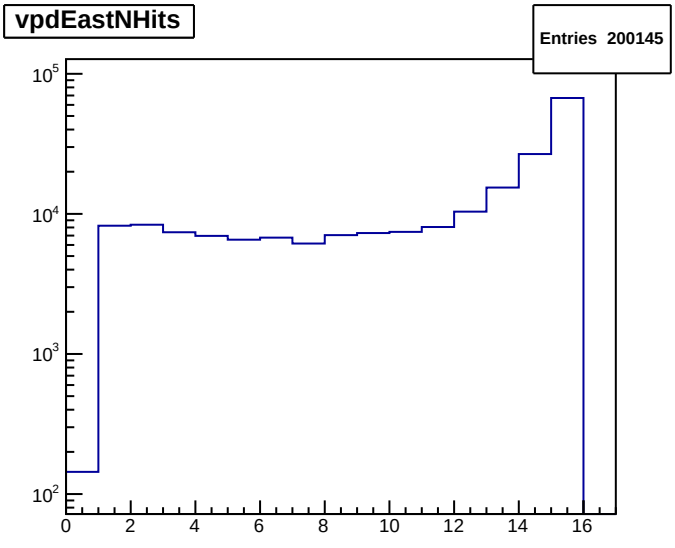

bbcLargeEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

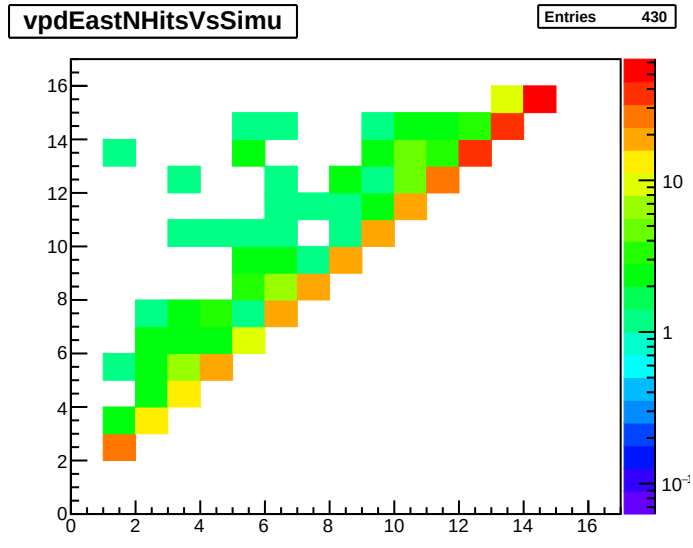

bbcLargeWestTacMax
Entries 200145
10^5
10^4
10^3
0
500
1000
1500
2000
2500
3000
3500
4000

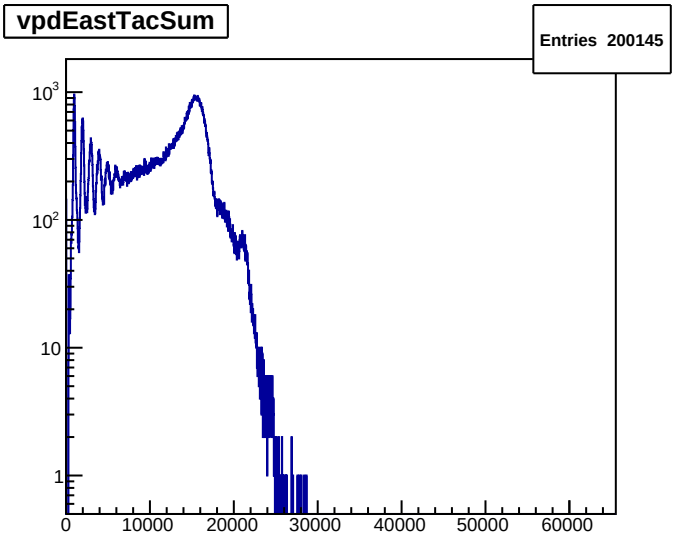

bbcLargeWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

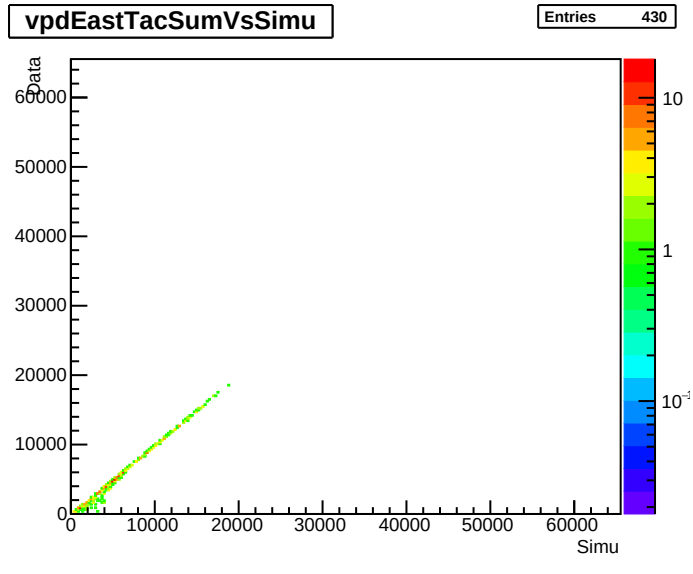

vpdEastAdcSum
Entries 200145

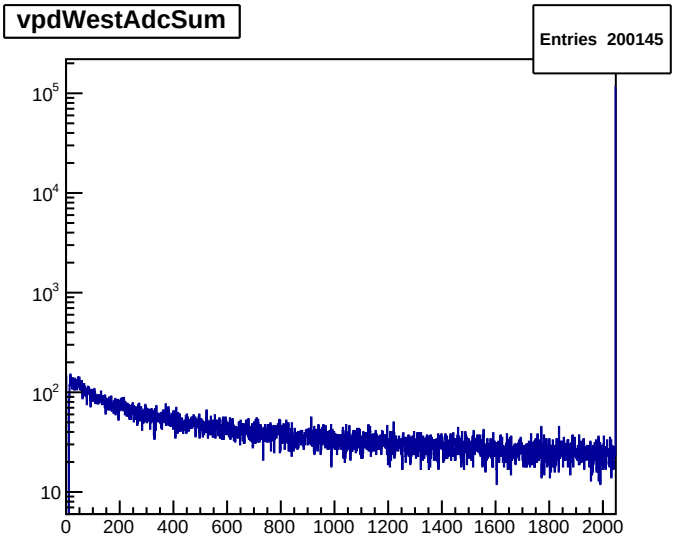

vpdEastAdcSumVsSimu
Entries 254
Data
Simu

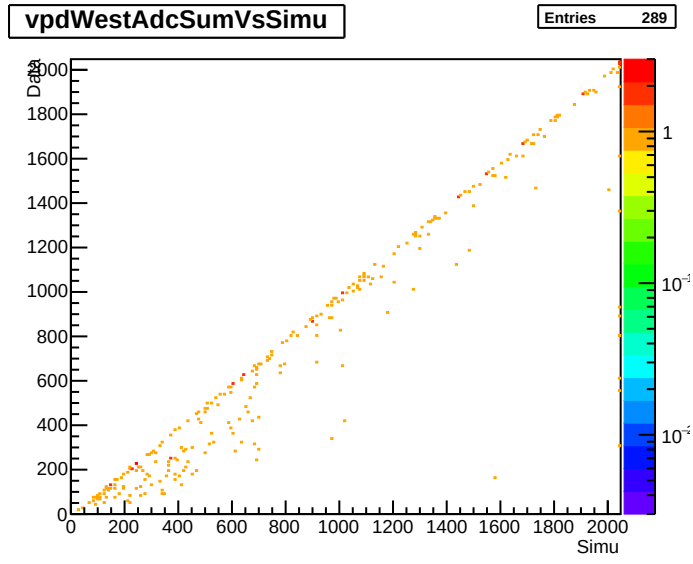

vpdEastNHits
Entries 200145

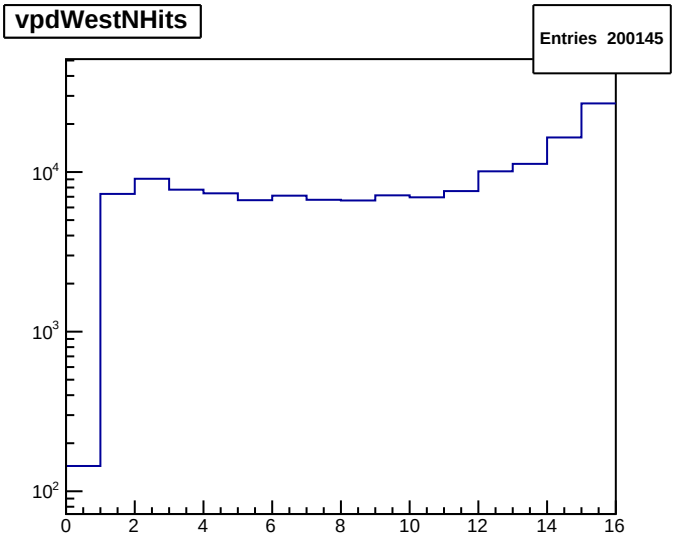

vpdEastNHitsVsSimu
Entries 430

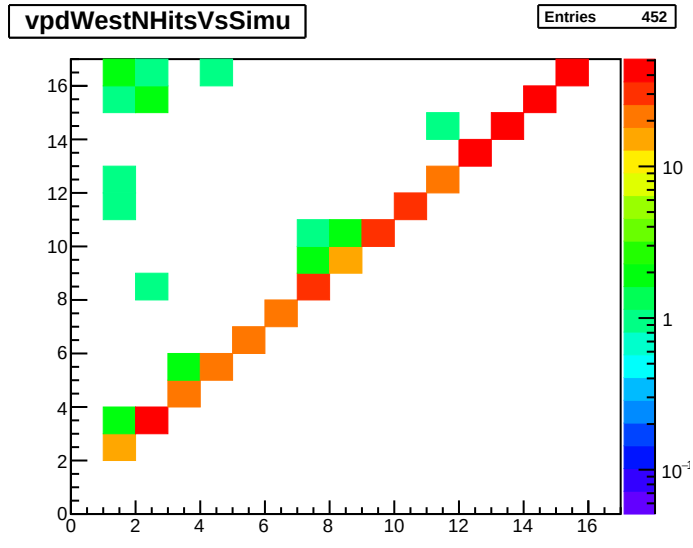

vpdEastTacSum
Entries 200145

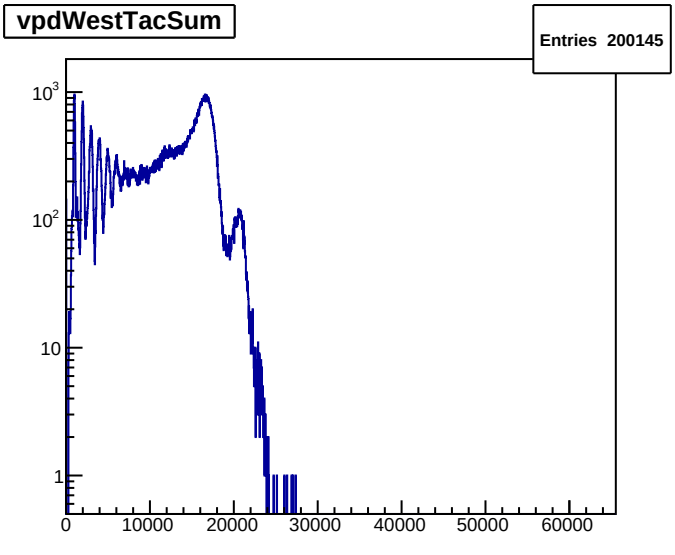

vpdEastTacSumVsSimu
Entries 430
Data
Simu

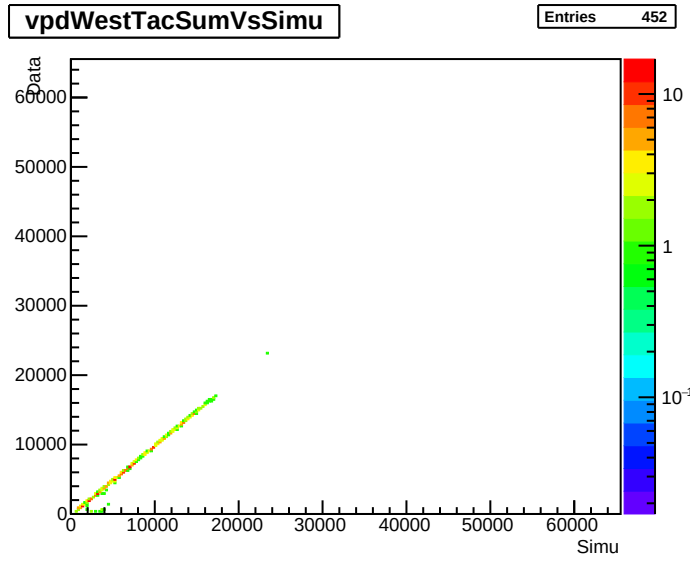

vpdWestAdcSum
Entries 200145

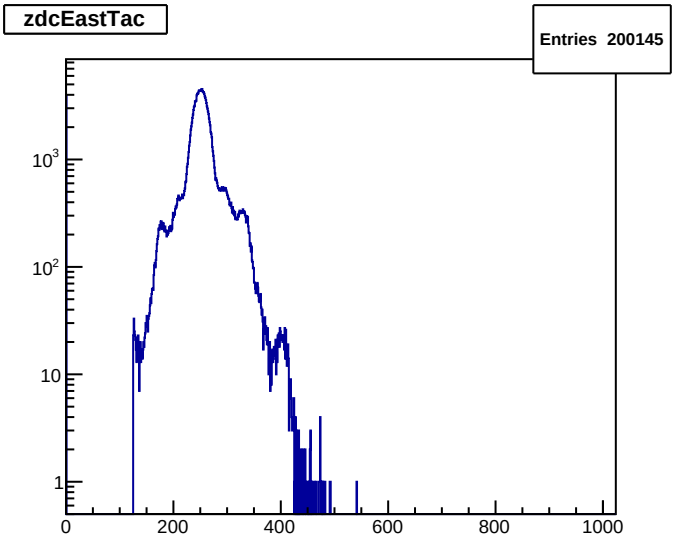

vpdWestAdcSumVsSimu
Entries 289
Data
Simu

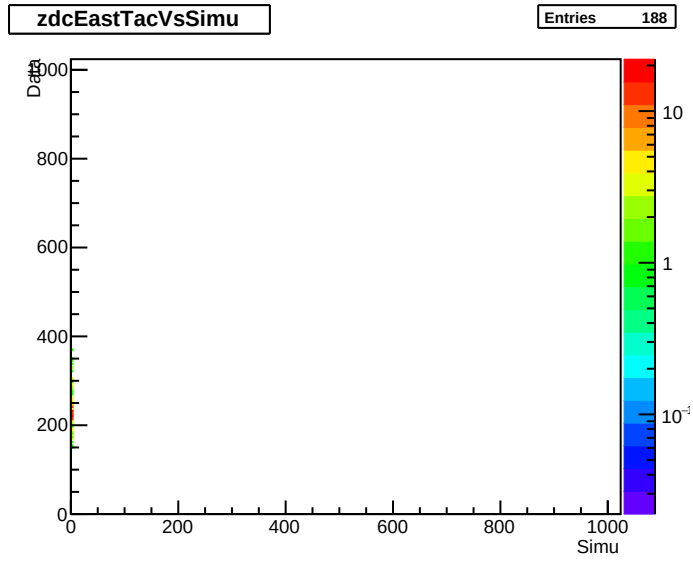

vpdWestNHits
Entries 200145

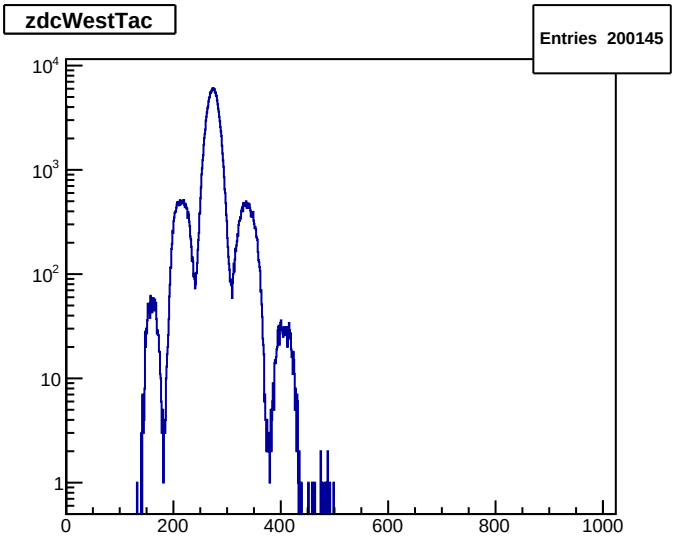

vpdWestNHitsVsSimu
Entries 452

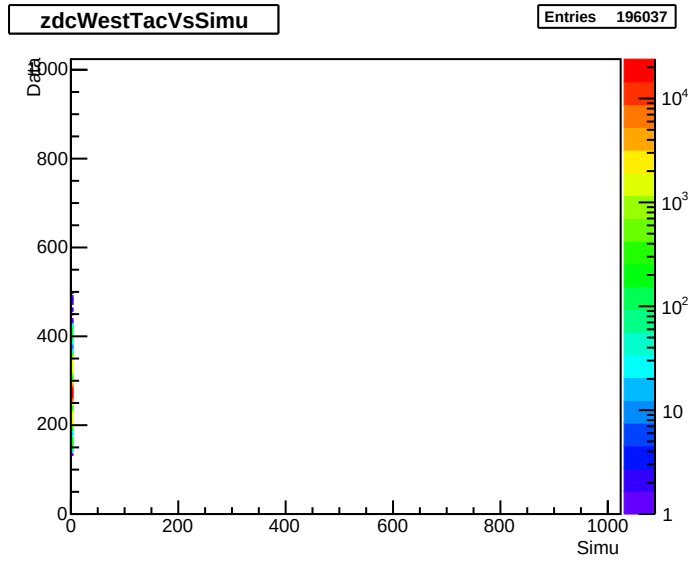

vpdWestTacSum
Entries 200145

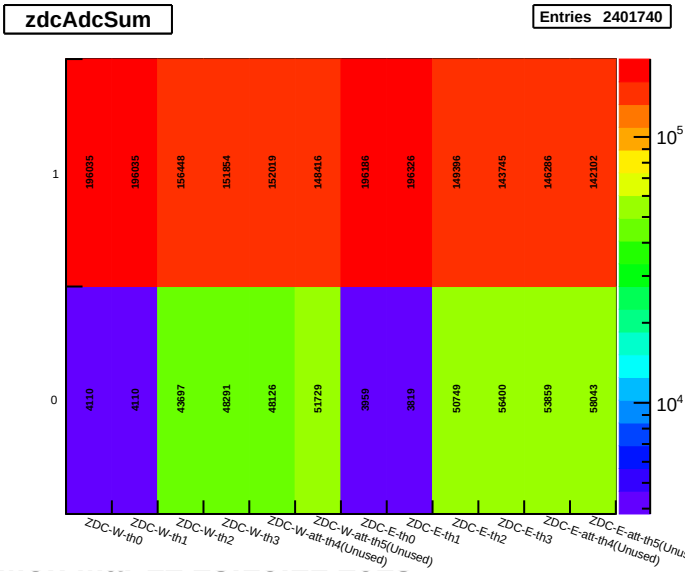

vpdWestTacSumVsSimu
Entries 452
Data
Simu

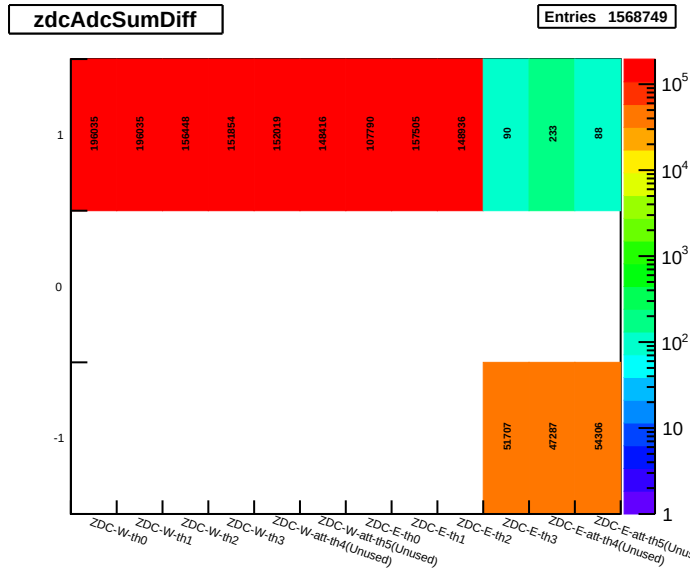

zdcEastTac
Entries 200145

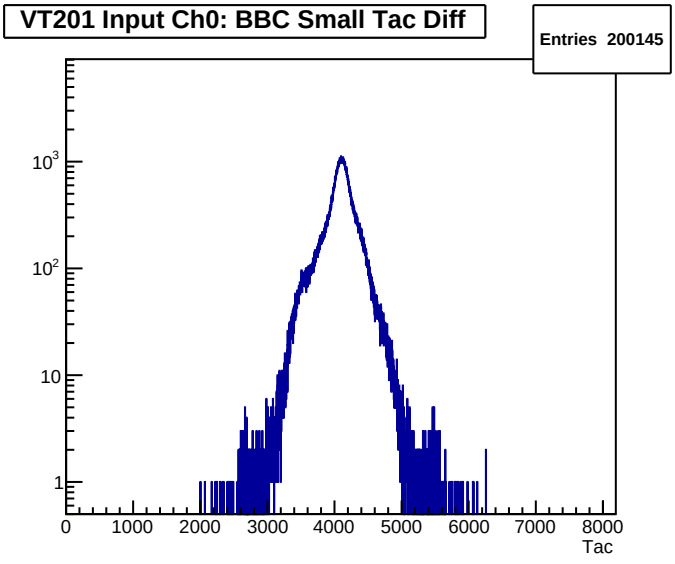

zdcEastTacVsSimu
Entries 188
Data
Simu

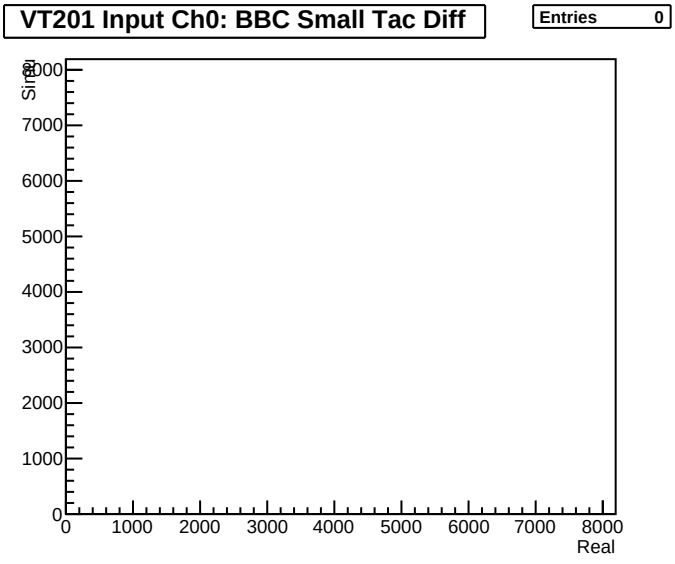

zdcWestTac
Entries 200145

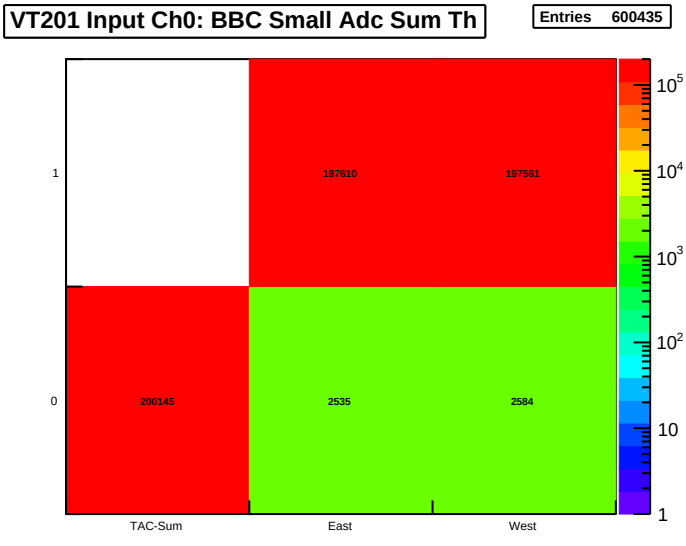

zdcWestTacVsSimu
Entries 196037
Data
Simu

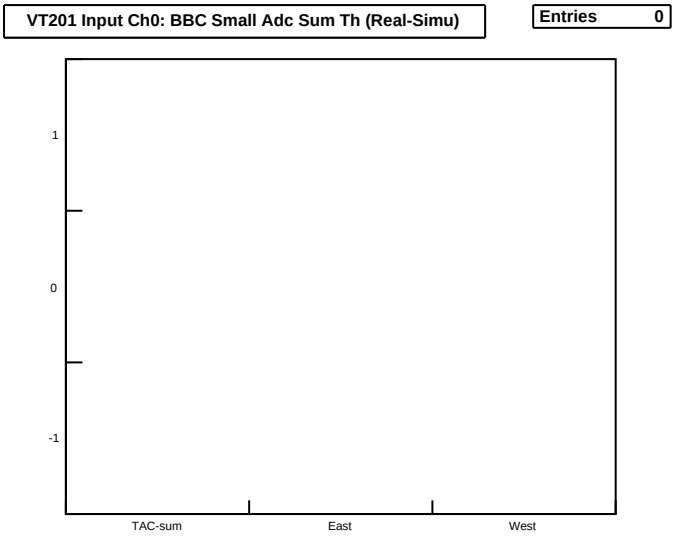

zdcAdcSum
Entries 2401740
196035
196035
156448
151854
152019
148416
196186
196326
143396
143745
146286
142102
4110
4110
43697
48291
48126
51729
3959
3919
50749
56400
53859
58843
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

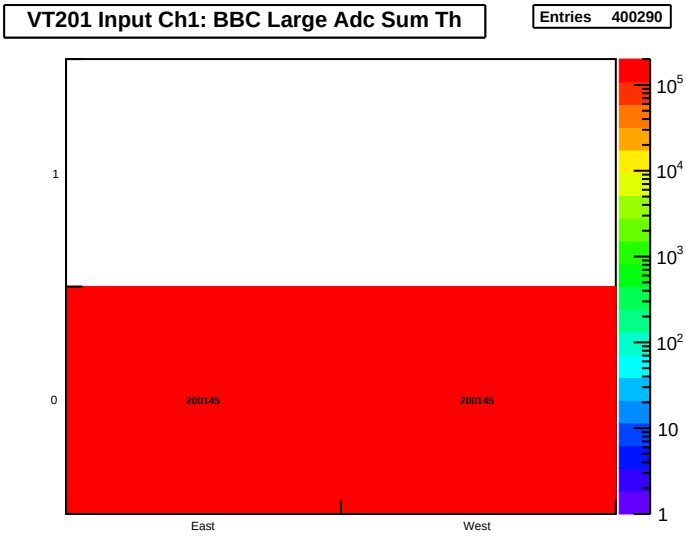

zdcAdcSumDiff
Entries 1568749
196035
196035
156448
151854
152019
148416
107790
157505
148336
90
233
88
51707
47287
54306
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

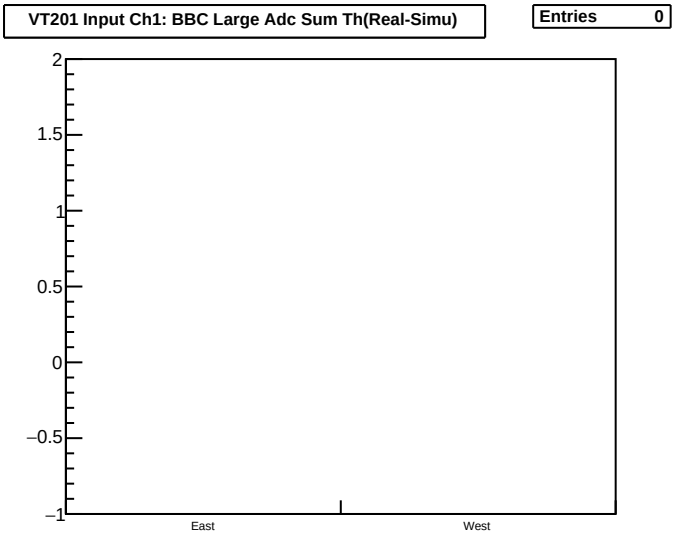

VT201 Input Ch0: BBC Small Tac Diff
Entries 200145
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

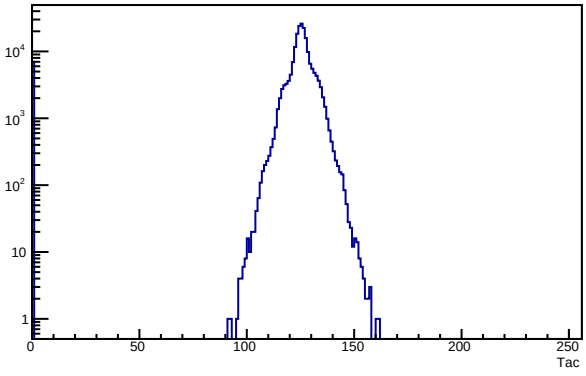

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 600435
197610
197561
200145
2535
2584
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

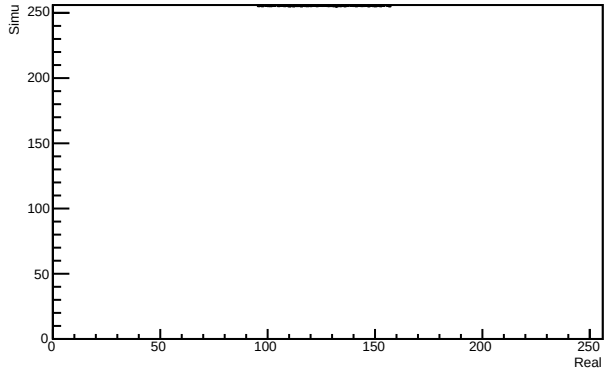

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 400290
200145
200145
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

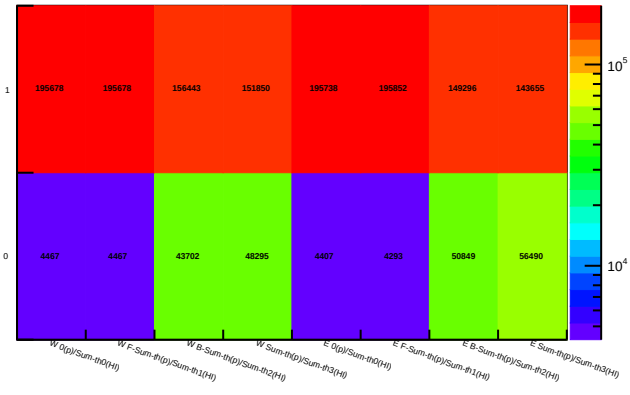
VT201 Input Ch2: ZDC Tac Diff



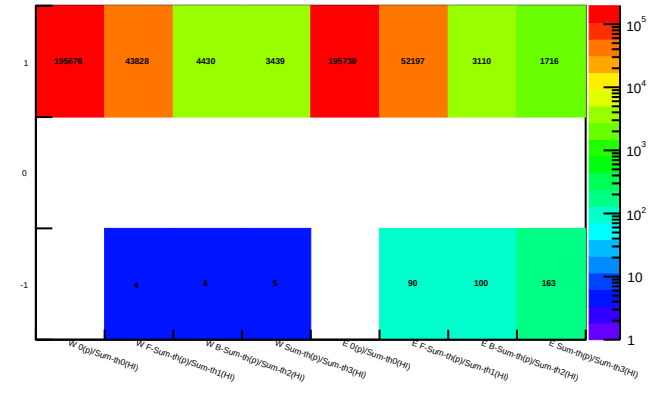
VT201 Input Ch2: ZDC Tac Diff



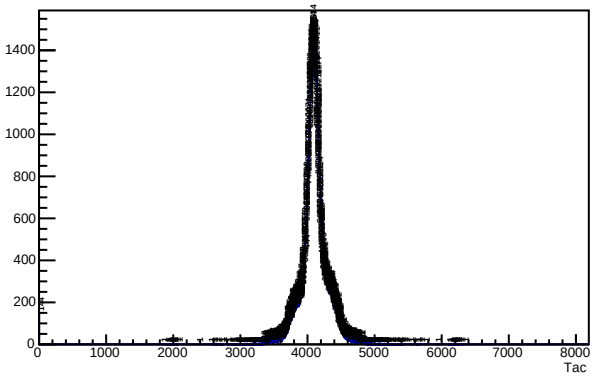
VT201 Input Ch2: ZDC Adc Sum Th



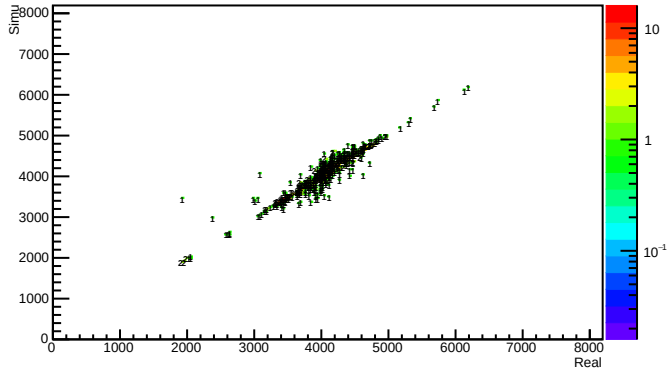
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



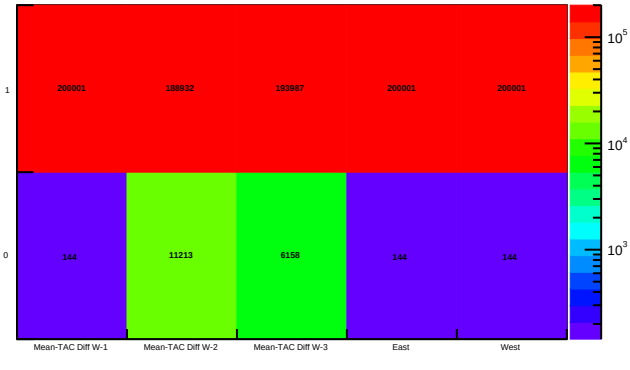
VT201 Input Ch4: VPD Tac Diff



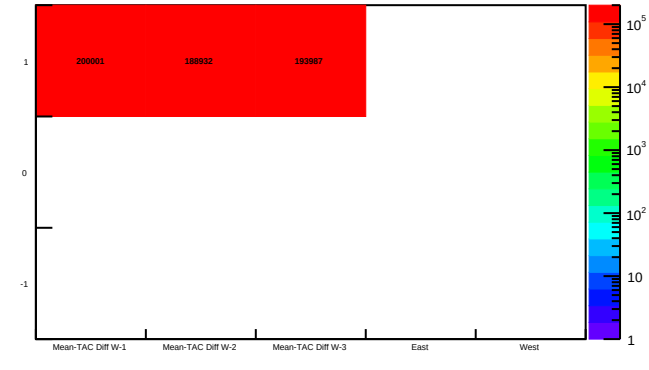
VT201 Input Ch4: VPD Tac Diff

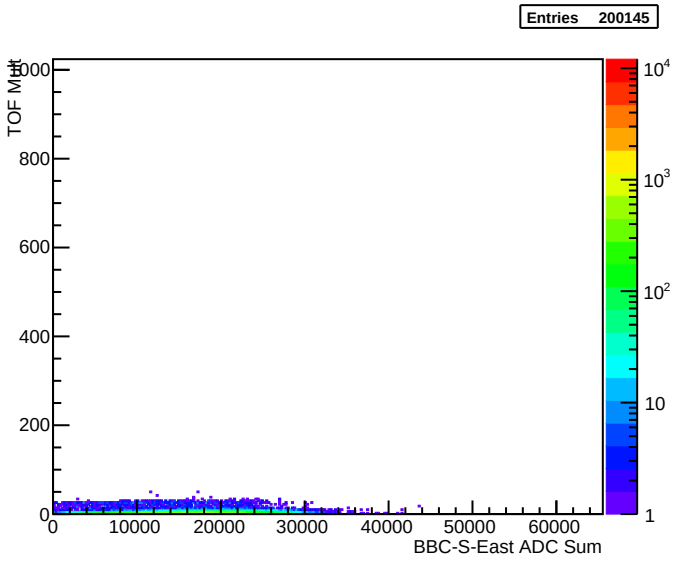


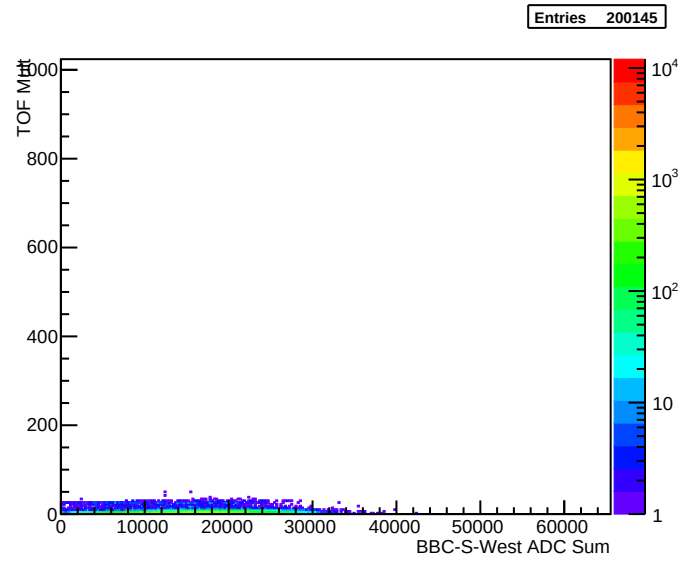
VT201 Input Ch4: VPD Adc Sum Th

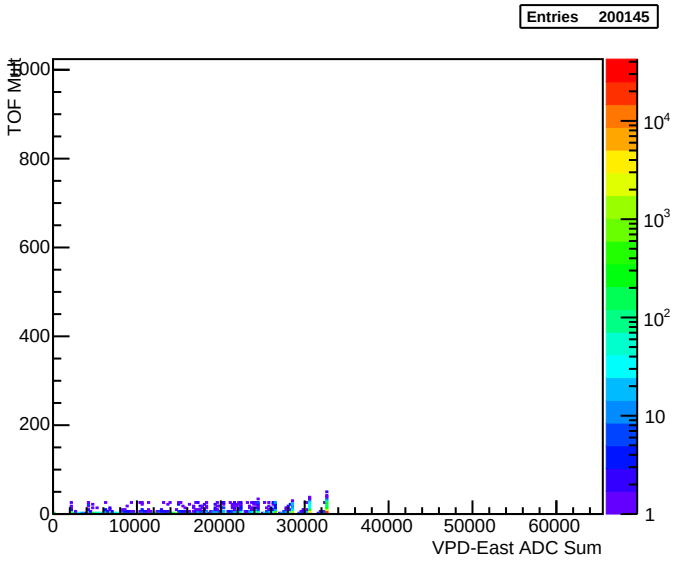


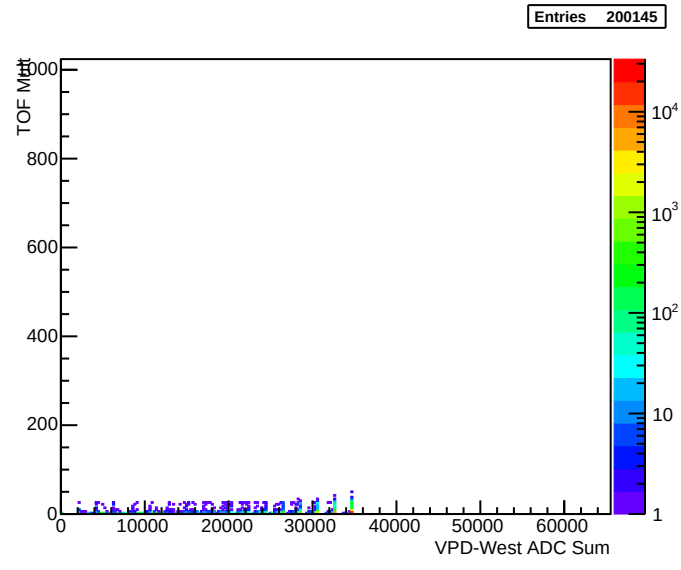
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

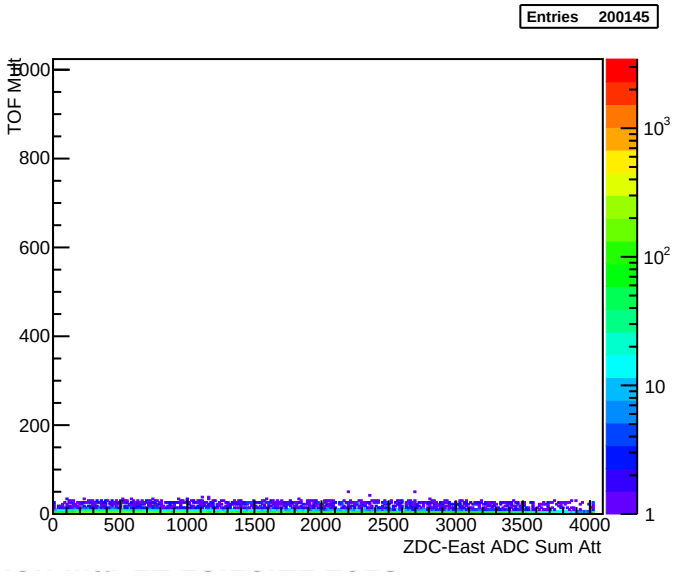


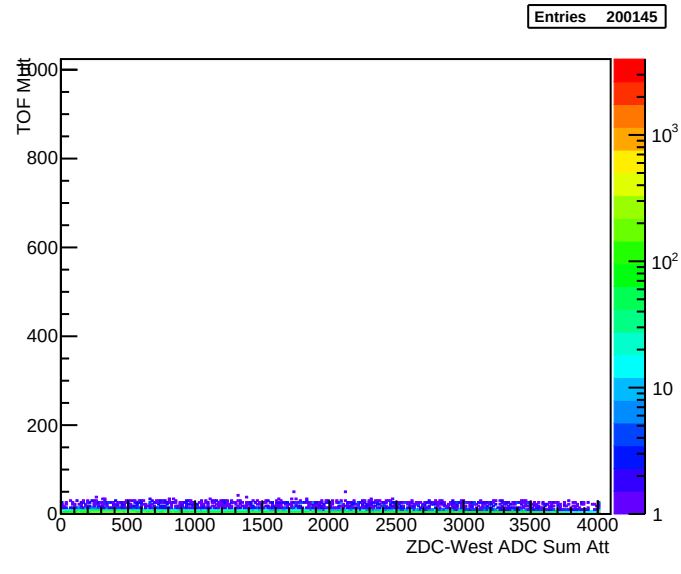

Entries 200145
TOF Mult
BBC-S-East ADC Sum

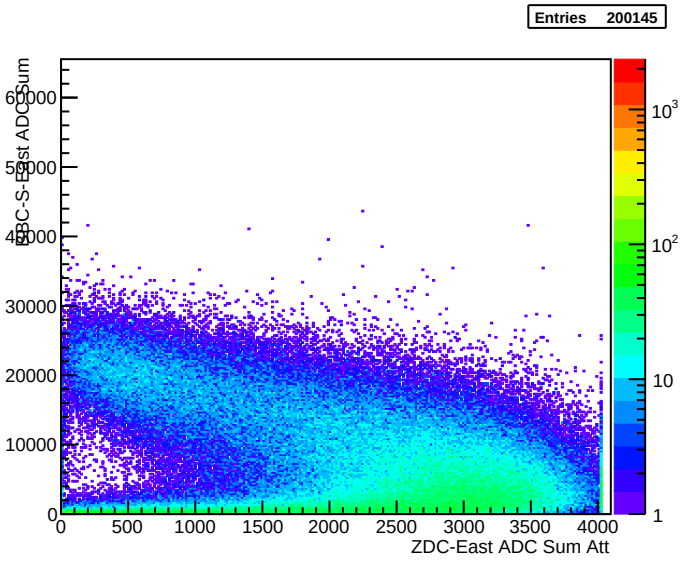

Entries 200145
TOF Mult
BBC-S-West ADC Sum

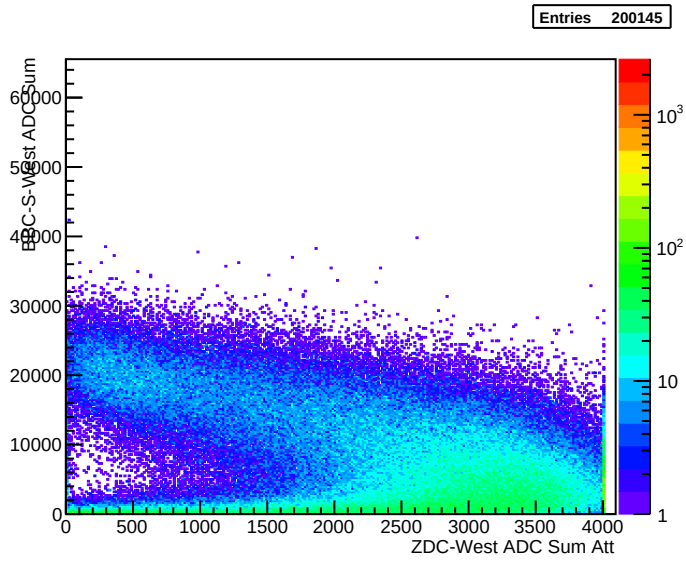

Entries 200145
TOF Mult
VPD-East ADC Sum

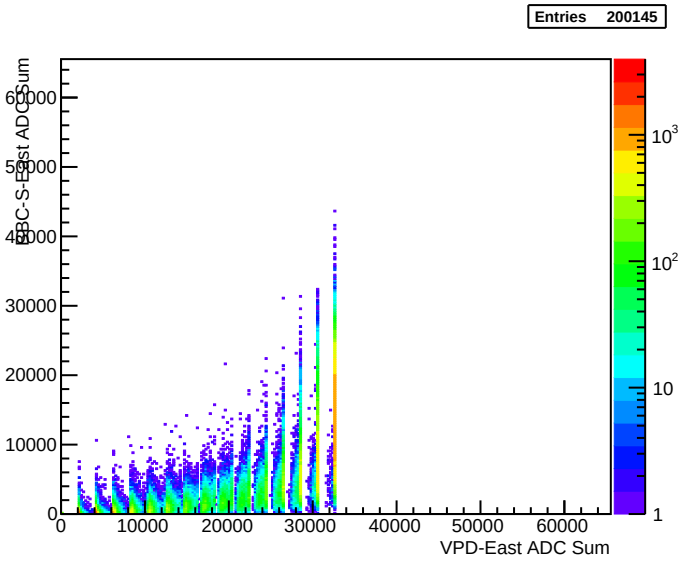

Entries 200145
TOF Mult
VPD-West ADC Sum

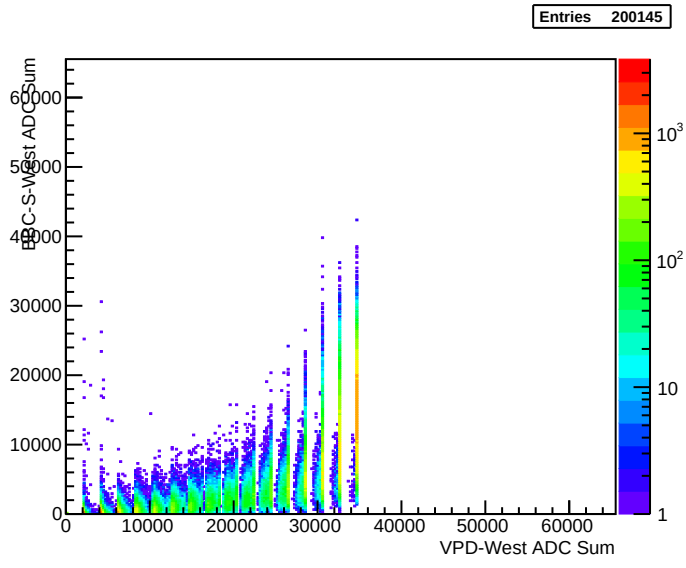

Entries 200145
TOF Mult
ZDC-East ADC Sum Att

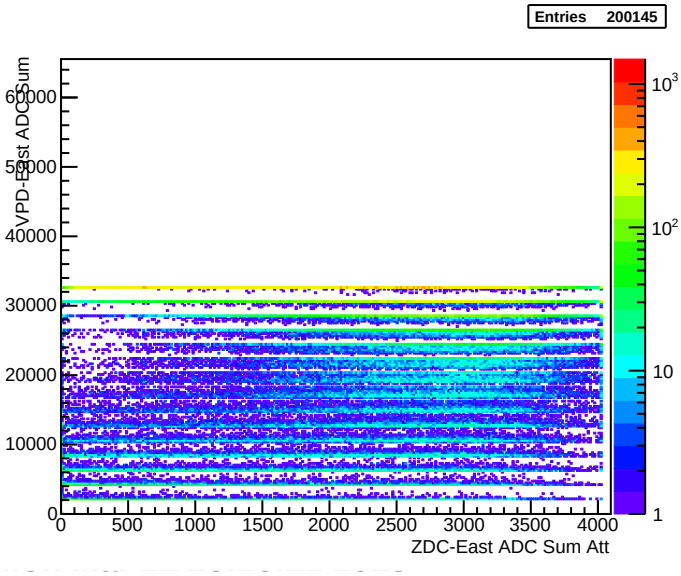

Entries 200145
TOF Mult
ZDC-West ADC Sum Att

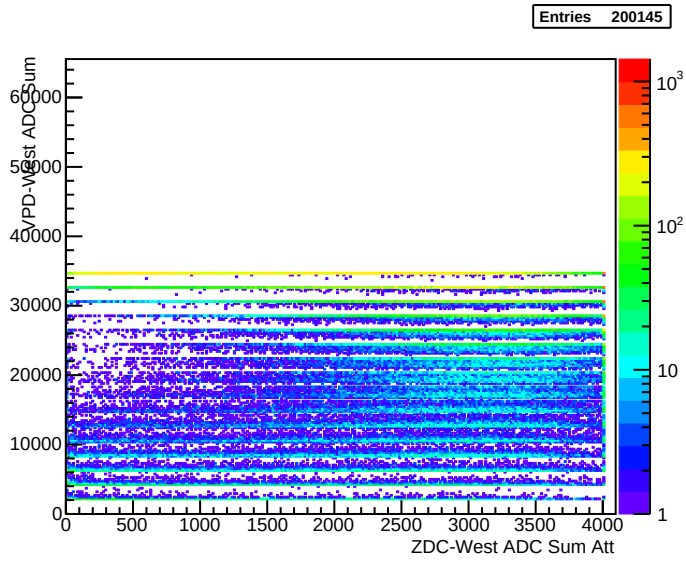

Entries 200145
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

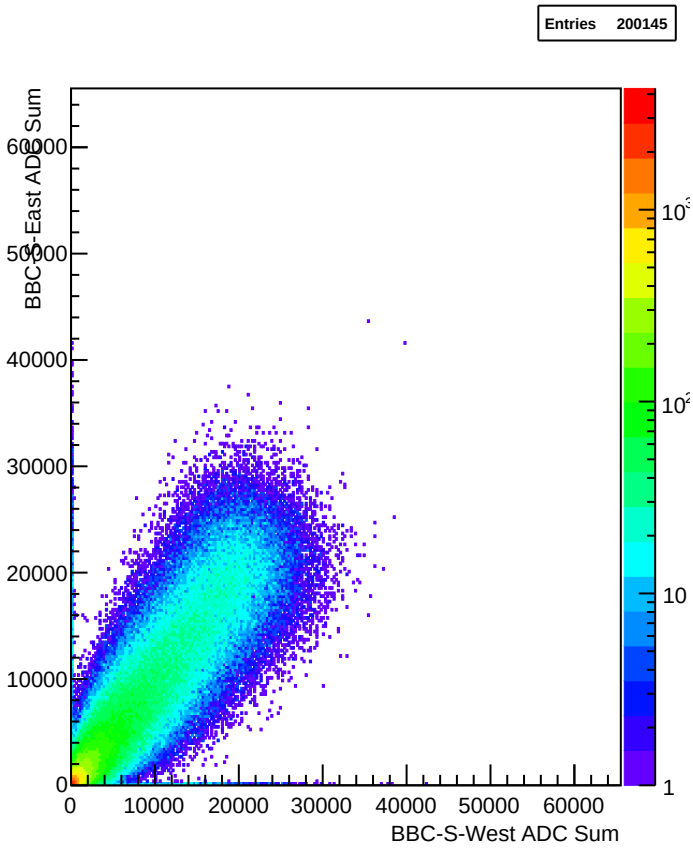

Entries 200145
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

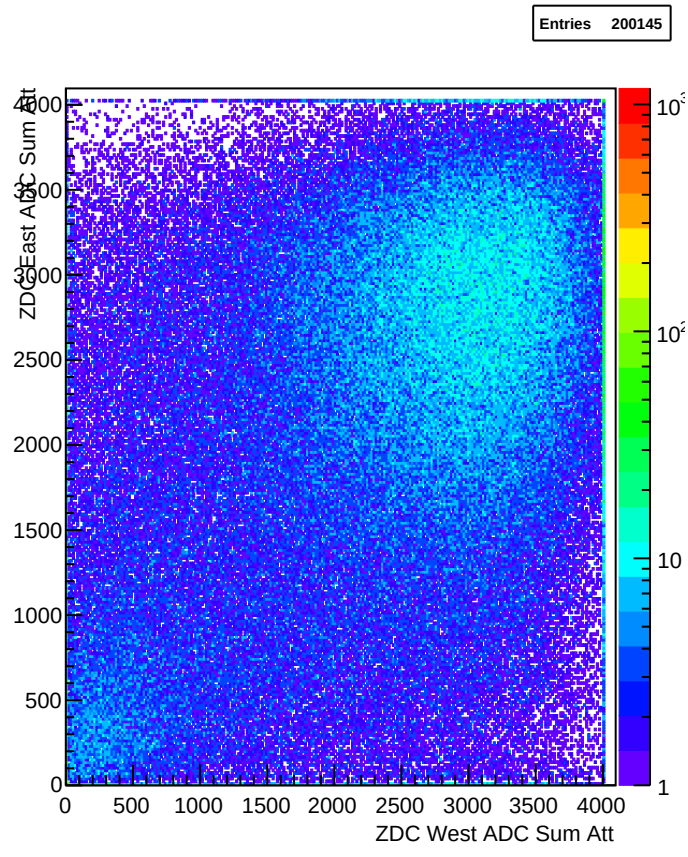

Entries 200145
BBC-S-East ADC Sum
VPD-East ADC Sum

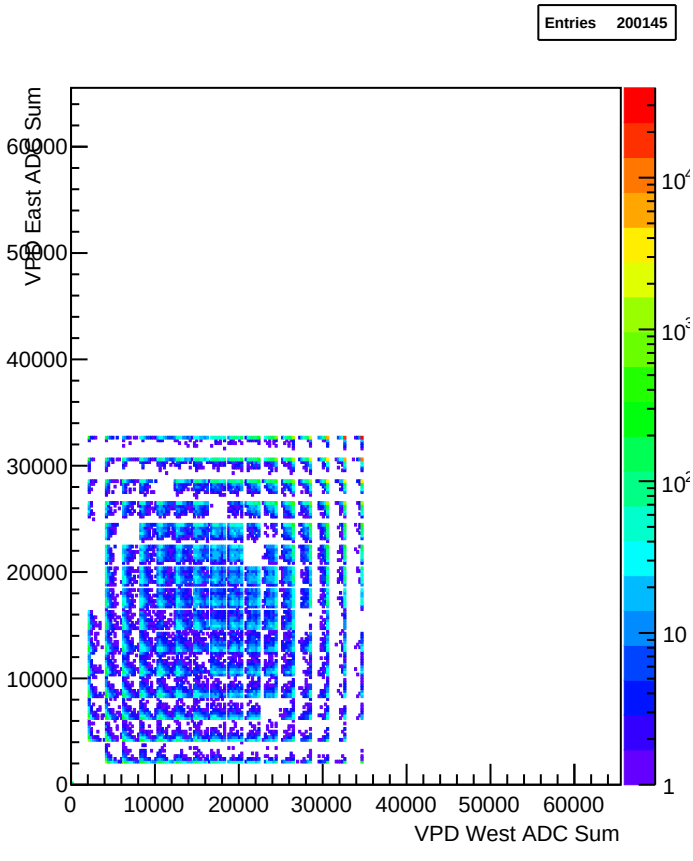

Entries 200145
BBC-S-West ADC Sum
VPD-West ADC Sum

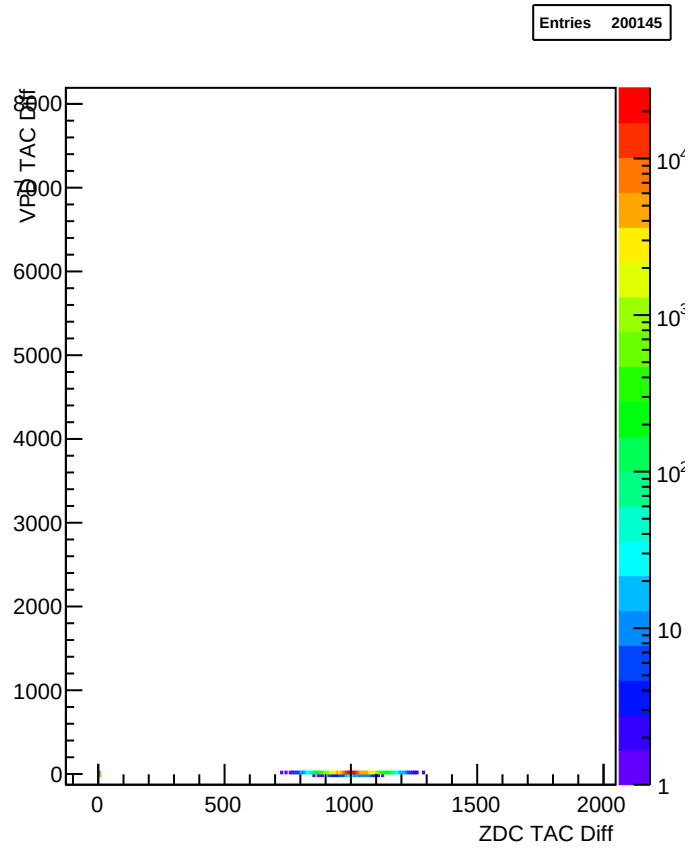

Entries 200145
VPD-East ADC Sum
ZDC-East ADC Sum Att

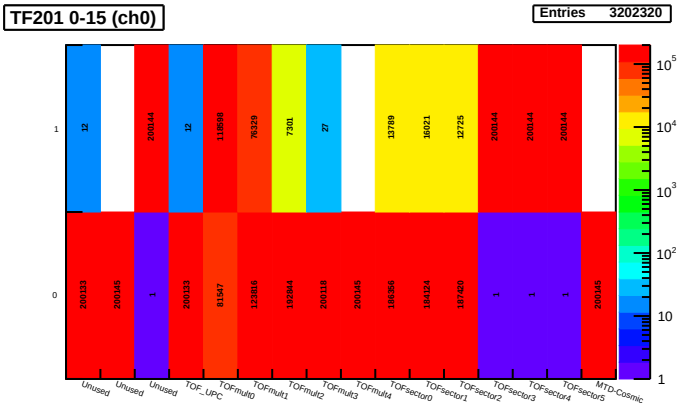

Entries 200145
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 200145
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 200145
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 200145
VPD East ADC Sum
VPD West ADC Sum


Entries 200145
VPD TAC Diff
ZDC TAC Diff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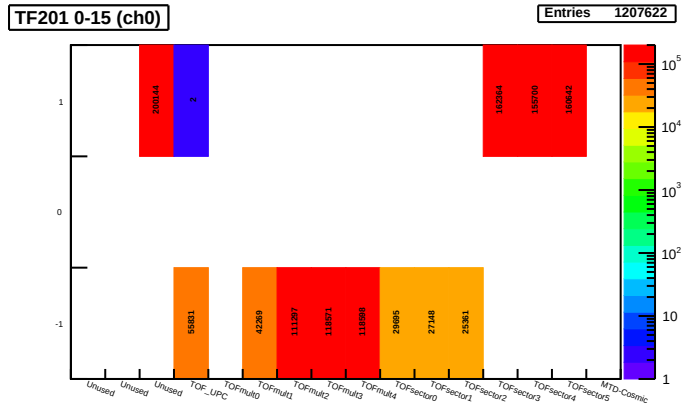
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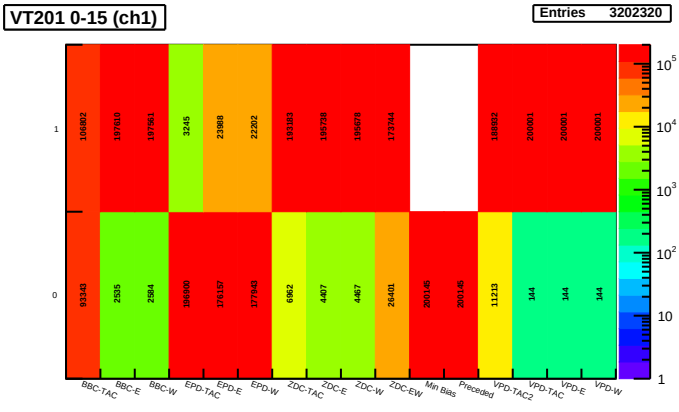
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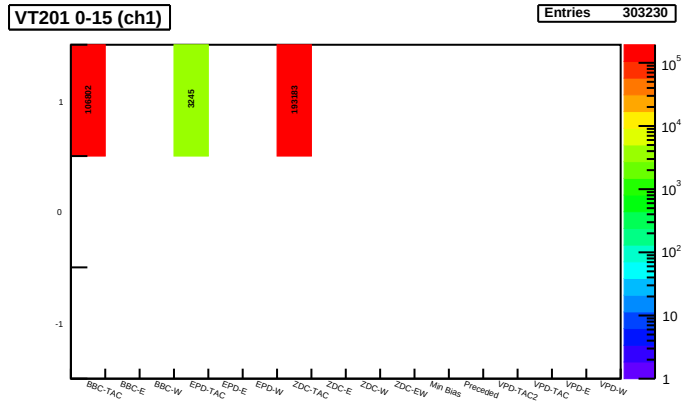
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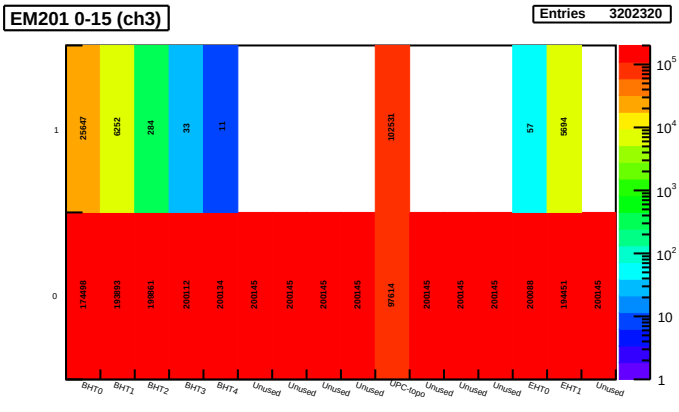
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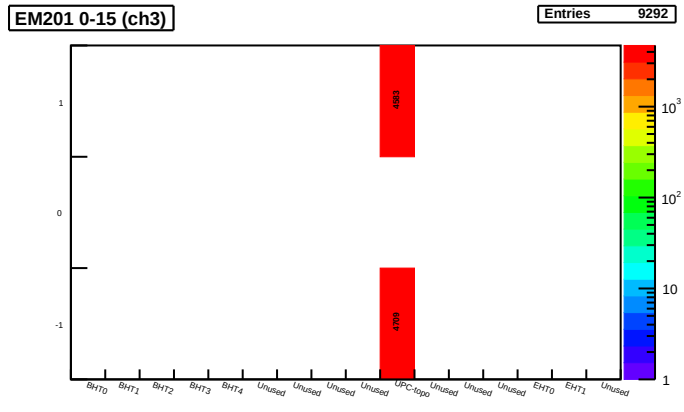
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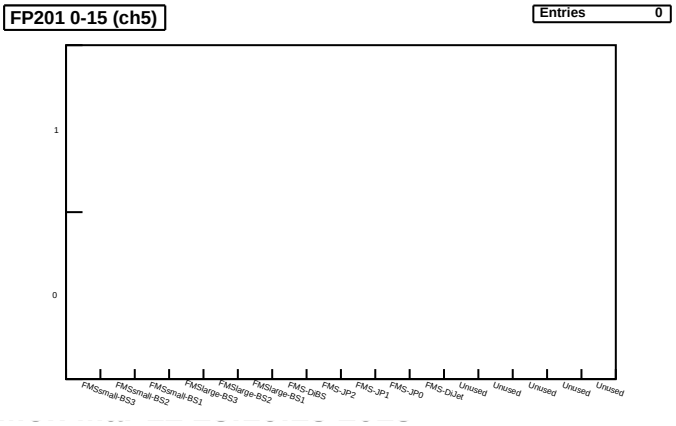
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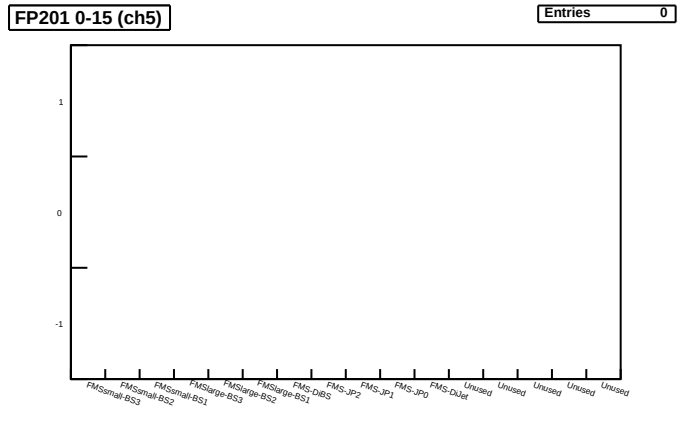
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