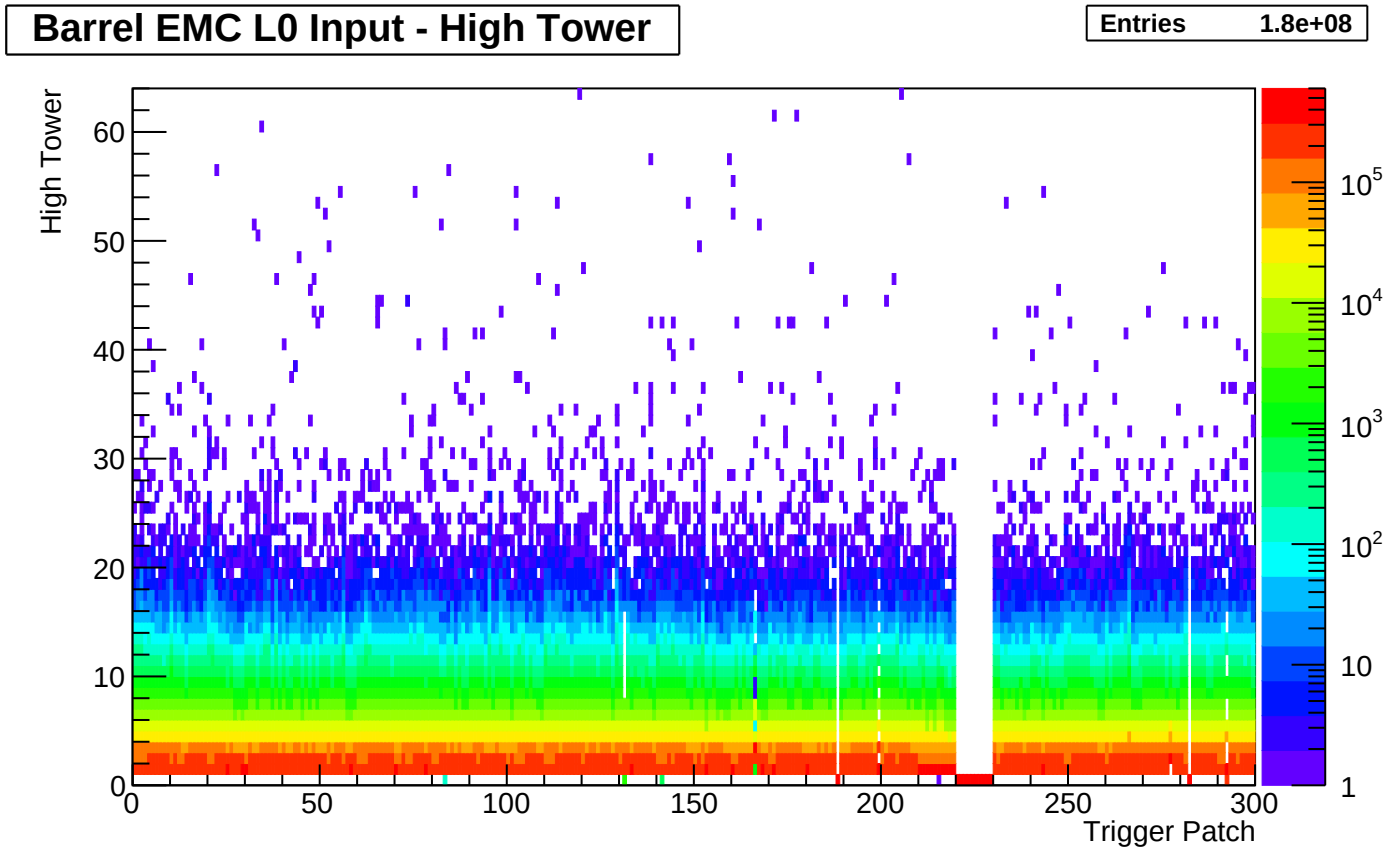
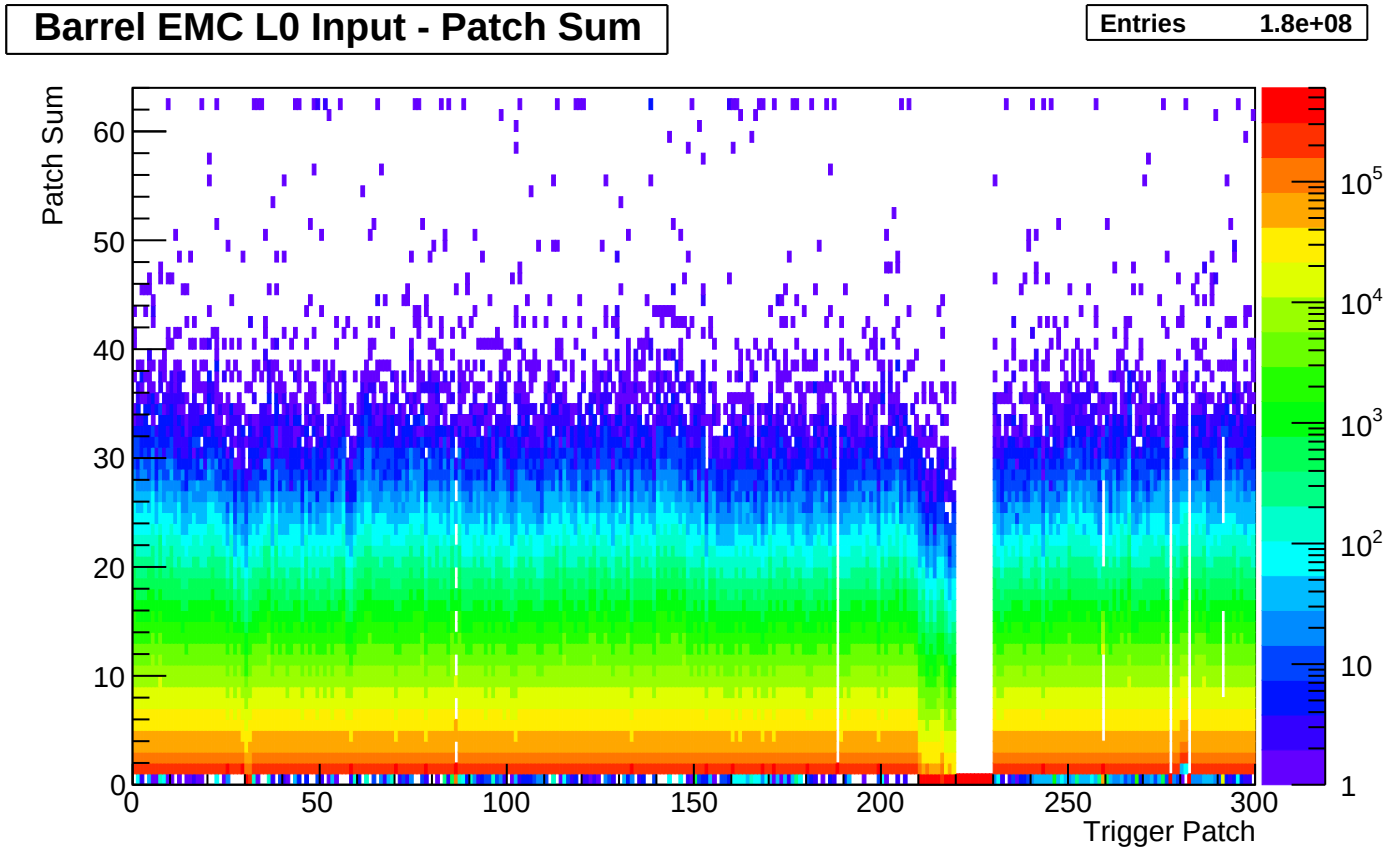
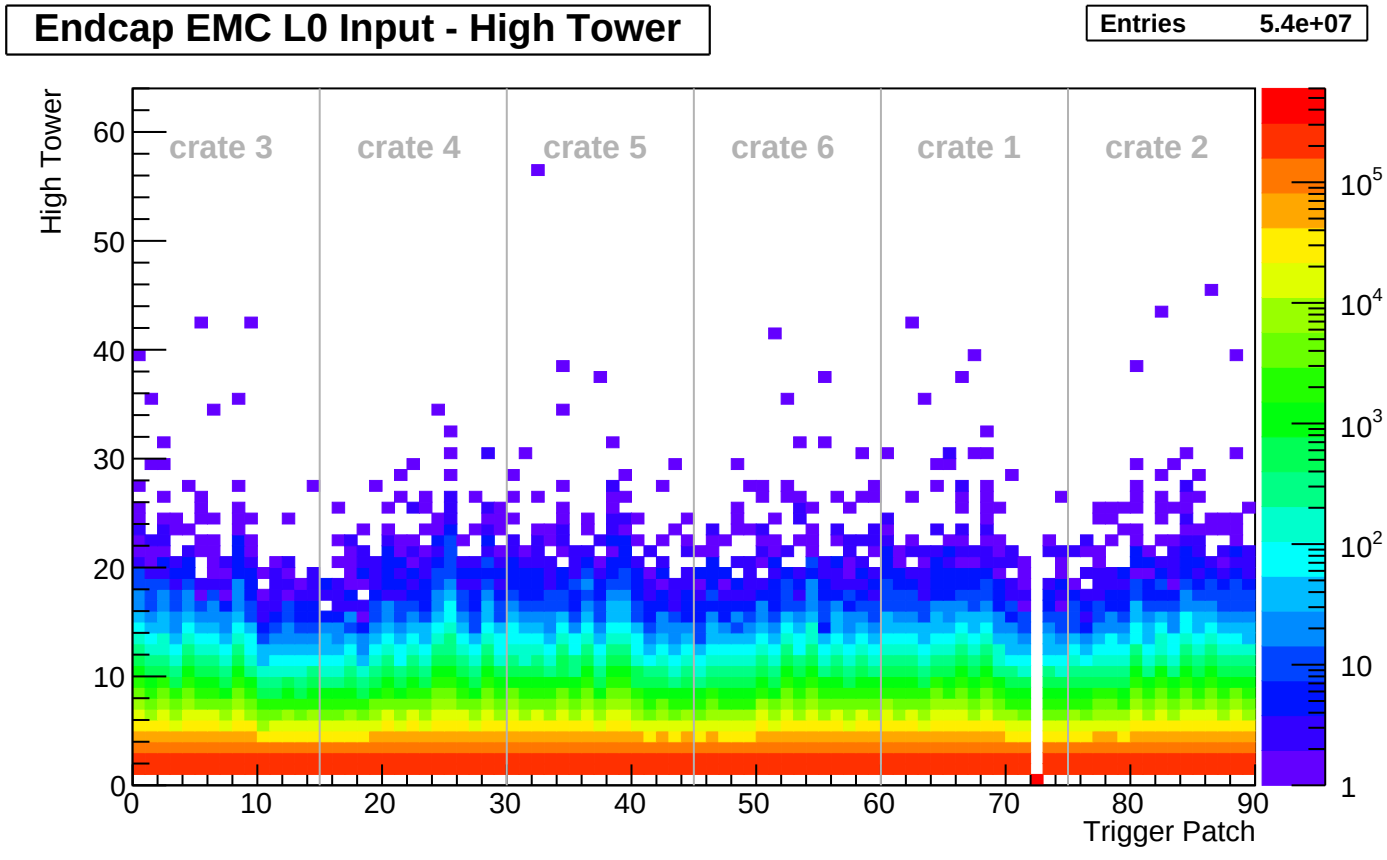
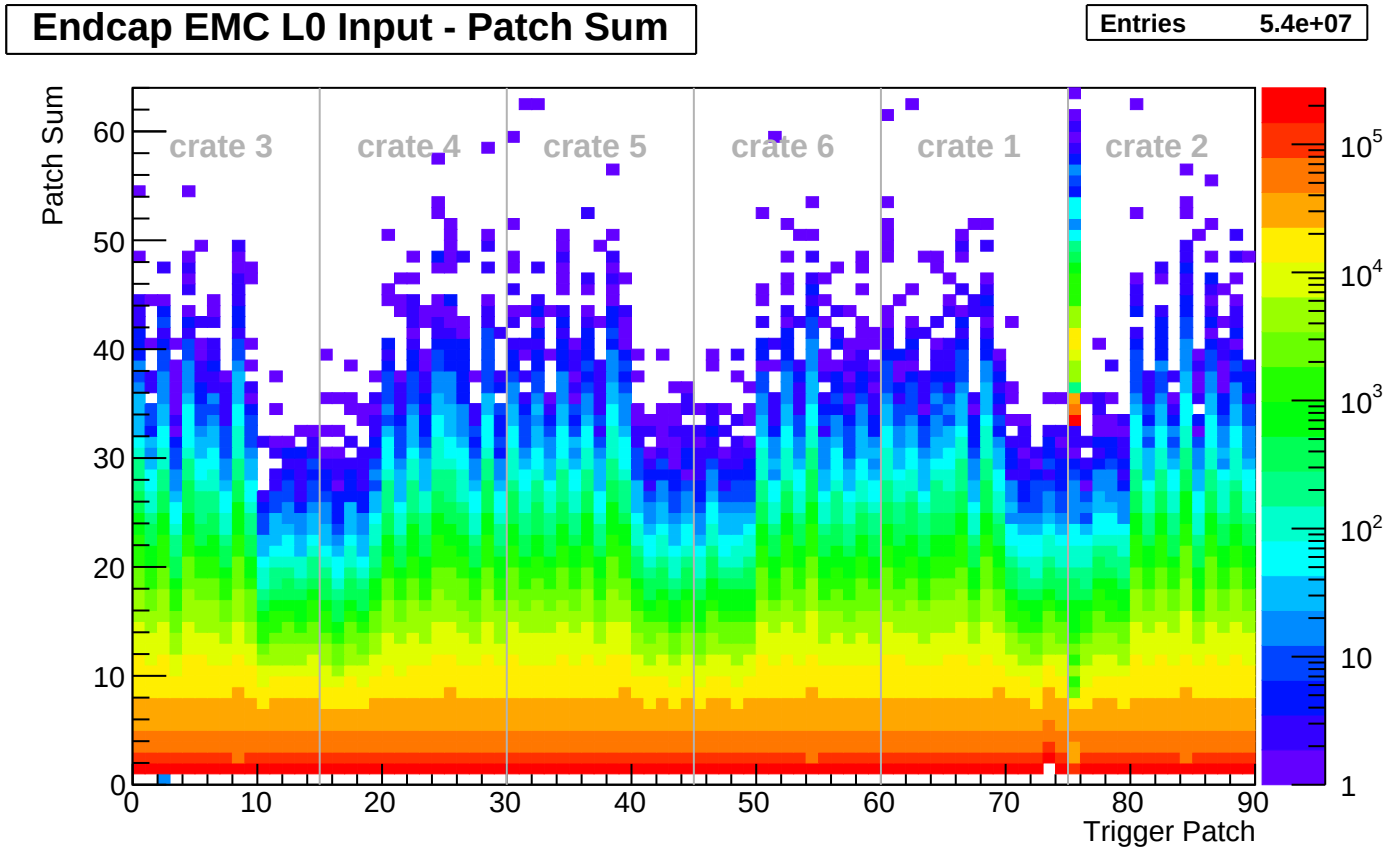
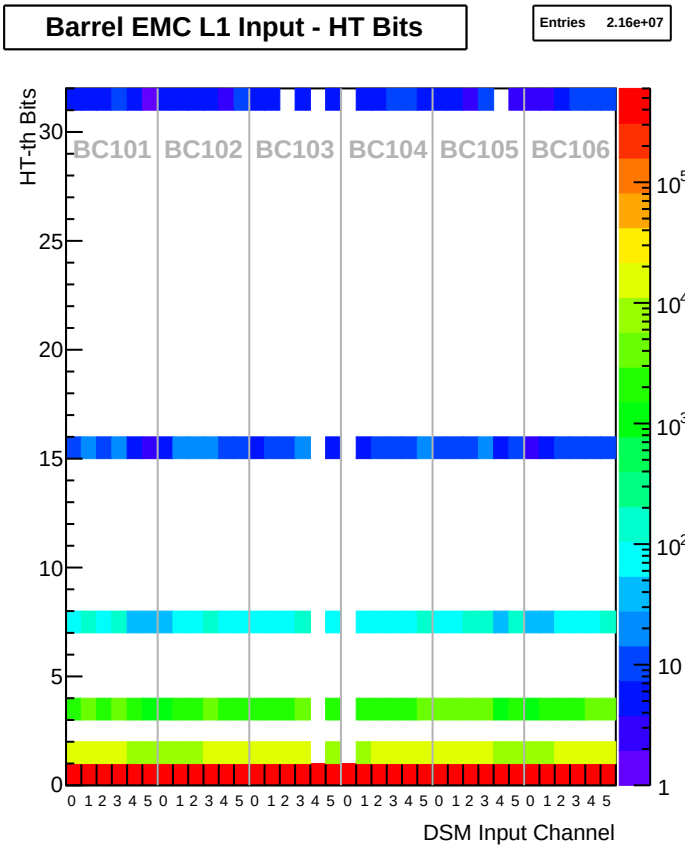
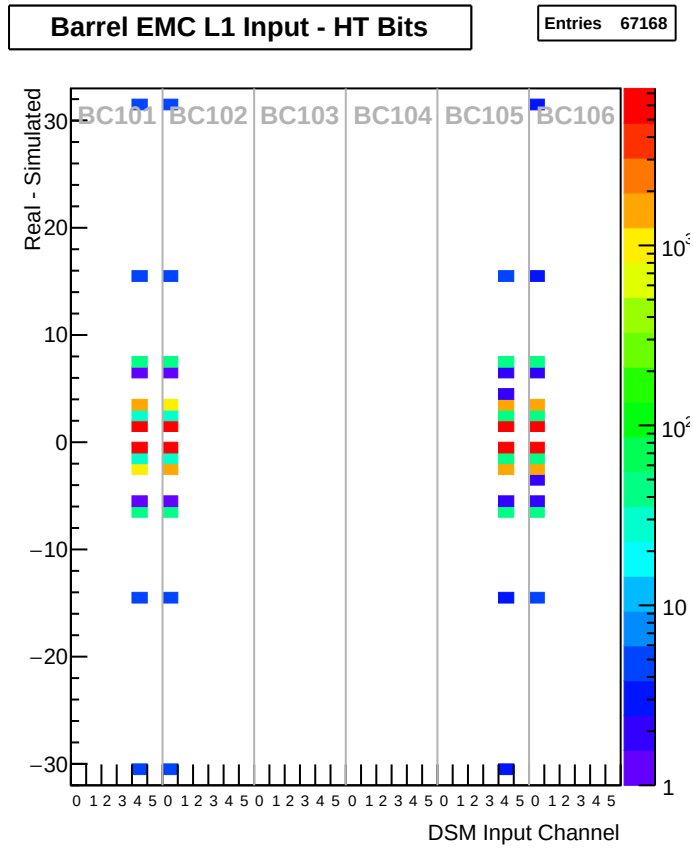

Barrel EMC L0 Input - High Tower
Entries 1.8e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

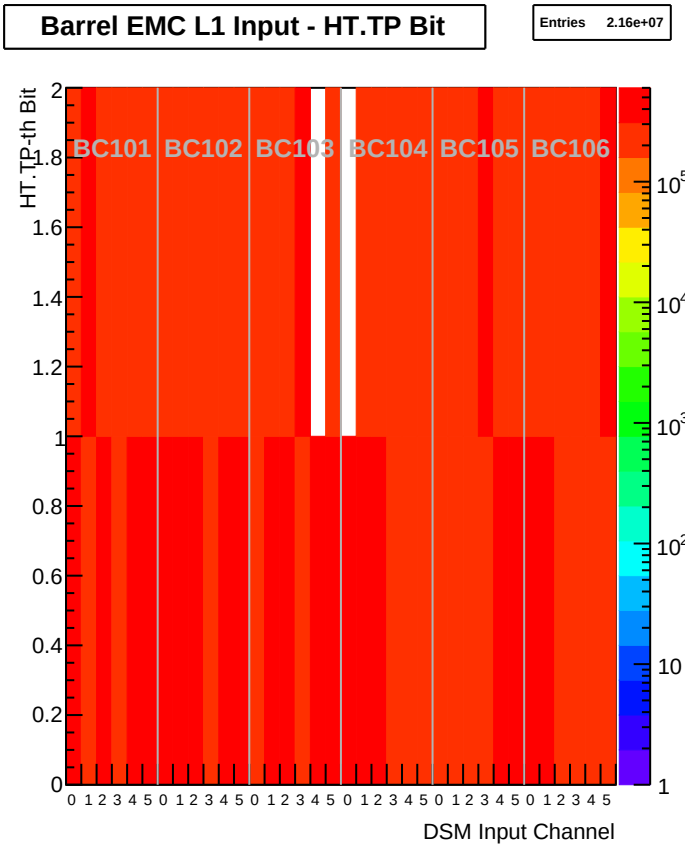

Barrel EMC L0 Input - Patch Sum
Entries 1.8e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

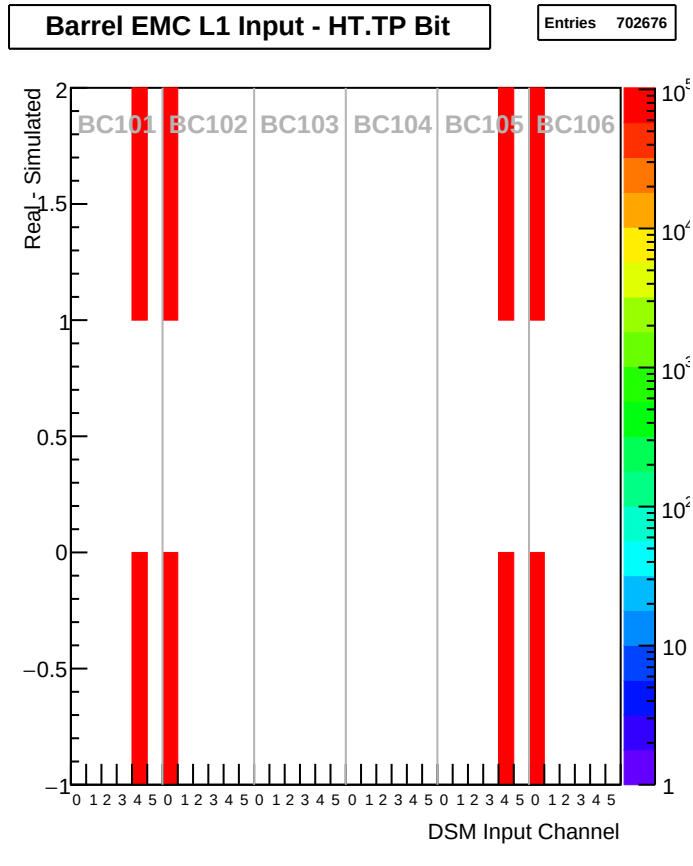

Endcap EMC L0 Input - High Tower
Entries 5.4e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

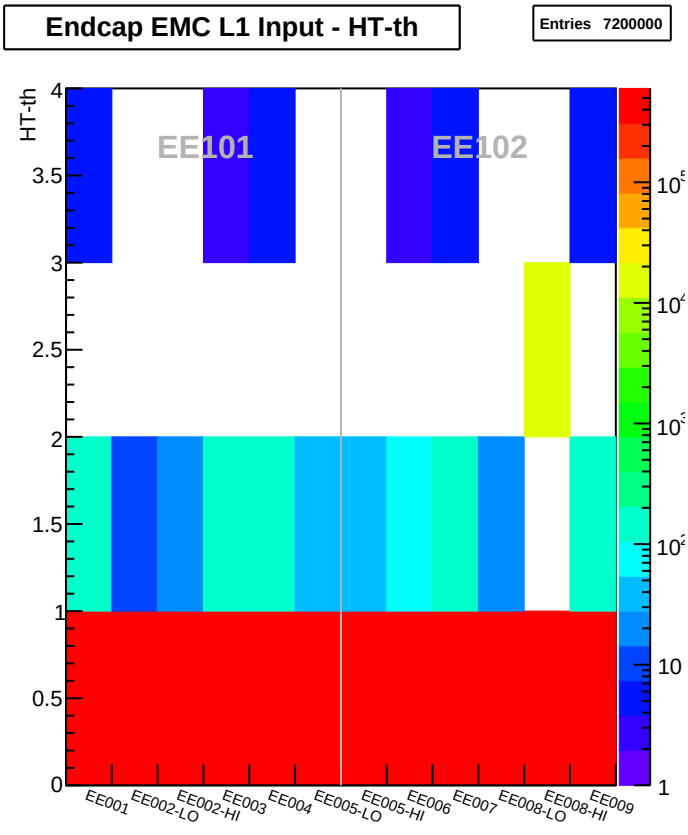

Endcap EMC L0 Input - Patch Sum
Entries 5.4e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

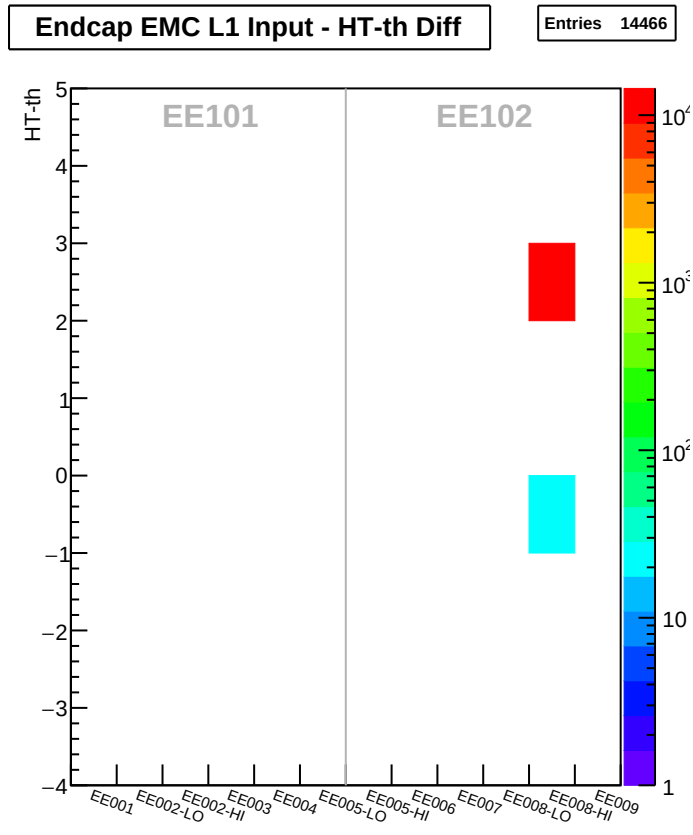

Barrel EMC L1 Input - HT Bits
Entries 2.16e+07
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

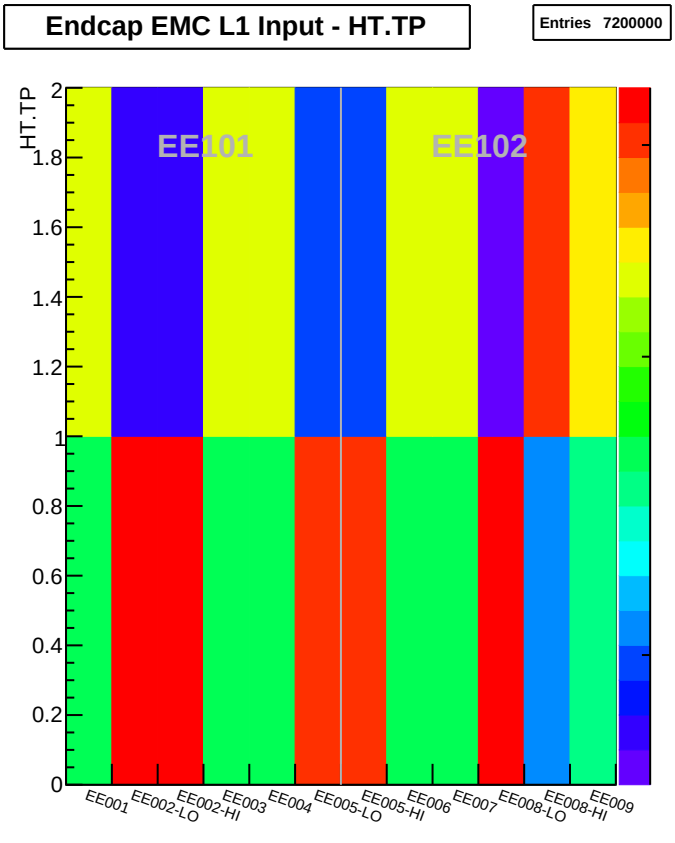

Barrel EMC L1 Input - HT Bits
Entries 67168
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

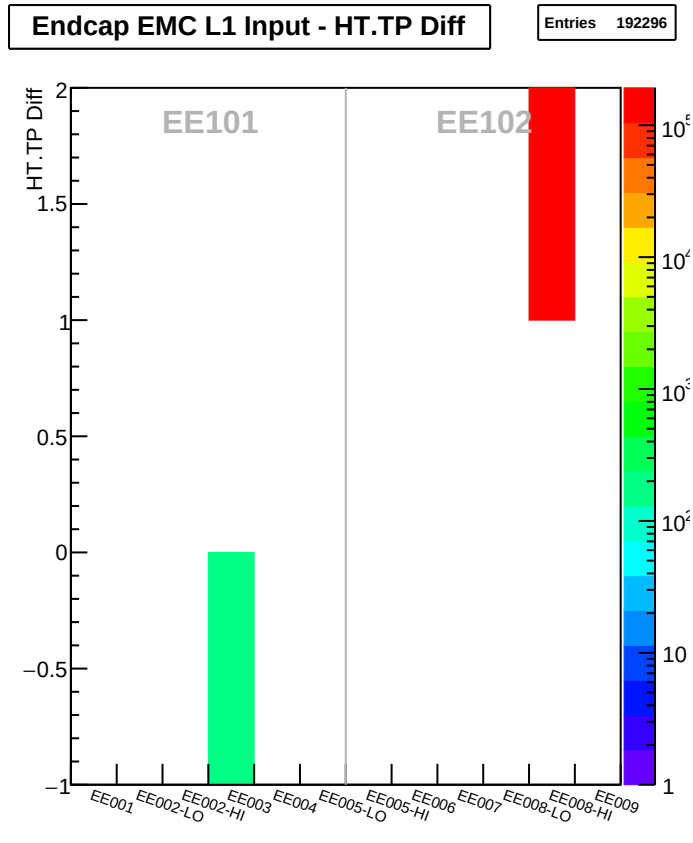

Barrel EMC L1 Input - HT.TP Bit
Entries 2.16e+07
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

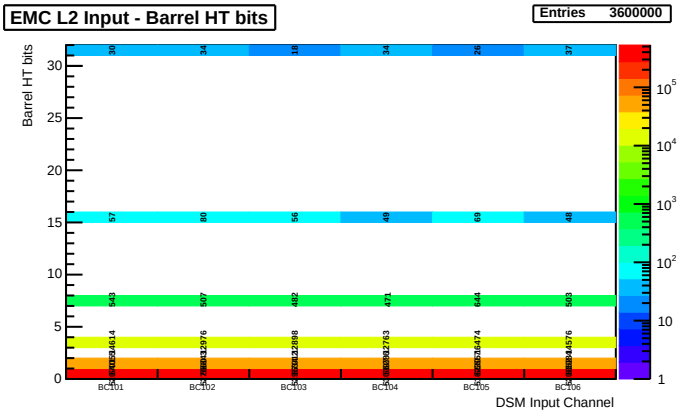

Barrel EMC L1 Input - HT.TP Bit
Entries 702676
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

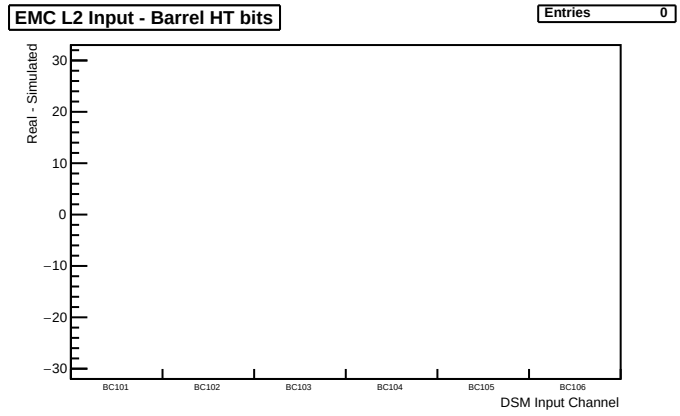

Endcap EMC L1 Input - HT-th
Entries 7200000
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

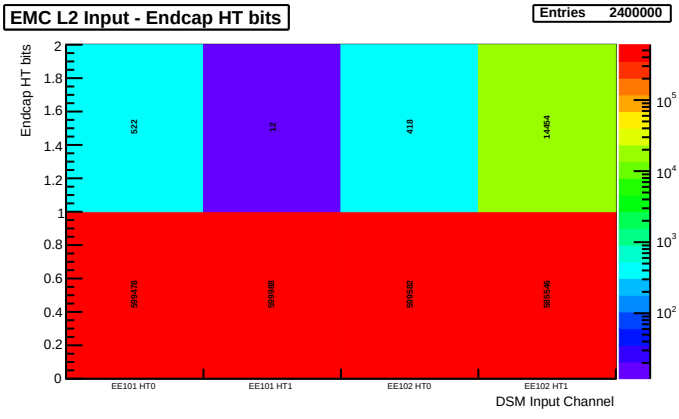

Endcap EMC L1 Input - HT-th Diff
Entries 14466
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

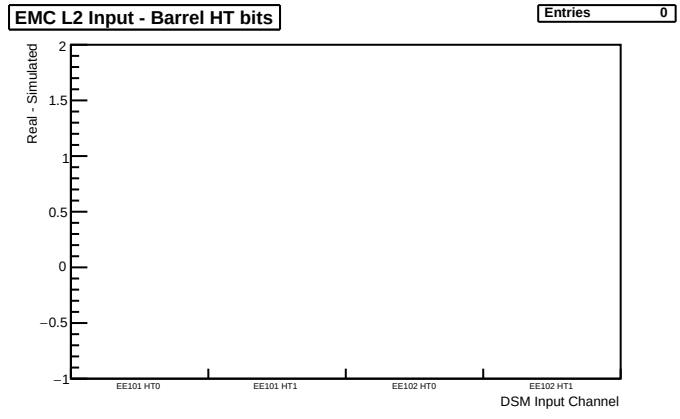

Endcap EMC L1 Input - HT.TP
Entries 7200000
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

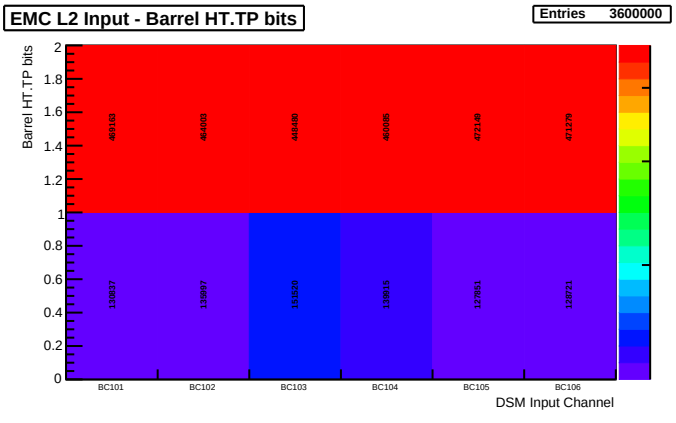

Endcap EMC L1 Input - HT.TP Diff
Entries 192296
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

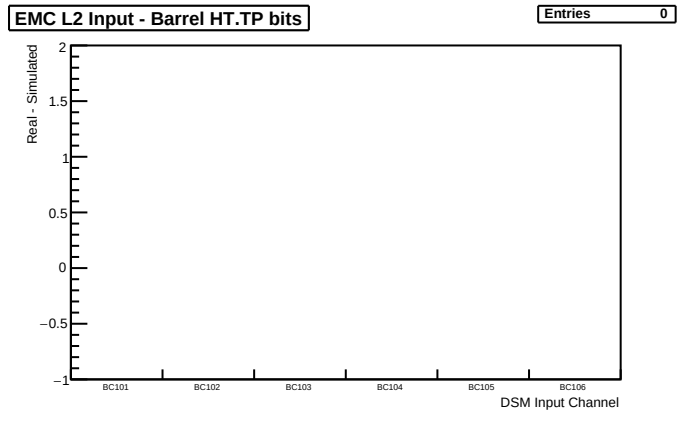

EMC L2 Input - Barrel HT bits
Entries 3600000
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

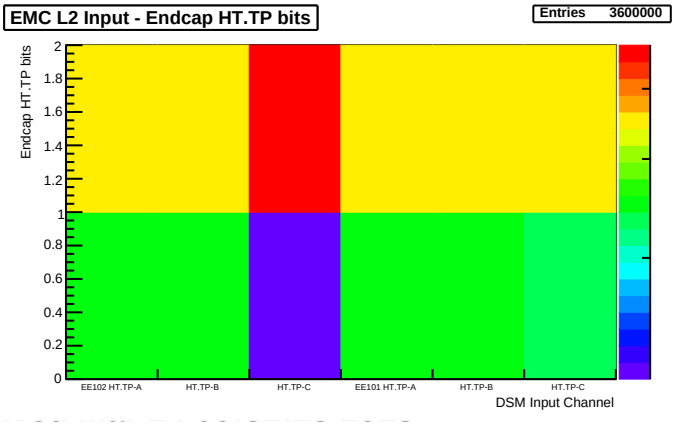

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

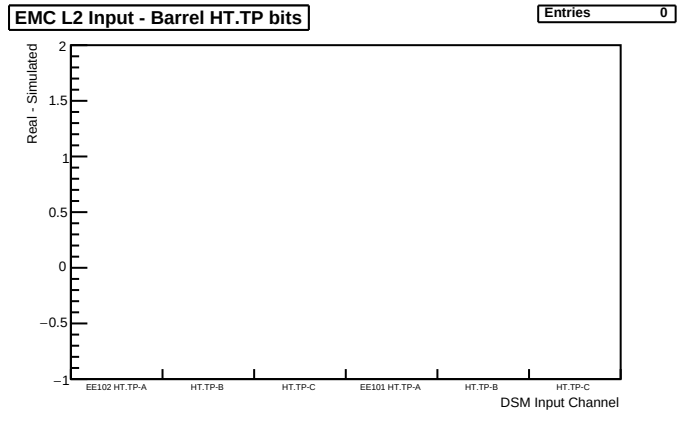

EMC L2 Input - Endcap HT bits
Entries 2400000
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

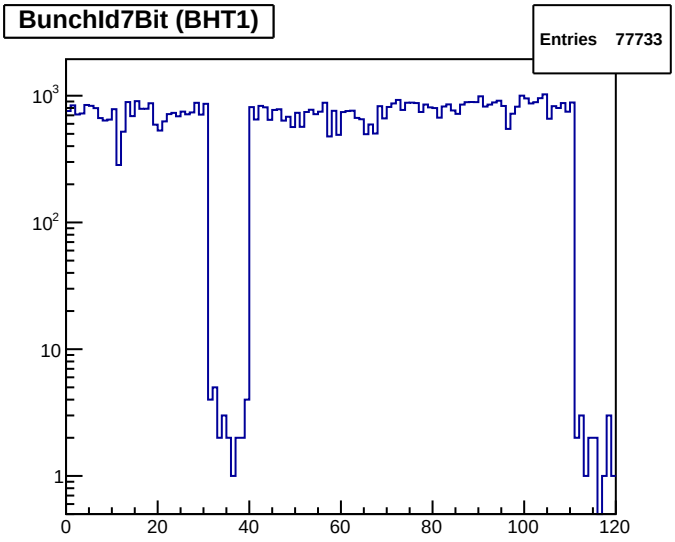

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

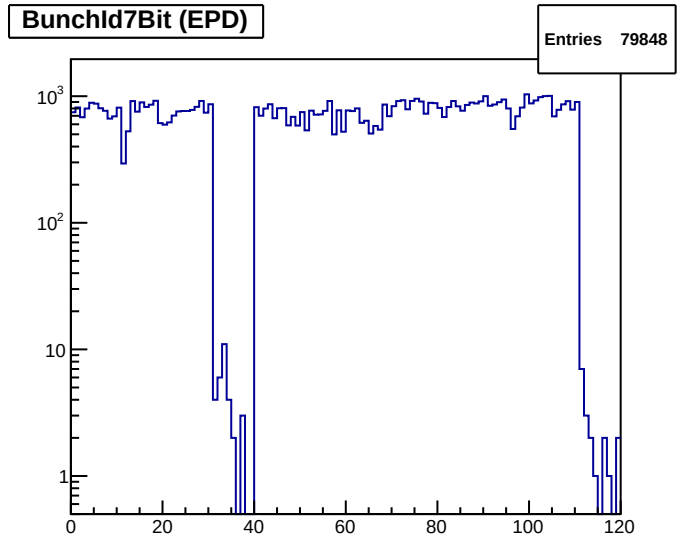

EMC L2 Input - Barrel HT.TP bits
Entries 3600000
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

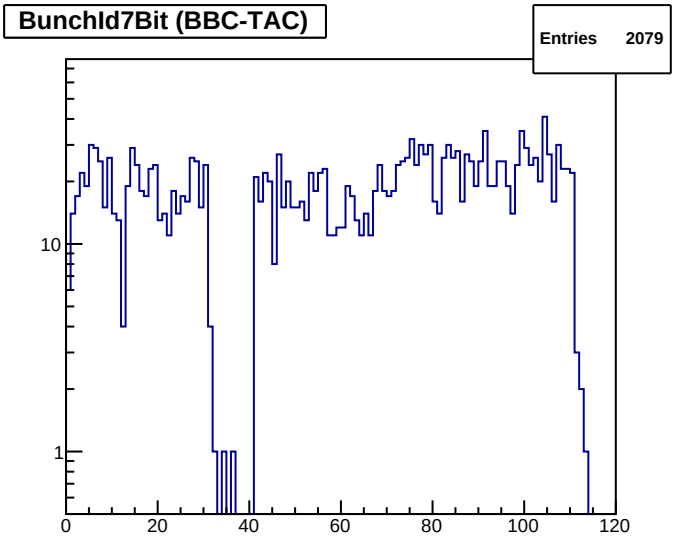

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

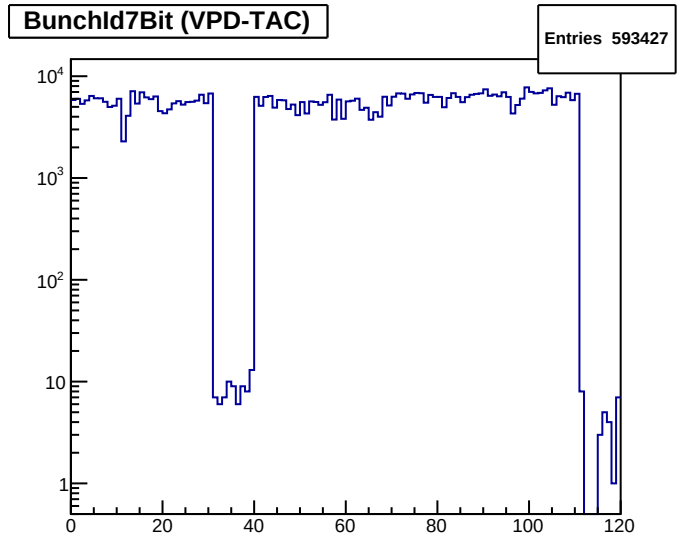

EMC L2 Input - Endcap HT.TP bits
Entries 3600000
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

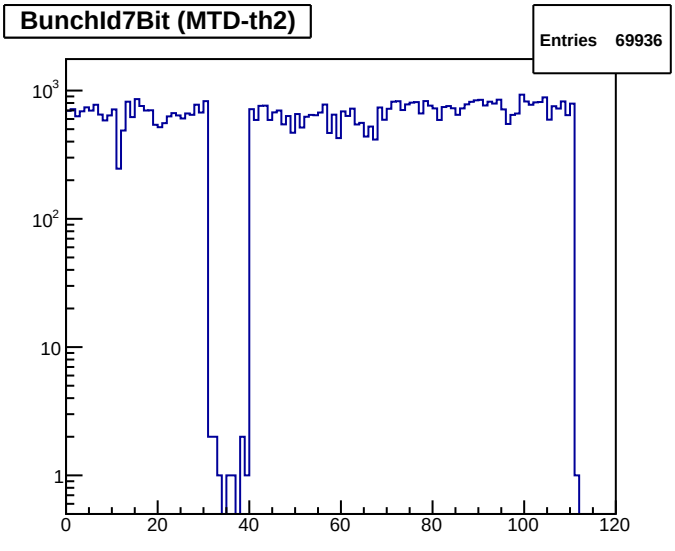

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

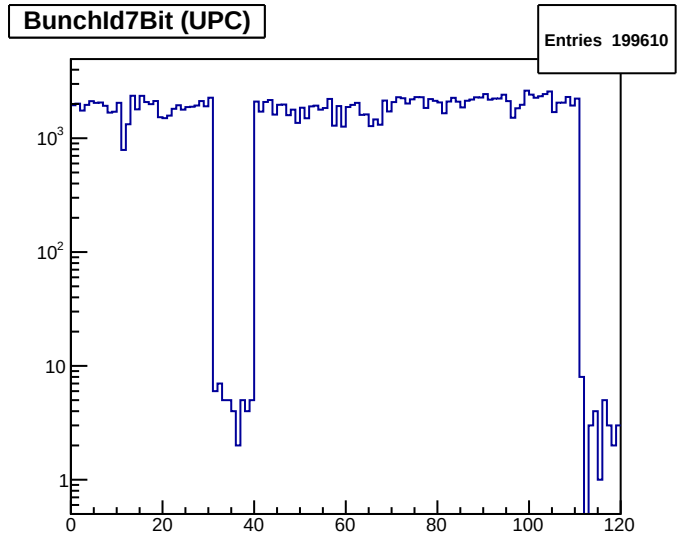

BunchId7Bit (BHT1)
Entries 77733

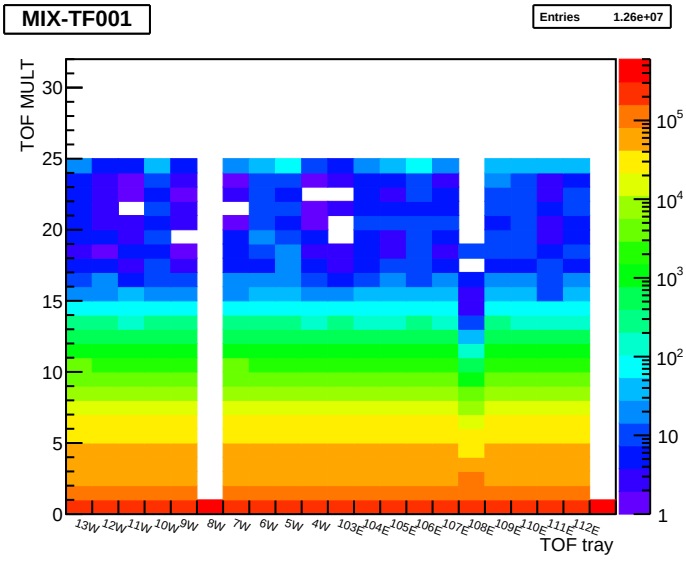

BunchId7Bit (EPD)
Entries 79848

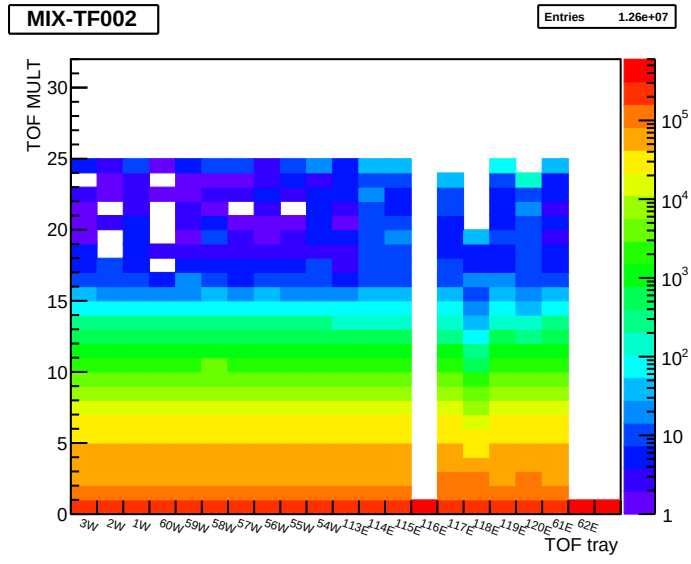

BunchId7Bit (BBC-TAC)
Entries 2079

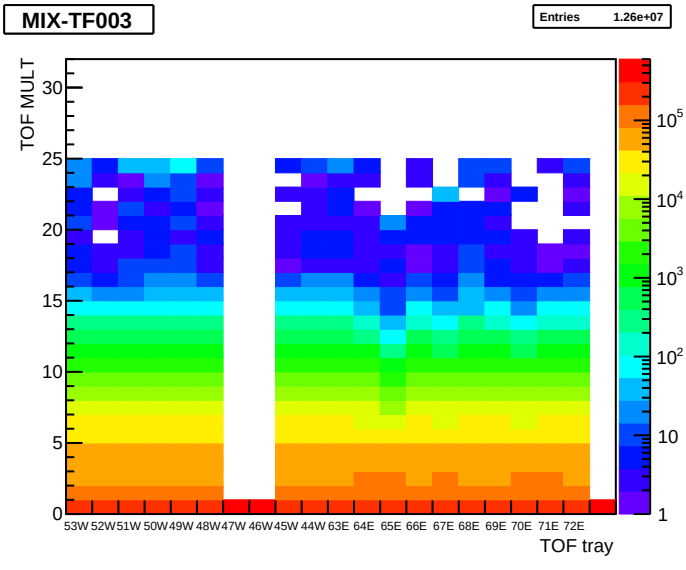

BunchId7Bit (VPD-TAC)
Entries 593427

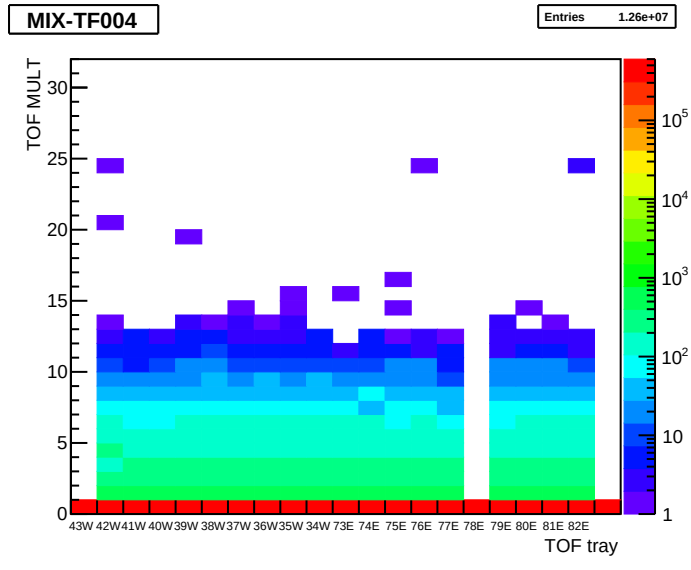

BunchId7Bit (MTD-th2)
Entries 69936

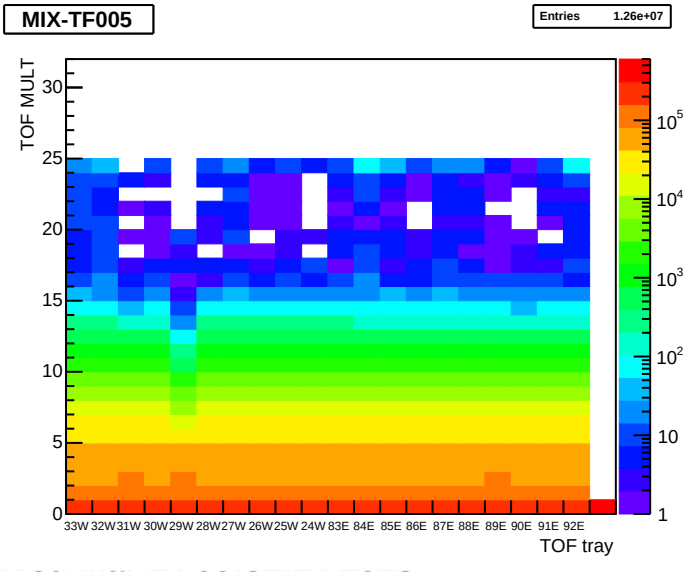

BunchId7Bit (UPC)
Entries 199610

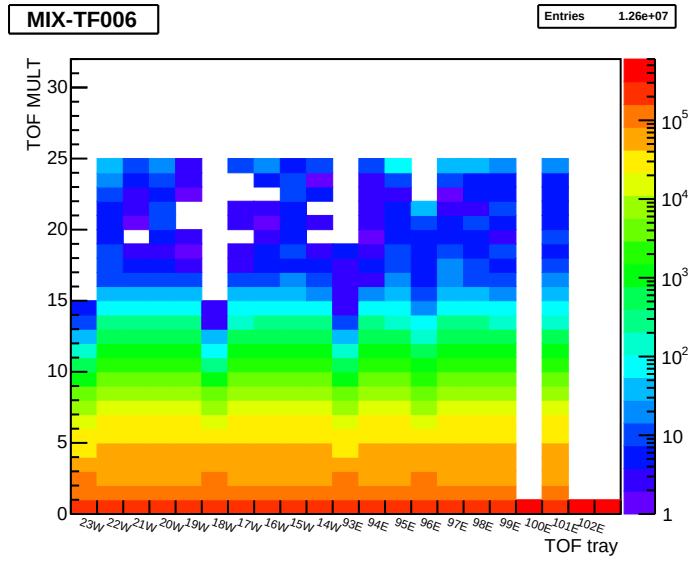

MIX-TF001
Entries 1.26e+07
TOF MULT
TOF tray

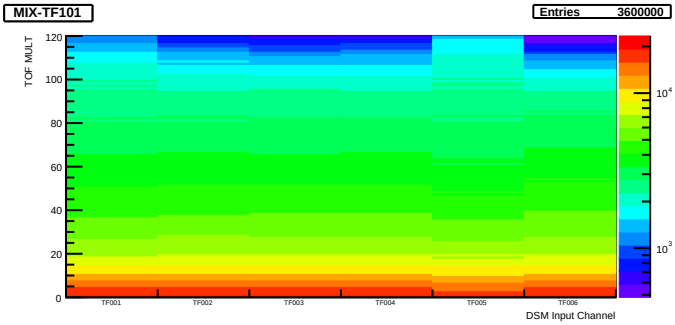

MIX-TF002
Entries 1.26e+07
TOF MULT
TOF tray

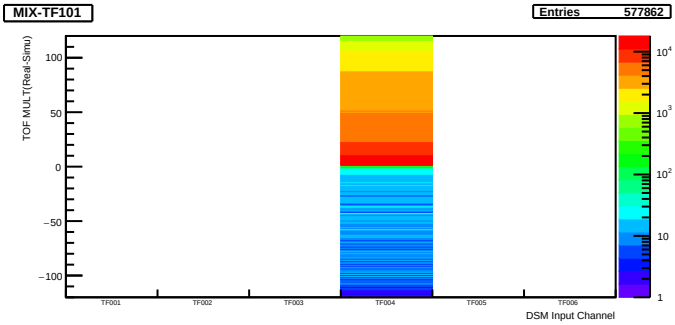

MIX-TF003
Entries 1.26e+07
TOF MULT
TOF tray

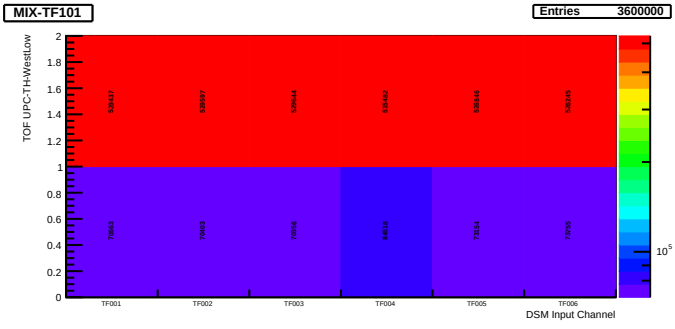

MIX-TF004
Entries 1.26e+07
TOF MULT
TOF tray

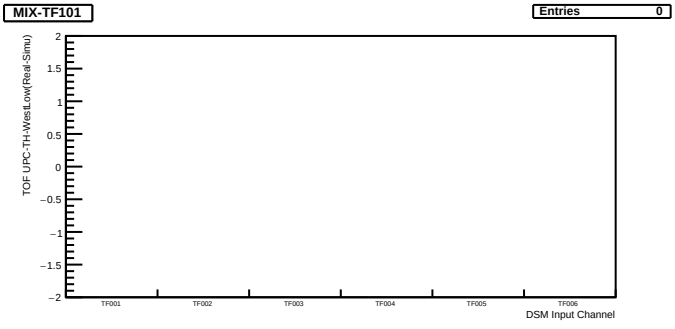

MIX-TF005
Entries 1.26e+07
TOF MULT
TOF tray

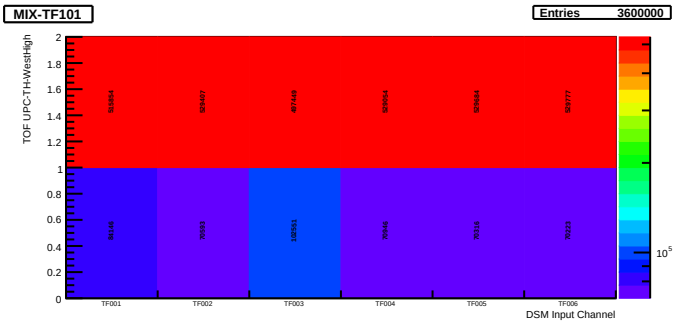

MIX-TF006
Entries 1.26e+07
TOF MULT
TOF tray

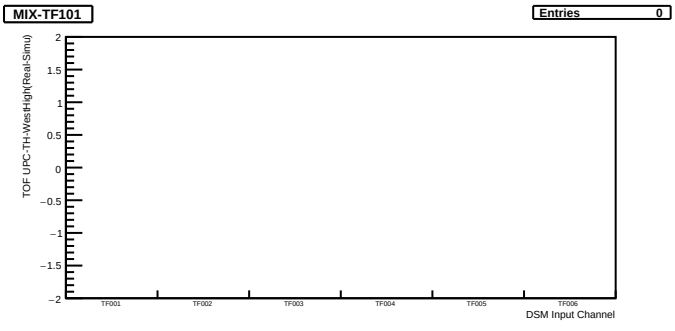

MIX-TF101
Entries 3600000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

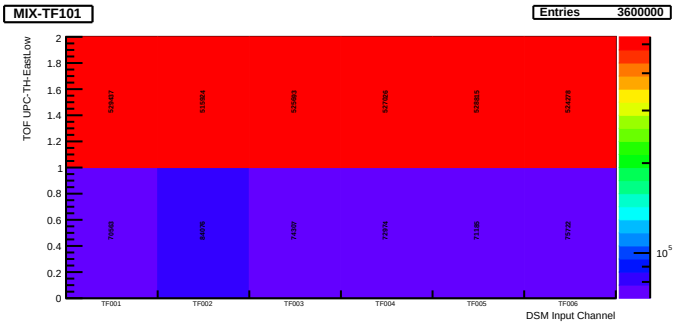

MIX-TF101
Entries 577862
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

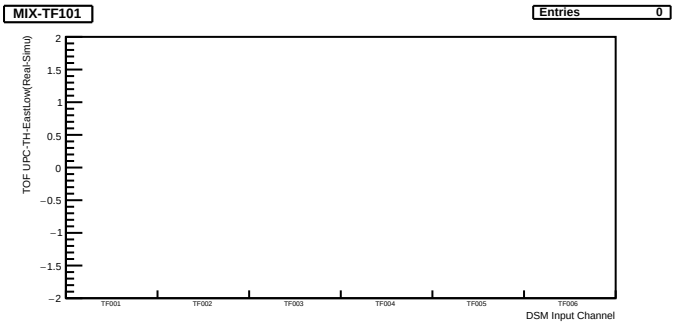

MIX-TF101
Entries 3600000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

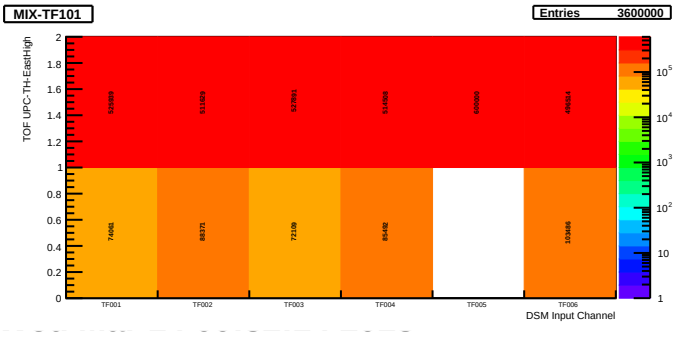

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

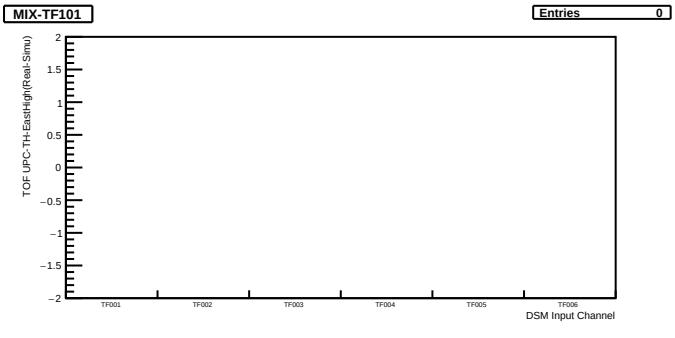

MIX-TF101
Entries 3600000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

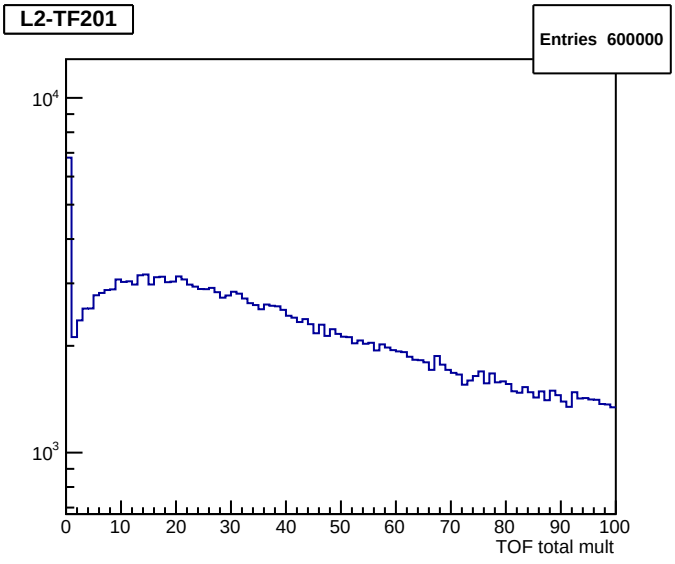

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

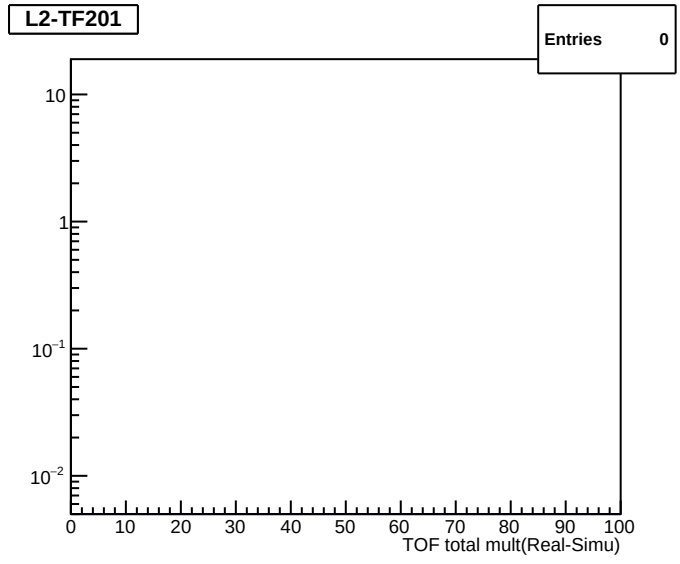

MIX-TF101
Entries 3600000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

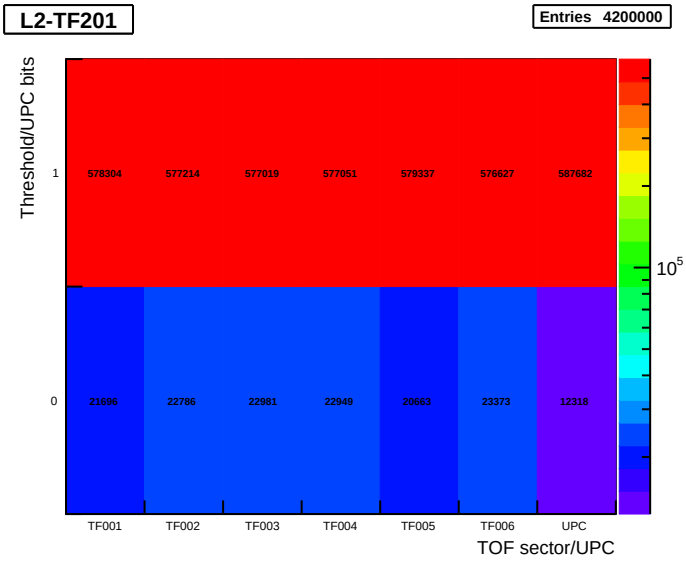

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

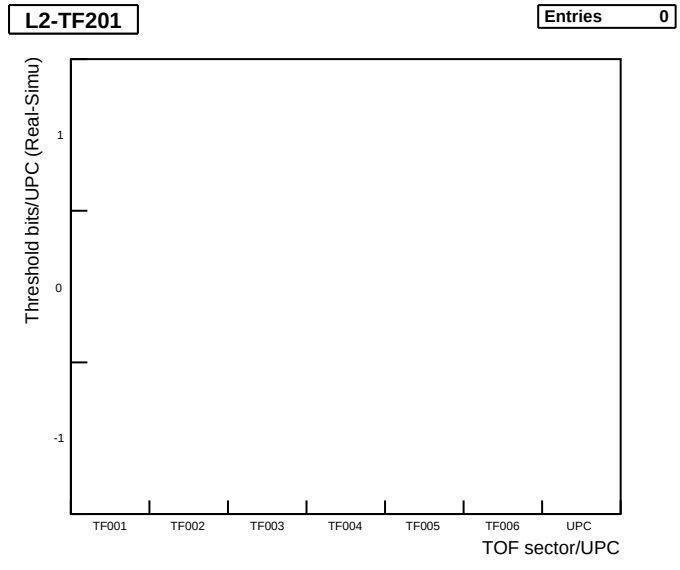

MIX-TF101
Entries 3600000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

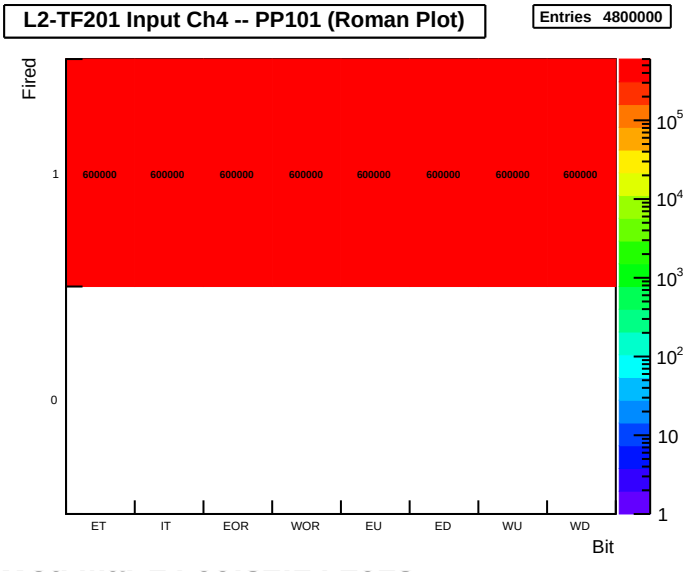

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

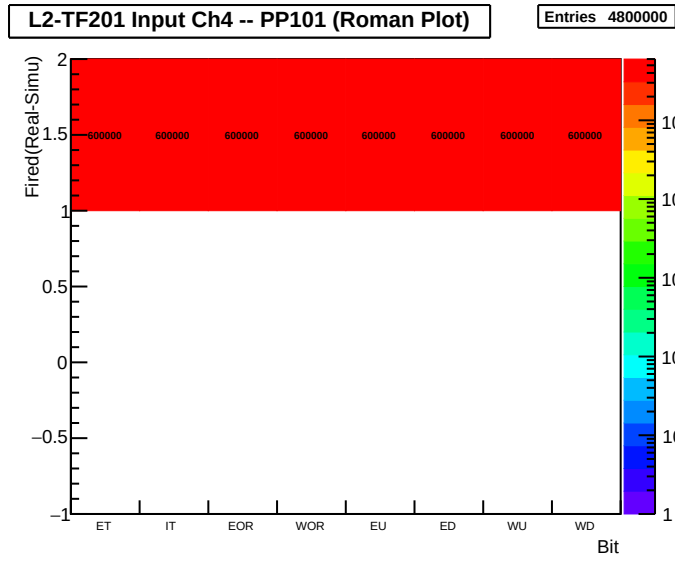

L2-TF201
Entries 600000
TOF total mult

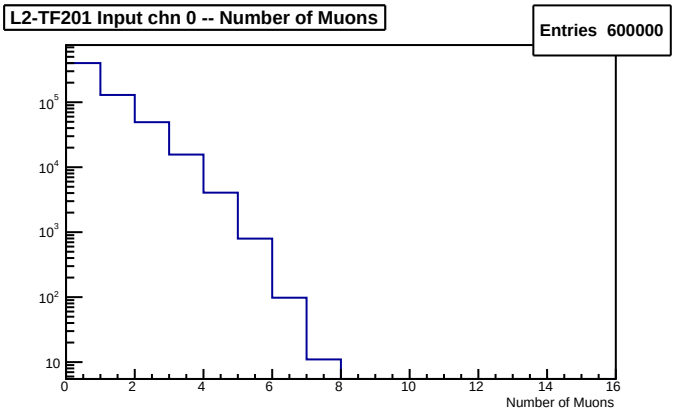

L2-TF201
Entries 0
TOF total mult(Real-Simu)

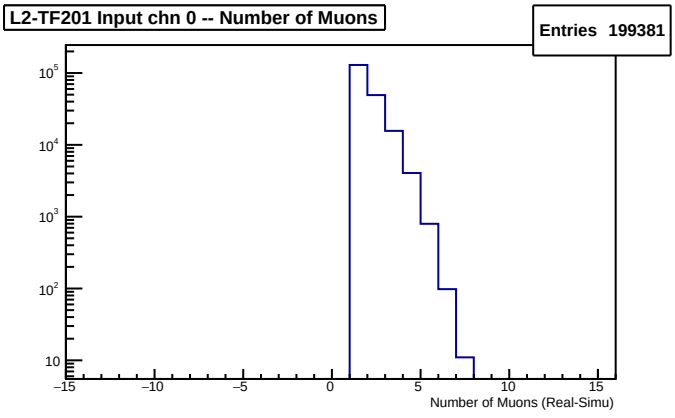

L2-TF201
Entries 4200000
Threshold/UPC bits
578304 577214 577019 577051 579337 576627 587682
21696 22786 22981 22949 20663 23373 12318
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

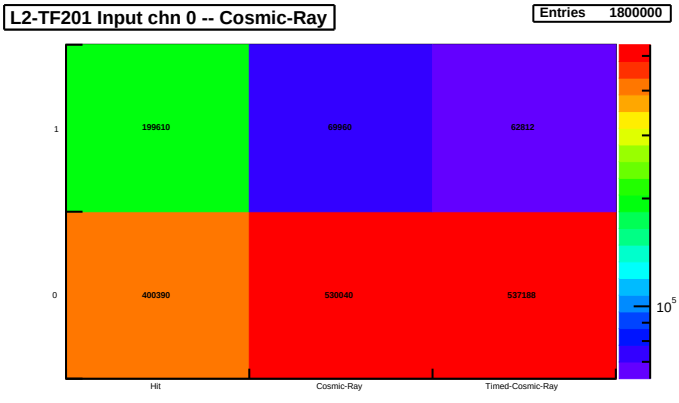

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

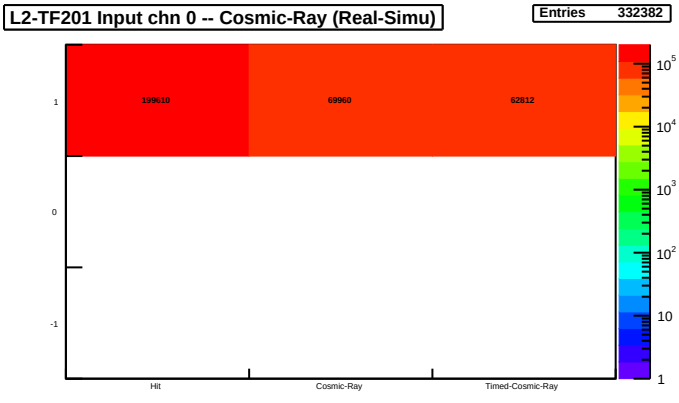

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 4800000
Fired
600000 600000 600000 600000 600000 600000 600000 600000
ET IT EOR WOR EU ED WU WD
Bit

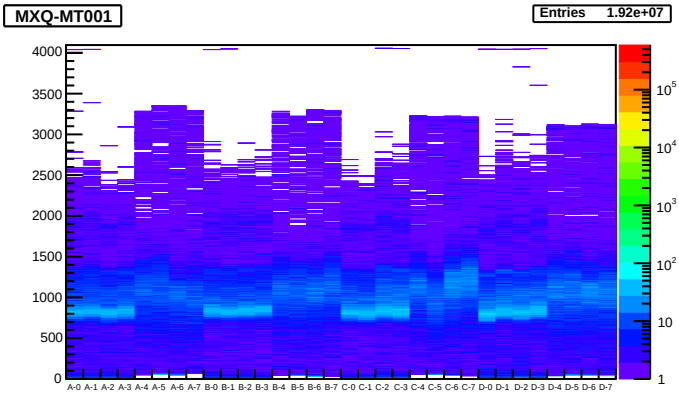

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 4800000
Fired(Real-Simu)
600000 600000 600000 600000 600000 600000 600000 600000
ET IT EOR WOR EU ED WU WD
Bit

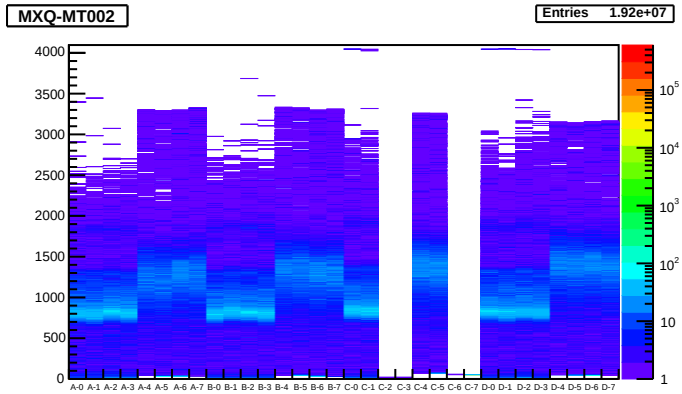

L2-TF201 Input chn 0 -- Number of Muons
Entries 600000
Number of Muons

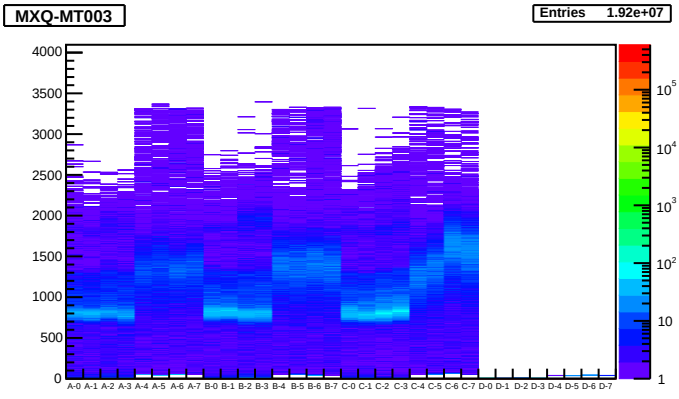

L2-TF201 Input chn 0 -- Number of Muons
Entries 199381
Number of Muons (Real-Simu)

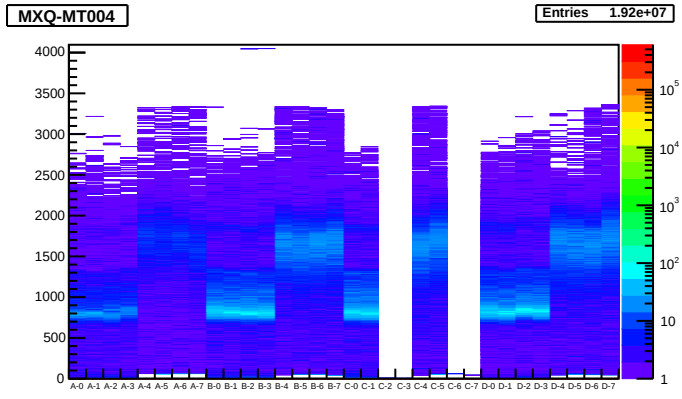

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 1800000
199610
69960
62812
400390
530040
537188
Hit
Cosmic-Ray
Timed-Cosmic-Ray

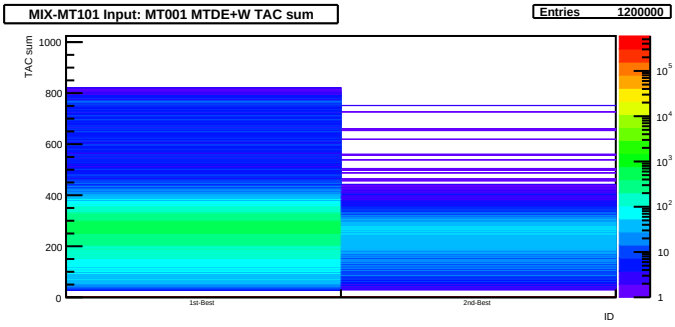

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 332382
199610
69960
62812
Hit
Cosmic-Ray
Timed-Cosmic-Ray

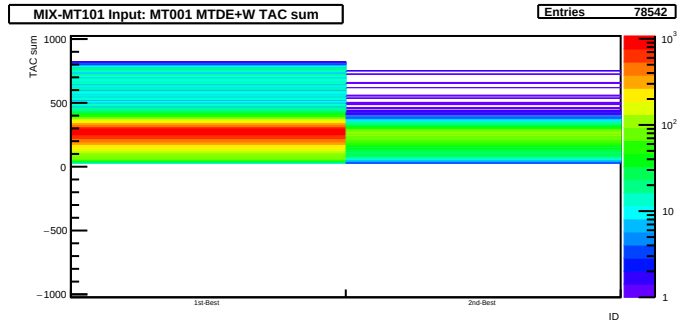

MXQ-MT001
Entries 1.92e+07

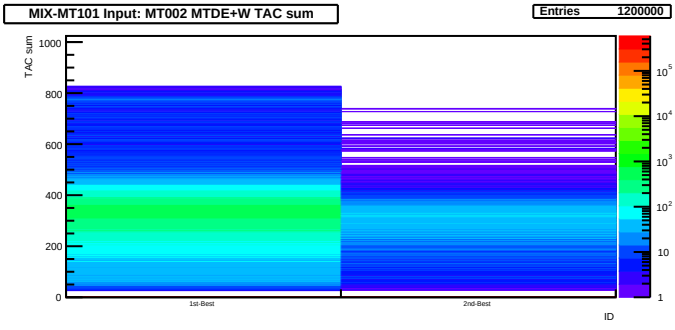

MXQ-MT002
Entries 1.92e+07

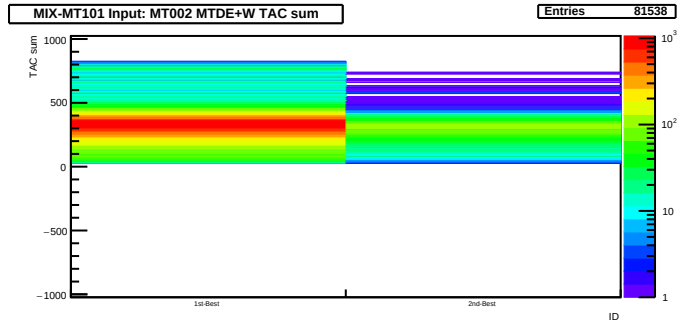

MXQ-MT003
Entries 1.92e+07

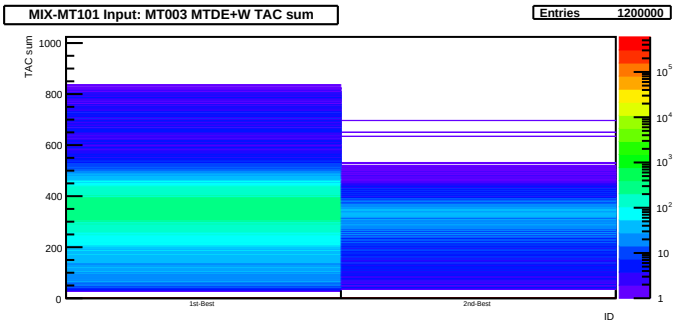

MXQ-MT004
Entries 1.92e+07

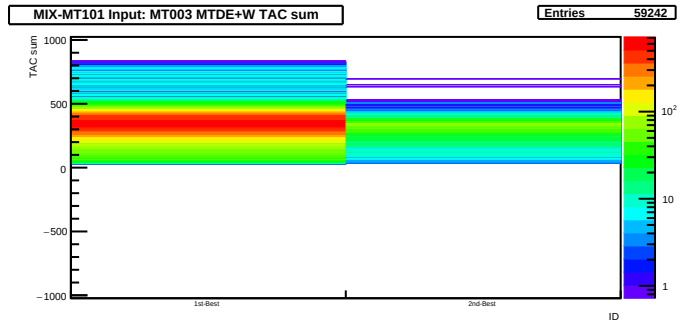

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 1200000
TAC sum
1st-Best
2nd-Best
ID

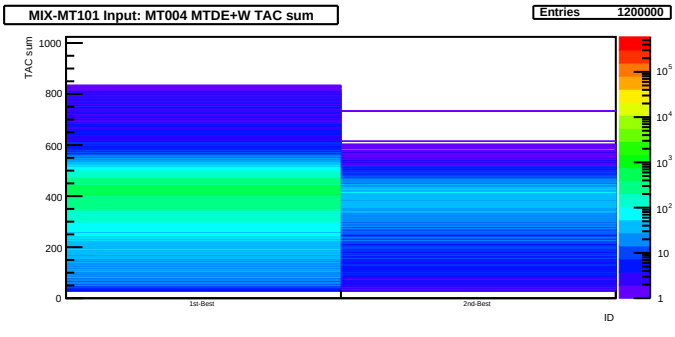

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 78542
TAC sum
1st-Best
2nd-Best
ID

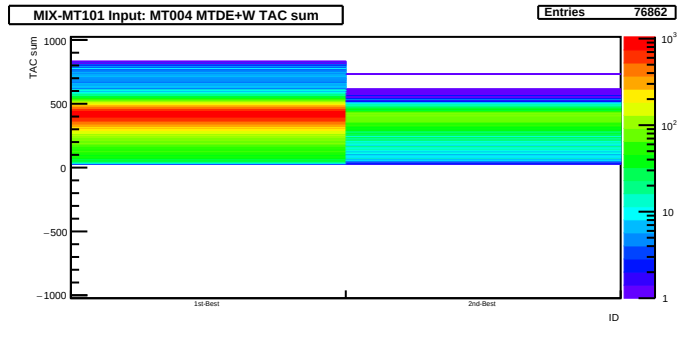

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 1200000
TAC sum
1st-Best
2nd-Best
ID

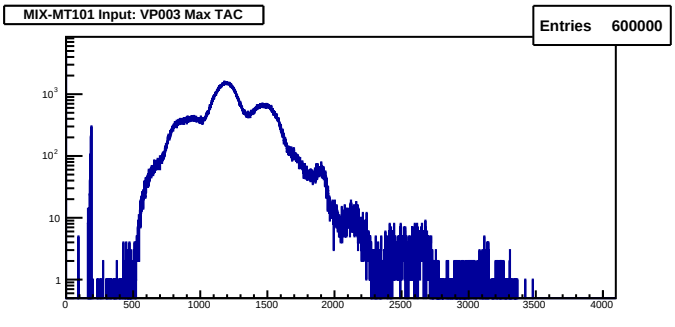

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 81538
TAC sum
1st-Best
2nd-Best
ID

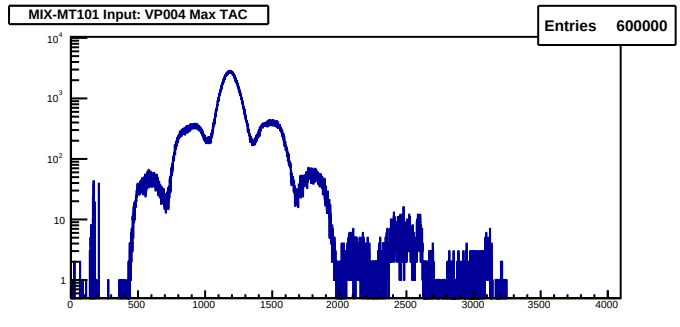

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 1200000
TAC sum
1st-Best
2nd-Best
ID

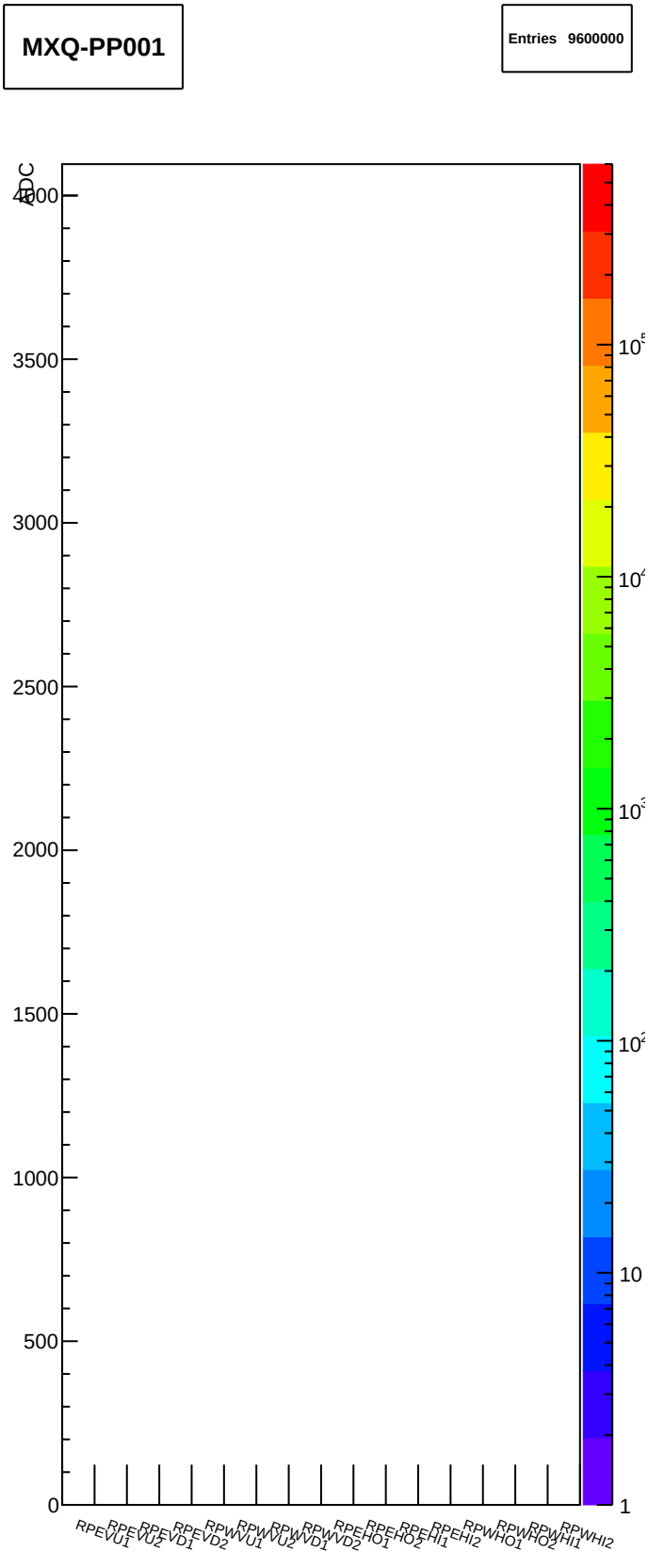

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 59242
TAC sum
1st-Best
2nd-Best
ID

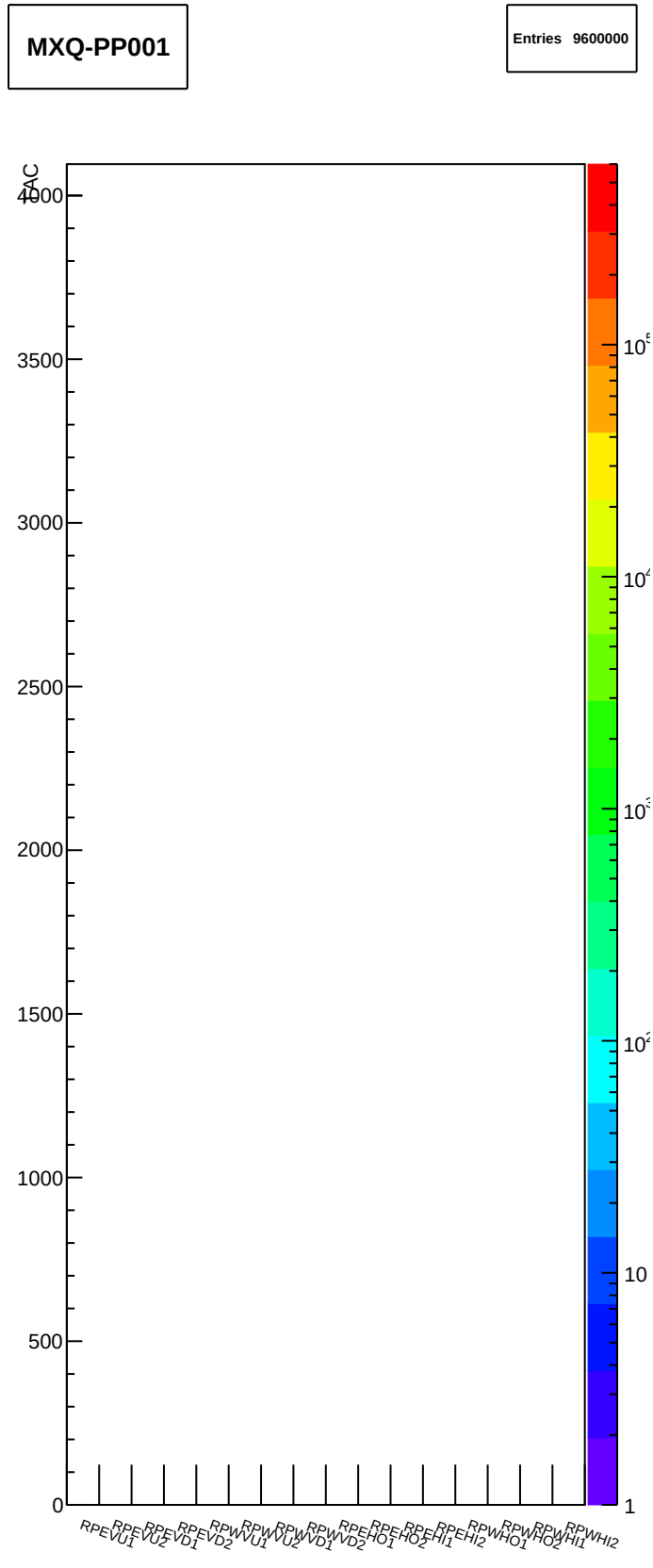

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 1200000
TAC sum
1st-Best
2nd-Best
ID

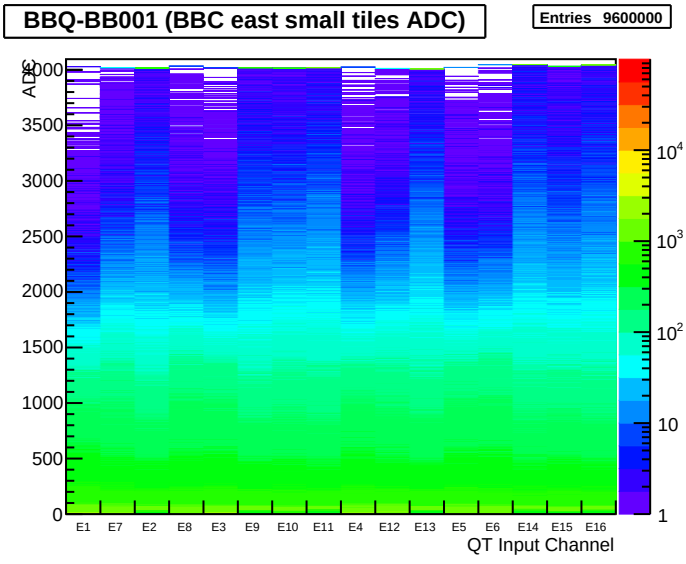

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 76862
TAC sum
1st-Best
2nd-Best
ID

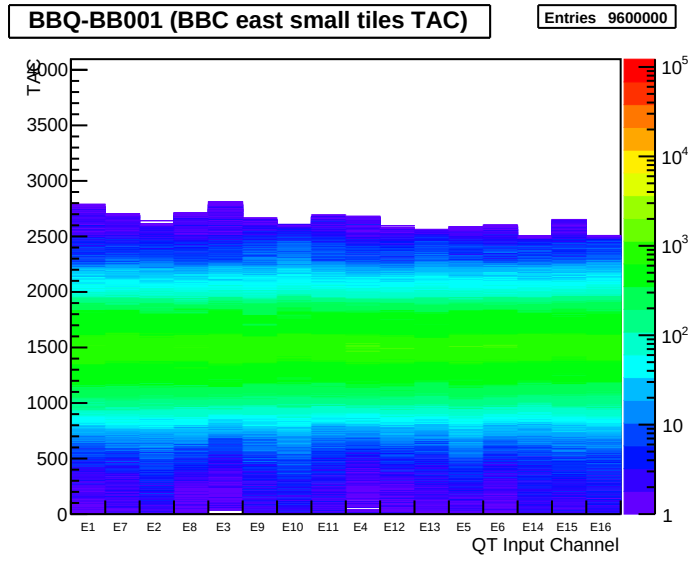

MIX-MT101 Input: VP003 Max TAC
Entries 600000

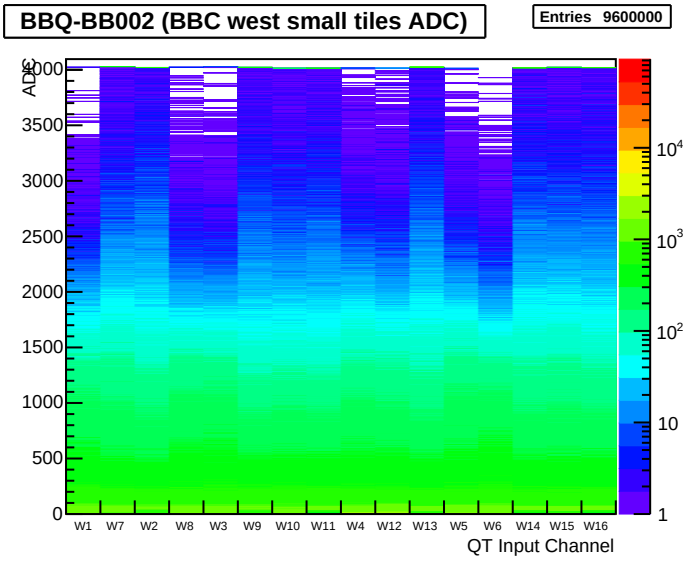

MIX-MT101 Input: VP004 Max TAC
Entries 600000

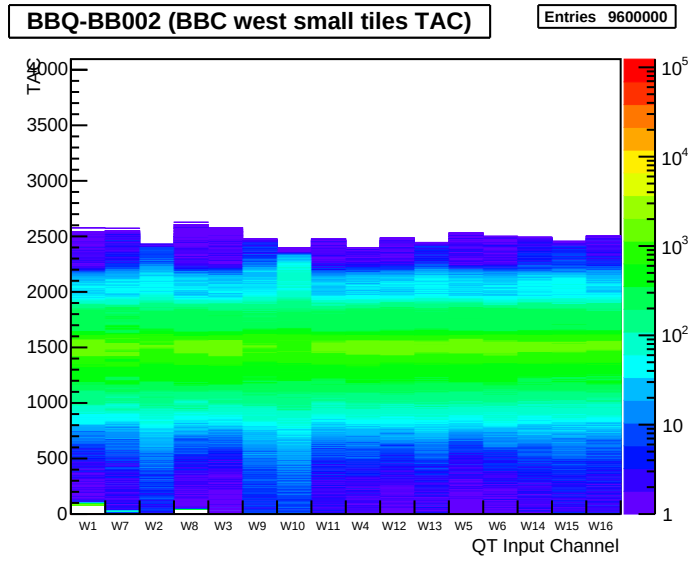

MXQ-PP001
Entries 9600000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

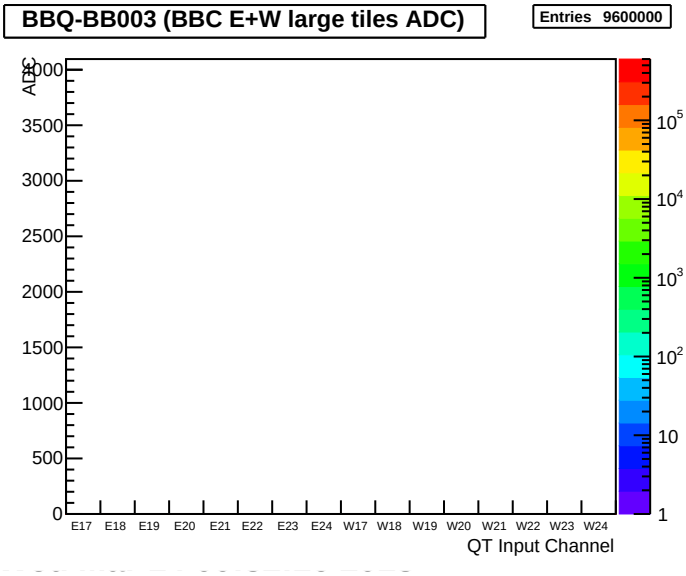

MXQ-PP001
Entries 9600000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

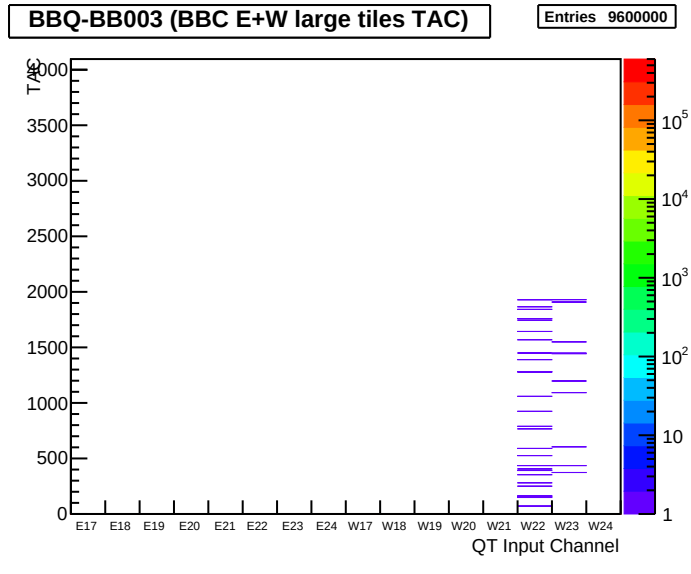

BBQ-BB001 (BBC east small tiles ADC)
Entries 9600000
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

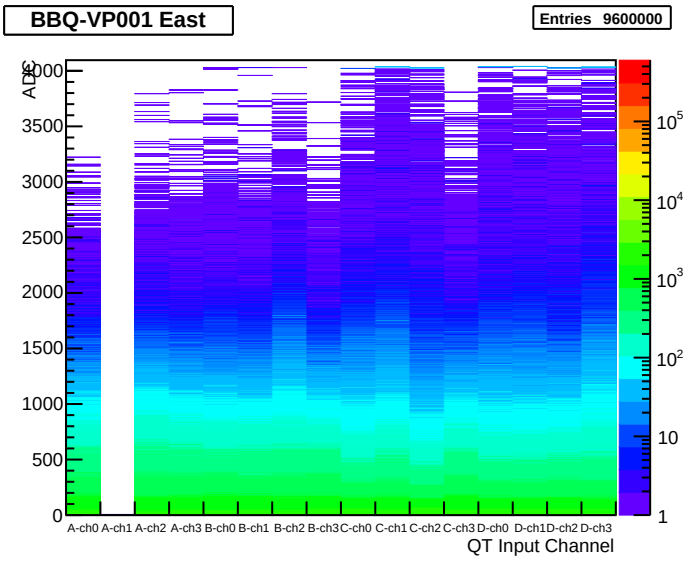

BBQ-BB001 (BBC east small tiles TAC)
Entries 9600000
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

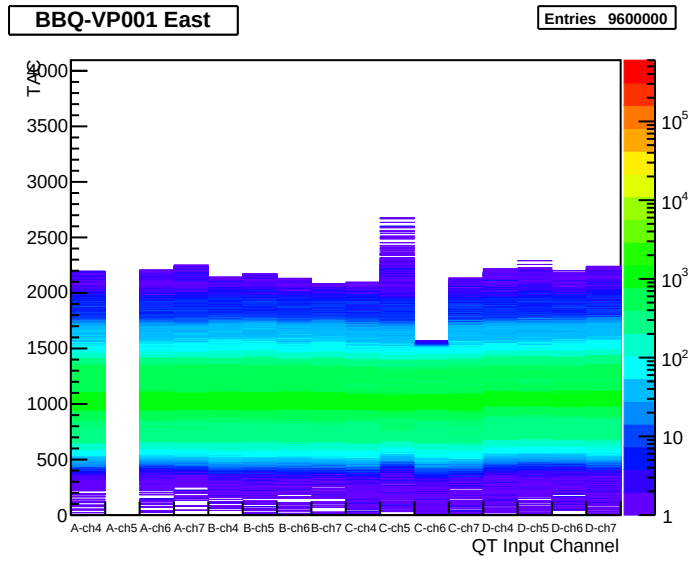

BBQ-BB002 (BBC west small tiles ADC)
Entries 9600000
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

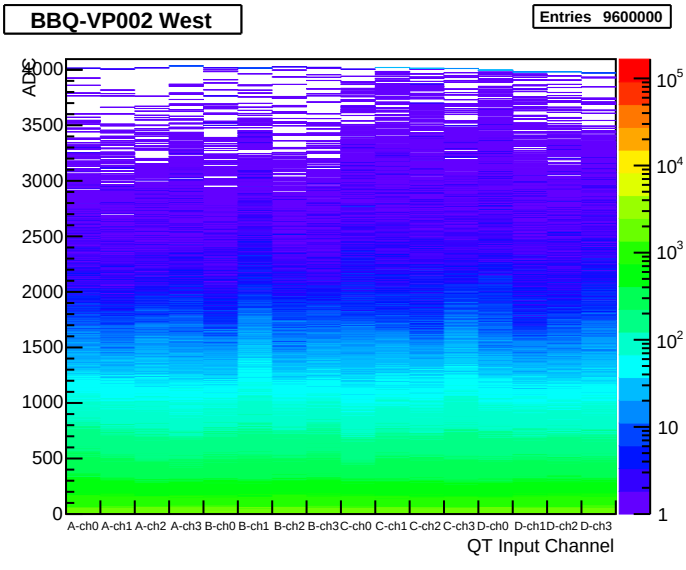

BBQ-BB002 (BBC west small tiles TAC)
Entries 9600000
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

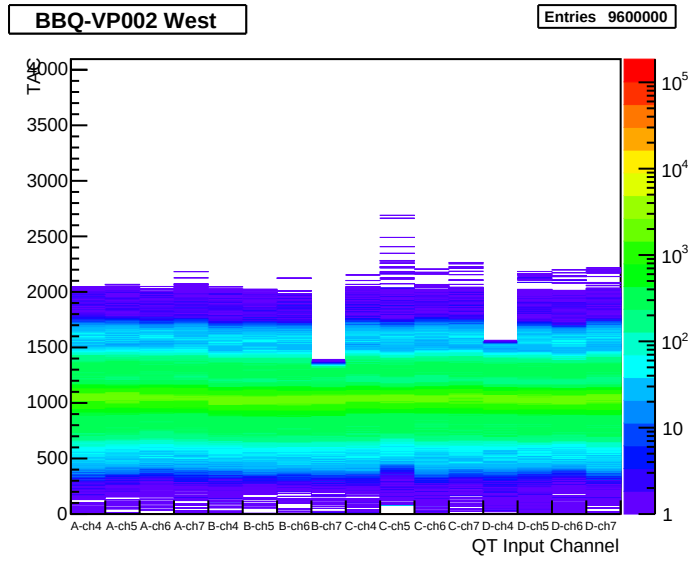

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9600000
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

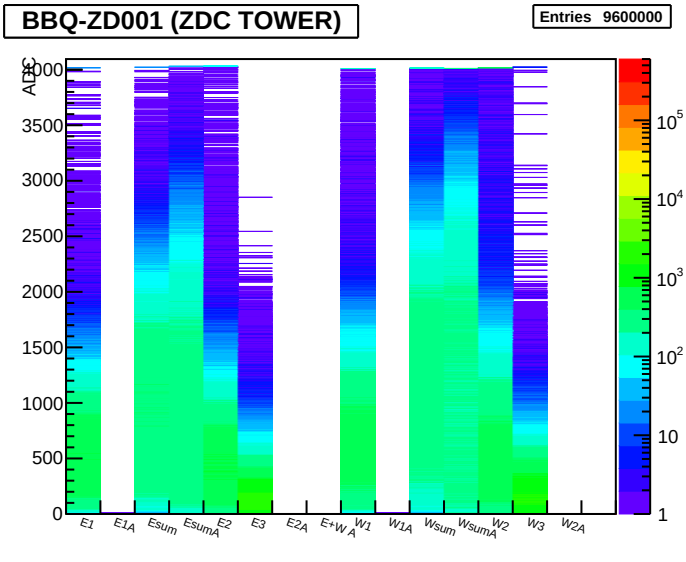

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9600000
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

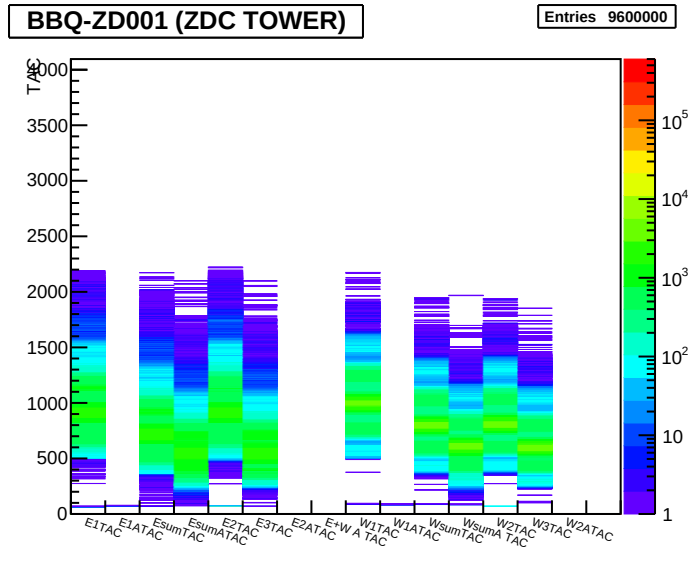

BBQ-VP001 East
Entries 9600000
ADC
QT Input Channel

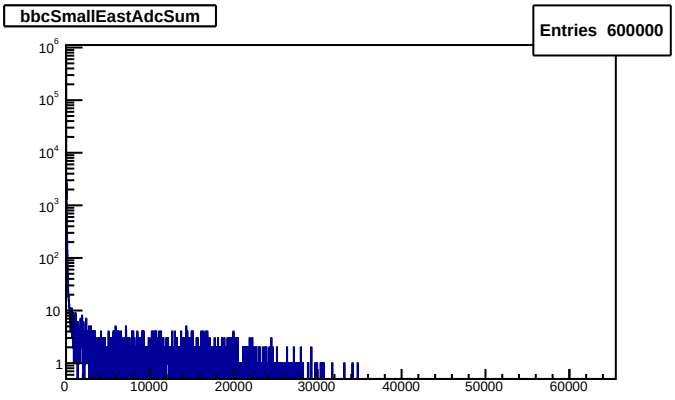

BBQ-VP001 East
Entries 9600000
TAC
QT Input Channel

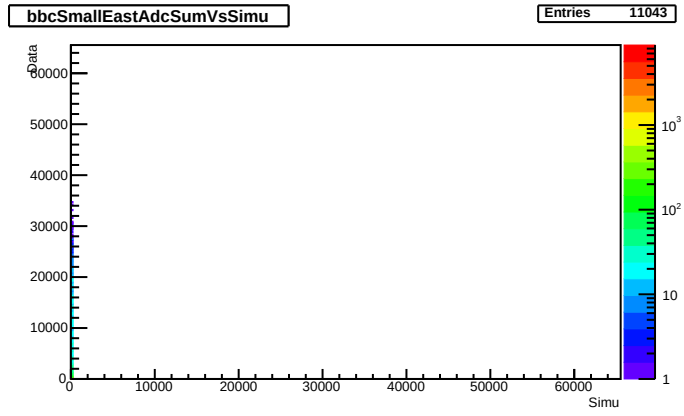

BBQ-VP002 West
Entries 9600000
ADC
QT Input Channel

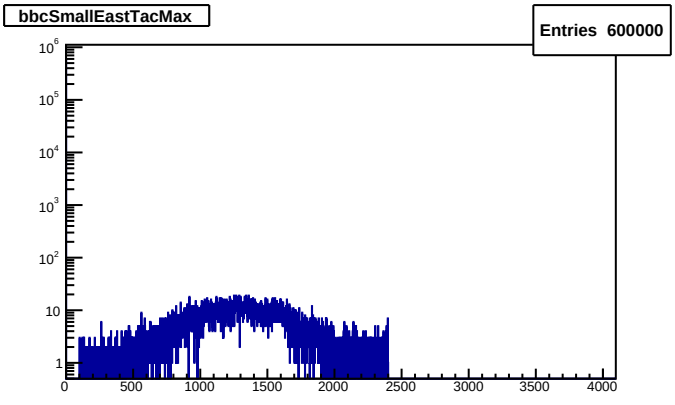

BBQ-VP002 West
Entries 9600000
TAC
QT Input Channel

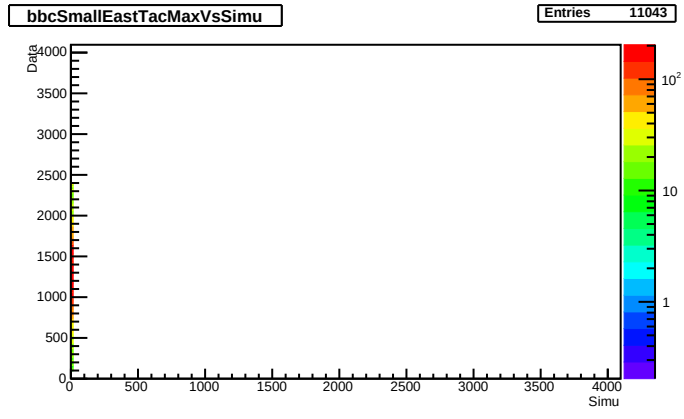

BBQ-ZD001 (ZDC TOWER)
Entries 9600000
ADC

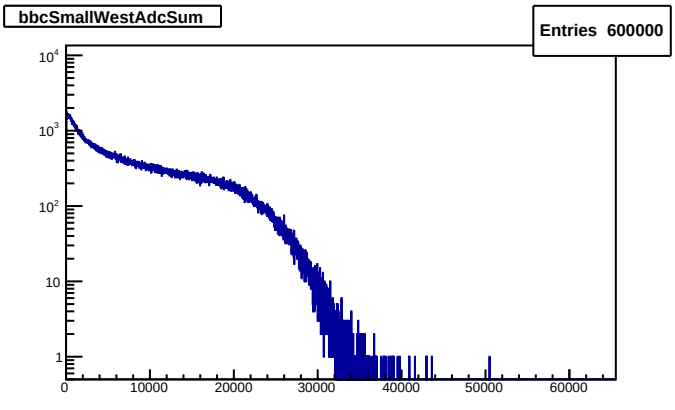

BBQ-ZD001 (ZDC TOWER)
Entries 9600000
TAC

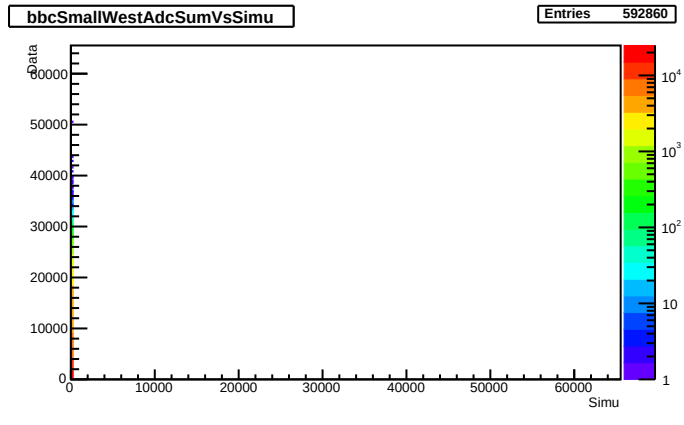

bbcSmallEastAdcSum
Entries 600000

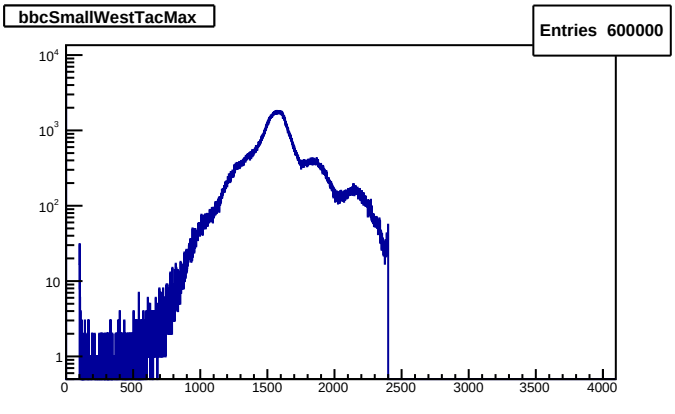

bbcSmallEastAdcSumVsSimu
Entries 11043
Data
Simu

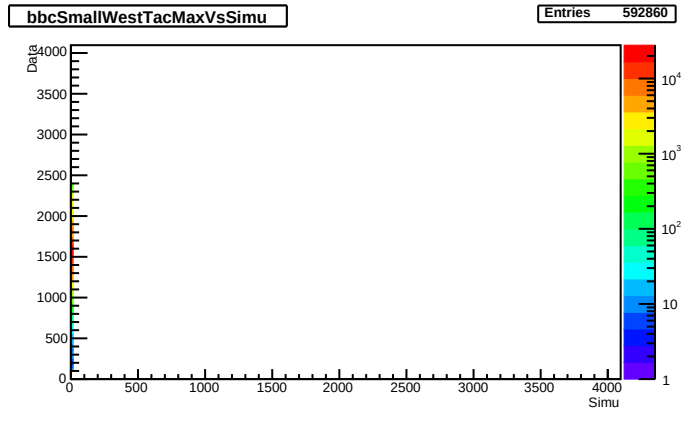

bbcSmallEastTacMax
Entries 600000

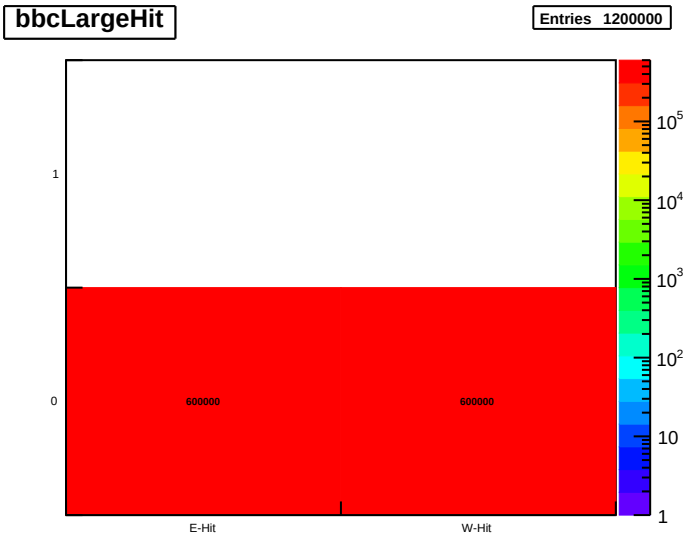

bbcSmallEastTacMaxVsSimu
Entries 11043
Data
Simu

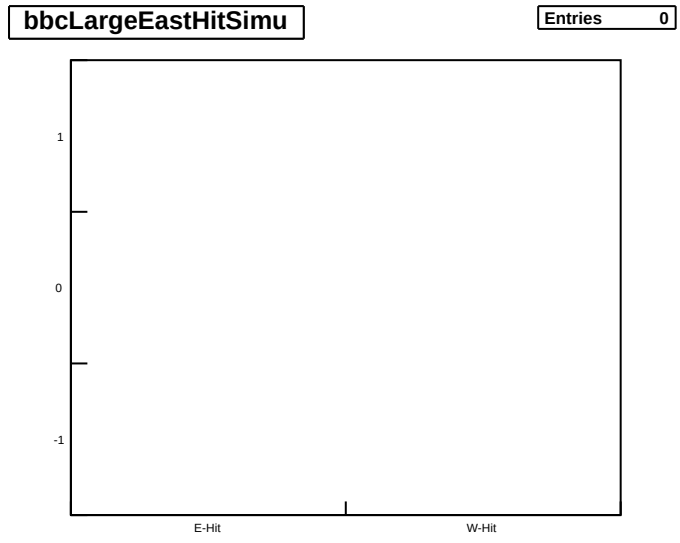

bbcSmallWestAdcSum
Entries 600000

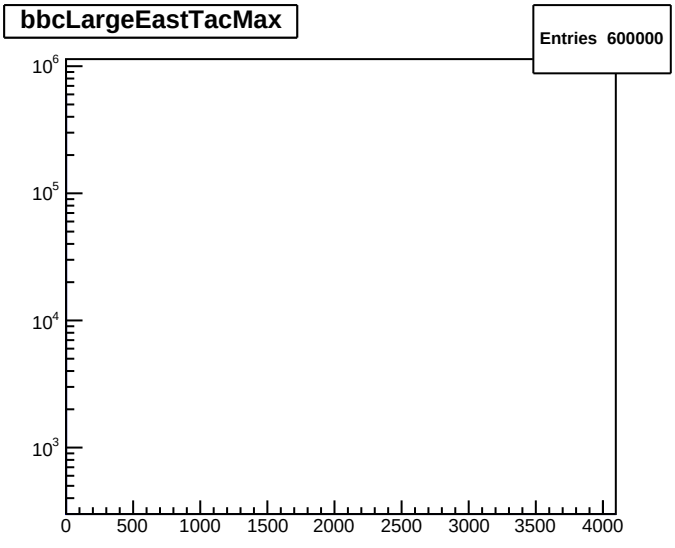

bbcSmallWestAdcSumVsSimu
Entries 592860
Data
Simu

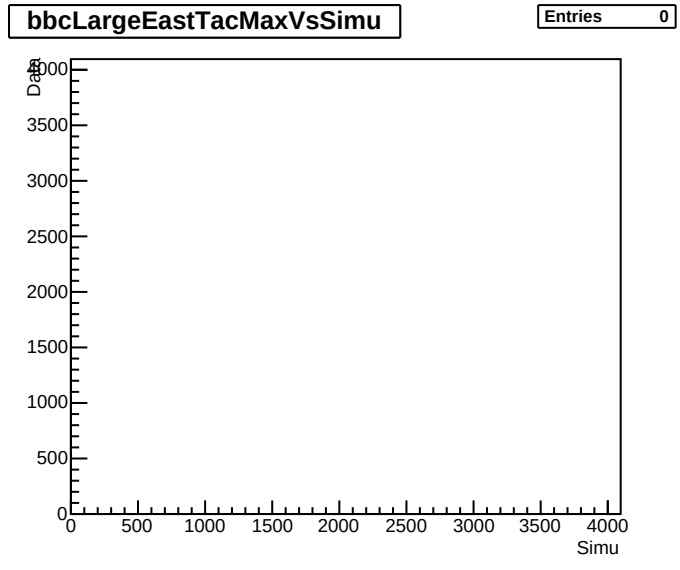

bbcSmallWestTacMax
Entries 600000

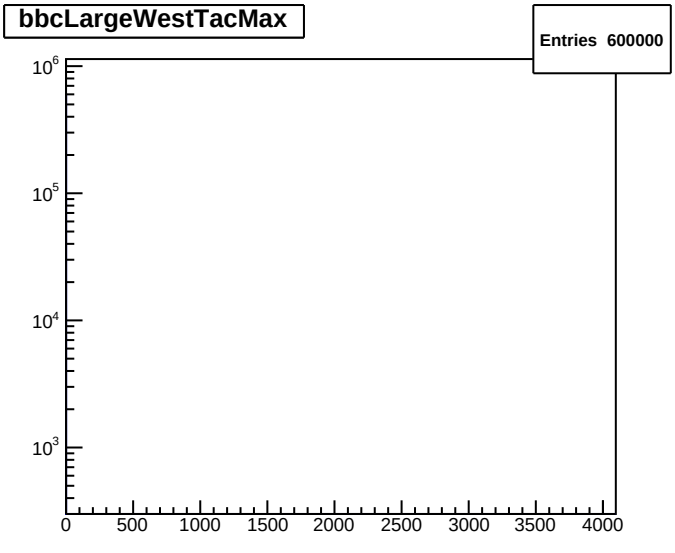

bbcSmallWestTacMaxVsSimu
Entries 592860
Data
Simu

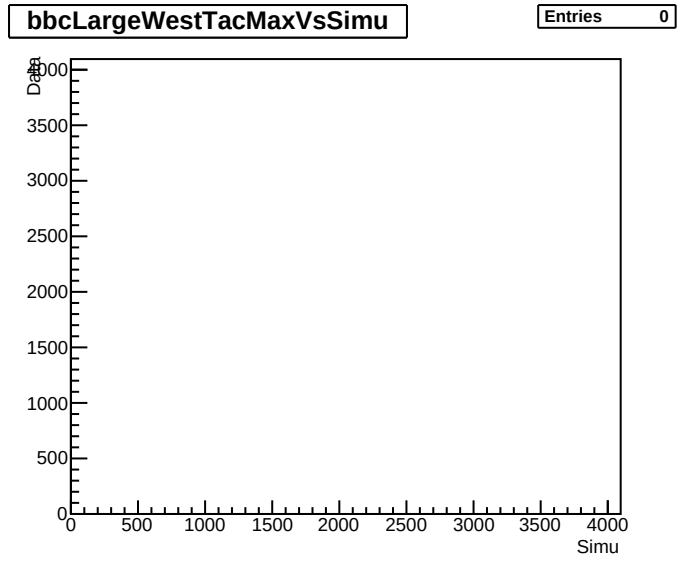

bbcLargeHit
Entries 1200000
1
0
600000
600000
E-Hit
W-Hit
10^5
10^4
10^3
10^2
10
1

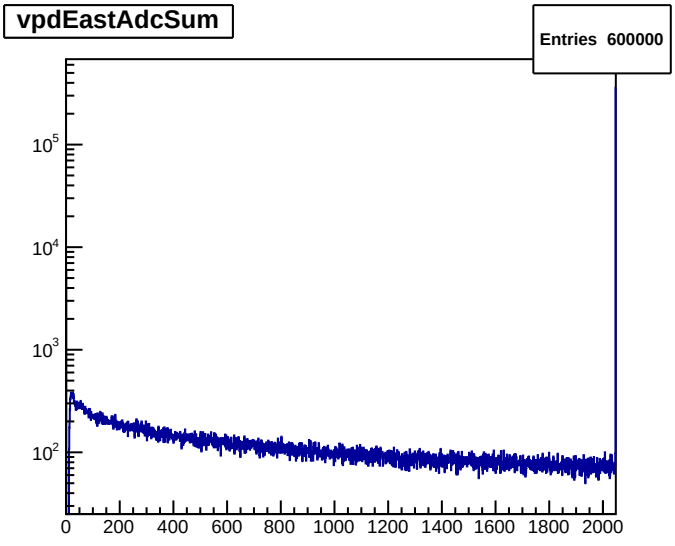

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

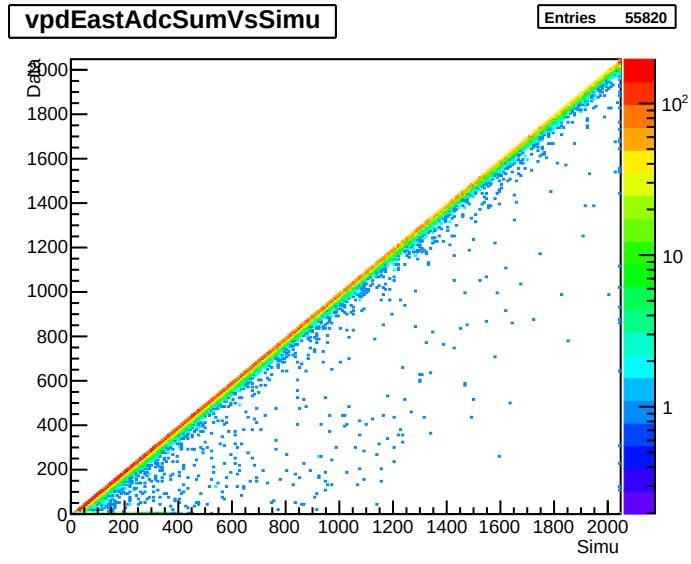

bbcLargeEastTacMax
Entries 600000
10^6
10^5
10^4
10^3
0
500
1000
1500
2000
2500
3000
3500
4000

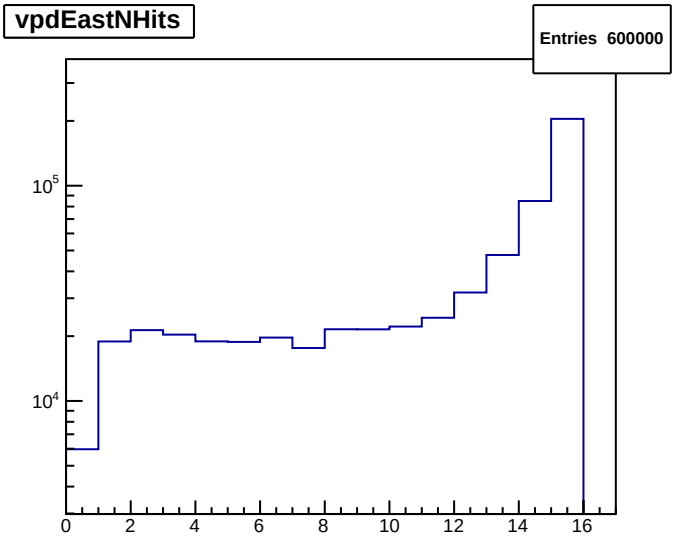

bbcLargeEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

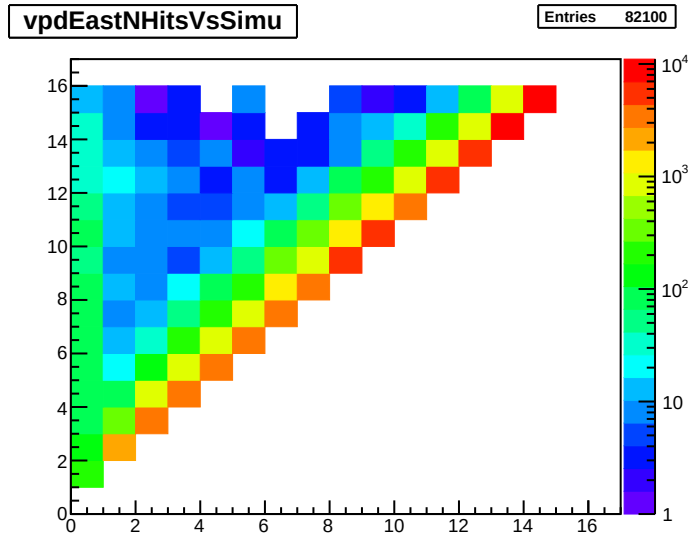

bbcLargeWestTacMax
Entries 600000
10^6
10^5
10^4
10^3
0
500
1000
1500
2000
2500
3000
3500
4000

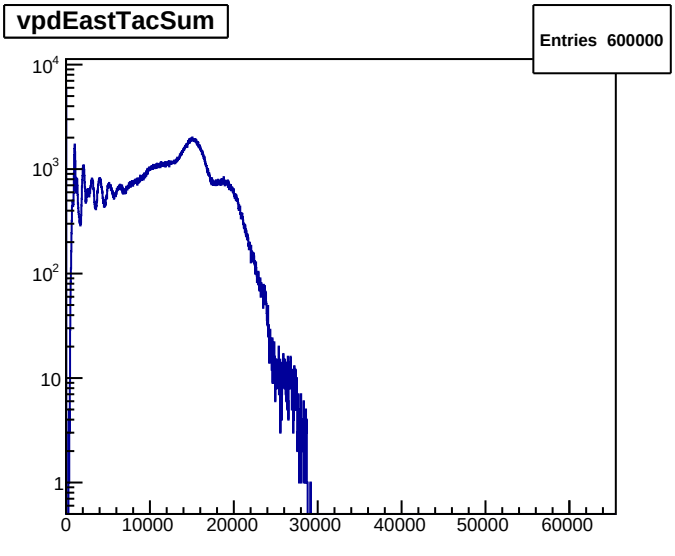

bbcLargeWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

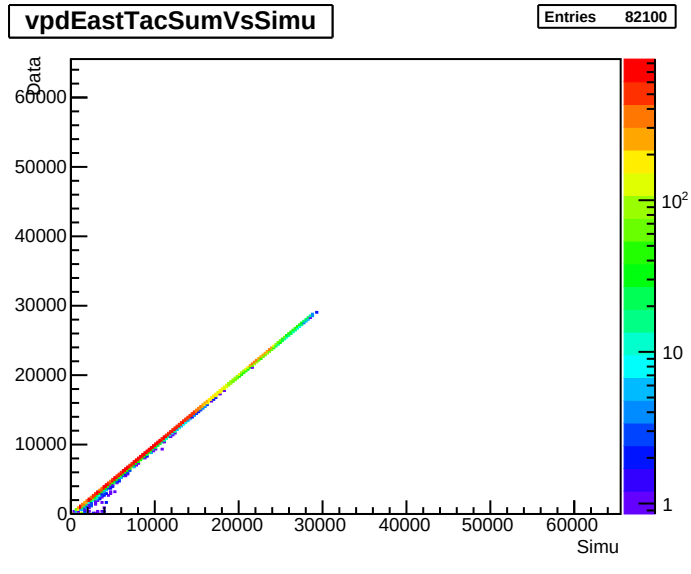

vpdEastAdcSum
Entries 600000

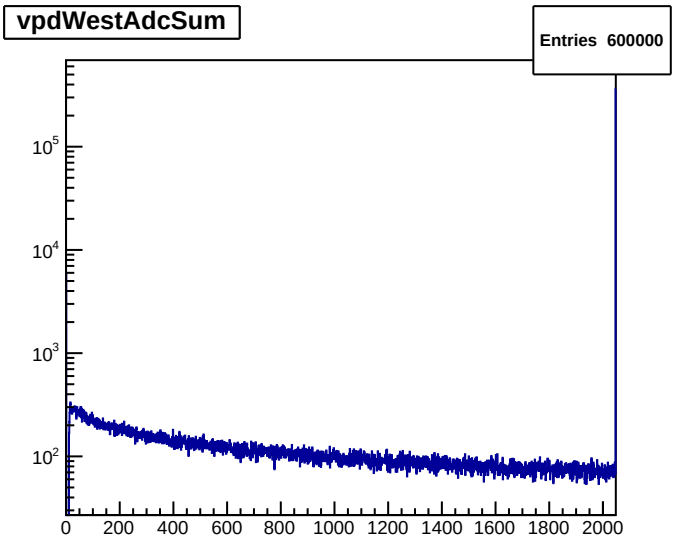

vpdEastAdcSumVsSimu
Entries 55820
Data
Simu

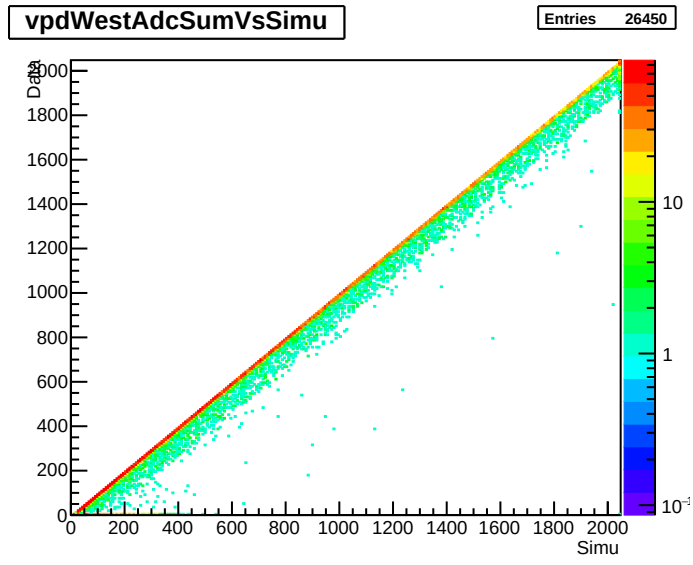

vpdEastNHits
Entries 600000

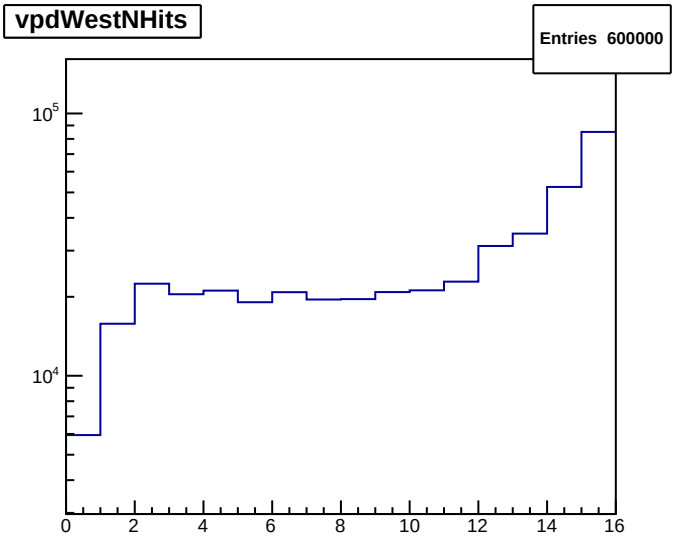

vpdEastNHitsVsSimu
Entries 82100

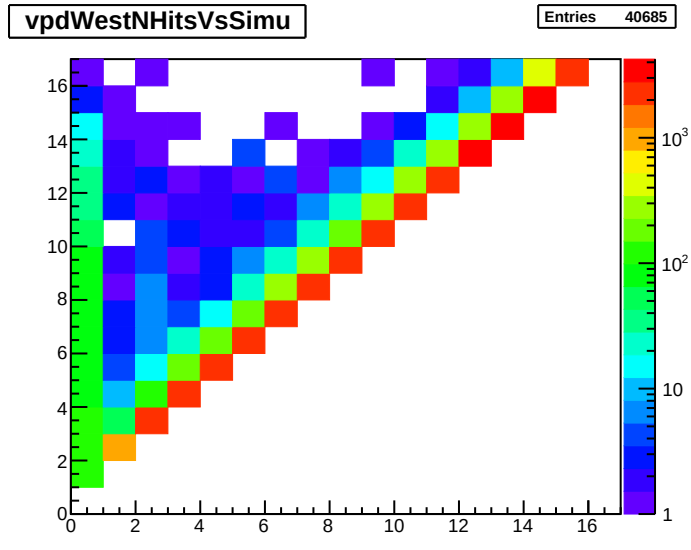

vpdEastTacSum
Entries 600000

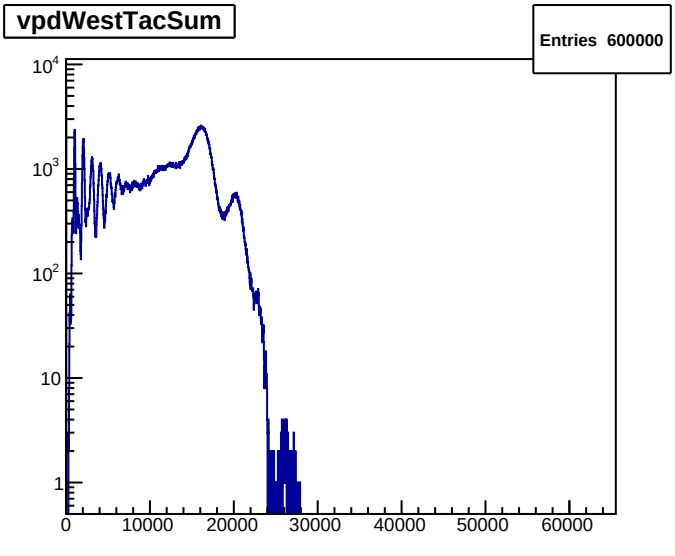

vpdEastTacSumVsSimu
Entries 82100
Data
Simu

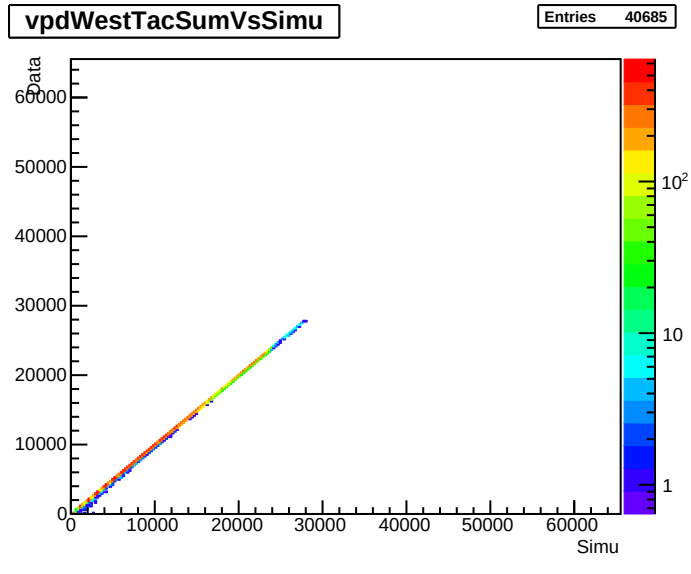

vpdWestAdcSum
Entries 600000

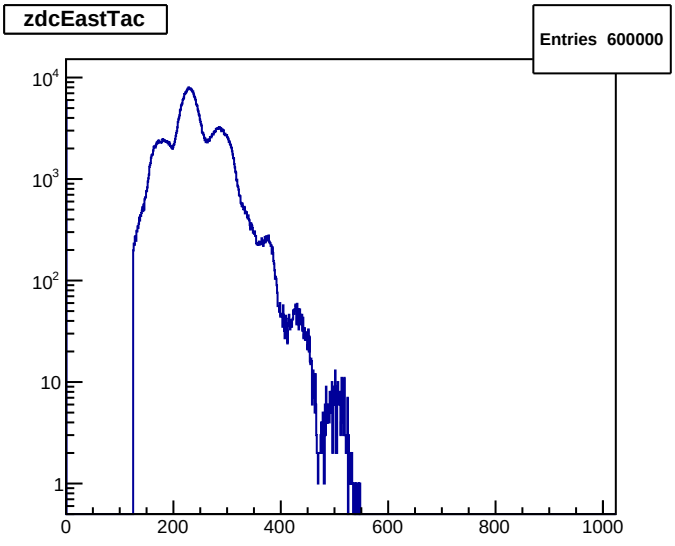

vpdWestAdcSumVsSimu
Entries 26450
Data
Simu

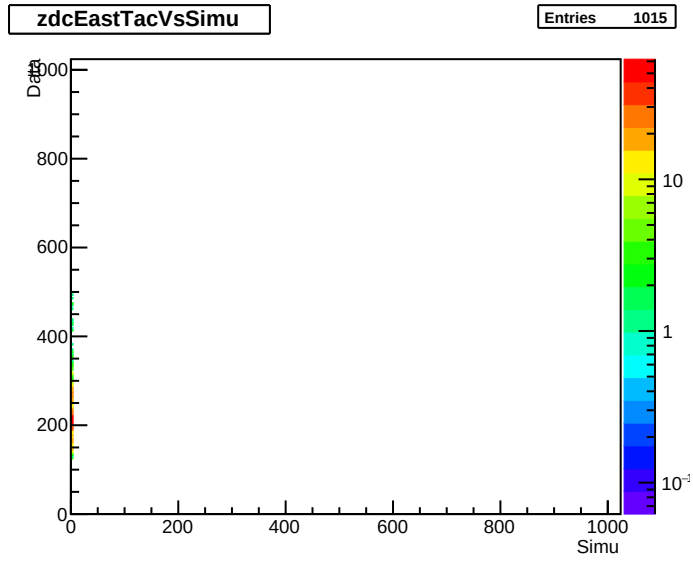

vpdWestNHits
Entries 600000

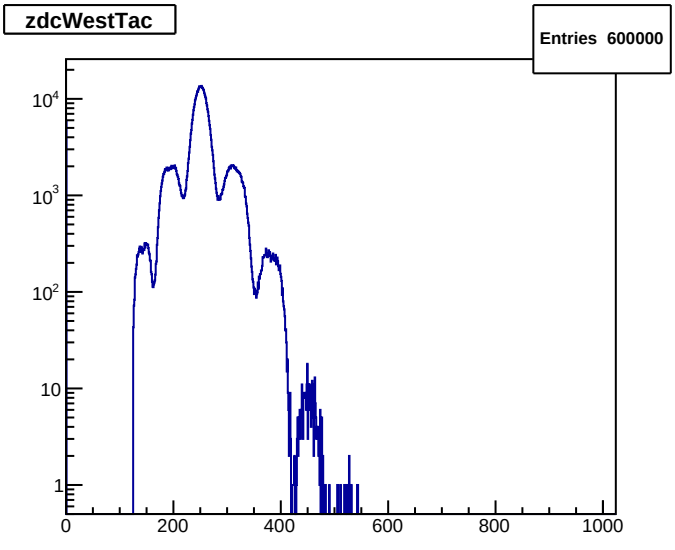

vpdWestNHitsVsSimu
Entries 40685

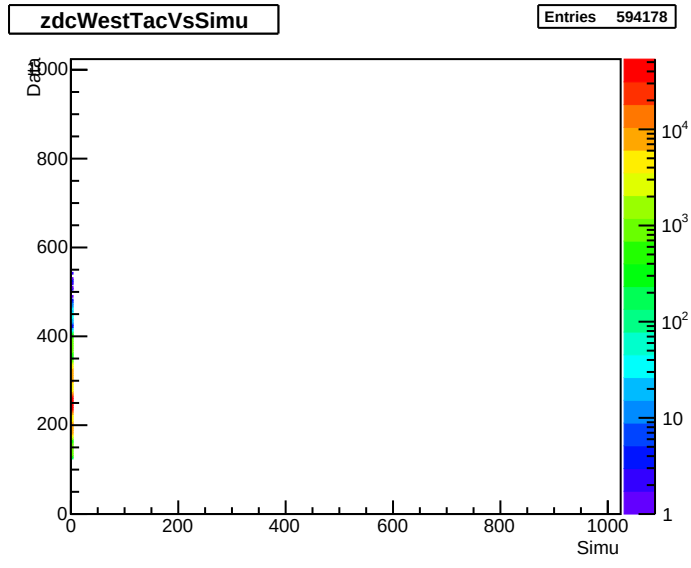

vpdWestTacSum
Entries 600000

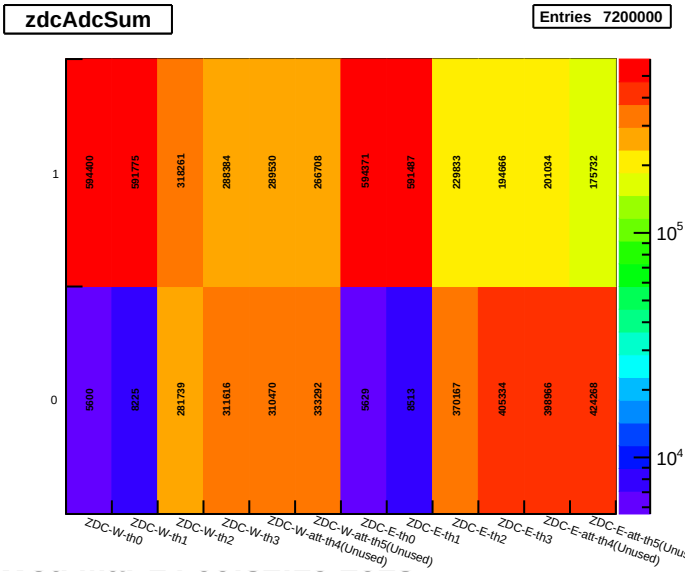

vpdWestTacSumVsSimu
Entries 40685
Data
Simu

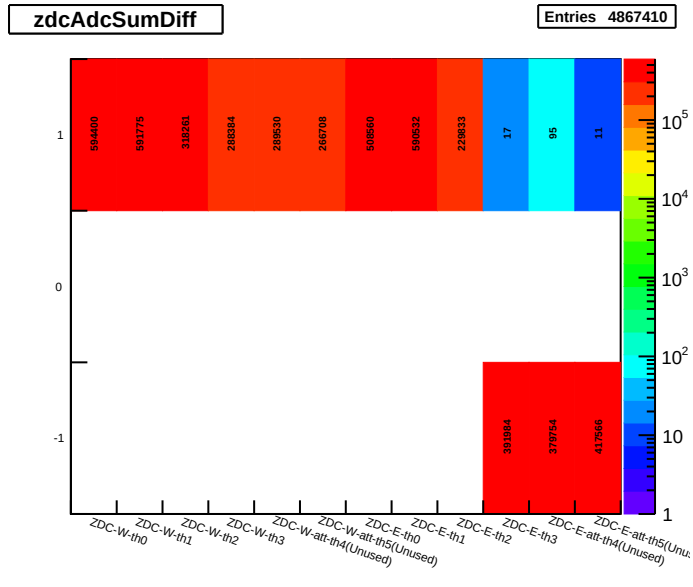

zdcEastTac
Entries 600000

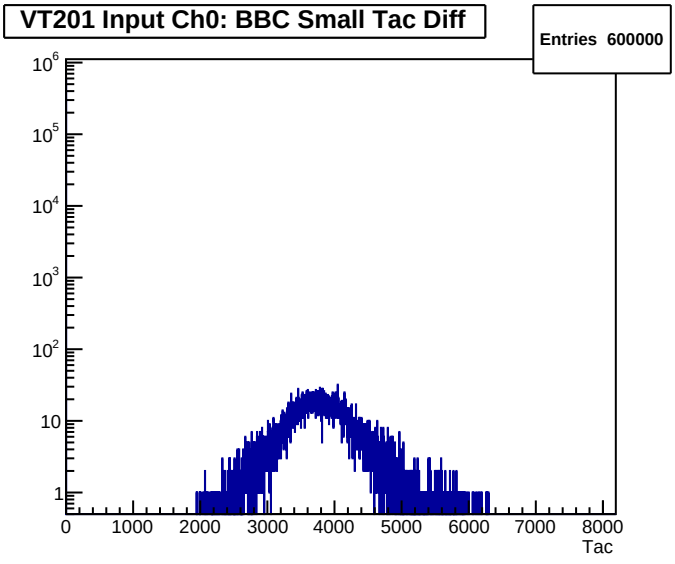

zdcEastTacVsSimu
Entries 1015
Data
Simu

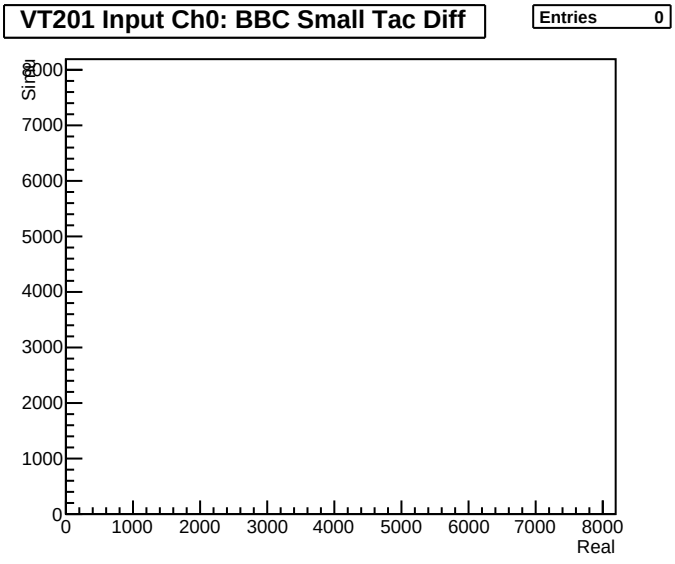

zdcWestTac
Entries 600000

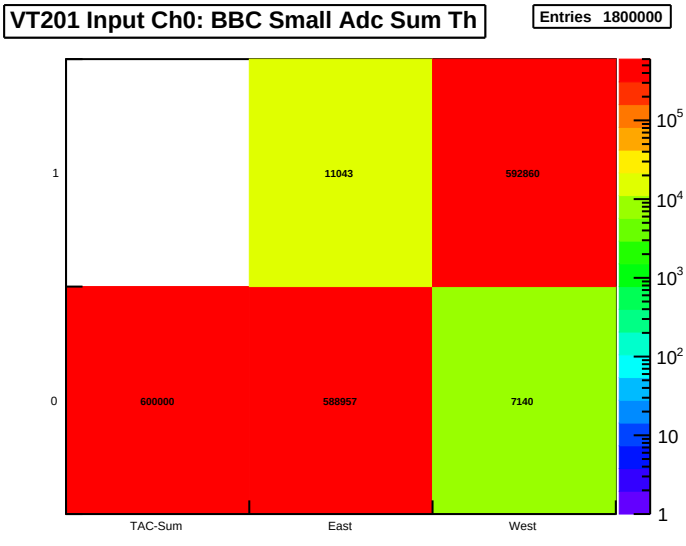

zdcWestTacVsSimu
Entries 594178
Data
Simu

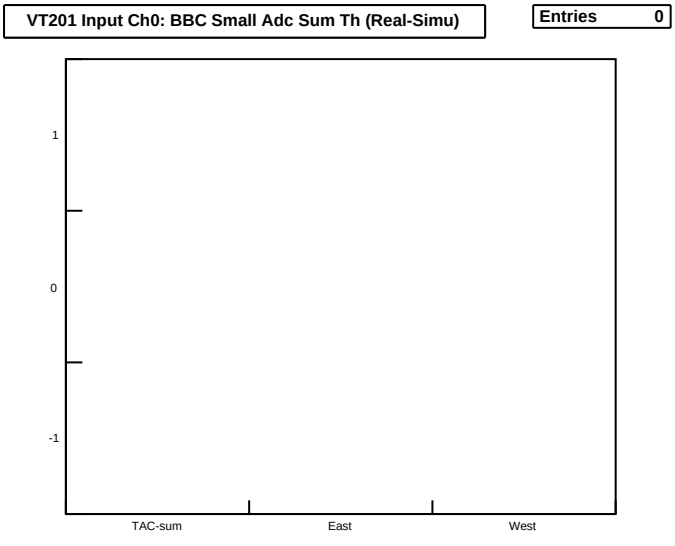

zdcAdcSum
Entries 7200000
594400
591775
318261
288384
289530
266708
594371
591487
229833
194666
201034
175732
5600
6225
281739
311616
310470
333292
5629
8513
370167
405334
398966
424288
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

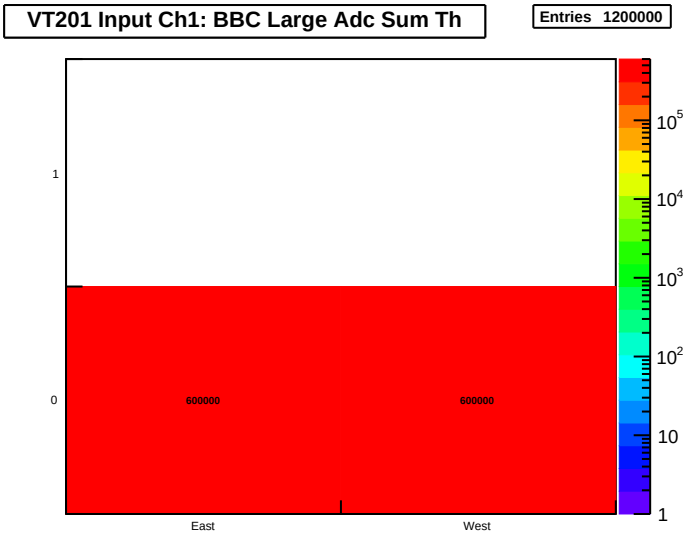

zdcAdcSumDiff
Entries 4867410
594400
591775
318261
288384
289530
266708
508560
590532
229833
17
95
11
391984
379754
417566
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

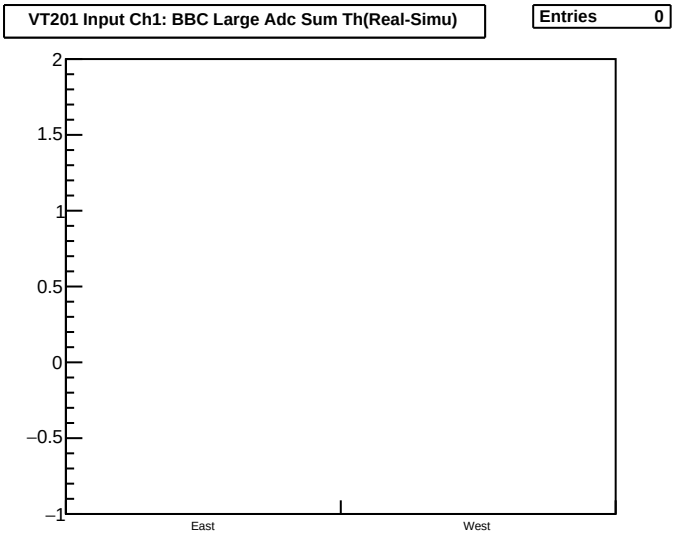

VT201 Input Ch0: BBC Small Tac Diff
Entries 600000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

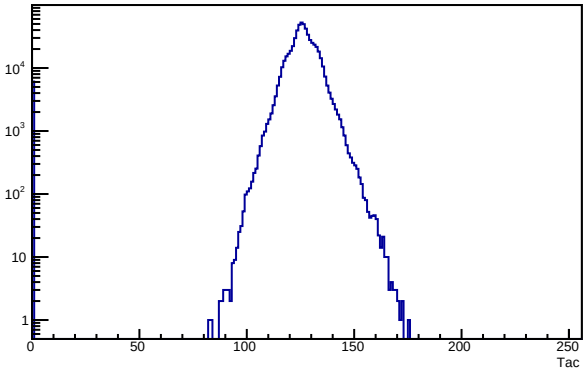

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 1800000
11043
592860
600000
588957
7140
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

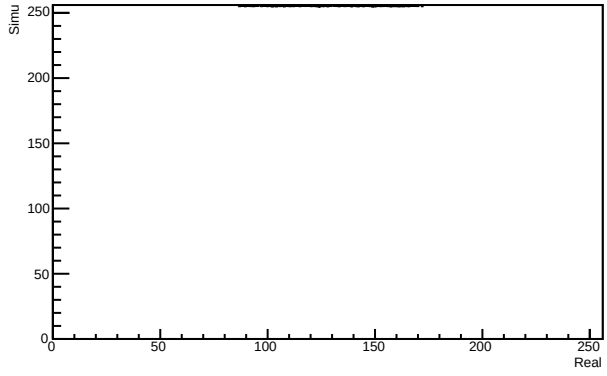

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 1200000
600000
600000
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

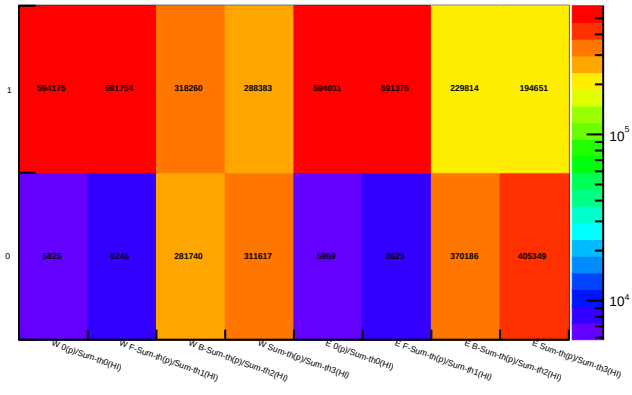
VT201 Input Ch2: ZDC Tac Diff



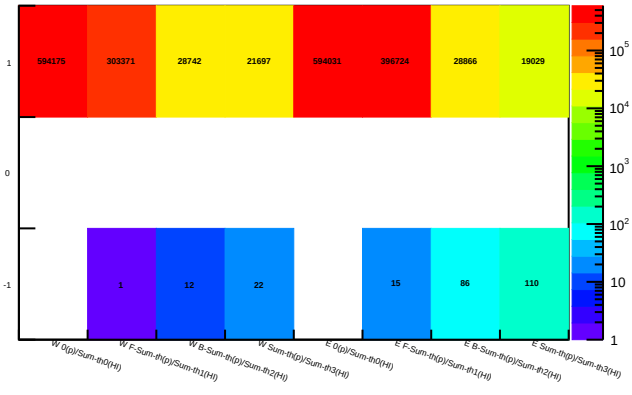
VT201 Input Ch2: ZDC Tac Diff



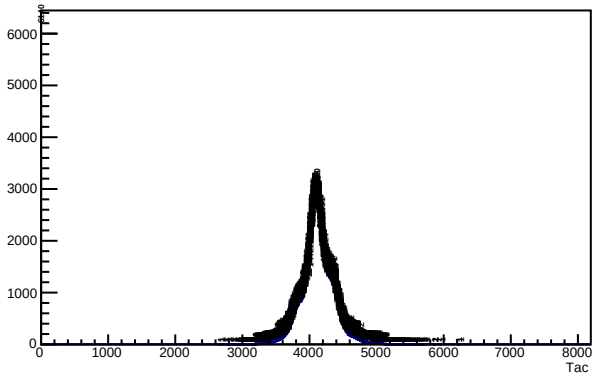
VT201 Input Ch2: ZDC Adc Sum Th



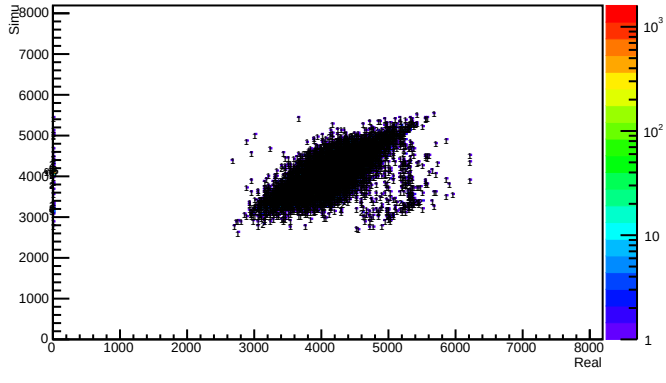
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



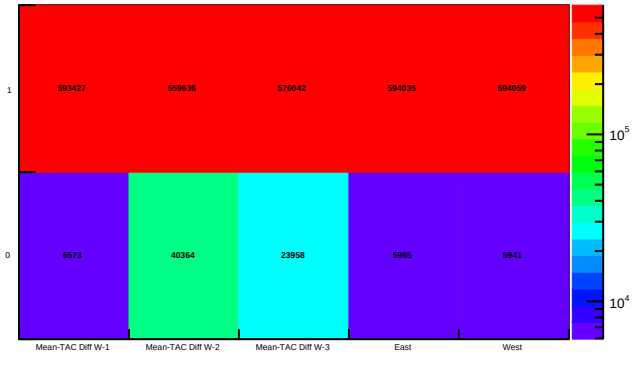
VT201 Input Ch4: VPD Tac Diff



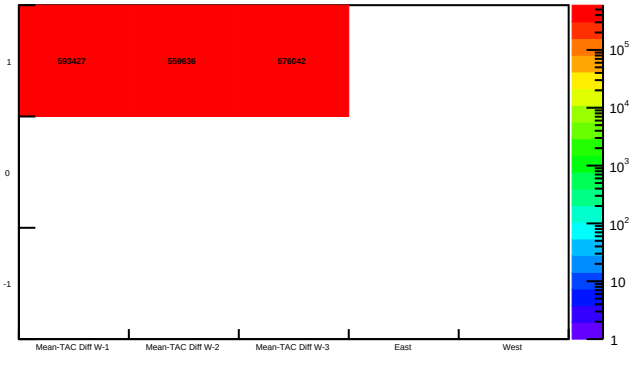
VT201 Input Ch4: VPD Tac Diff

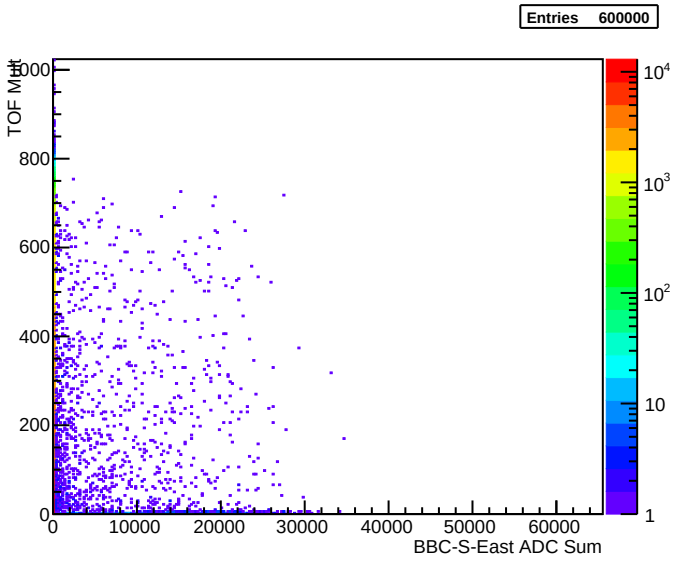


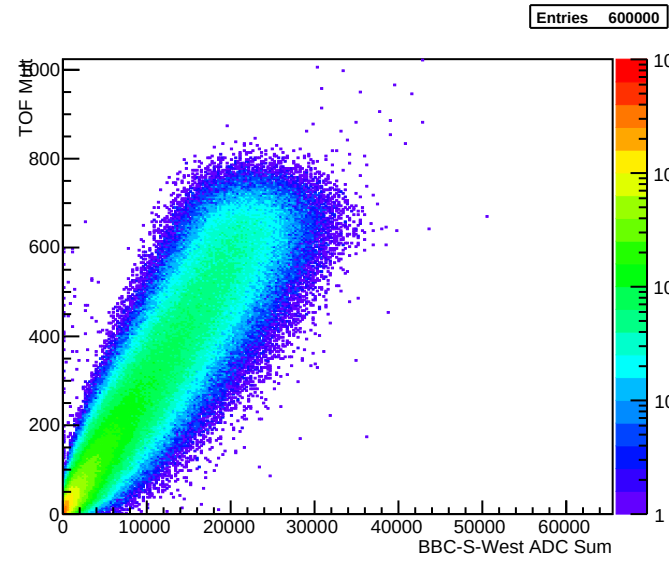
VT201 Input Ch4: VPD Adc Sum Th

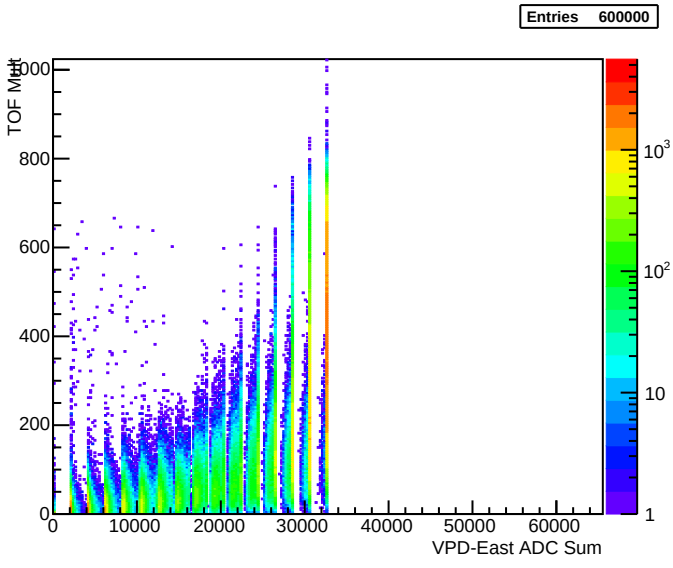


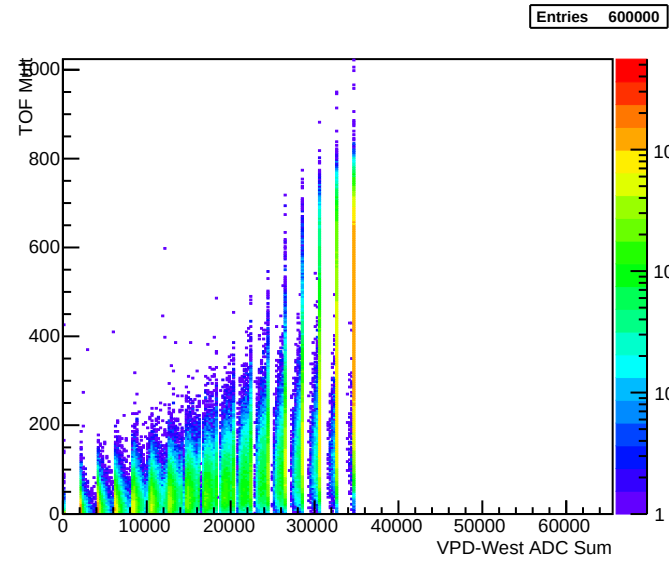
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

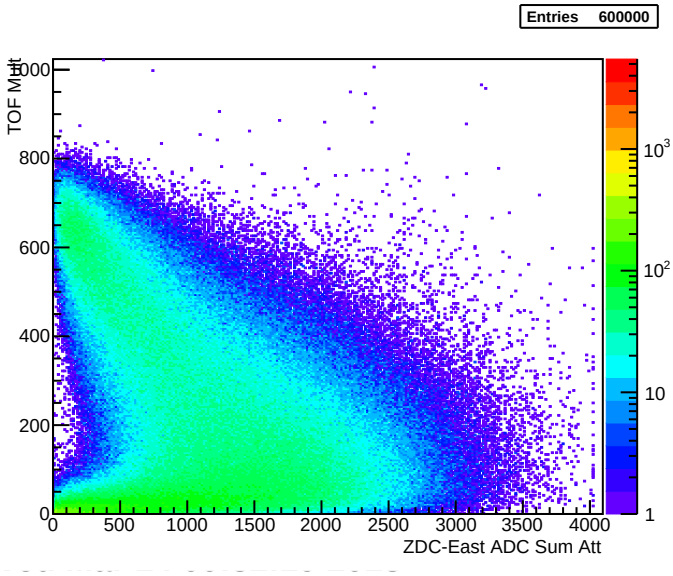


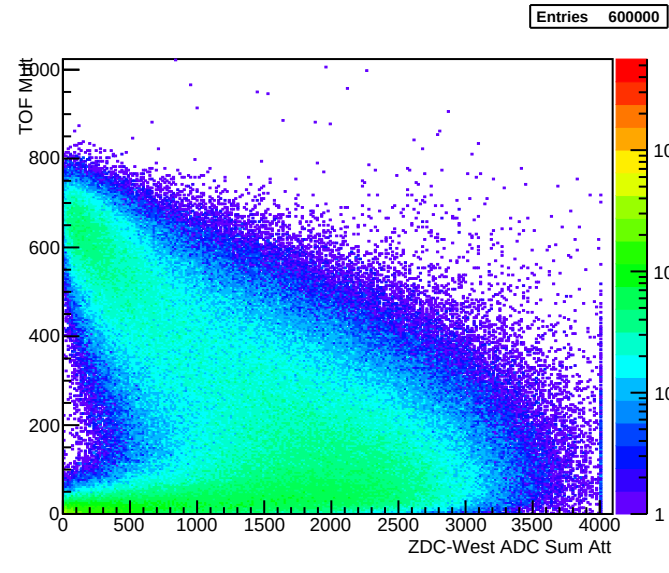

Entries 600000
TOF Mult
BBC-S-East ADC Sum

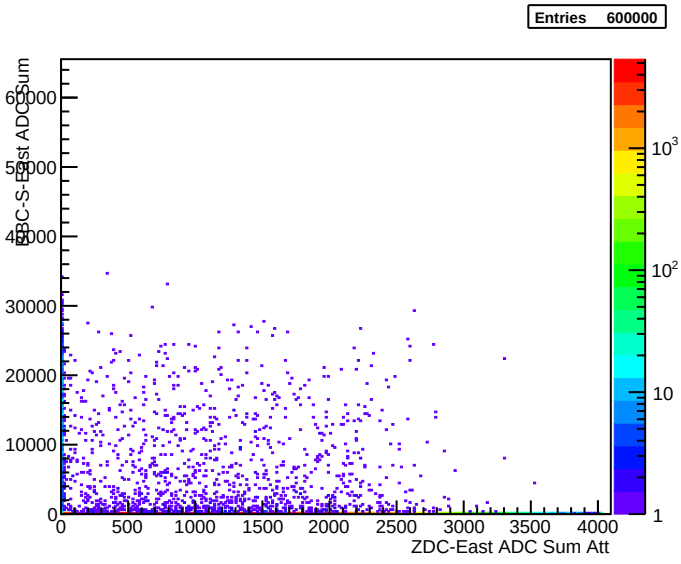

Entries 600000
TOF Mult
BBC-S-West ADC Sum

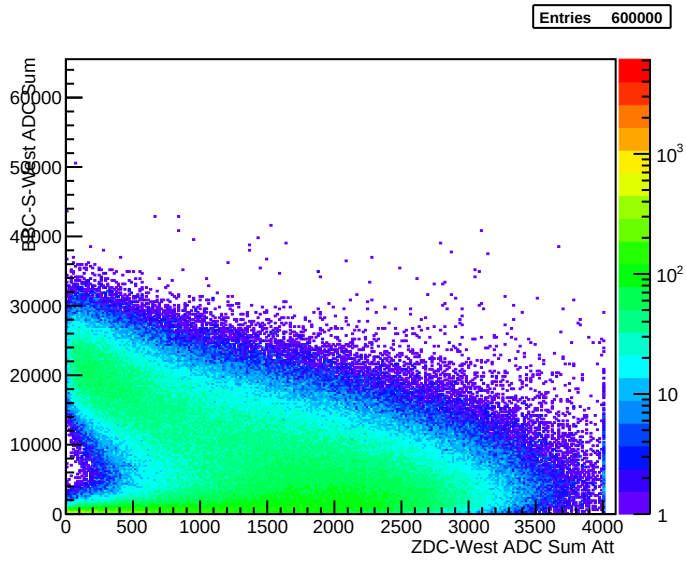

Entries 600000
TOF Mult
VPD-East ADC Sum

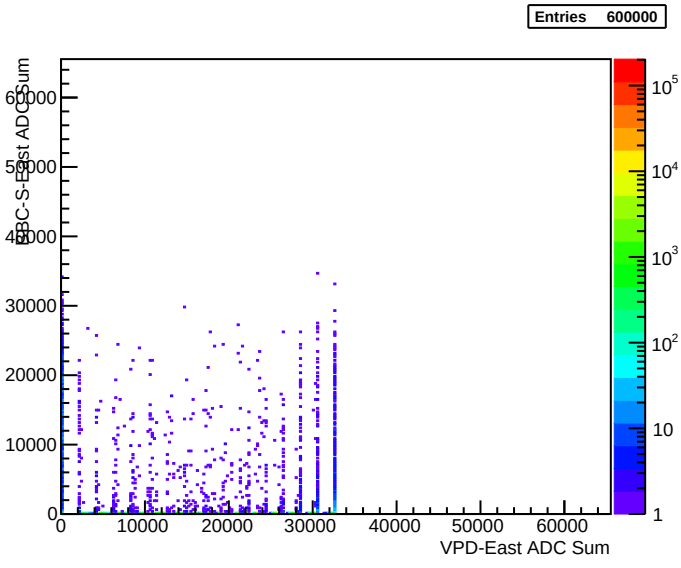

Entries 600000
TOF Mult
VPD-West ADC Sum

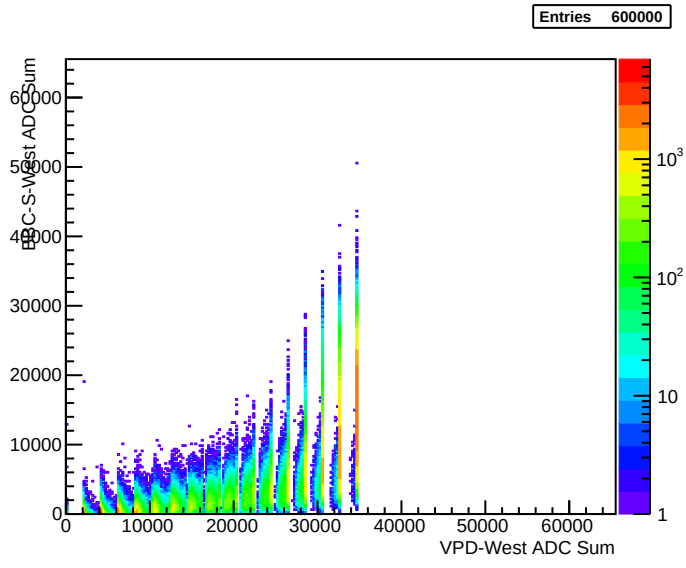

Entries 600000
TOF Mult
ZDC-East ADC Sum Att

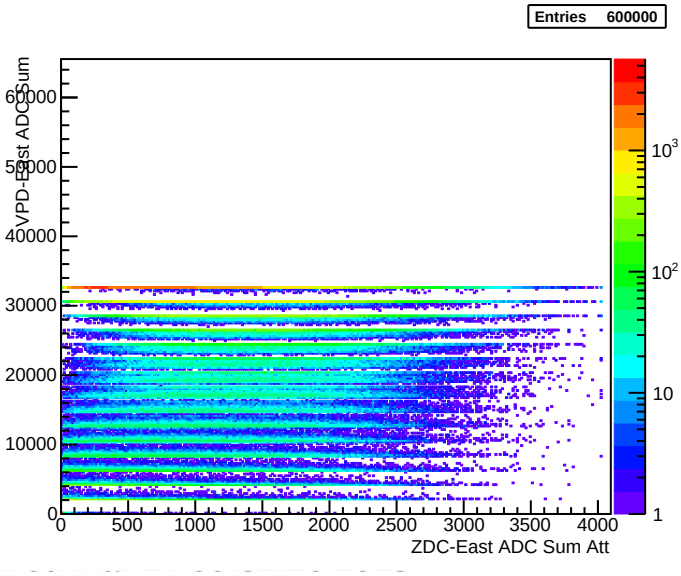

Entries 600000
TOF Mult
ZDC-West ADC Sum Att

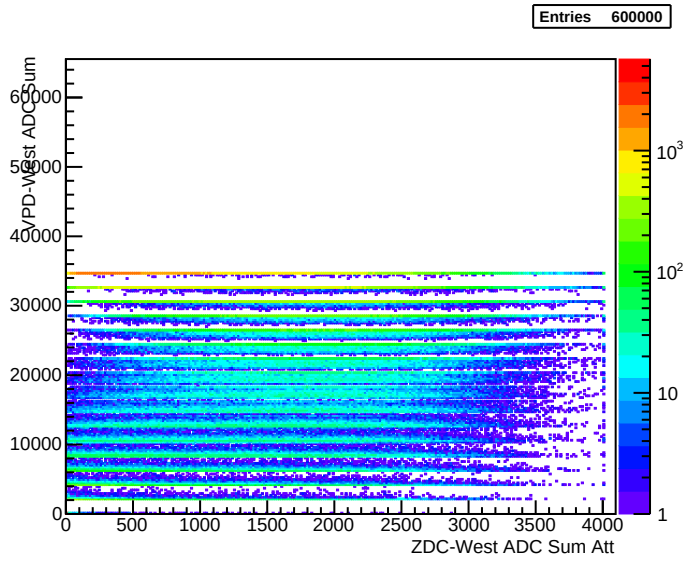

Entries 600000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

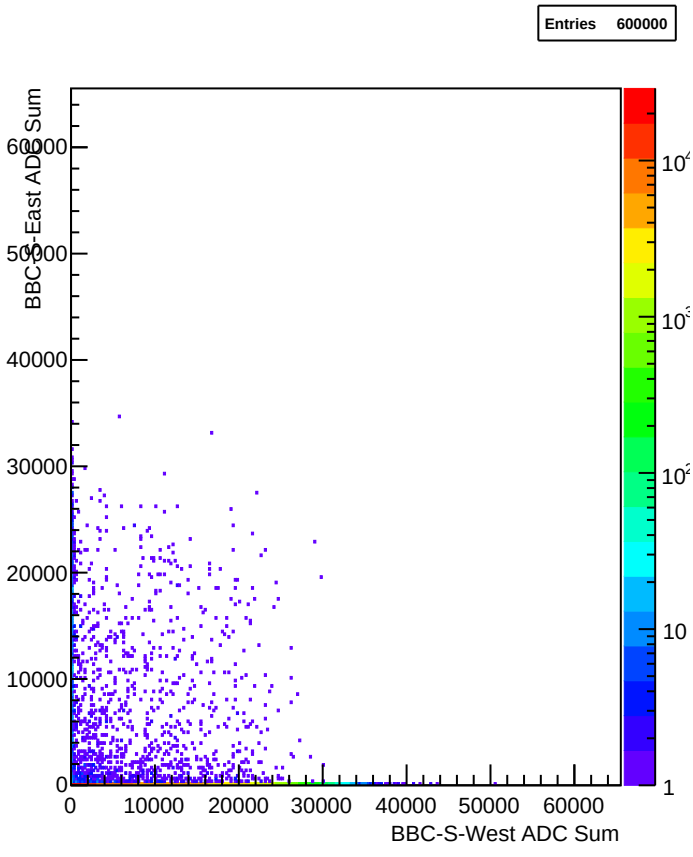

Entries 600000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

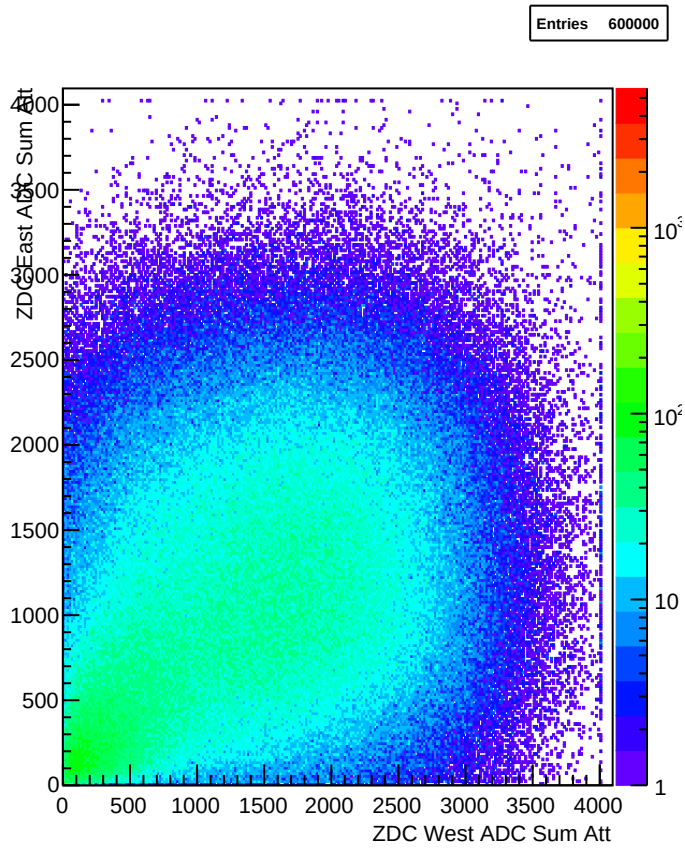

Entries 600000
BBC-S-East ADC Sum
VPD-East ADC Sum

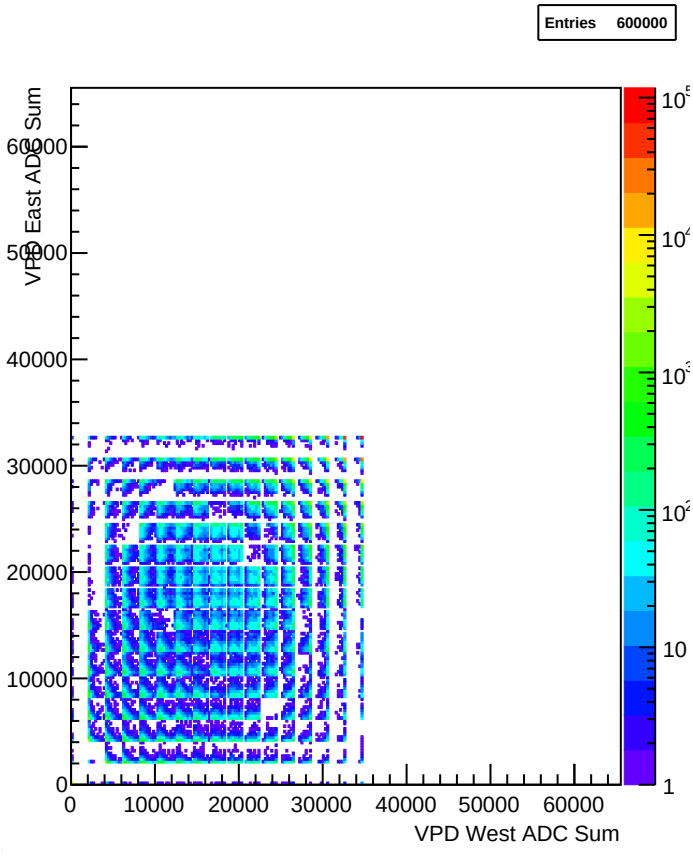

Entries 600000
BBC-S-West ADC Sum
VPD-West ADC Sum

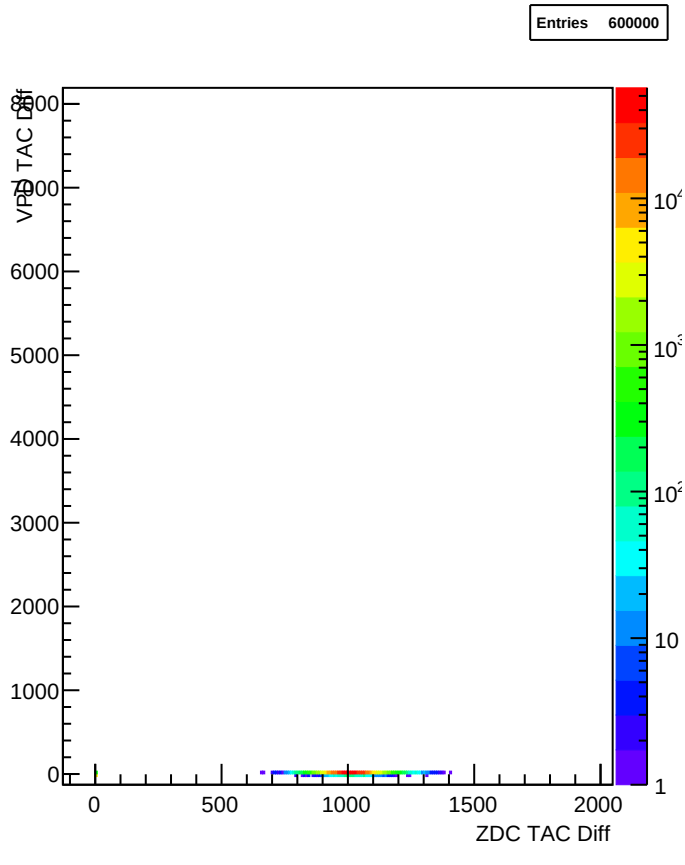

Entries 600000
VPD-East ADC Sum
ZDC-East ADC Sum Att

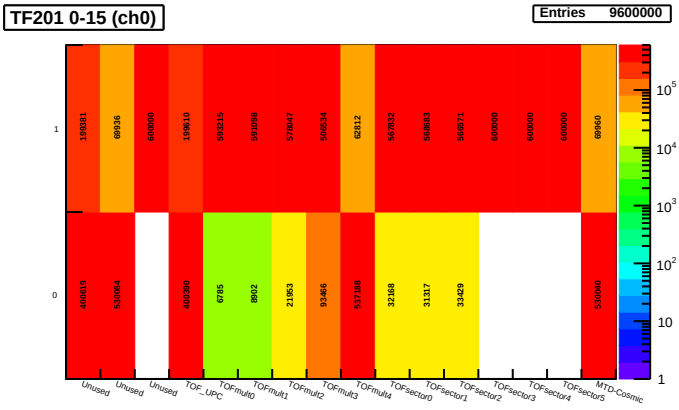

Entries 600000
VPD-West ADC Sum
ZDC-West ADC Sum Att

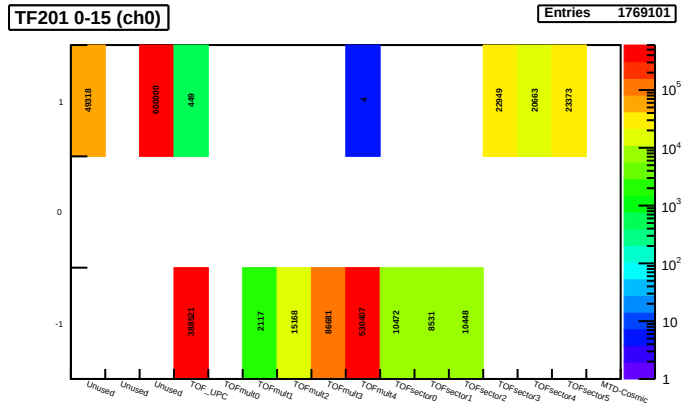

Entries 600000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

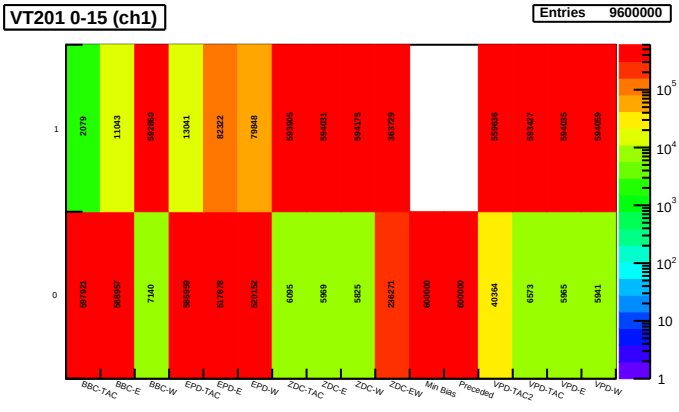

Entries 600000
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 600000
VPD East ADC Sum
VPD West ADC Sum


Entries 600000
VPD TAC Diff
ZDC TAC Diff


TF201 0-15 (ch0)
Entries 9600000
199361 69936 600000 199610 593215 591098 578047 506534 62812 597832 568583 566871 600000 600000 600000 69960
400619 530064 400390 6785 8902 21953 93466 537188 32168 31317 33429 530040
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 1769101
48918 600000 449 4 22949 20663 23373
388321 2117 15188 86681 530407 10472 8531 10448
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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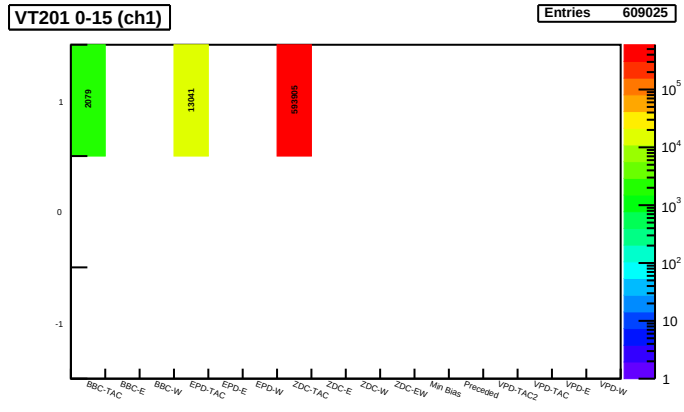
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W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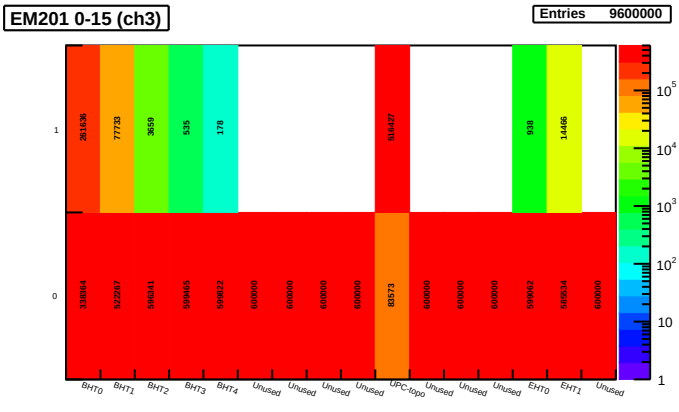
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338584 522267 592441 599465 598222 600000 600000 600000 600000 83573 600000 600000 600000 599662 585534 600000
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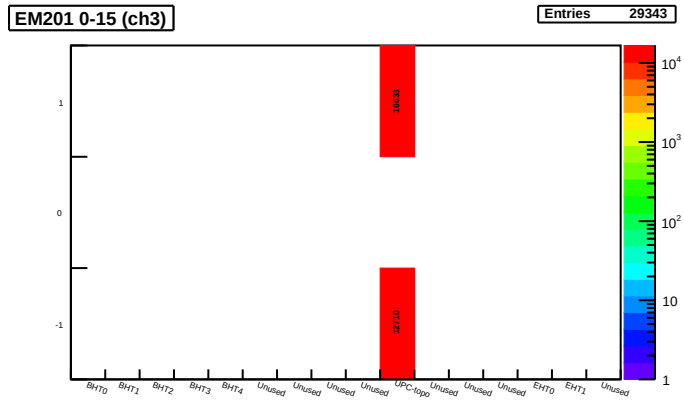
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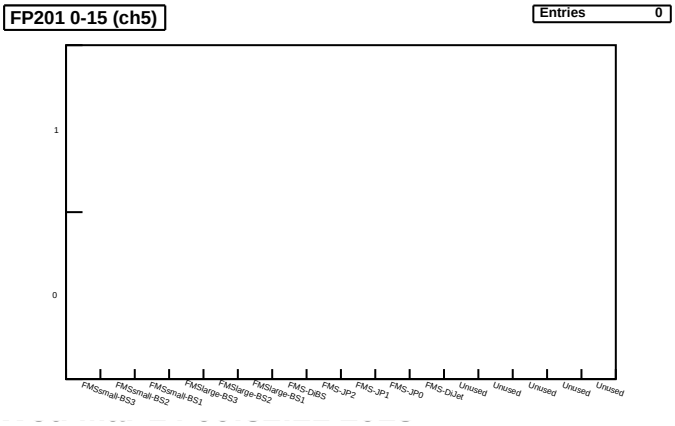
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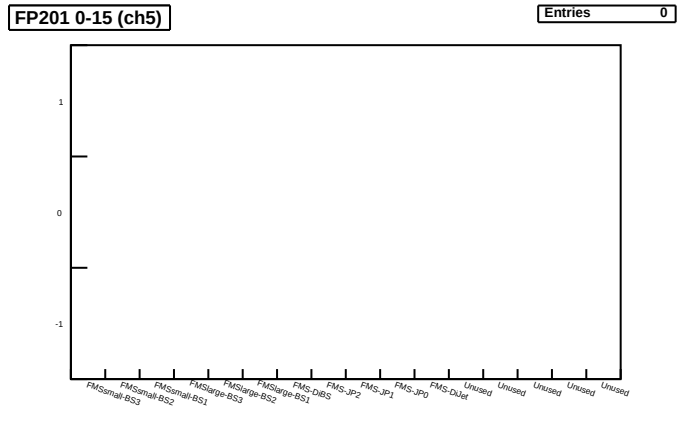
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