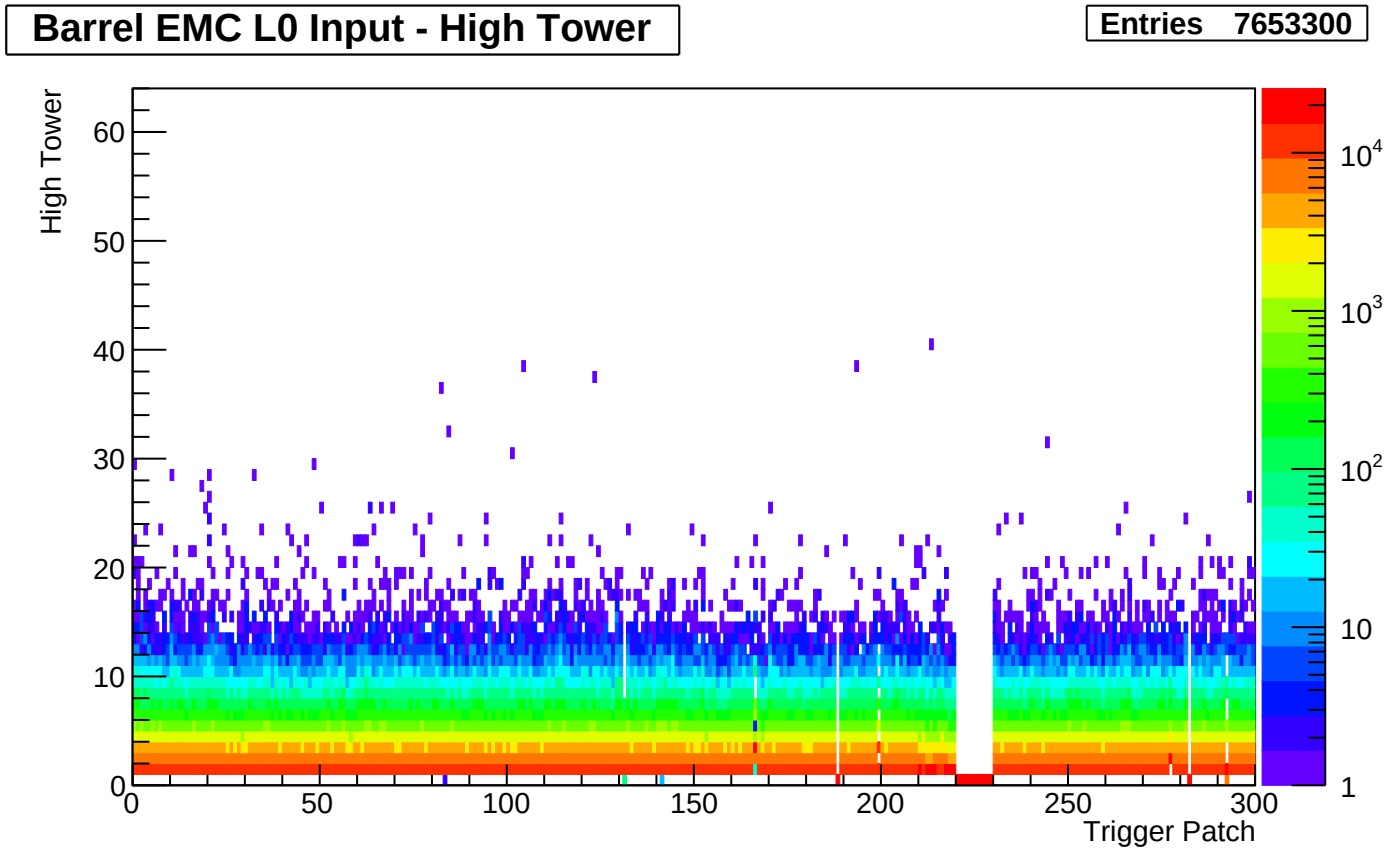
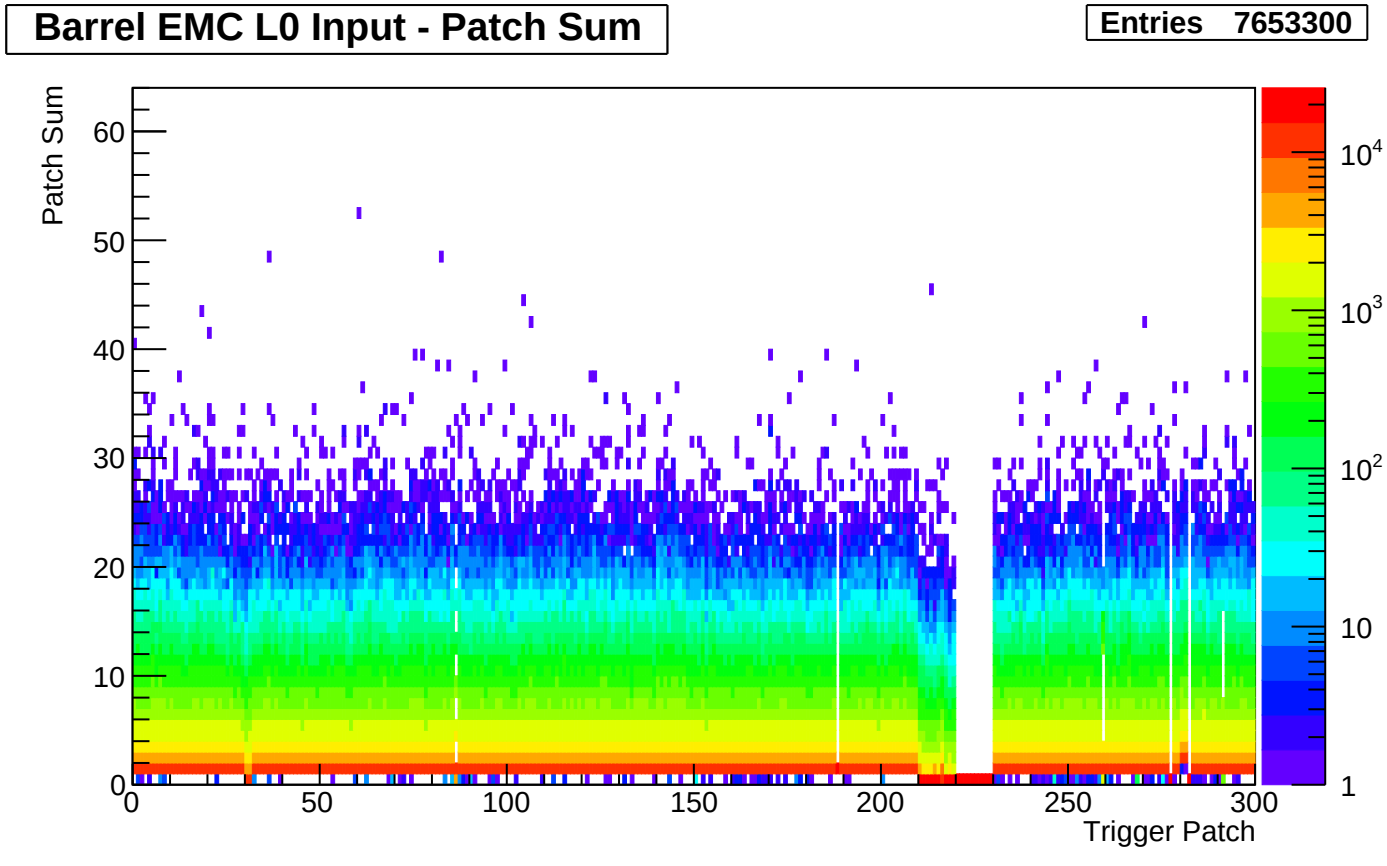
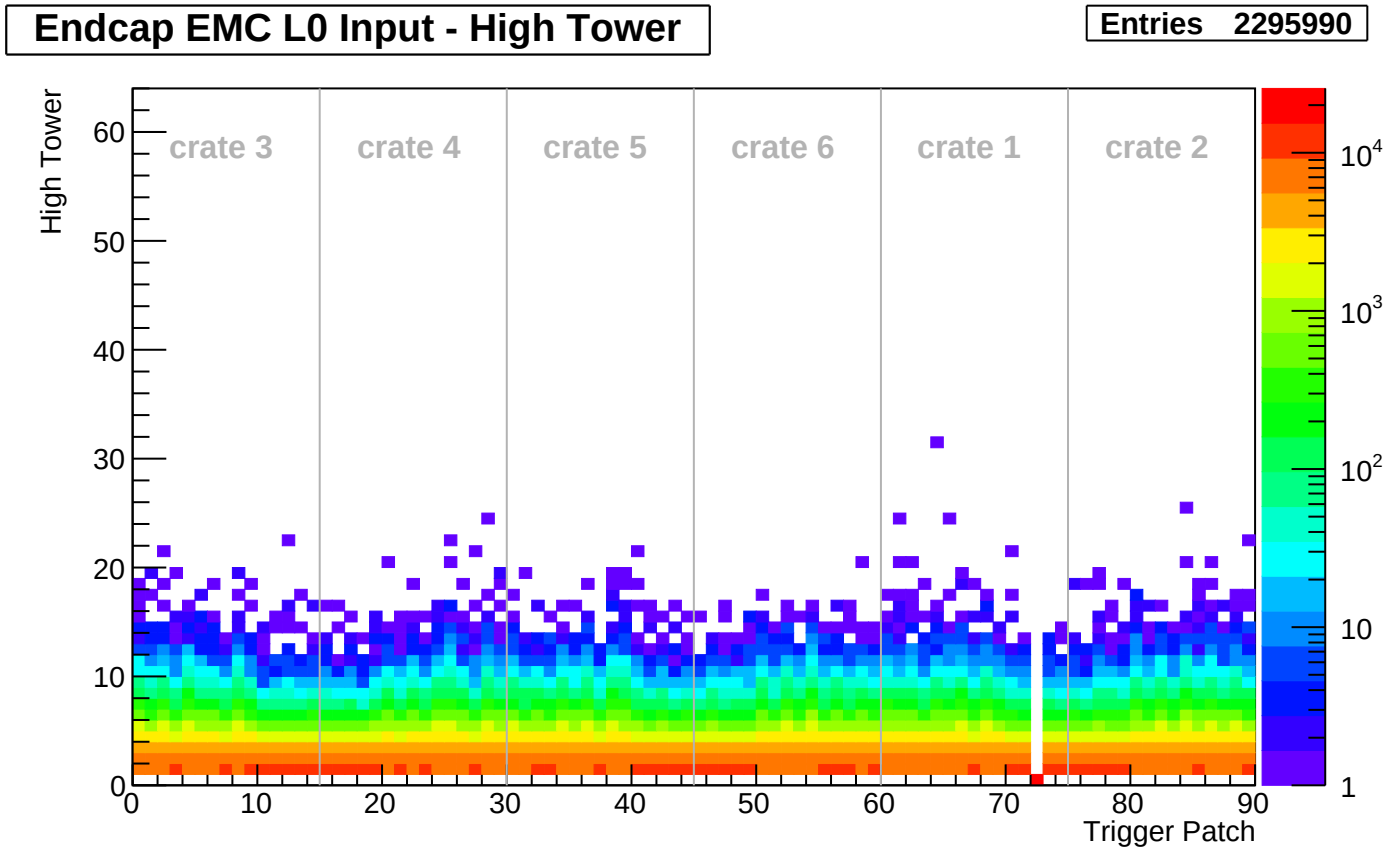
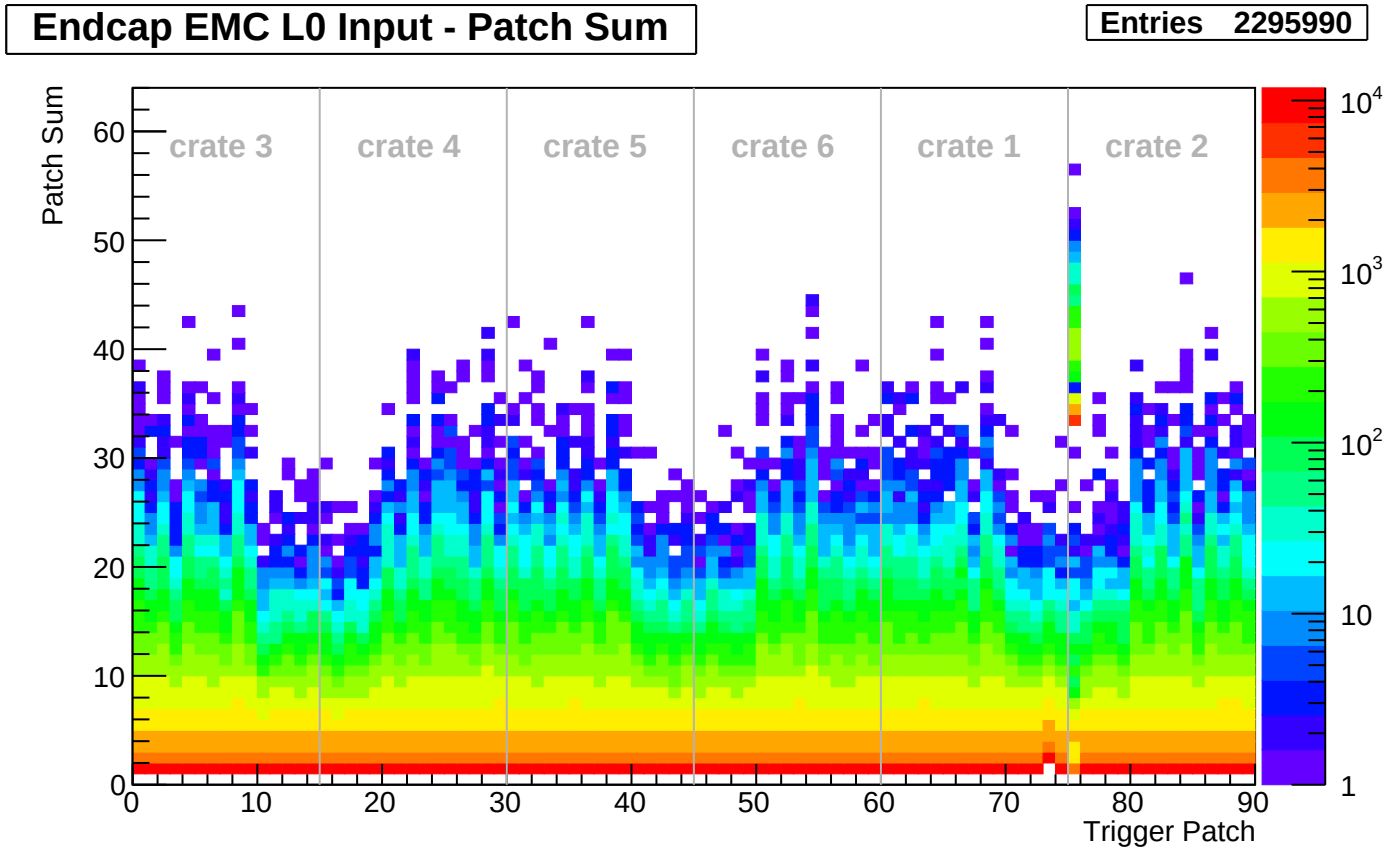
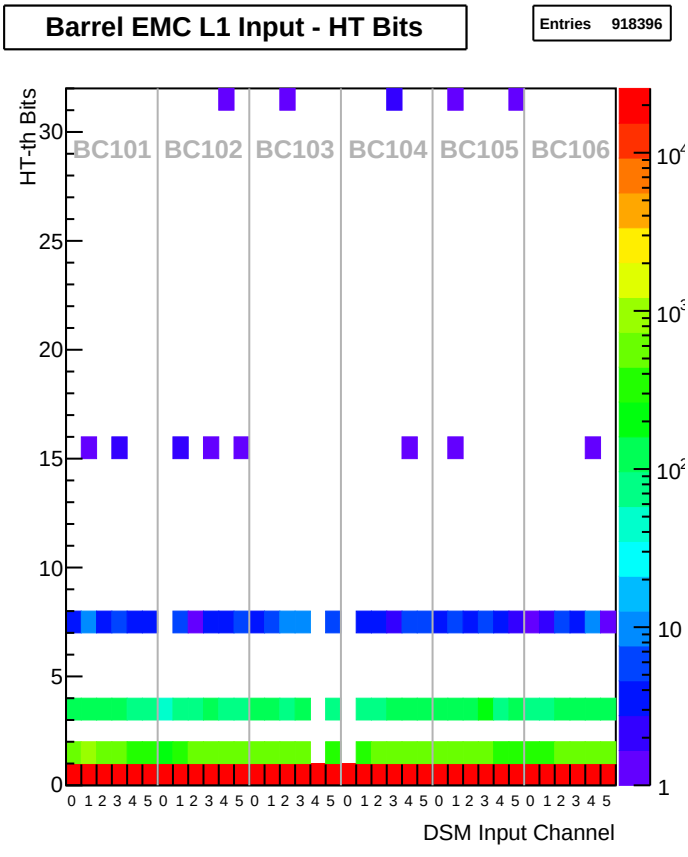
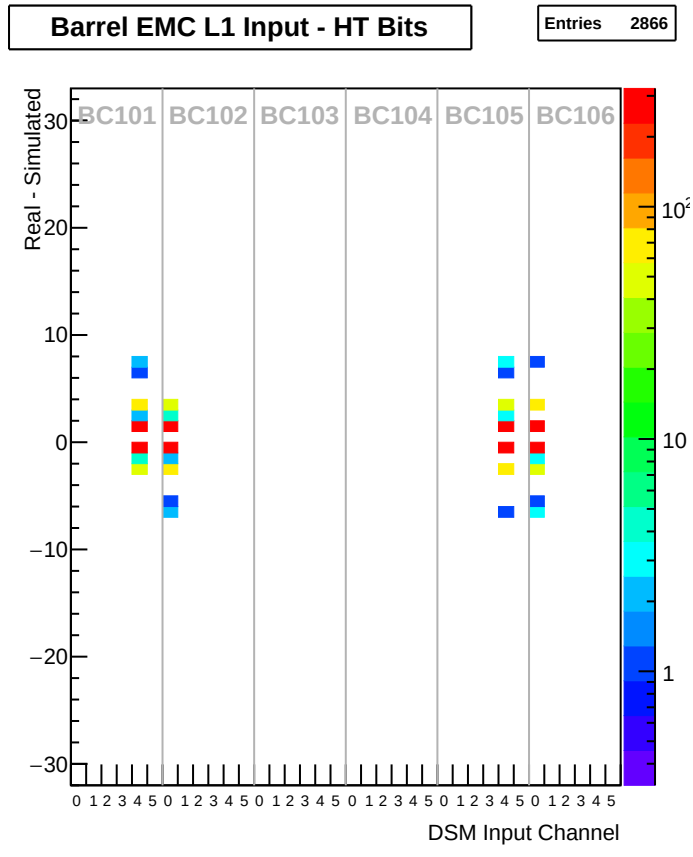

Barrel EMC L0 Input - High Tower
Entries 7653300
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

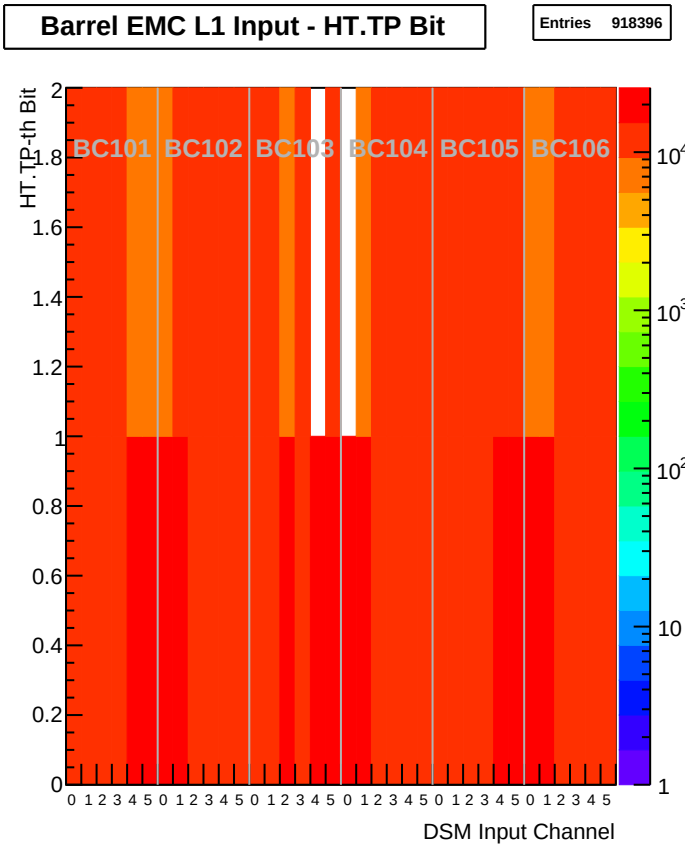

Barrel EMC L0 Input - Patch Sum
Entries 7653300
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

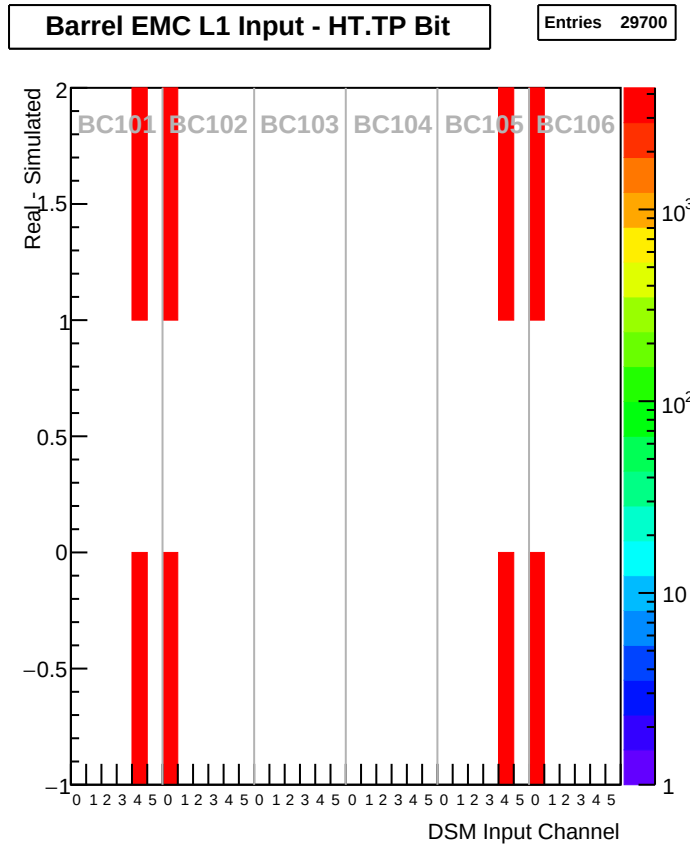

Endcap EMC L0 Input - High Tower
Entries 2295990
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

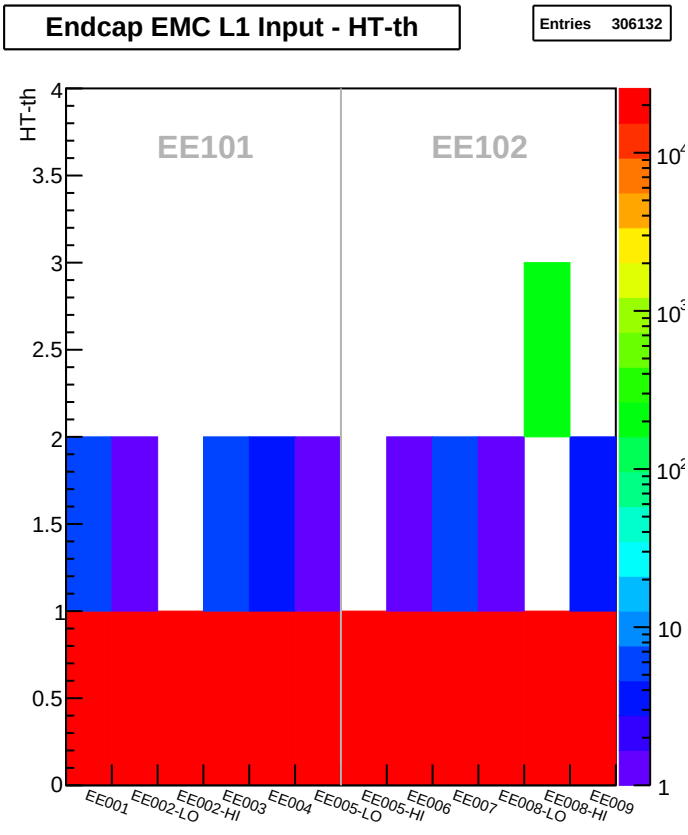

Endcap EMC L0 Input - Patch Sum
Entries 2295990
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

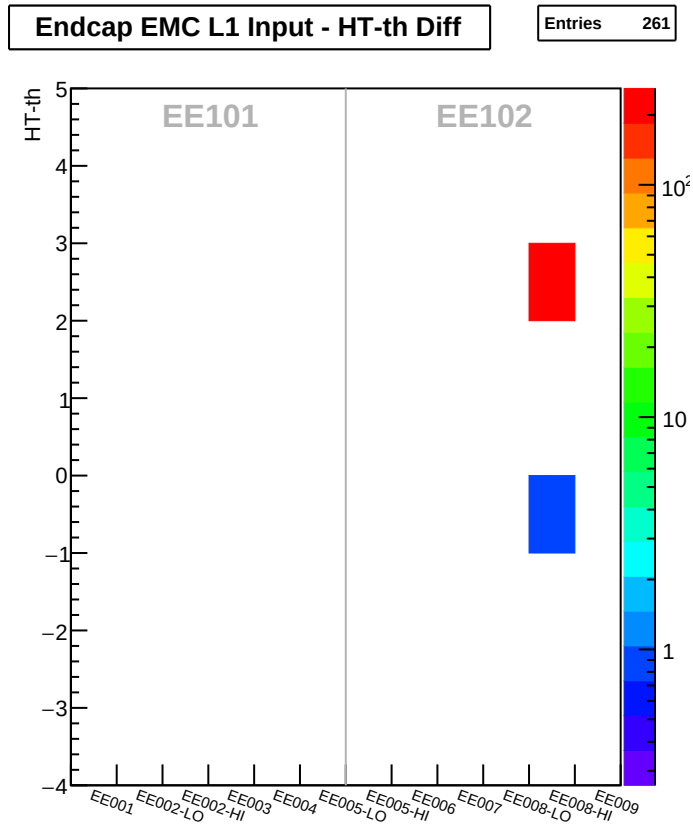

Barrel EMC L1 Input - HT Bits
Entries 918396
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

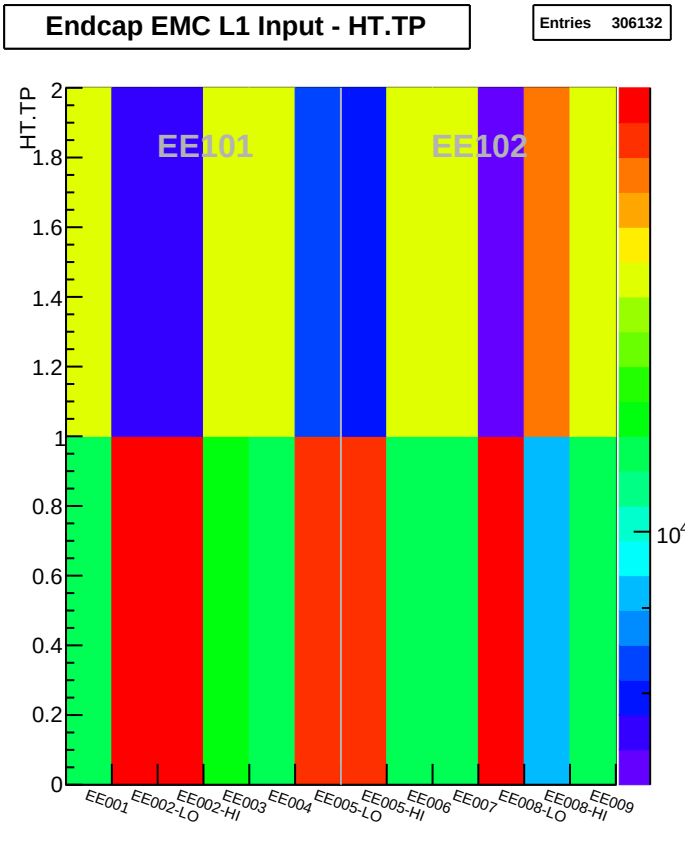

Barrel EMC L1 Input - HT Bits
Entries 2866
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

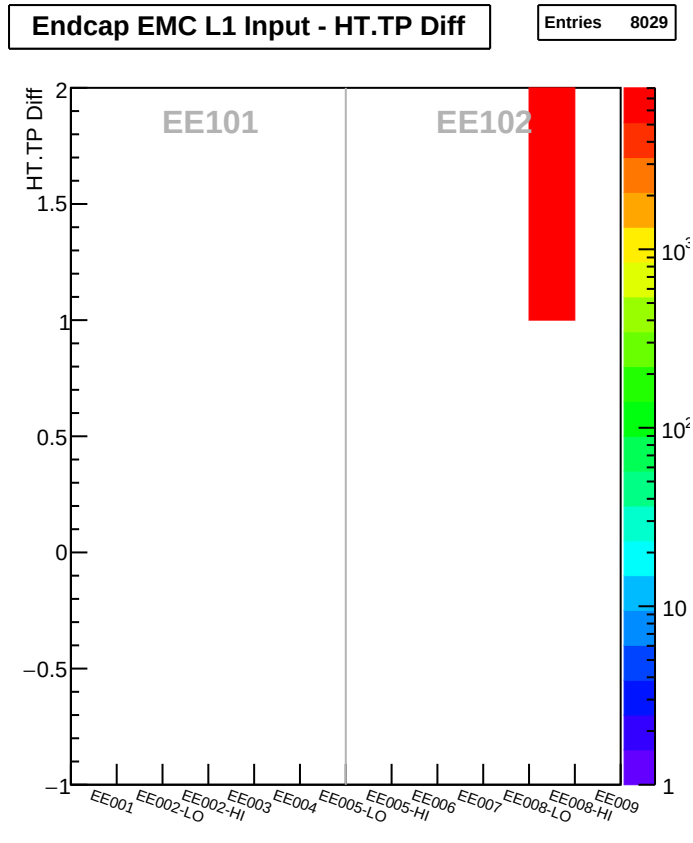

Barrel EMC L1 Input - HT.TP Bit
Entries 918396
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

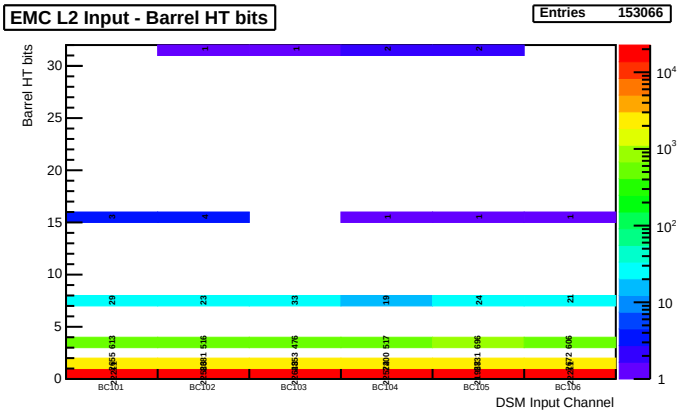

Barrel EMC L1 Input - HT.TP Bit
Entries 29700
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

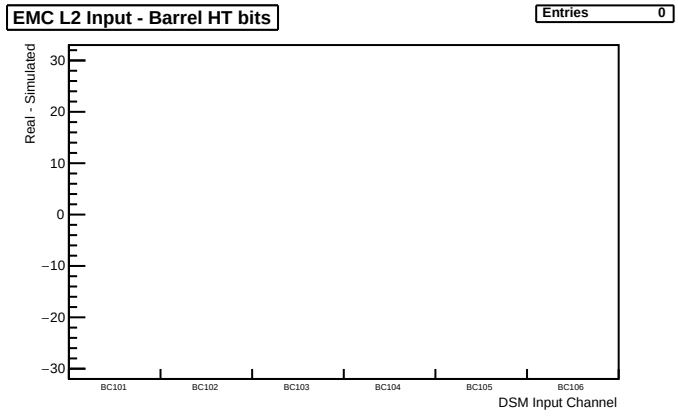

Endcap EMC L1 Input - HT-th
Entries 306132
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

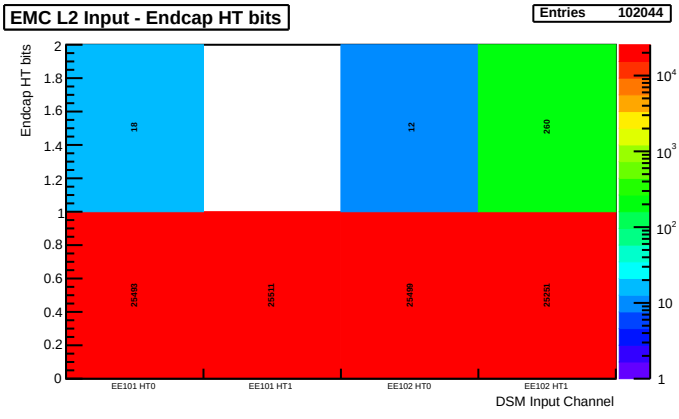

Endcap EMC L1 Input - HT-th Diff
Entries 261
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

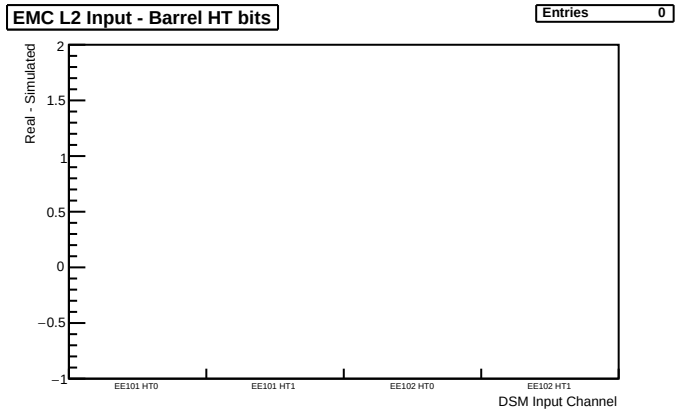

Endcap EMC L1 Input - HT.TP
Entries 306132
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

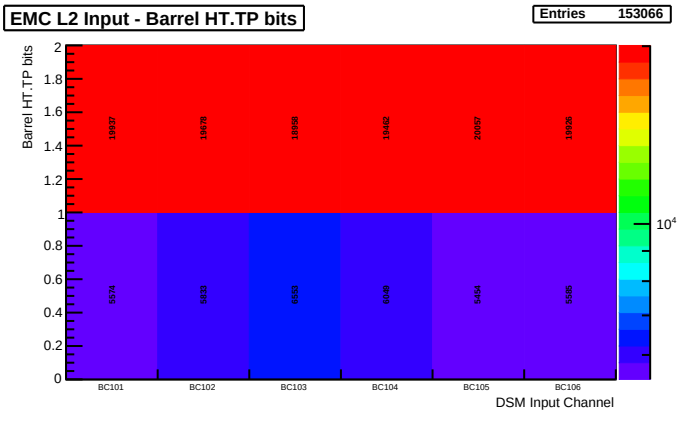

Endcap EMC L1 Input - HT.TP Diff
Entries 8029
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

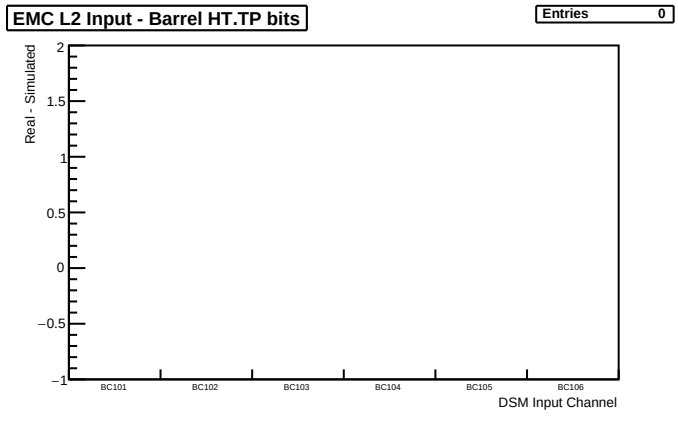

EMC L2 Input - Barrel HT bits
Entries 153066
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

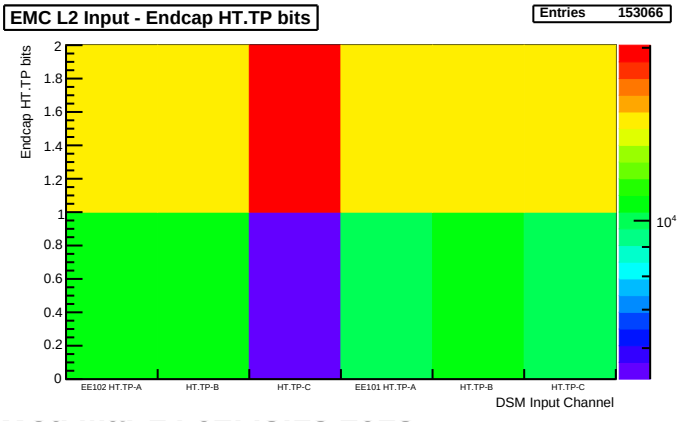

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

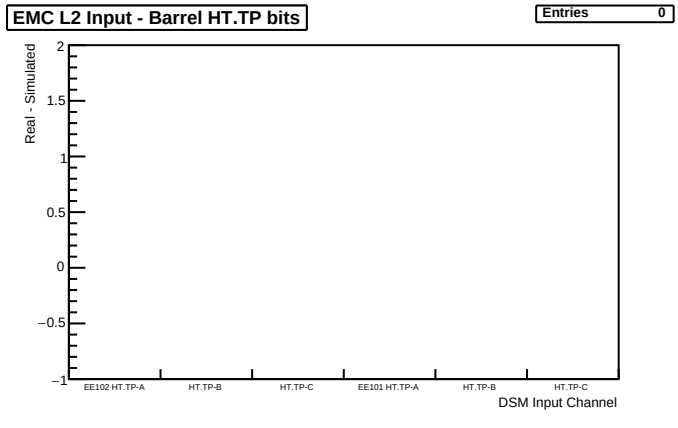

EMC L2 Input - Endcap HT bits
Entries 102044
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

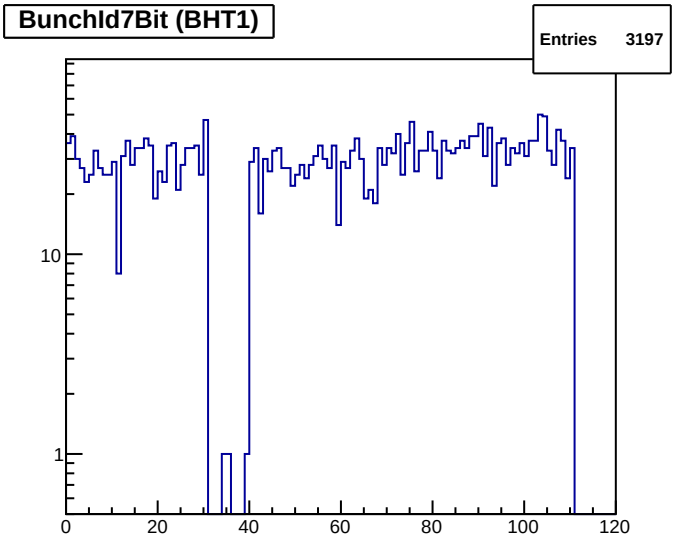

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

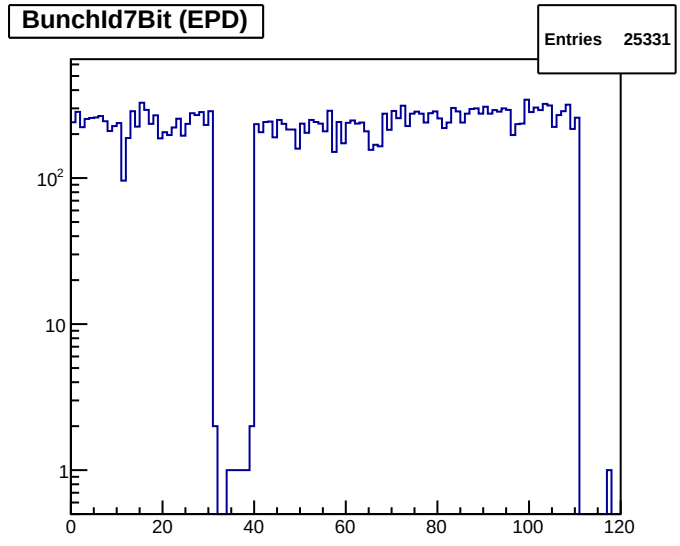

EMC L2 Input - Barrel HT.TP bits
Entries 153066
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

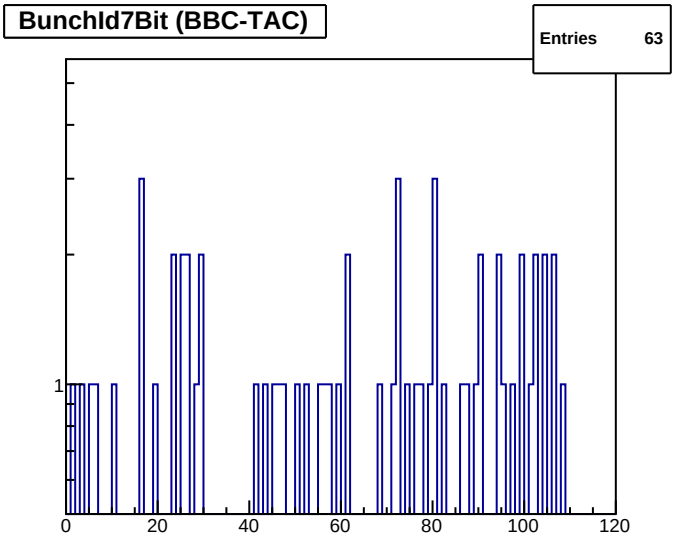

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

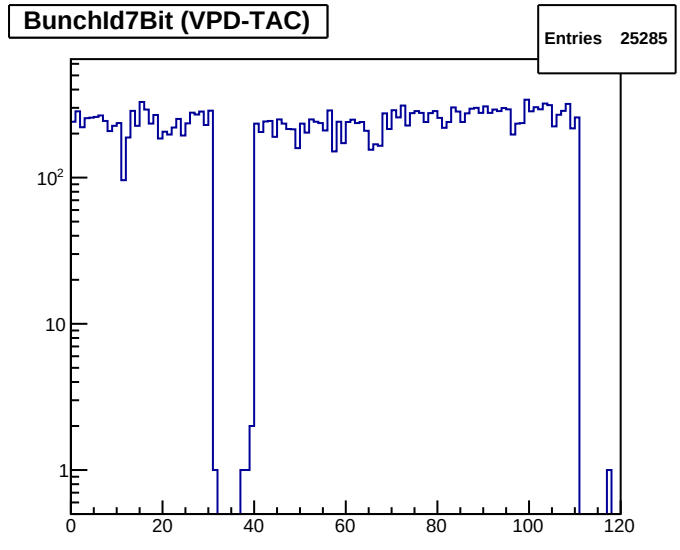

EMC L2 Input - Endcap HT.TP bits
Entries 153066
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

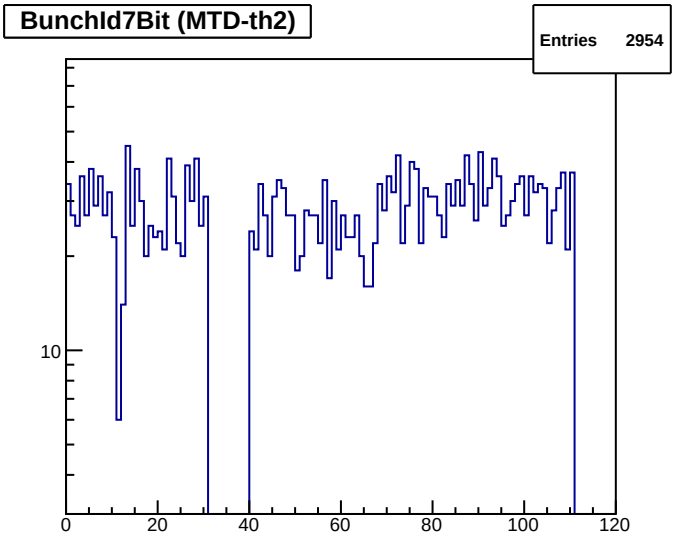

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

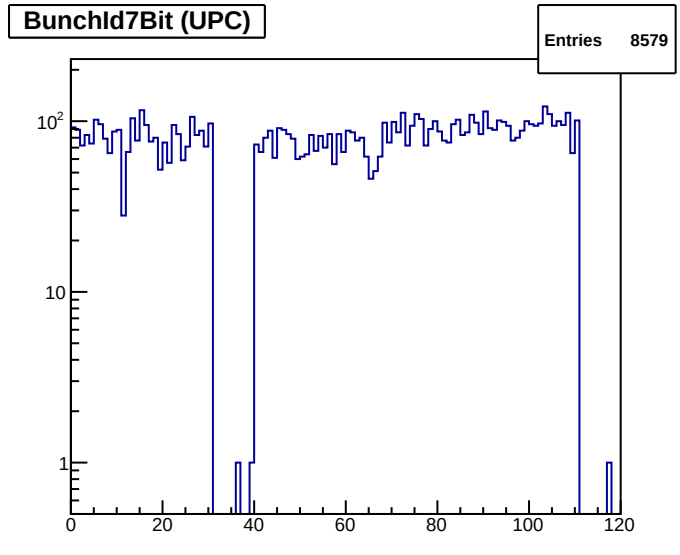

BunchId7Bit (BHT1)
Entries 3197
10
1
0
20
40
60
80
100
120

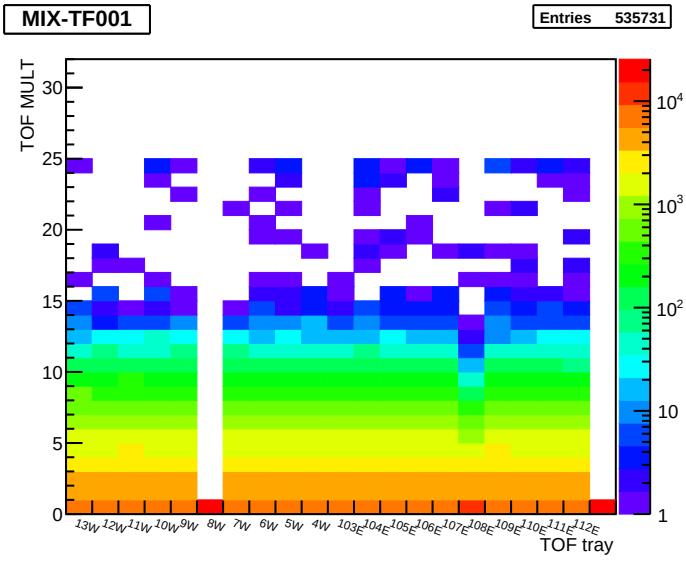

BunchId7Bit (EPD)
Entries 25331
10^2
10
1
0
20
40
60
80
100
120

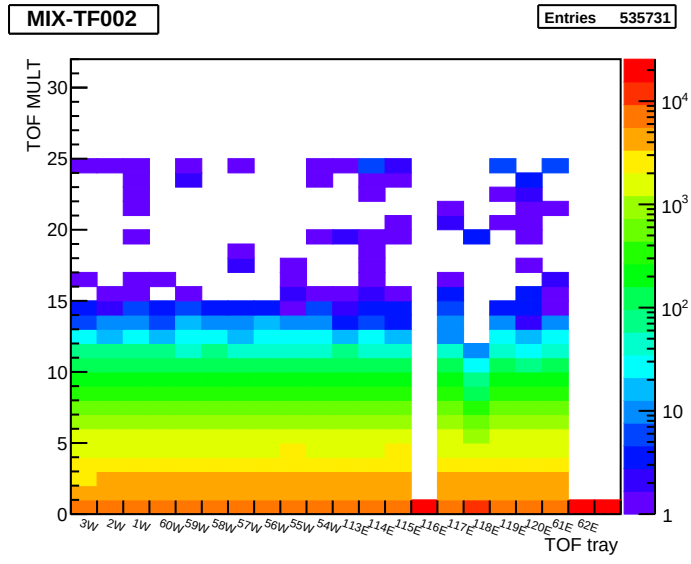

BunchId7Bit (BBC-TAC)
Entries 63
1
0
20
40
60
80
100
120

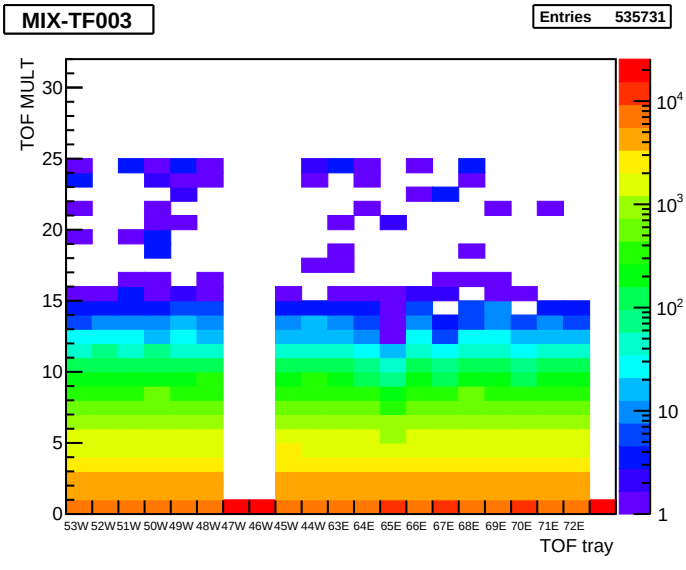

BunchId7Bit (VPD-TAC)
Entries 25285
10^2
10
1
0
20
40
60
80
100
120

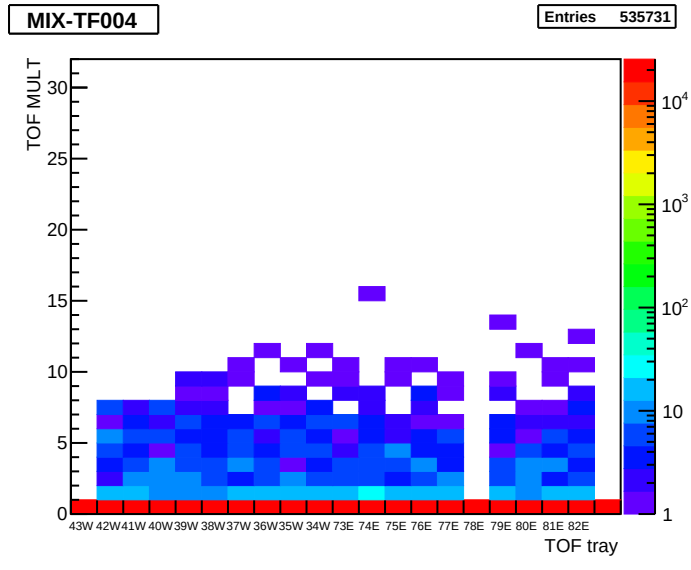

BunchId7Bit (MTD-th2)
Entries 2954
10
0
20
40
60
80
100
120

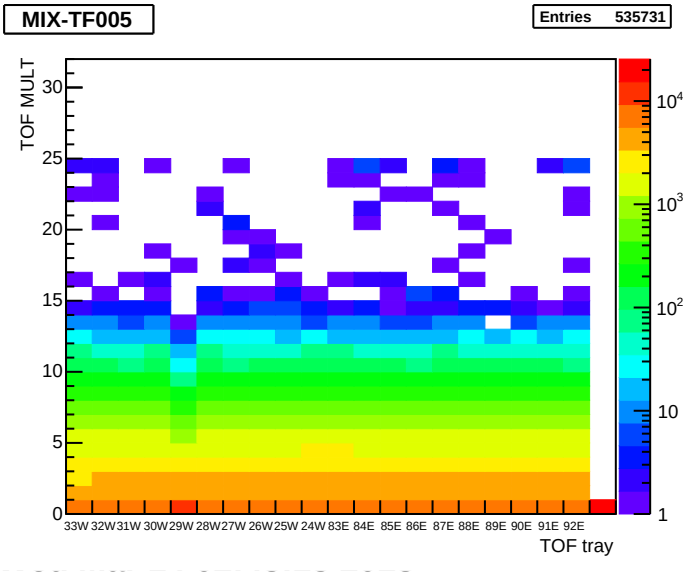

BunchId7Bit (UPC)
Entries 8579
10^2
10
1
0
20
40
60
80
100
120

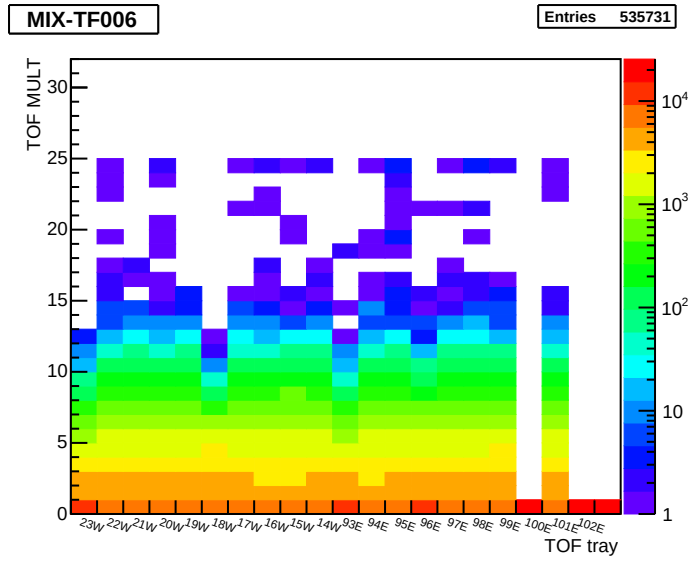

MIX-TF001
Entries 535731
TOF MULT
TOF tray

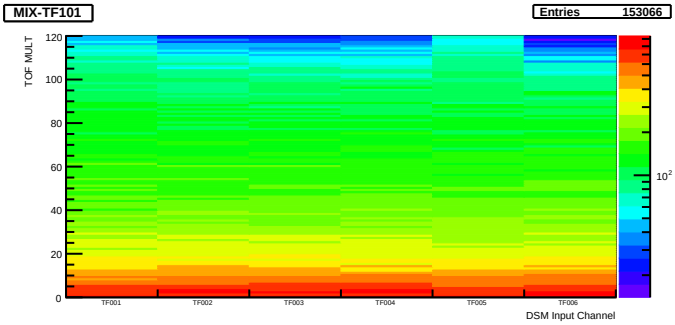

MIX-TF002
Entries 535731
TOF MULT
TOF tray

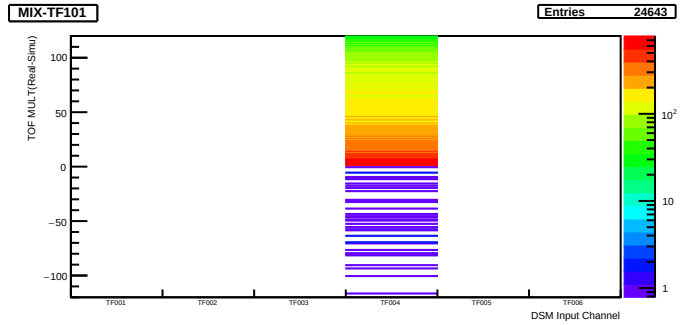

MIX-TF003
Entries 535731
TOF MULT
TOF tray

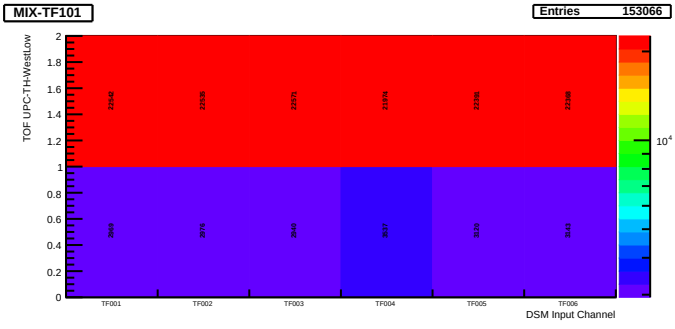

MIX-TF004
Entries 535731
TOF MULT
TOF tray

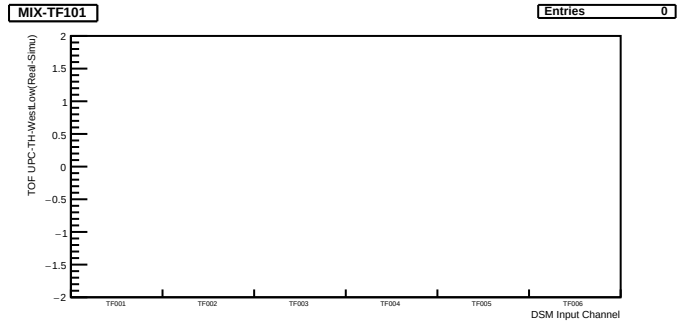

MIX-TF005
Entries 535731
TOF MULT
TOF tray

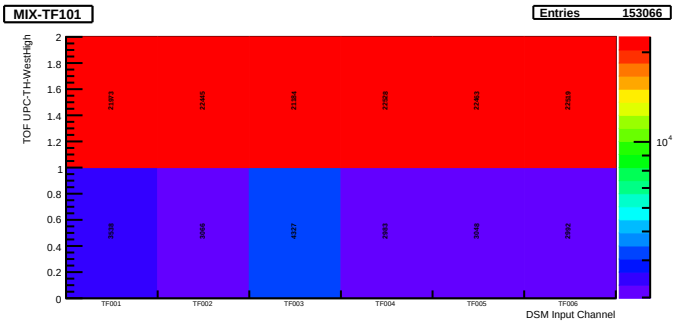

MIX-TF006
Entries 535731
TOF MULT
TOF tray

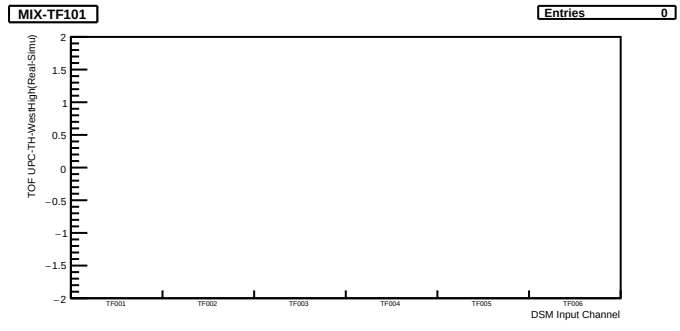

MIX-TF101
Entries 153066
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

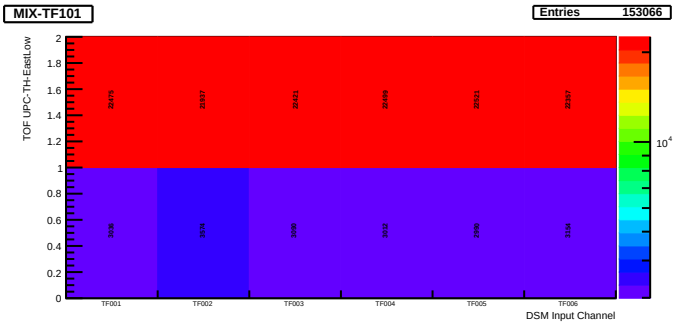

MIX-TF101
Entries 24643
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

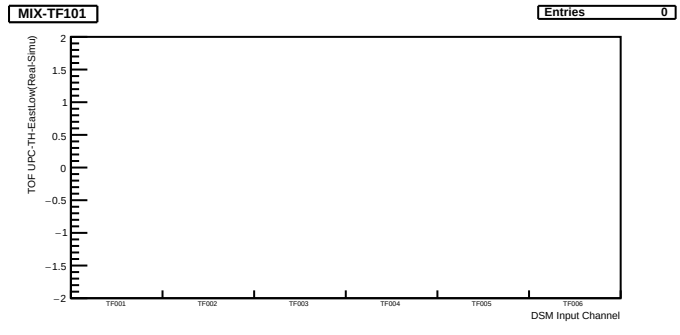

MIX-TF101
Entries 153066
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

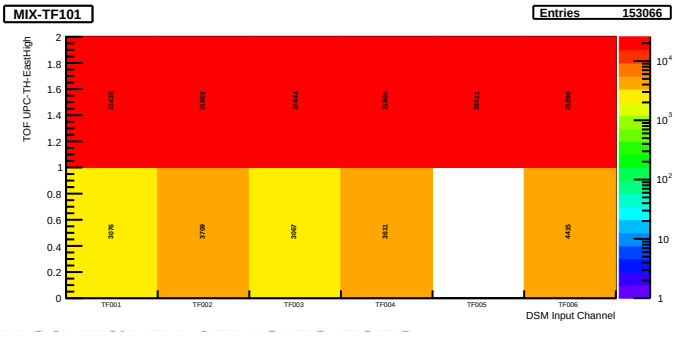

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

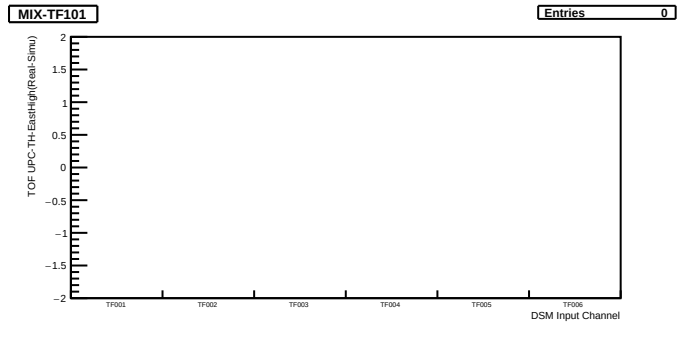

MIX-TF101
Entries 153066
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

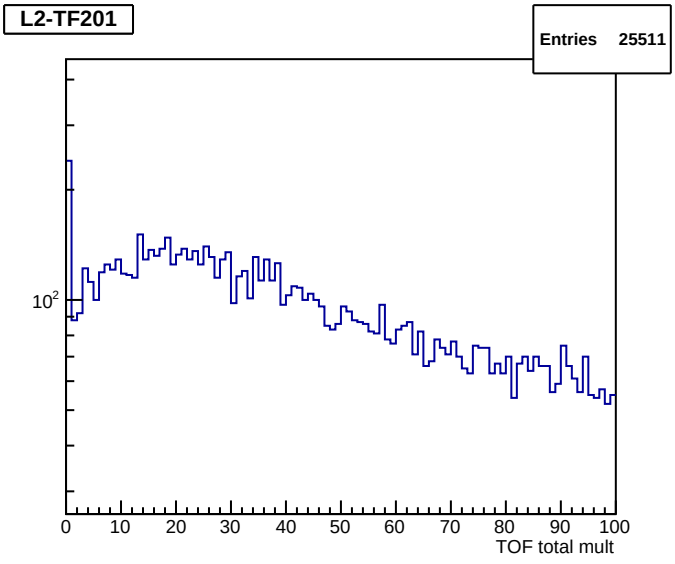

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

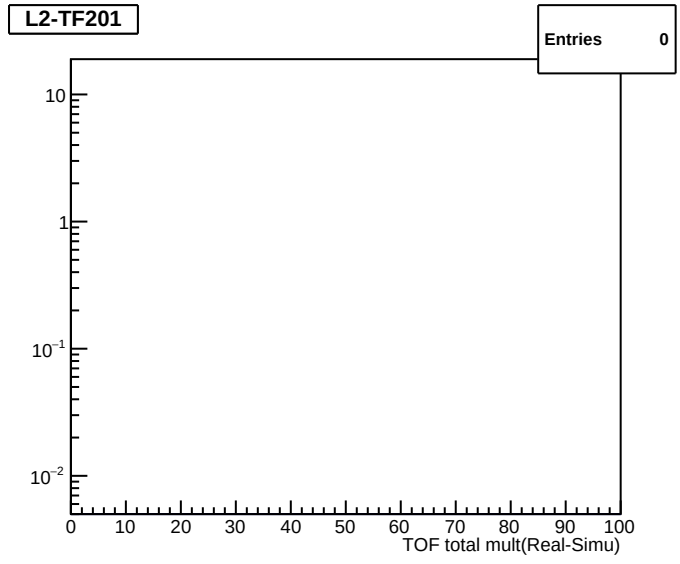

MIX-TF101
Entries 153066
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

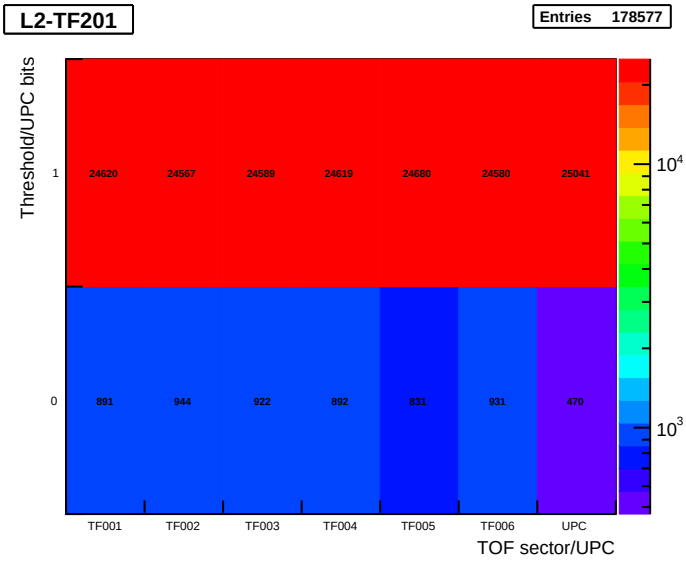

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

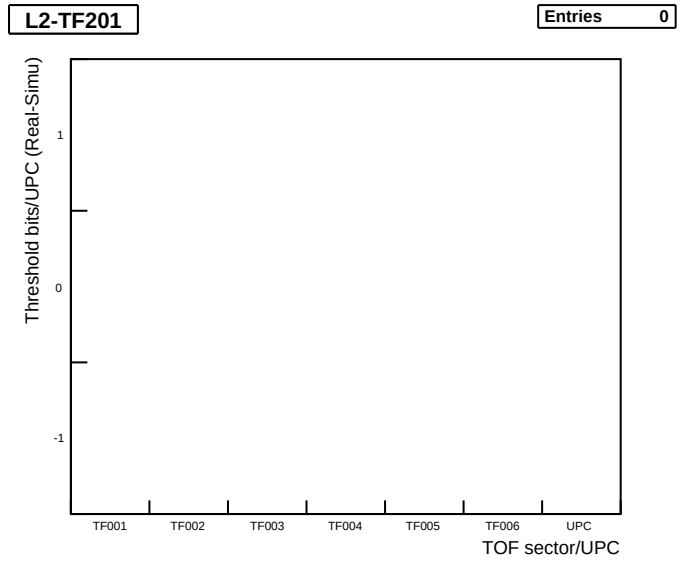

MIX-TF101
Entries 153066
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

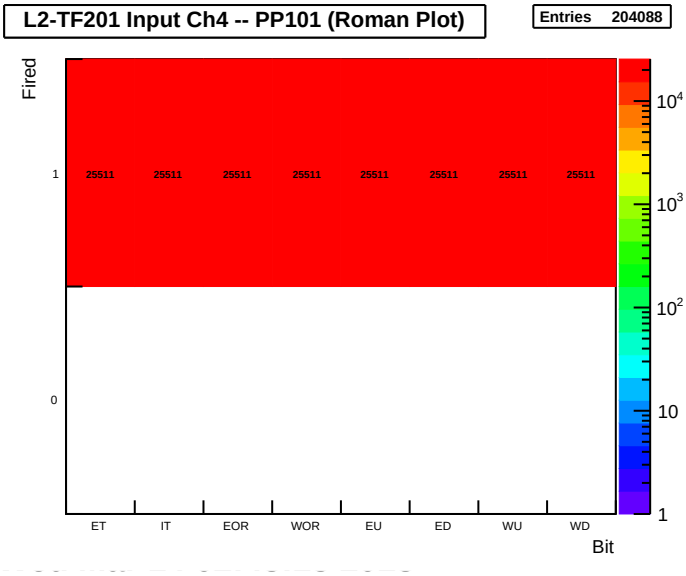

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

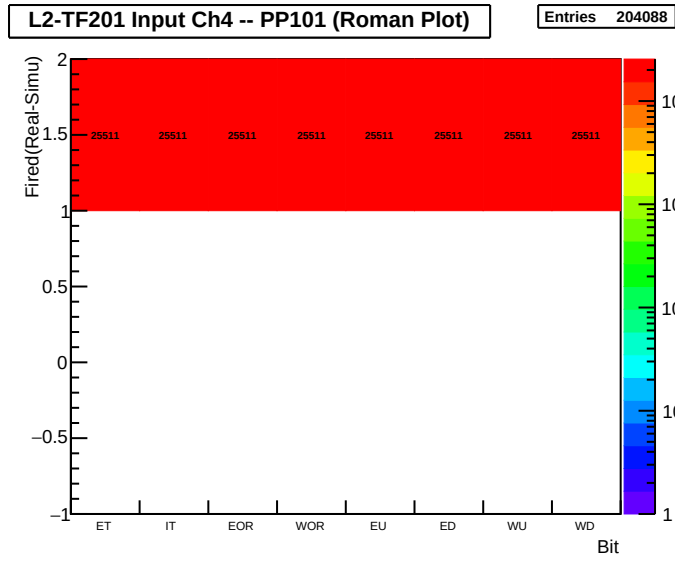

L2-TF201
Entries 25511
10^2
0 10 20 30 40 50 60 70 80 90 100
TOF total mult

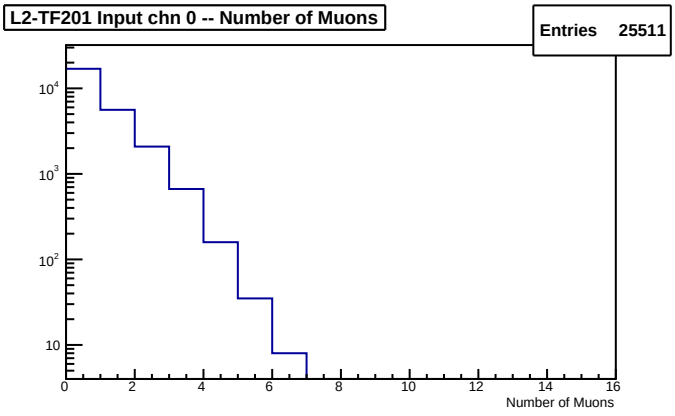

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
0 10 20 30 40 50 60 70 80 90 100
TOF total mult(Real-Simu)

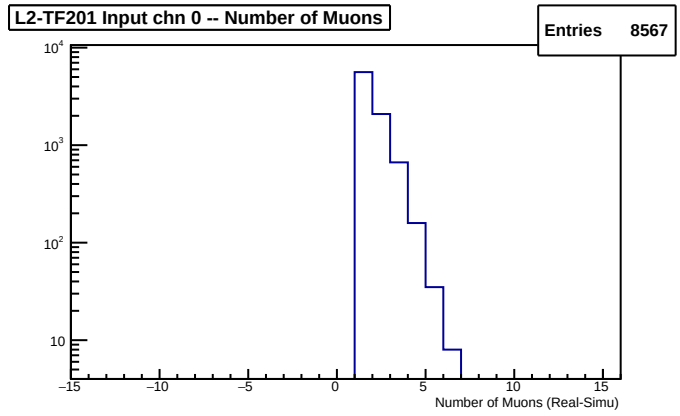

L2-TF201
Entries 178577
Threshold/UPC bits
1
0
24620 24567 24589 24619 24680 24580 25041
891 944 922 892 831 931 470
10^4
10^3
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

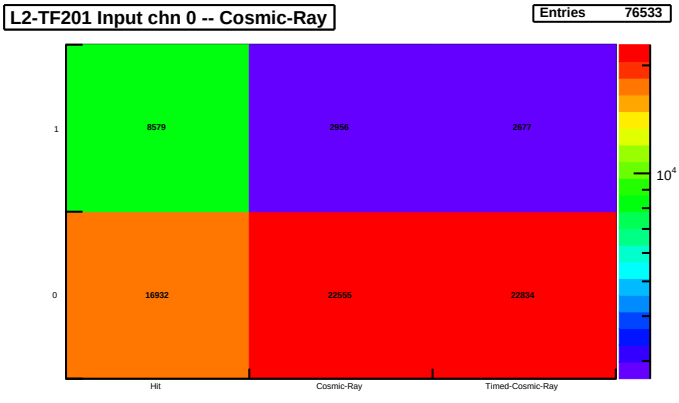

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

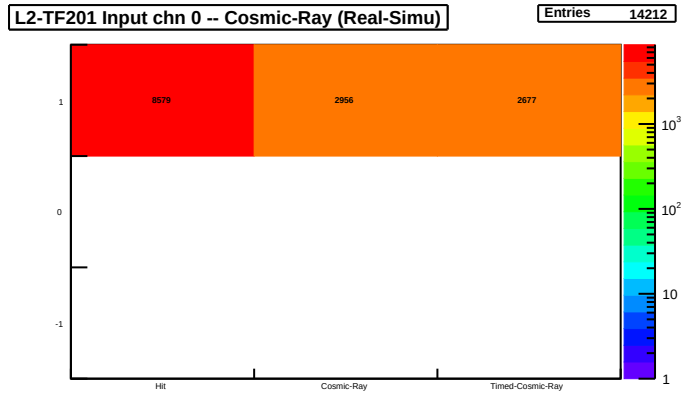

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 204088
Fired
1
0
25511 25511 25511 25511 25511 25511 25511 25511
10^4
10^3
10^2
10
1
ET IT EOR WOR EU ED WU WD
Bit

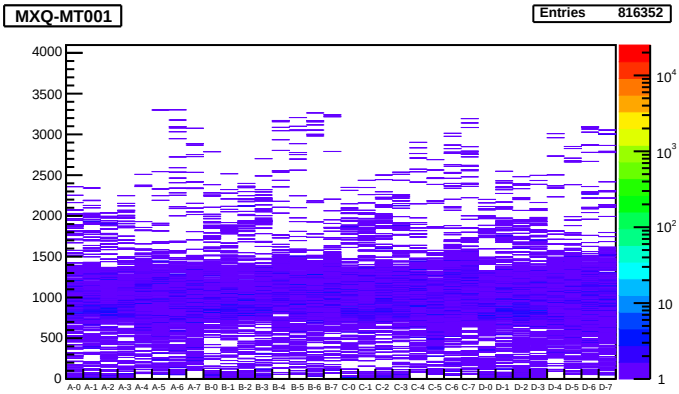

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 204088
Fired(Real-Simu)
2
1.5
1
0.5
0
−0.5
−1
25511 25511 25511 25511 25511 25511 25511 25511
10^4
10^3
10^2
10
1
ET IT EOR WOR EU ED WU WD
Bit

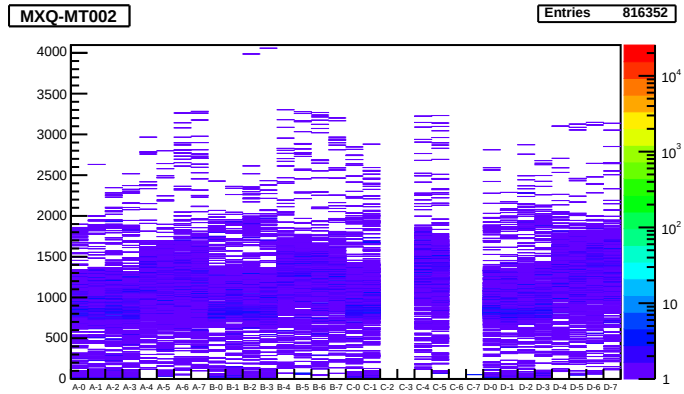

L2-TF201 Input chn 0 -- Number of Muons
Entries 25511
Number of Muons

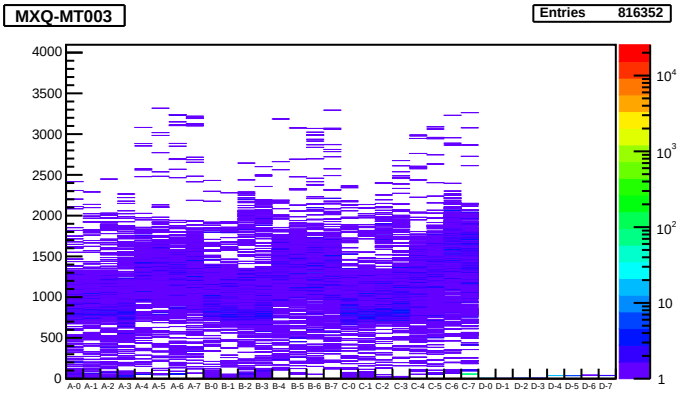

L2-TF201 Input chn 0 -- Number of Muons
Entries 8567
Number of Muons (Real-Simu)

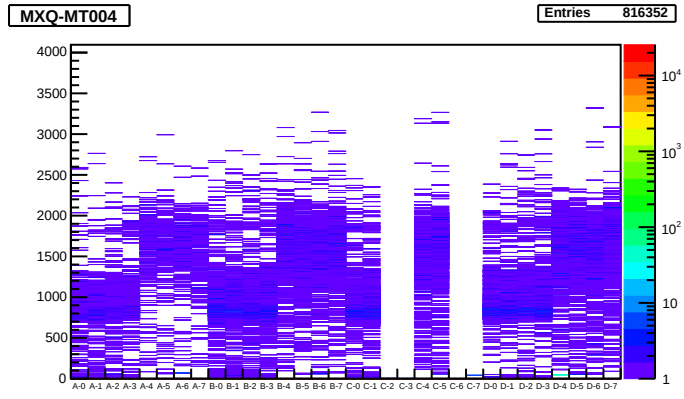

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 76533
8579
2956
2677
16932
22555
22834
Hit
Cosmic-Ray
Timed-Cosmic-Ray

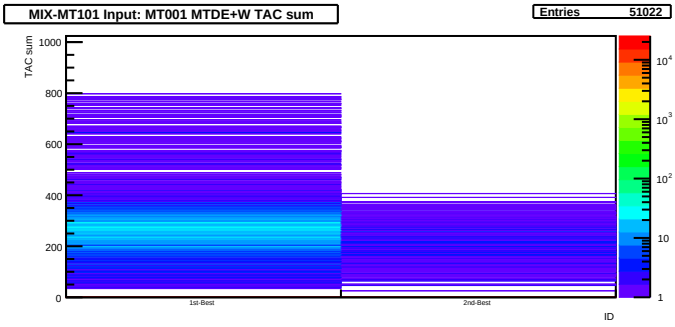

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 14212
8579
2956
2677
Hit
Cosmic-Ray
Timed-Cosmic-Ray

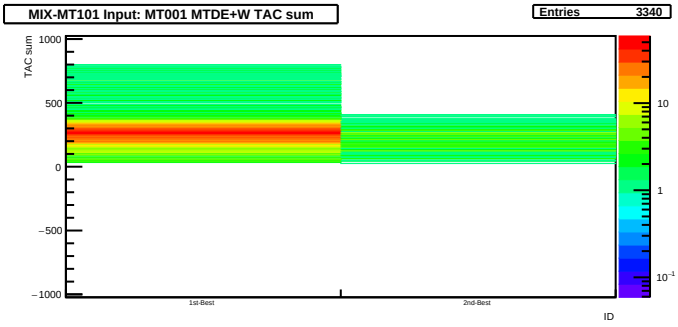

MXQ-MT001
Entries 816352
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

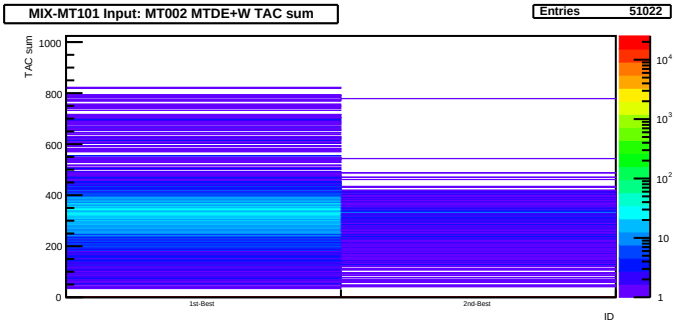

MXQ-MT002
Entries 816352
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

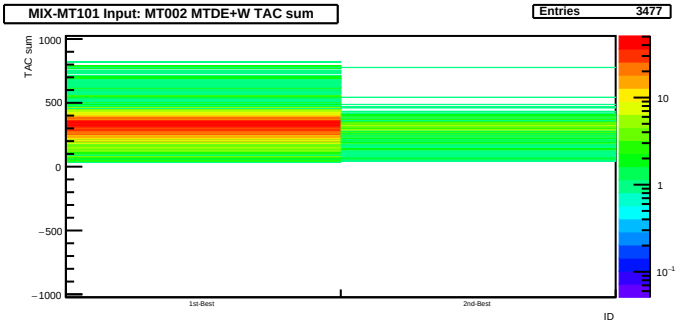

MXQ-MT003
Entries 816352
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

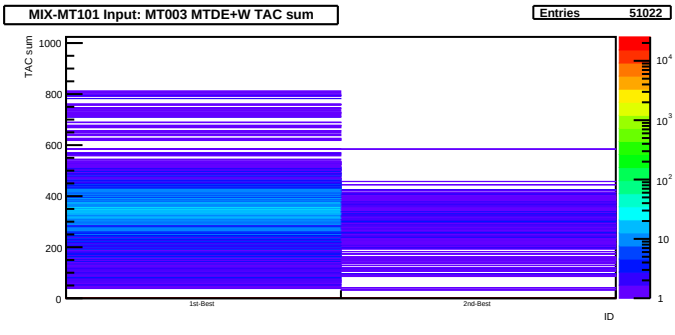

MXQ-MT004
Entries 816352
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

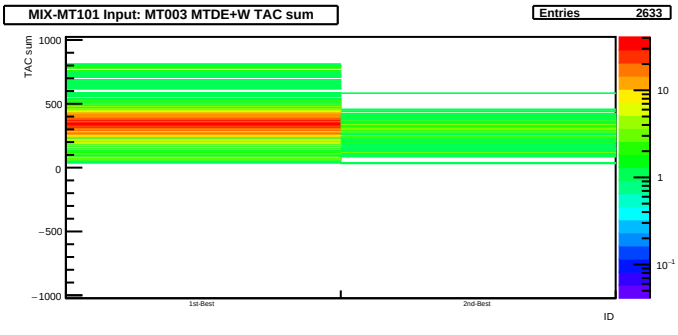

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 51022
TAC sum
1st-Best
2nd-Best
ID

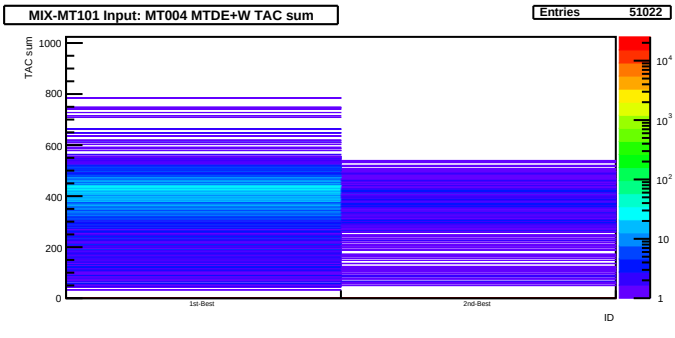

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 3340
TAC sum
1st-Best
2nd-Best
ID

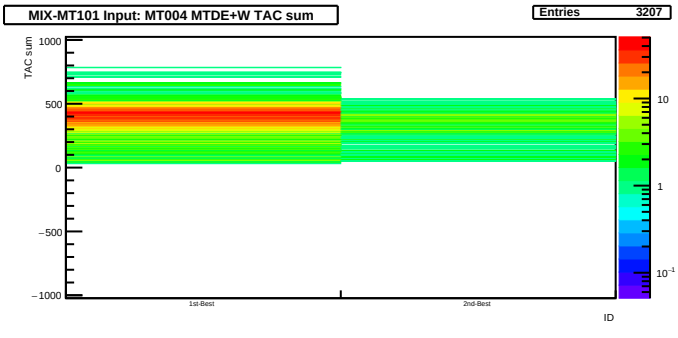

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 51022
TAC sum
1st-Best
2nd-Best
ID

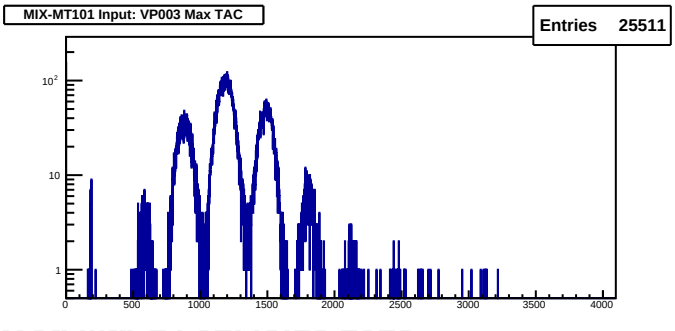

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 3477
TAC sum
1st-Best
2nd-Best
ID

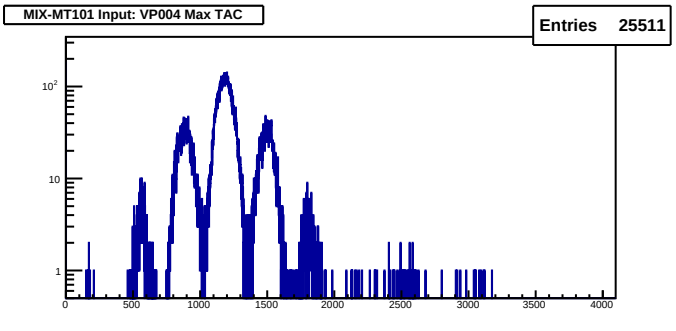

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 51022
TAC sum
1st-Best
2nd-Best
ID

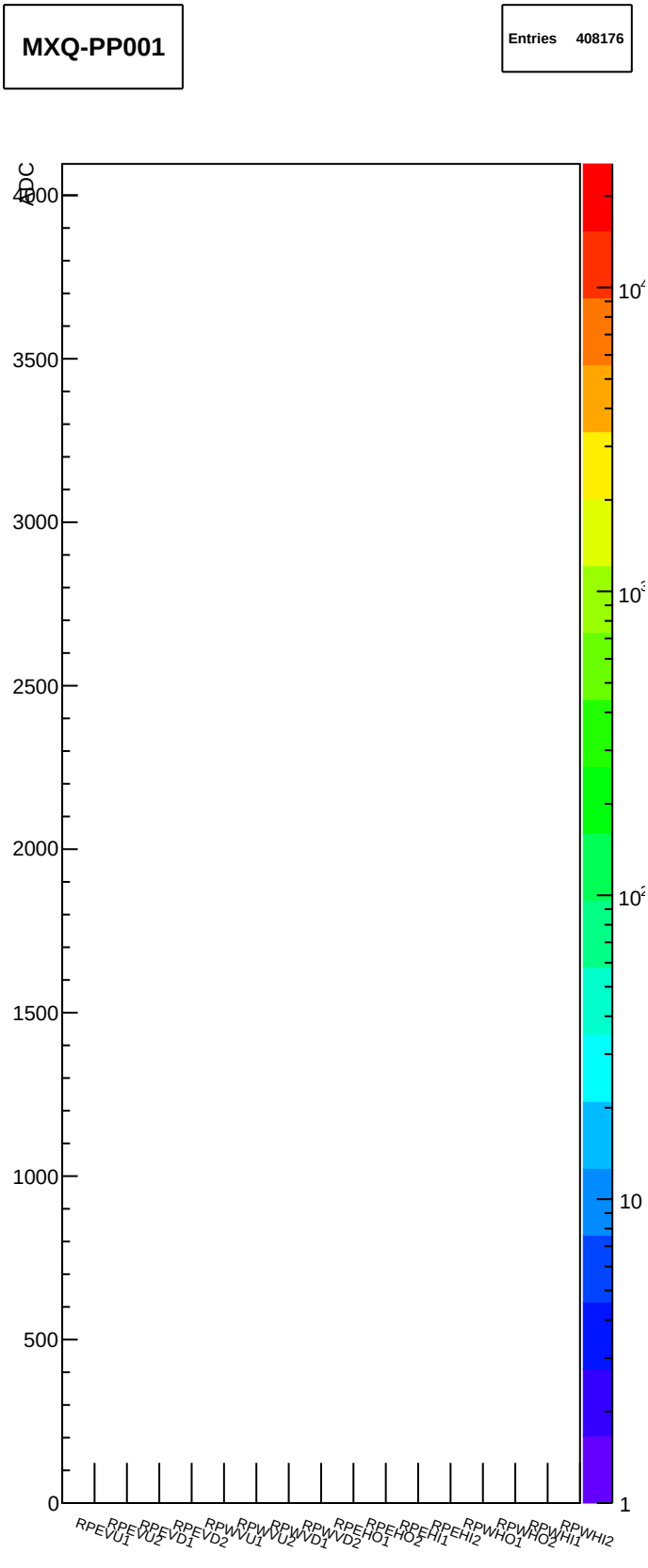

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 2633
TAC sum
1st-Best
2nd-Best
ID

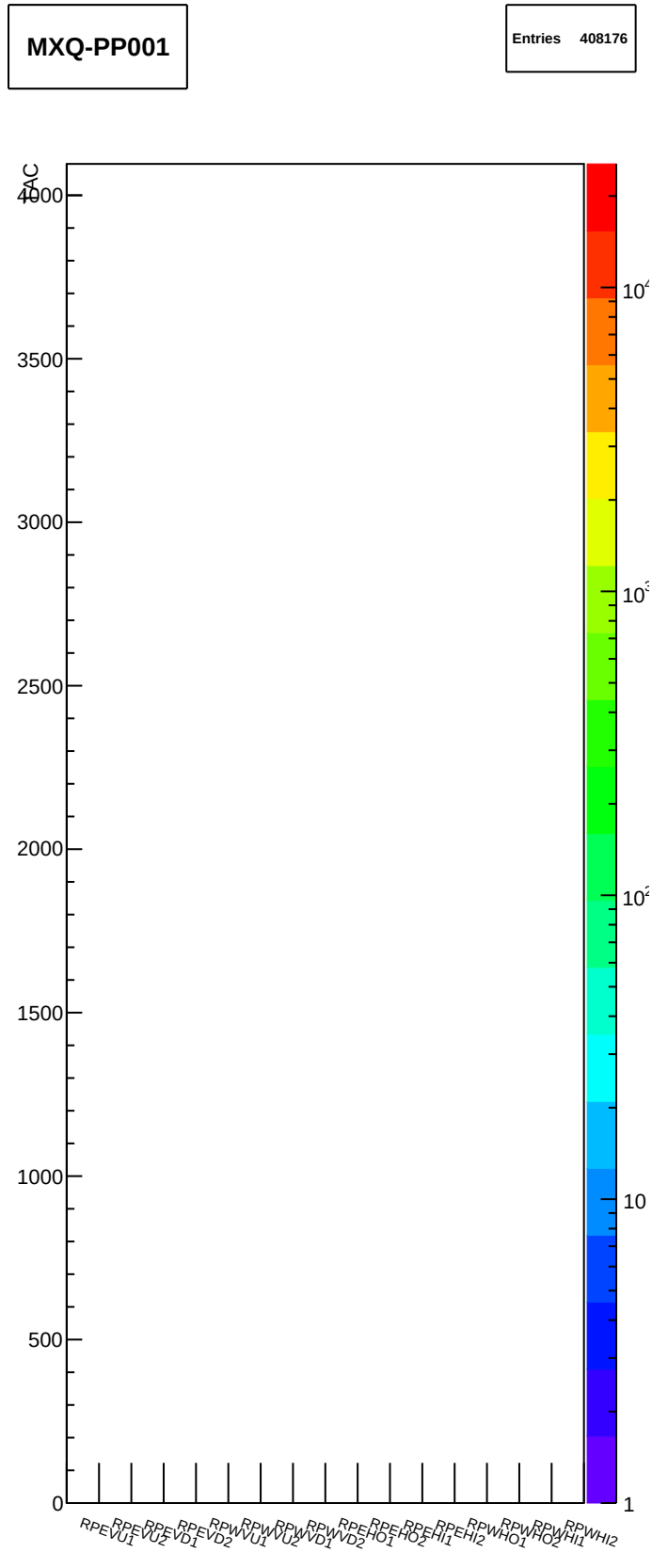

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 51022
TAC sum
1st-Best
2nd-Best
ID

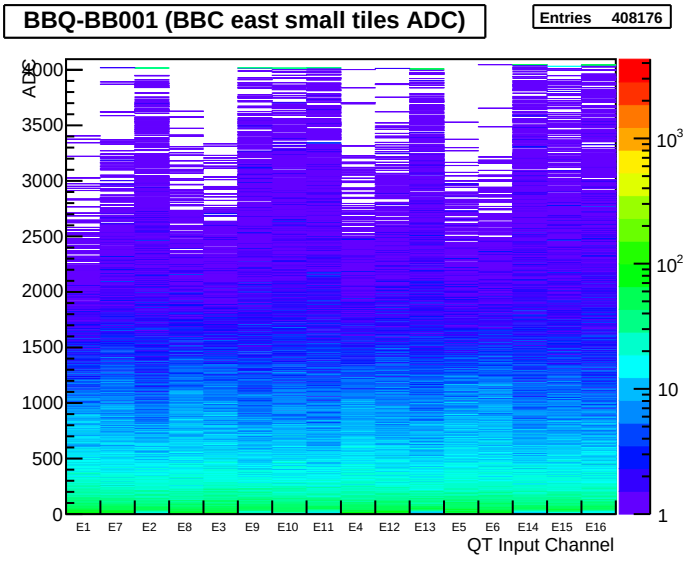

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 3207
TAC sum
1st-Best
2nd-Best
ID

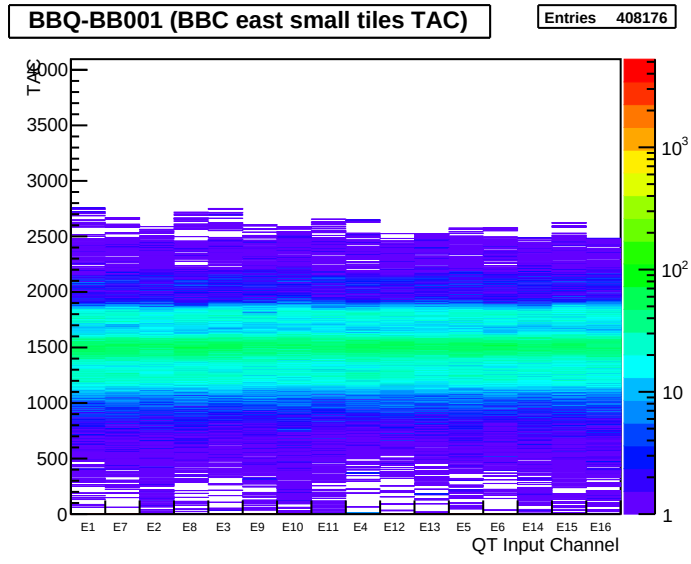

MIX-MT101 Input: VP003 Max TAC
Entries 25511

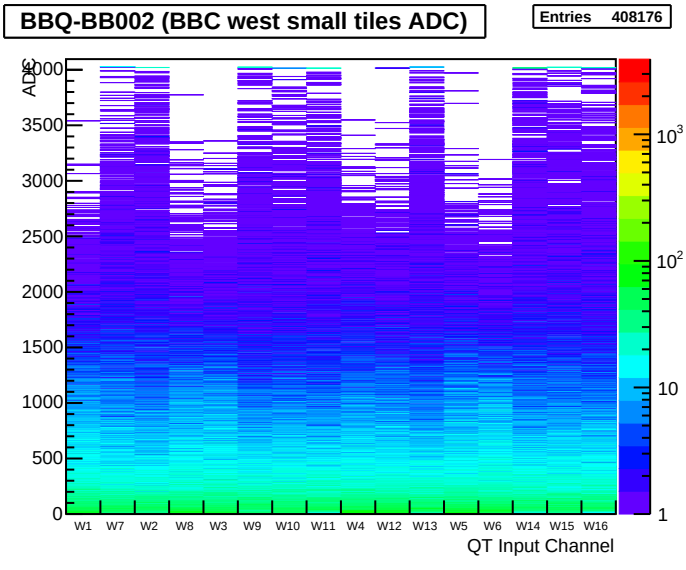

MIX-MT101 Input: VP004 Max TAC
Entries 25511

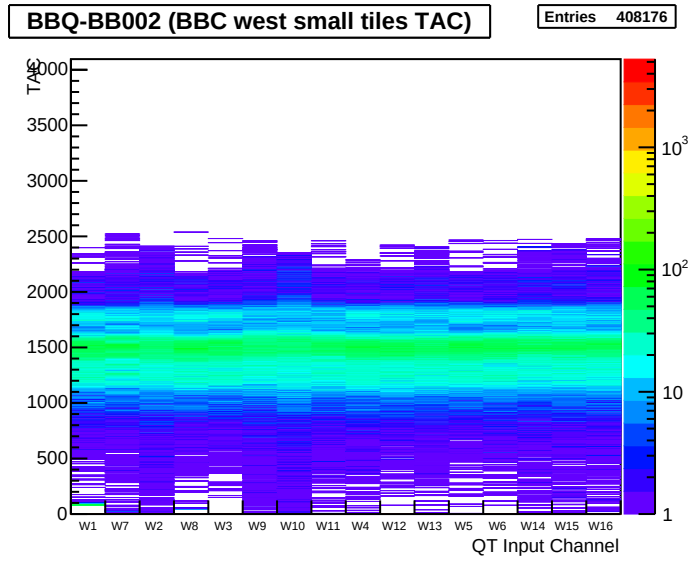

MXQ-PP001
Entries 408176
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

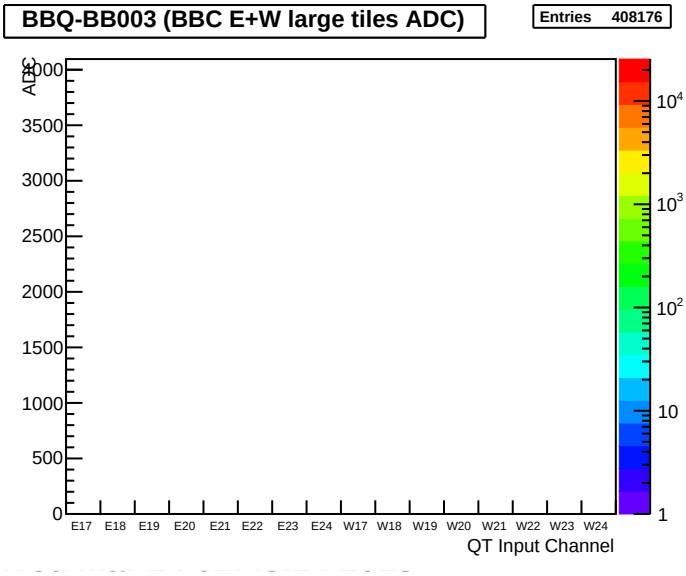

MXQ-PP001
Entries 408176
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

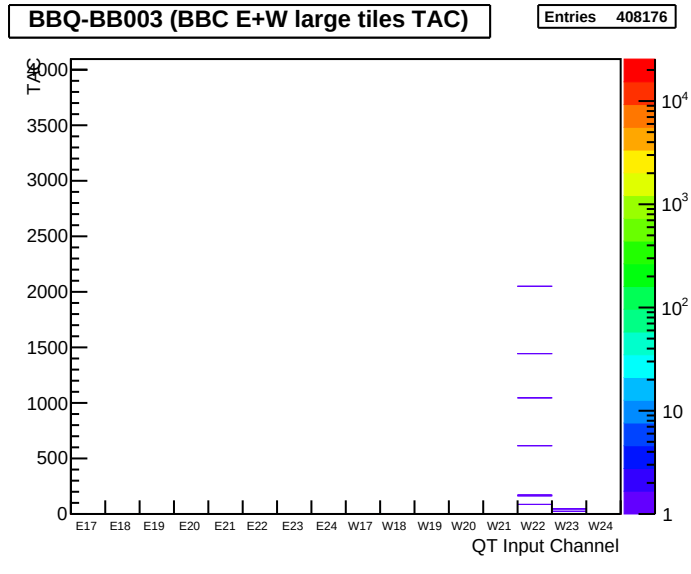

BBQ-BB001 (BBC east small tiles ADC)
Entries 408176
ADC
QT Input Channel

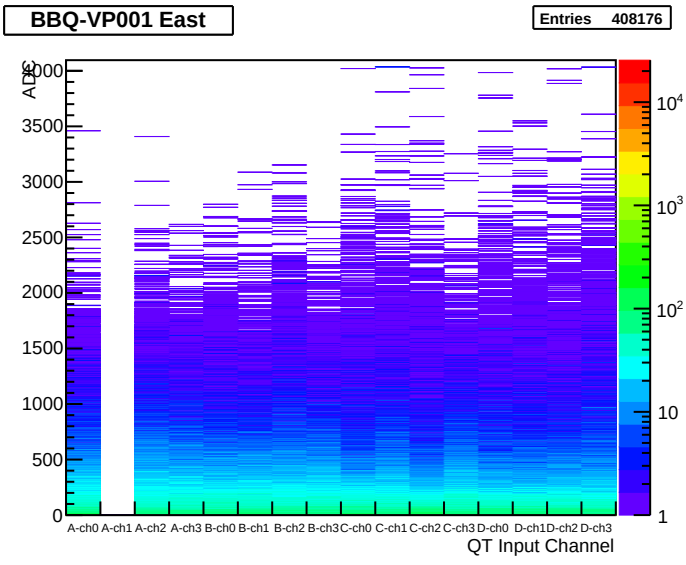

BBQ-BB001 (BBC east small tiles TAC)
Entries 408176
TAC
QT Input Channel

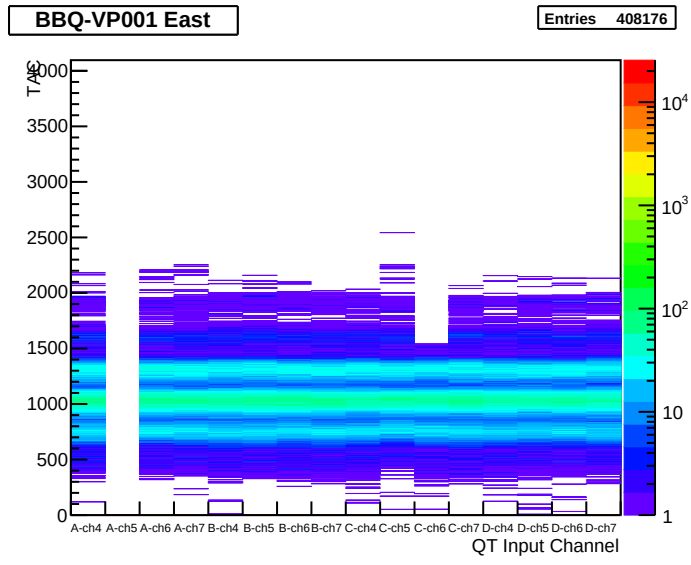

BBQ-BB002 (BBC west small tiles ADC)
Entries 408176
ADC
QT Input Channel

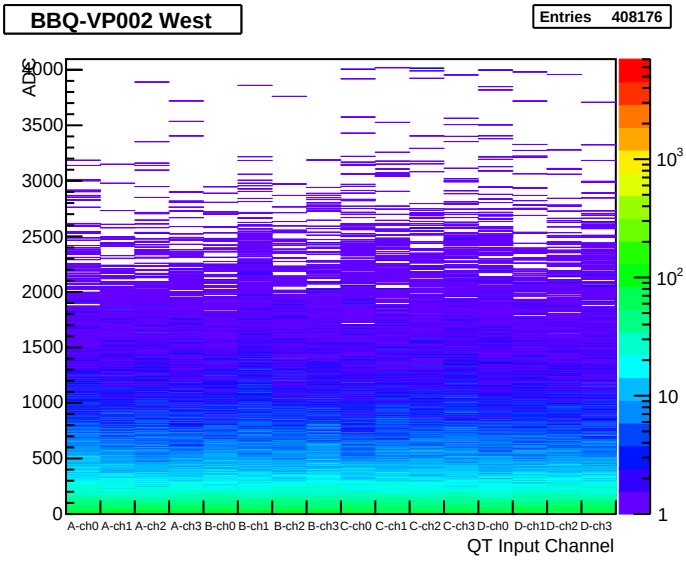

BBQ-BB002 (BBC west small tiles TAC)
Entries 408176
TAC
QT Input Channel

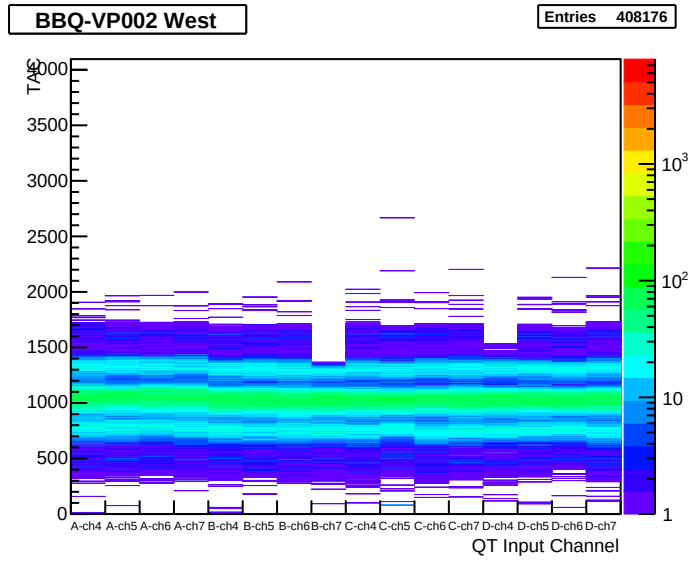

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 408176
ADC
QT Input Channel

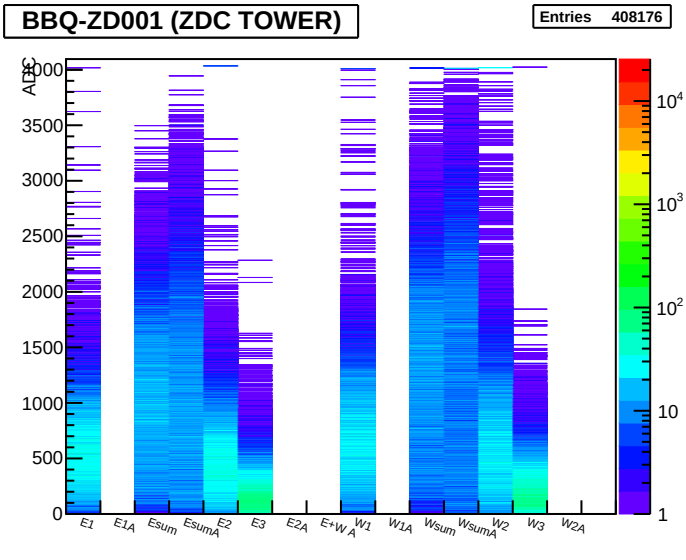

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 408176
TAC
QT Input Channel

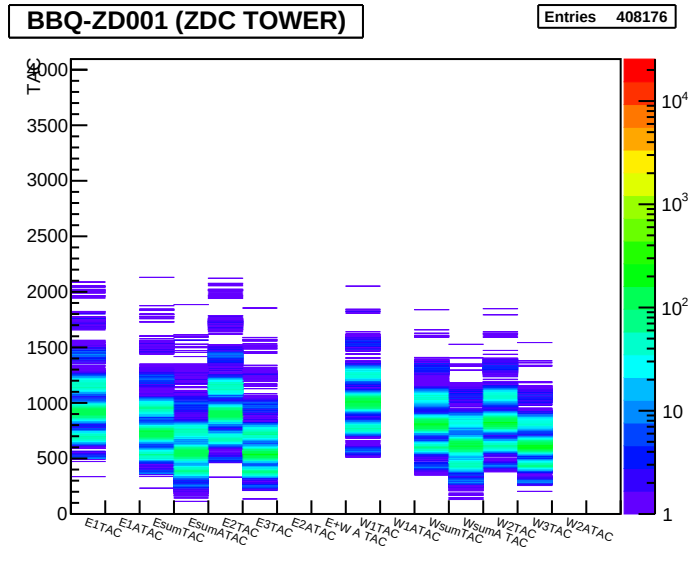

BBQ-VP001 East
Entries 408176
ADC
QT Input Channel

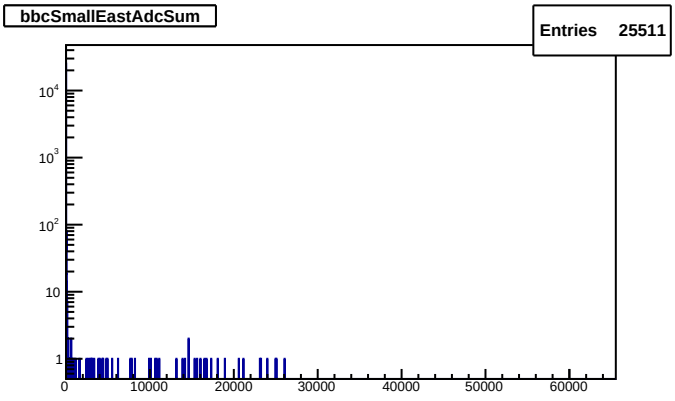

BBQ-VP001 East
Entries 408176
TAC
QT Input Channel

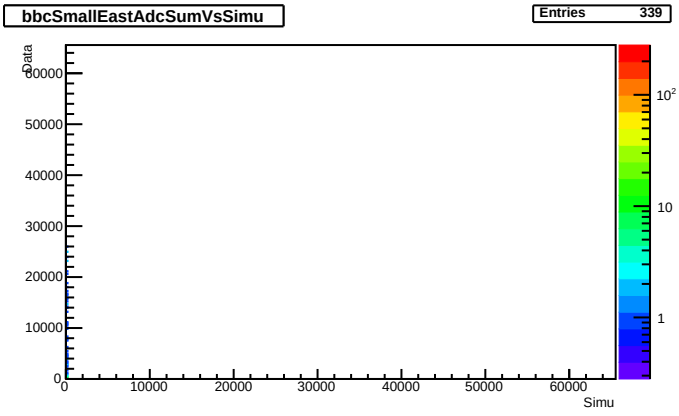

BBQ-VP002 West
Entries 408176
ADC
QT Input Channel

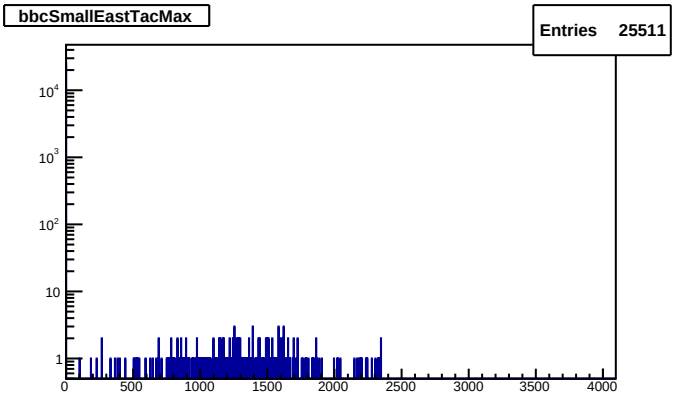

BBQ-VP002 West
Entries 408176
TAC
QT Input Channel

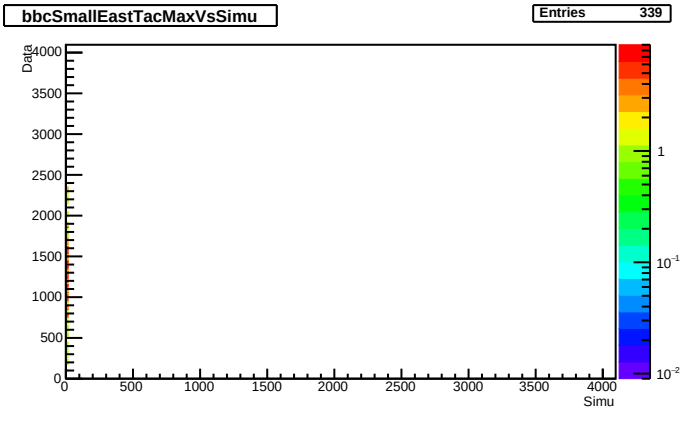

BBQ-ZD001 (ZDC TOWER)
Entries 408176
ADC

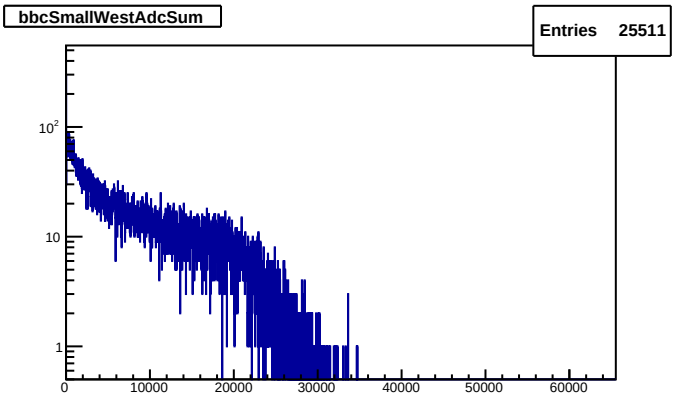

BBQ-ZD001 (ZDC TOWER)
Entries 408176
TAC

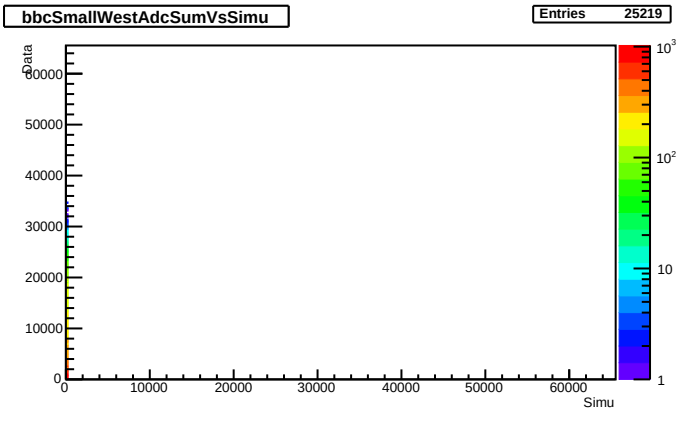

bbcSmallEastAdcSum
Entries 25511

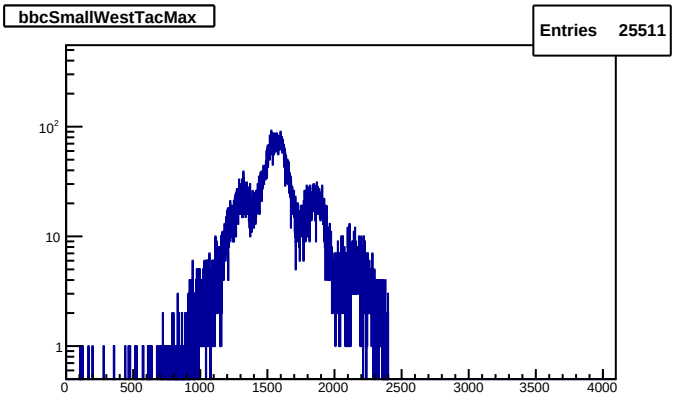

bbcSmallEastAdcSumVsSimu
Entries 339
Data
Simu

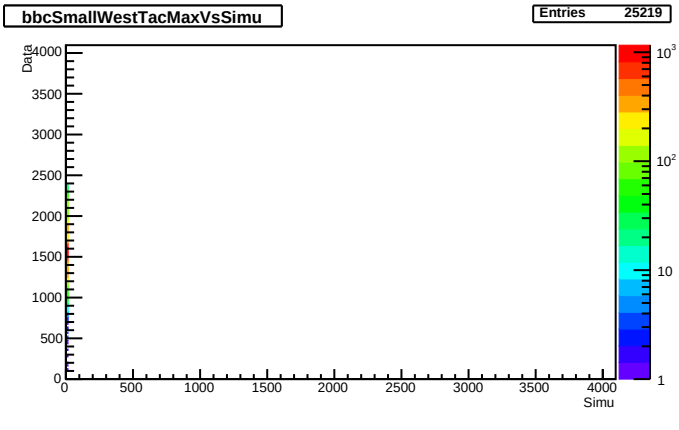

bbcSmallEastTacMax
Entries 25511

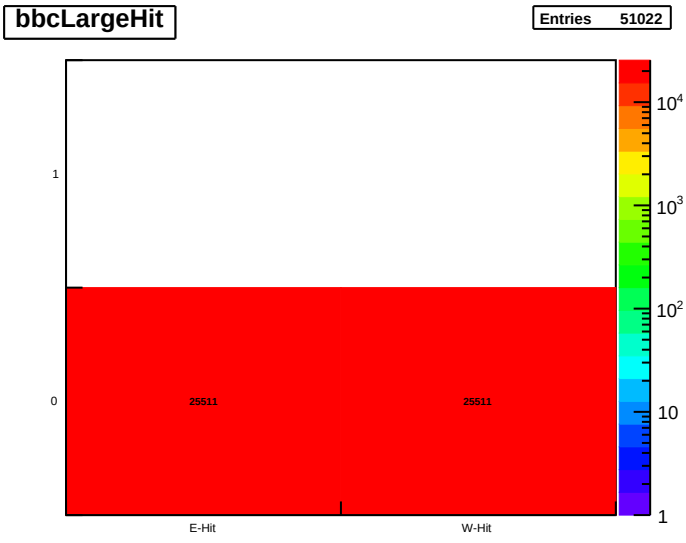

bbcSmallEastTacMaxVsSimu
Entries 339
Data
Simu

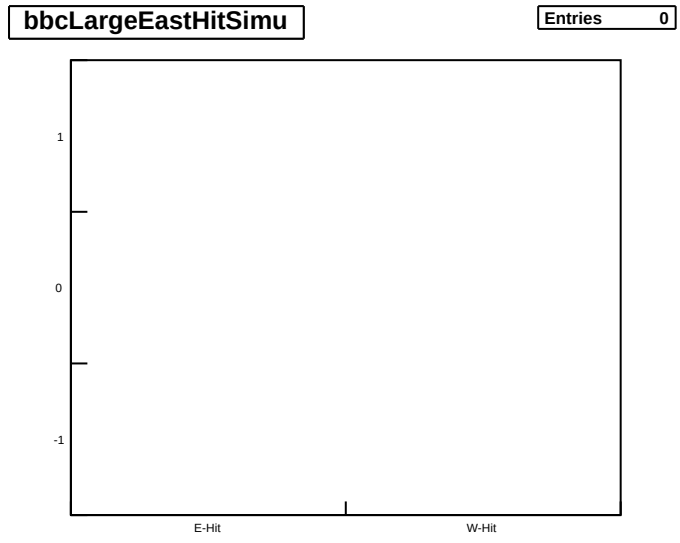

bbcSmallWestAdcSum
Entries 25511

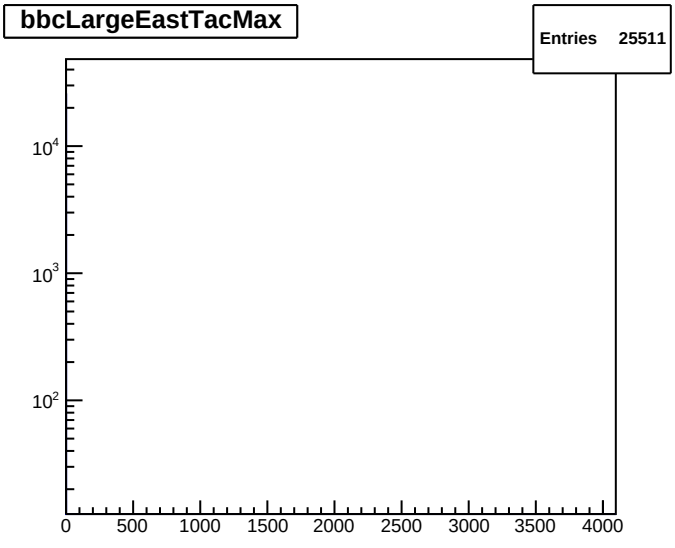

bbcSmallWestAdcSumVsSimu
Entries 25219
Data
Simu

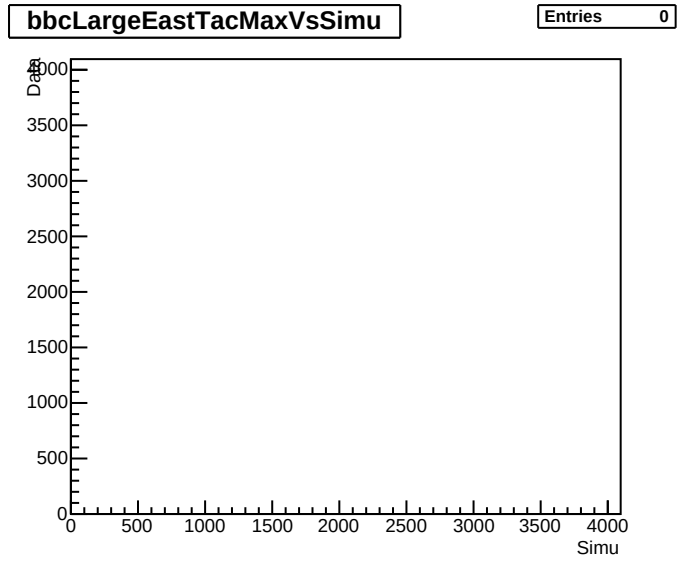

bbcSmallWestTacMax
Entries 25511

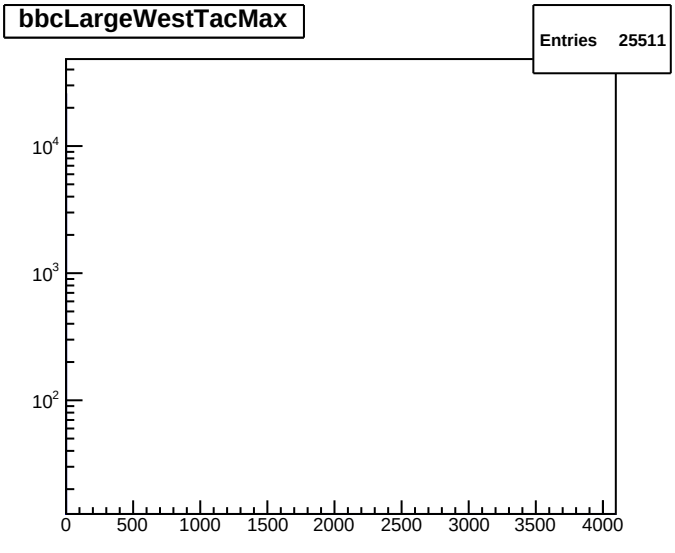

bbcSmallWestTacMaxVsSimu
Entries 25219
Data
Simu

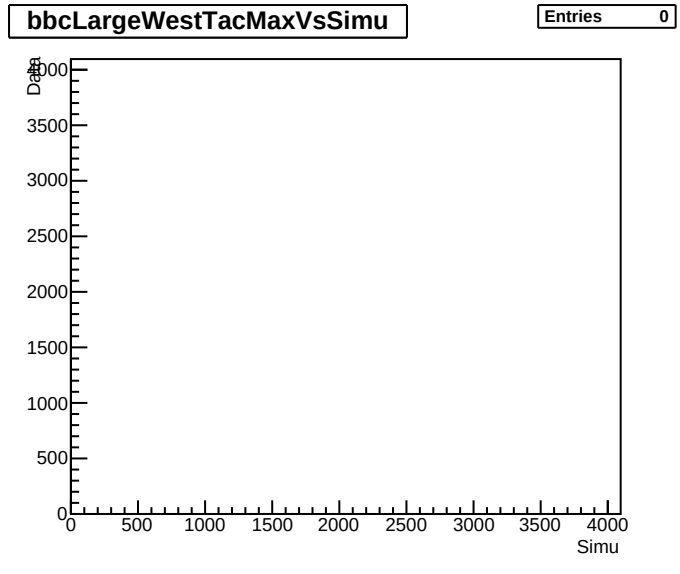

bbcLargeHit
Entries 51022
25511
25511
E-Hit
W-Hit

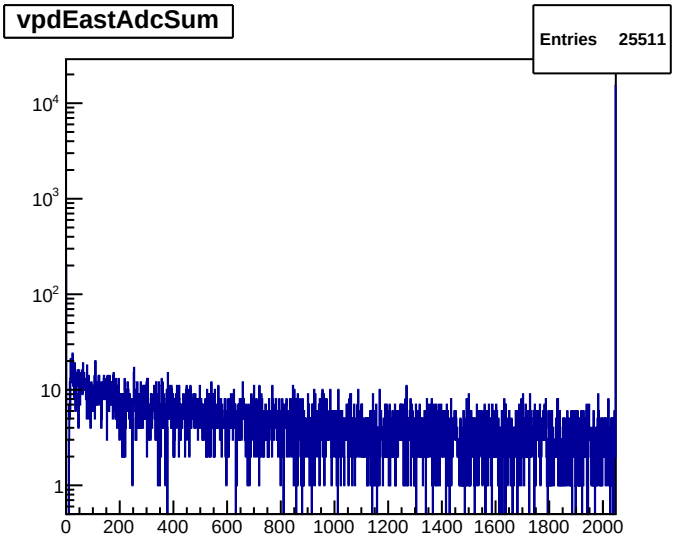

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

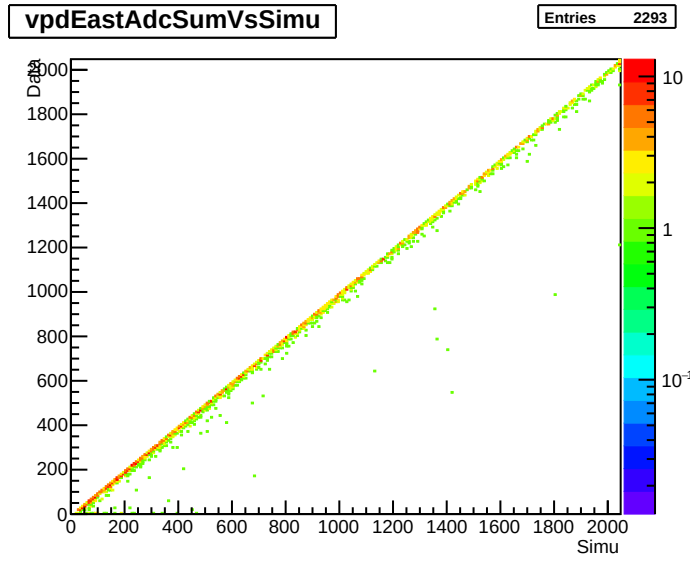

bbcLargeEastTacMax
Entries 25511

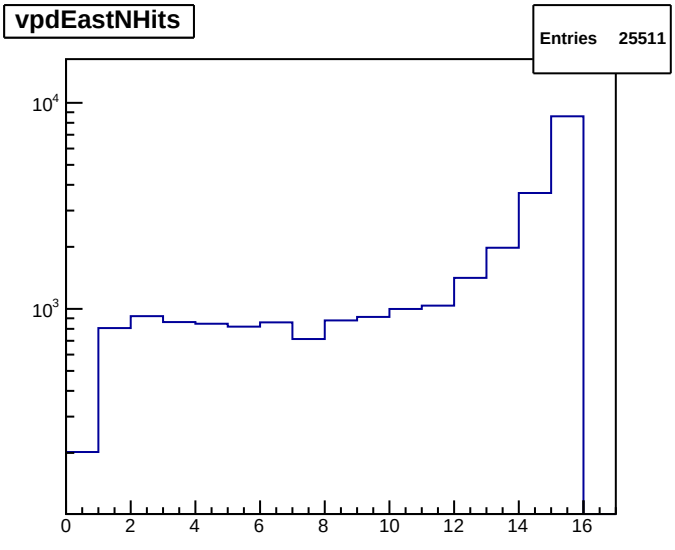

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

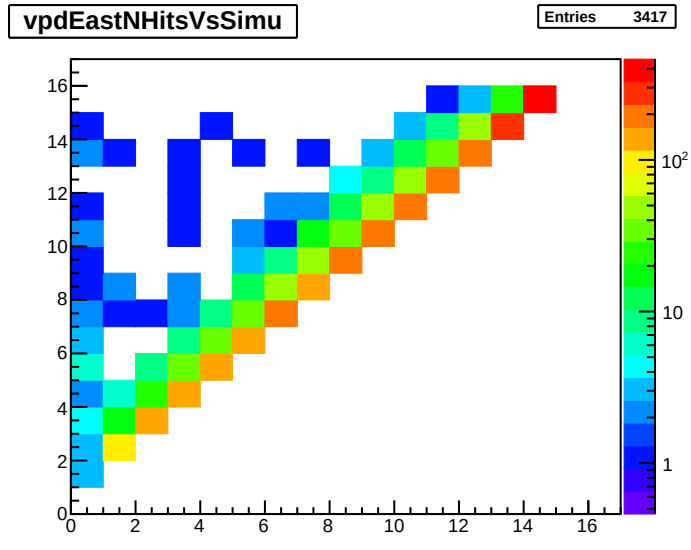

bbcLargeWestTacMax
Entries 25511

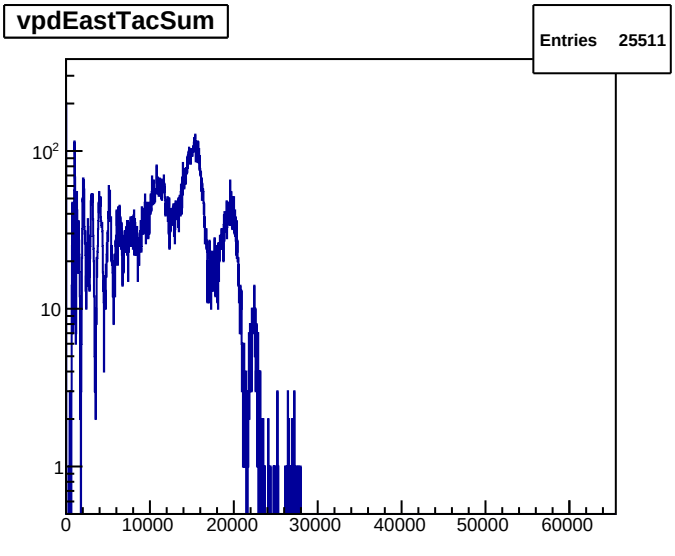

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

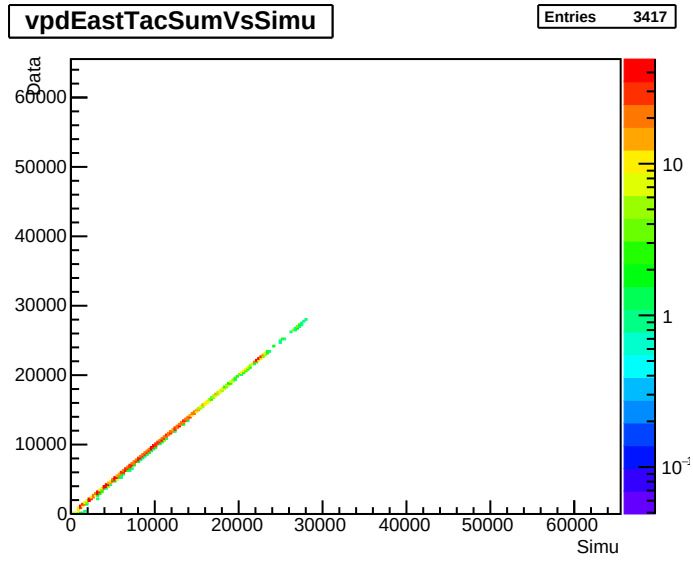

vpdEastAdcSum
Entries 25511

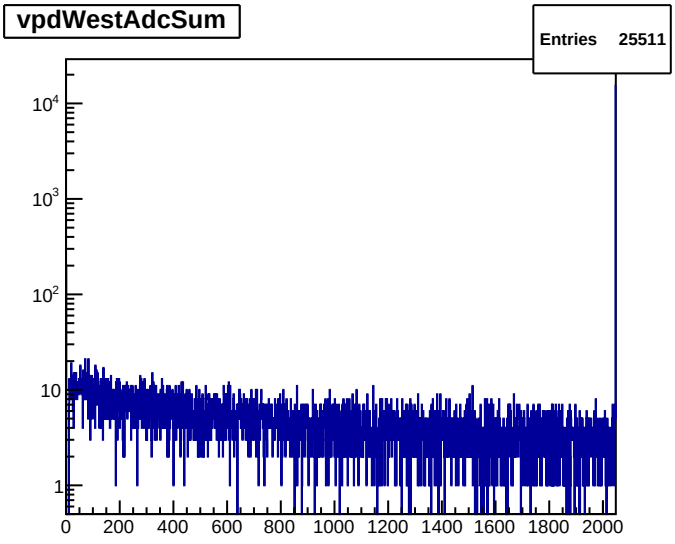

vpdEastAdcSumVsSimu
Entries 2293
Data
Simu

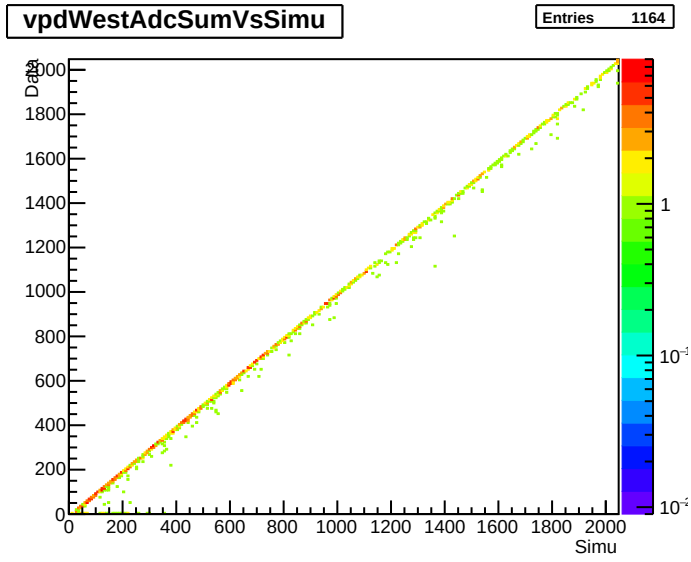

vpdEastNHits
Entries 25511

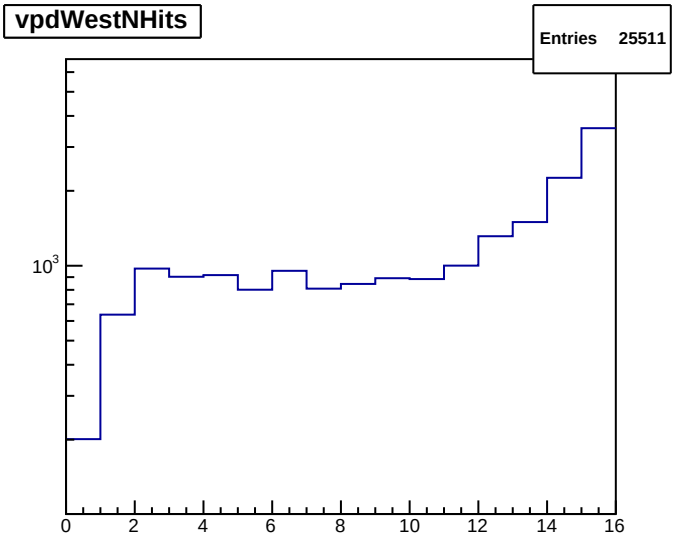

vpdEastNHitsVsSimu
Entries 3417

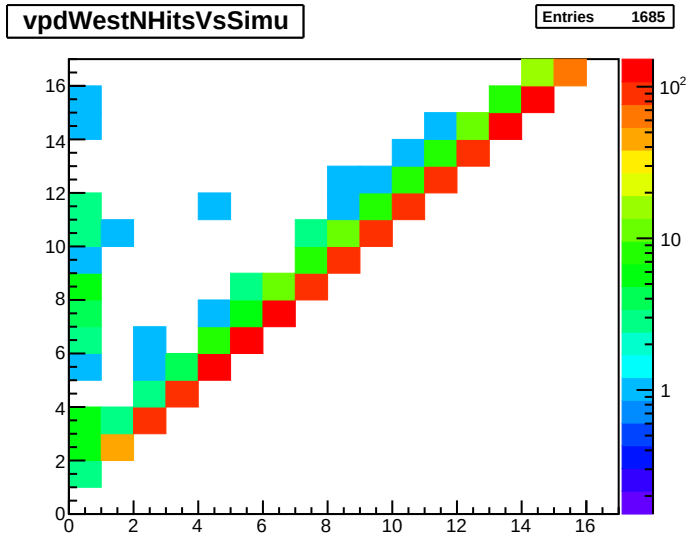

vpdEastTacSum
Entries 25511

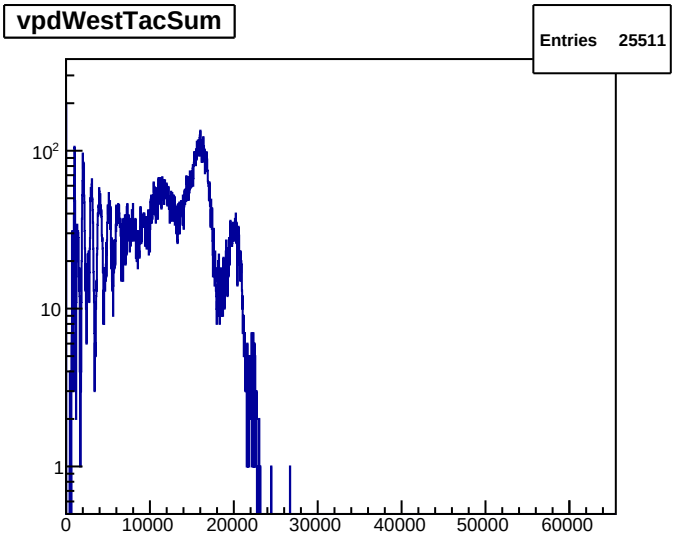

vpdEastTacSumVsSimu
Entries 3417
Data
Simu

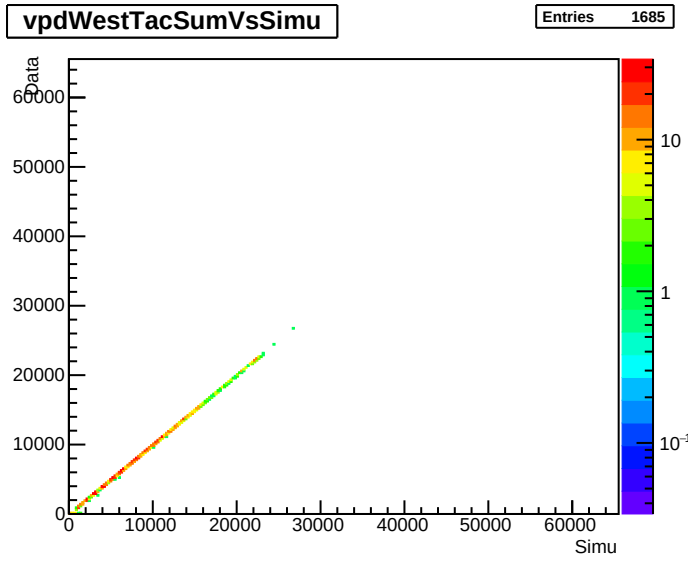

vpdWestAdcSum
Entries 25511

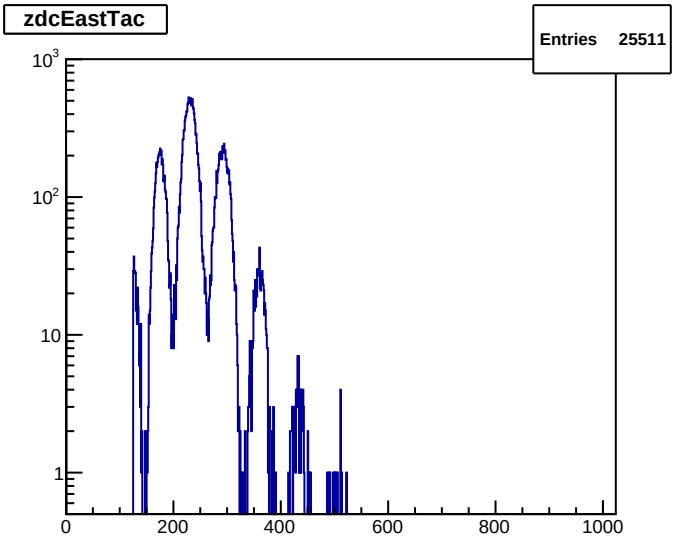

vpdWestAdcSumVsSimu
Entries 1164
Data
Simu

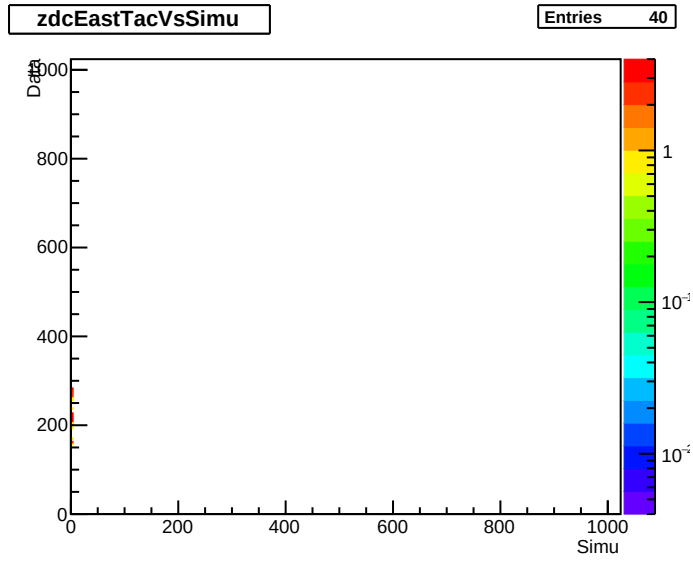

vpdWestNHits
Entries 25511

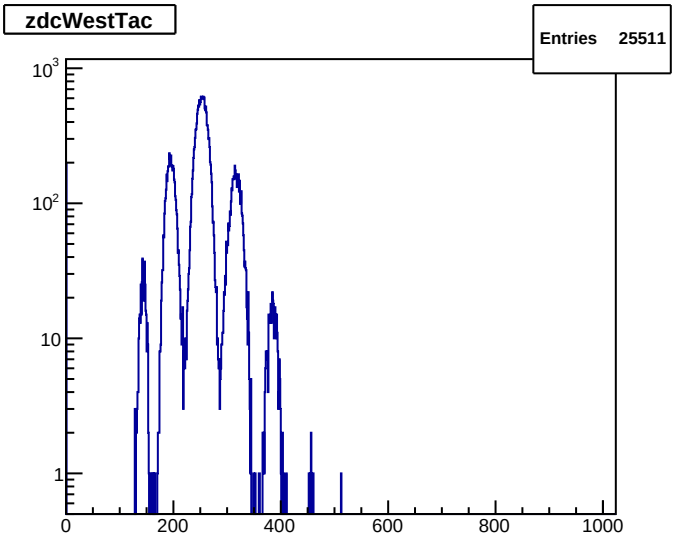

vpdWestNHitsVsSimu
Entries 1685

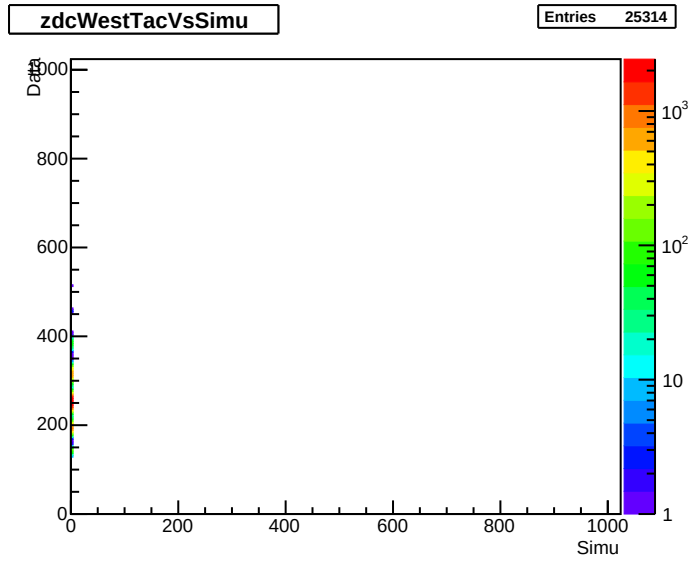

vpdWestTacSum
Entries 25511

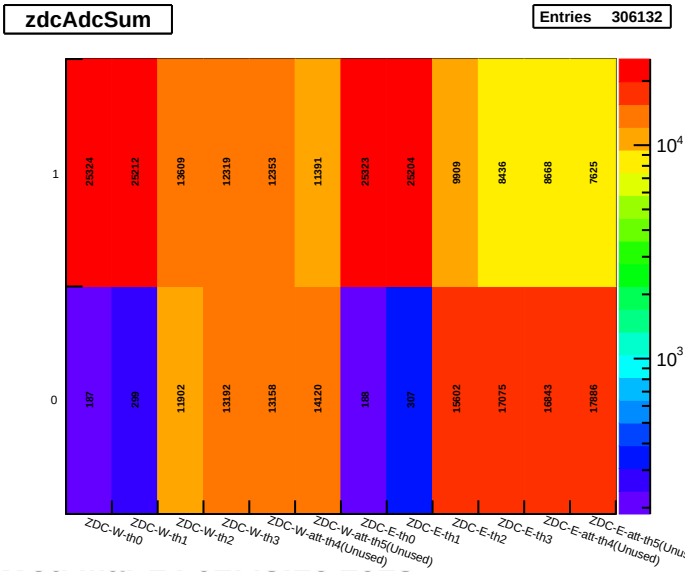

vpdWestTacSumVsSimu
Entries 1685
Data
Simu

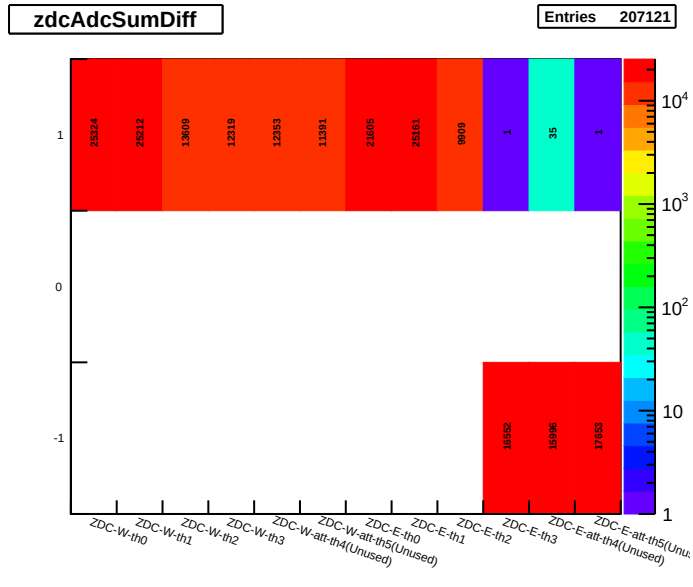

zdcEastTac
Entries 25511

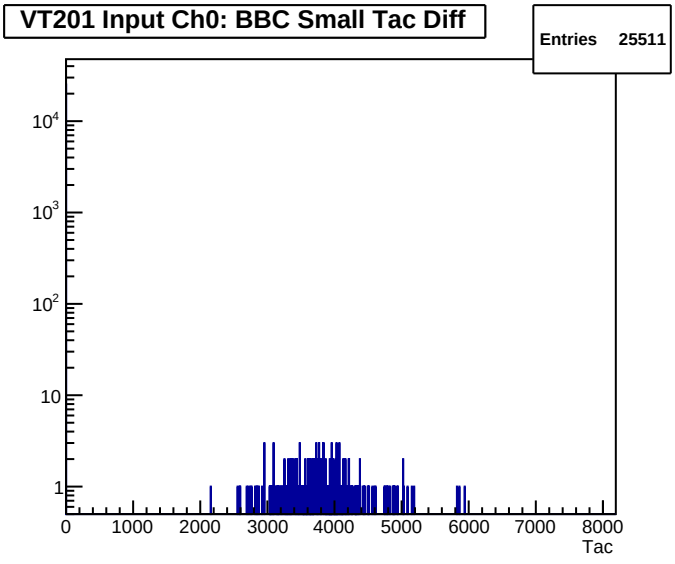

zdcEastTacVsSimu
Entries 40
Data
Simu

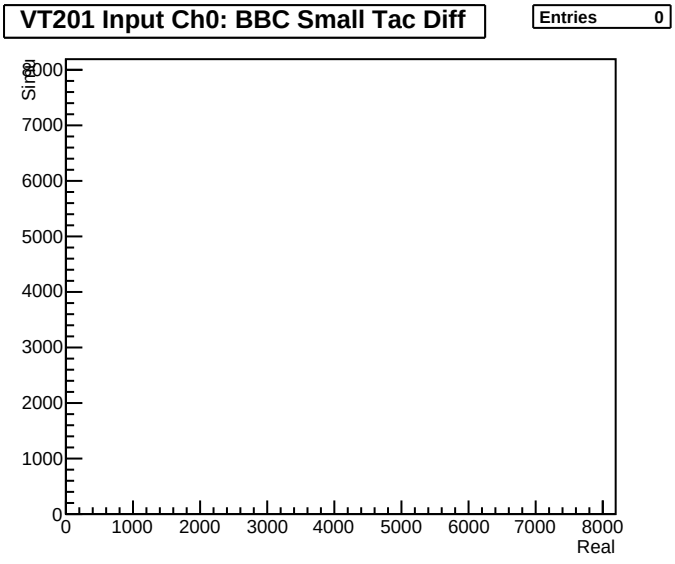

zdcWestTac
Entries 25511

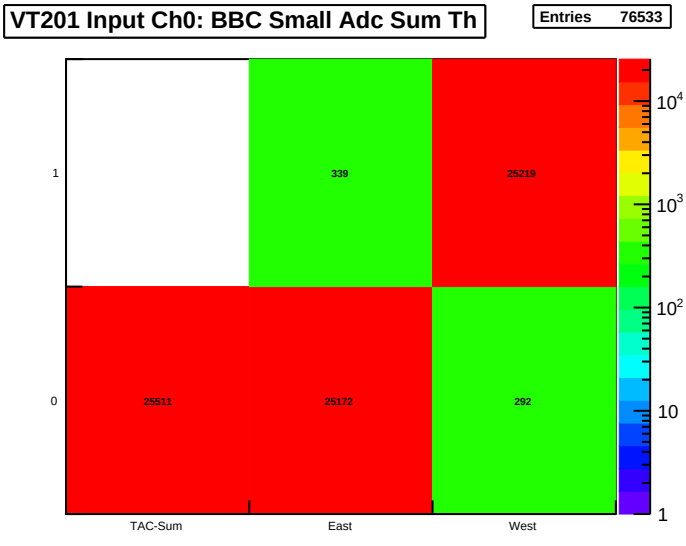

zdcWestTacVsSimu
Entries 25314
Data
Simu

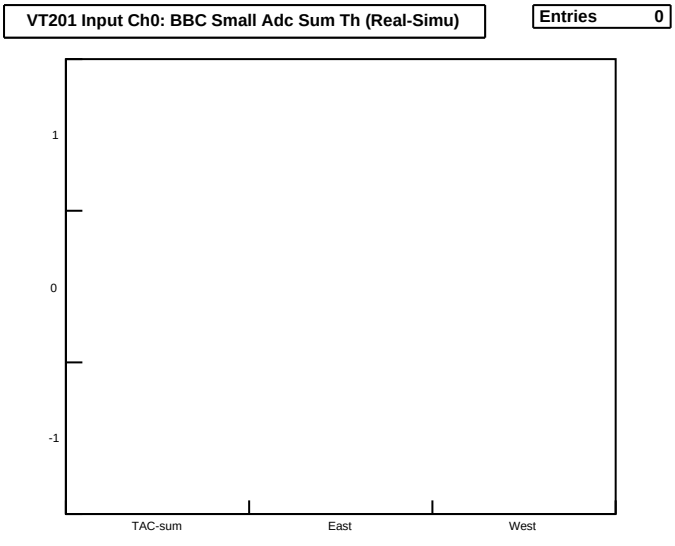

zdcAdcSum
Entries 306132
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

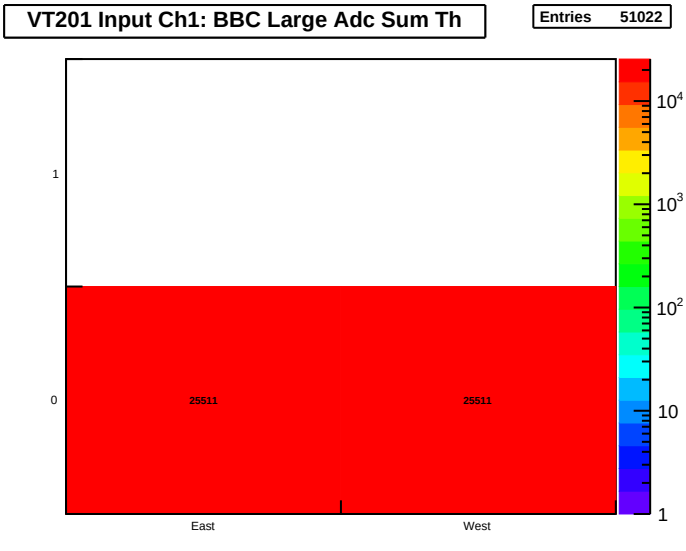

zdcAdcSumDiff
Entries 207121
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

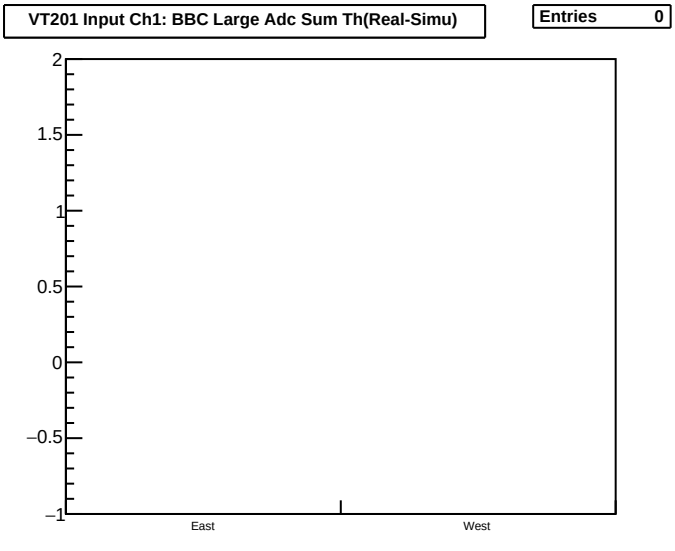

VT201 Input Ch0: BBC Small Tac Diff
Entries 25511
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

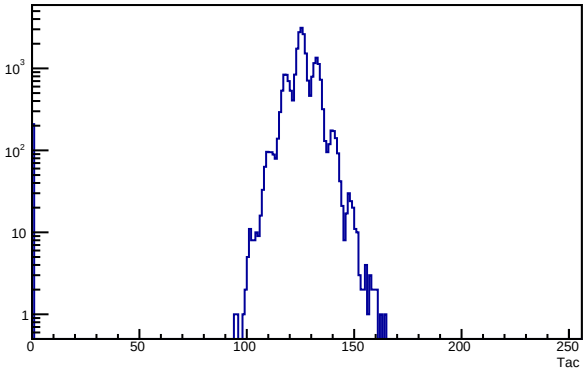

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 76533
339
25219
25511
25172
292
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

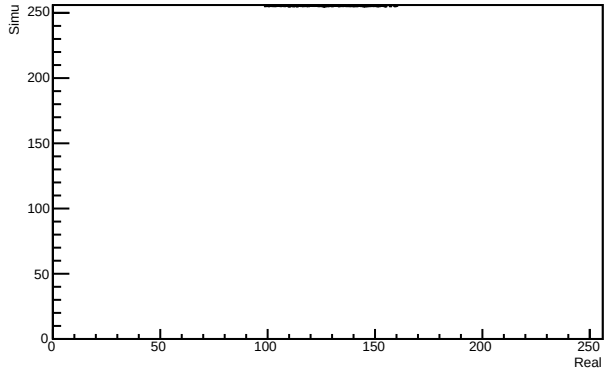

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 51022
25511
25511
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

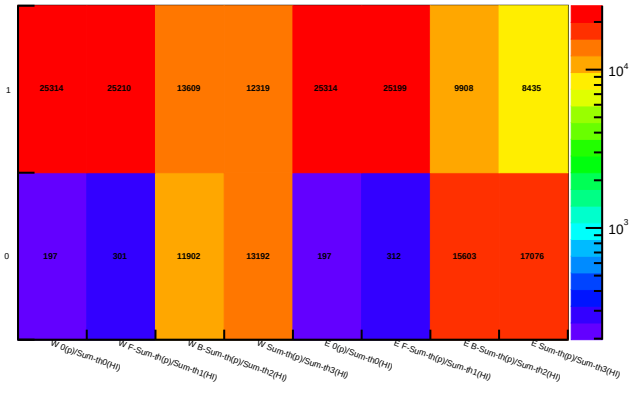
VT201 Input Ch2: ZDC Tac Diff



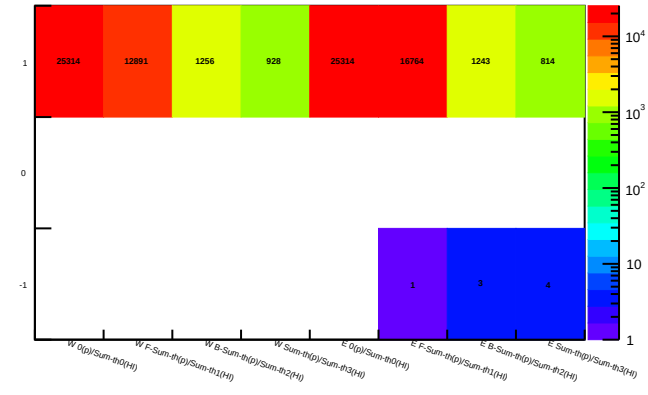
VT201 Input Ch2: ZDC Tac Diff



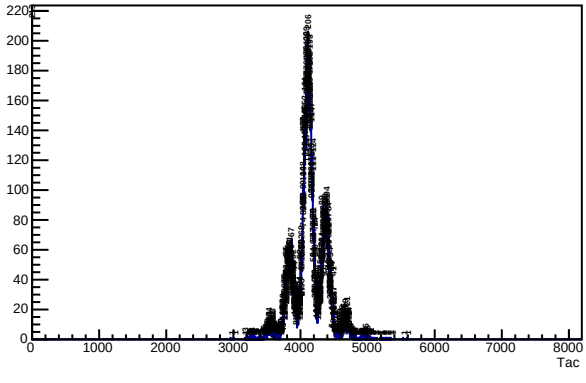
VT201 Input Ch2: ZDC Adc Sum Th



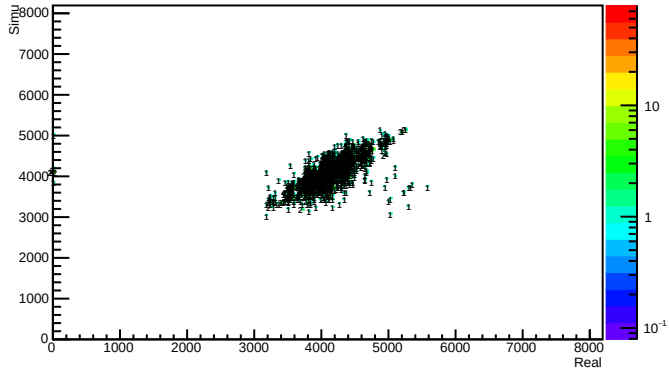
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



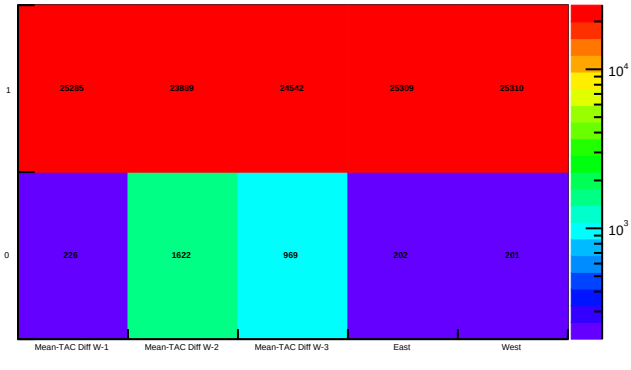
VT201 Input Ch4: VPD Tac Diff



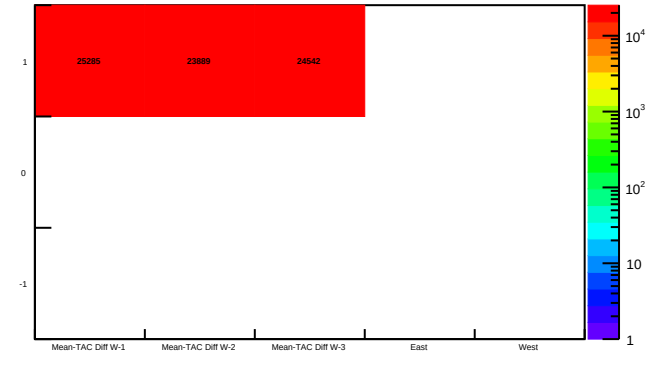
VT201 Input Ch4: VPD Tac Diff

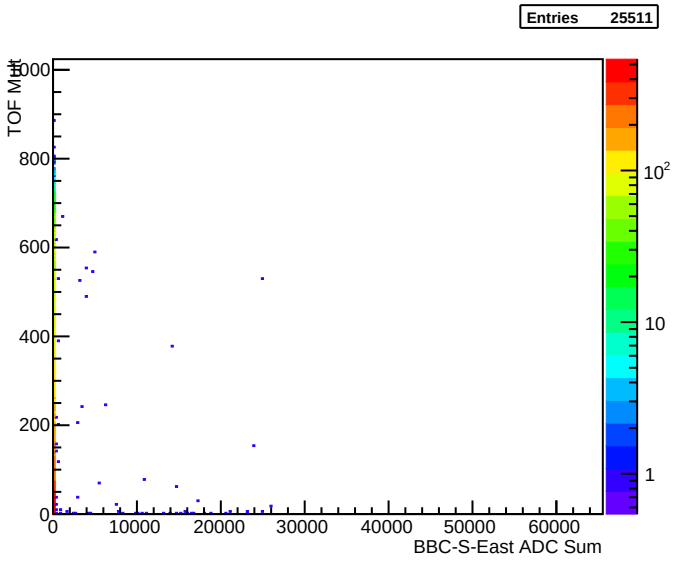


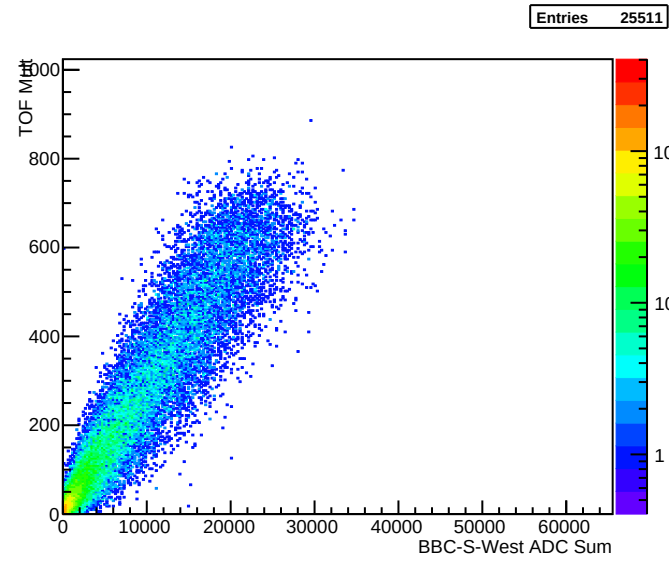
VT201 Input Ch4: VPD Adc Sum Th

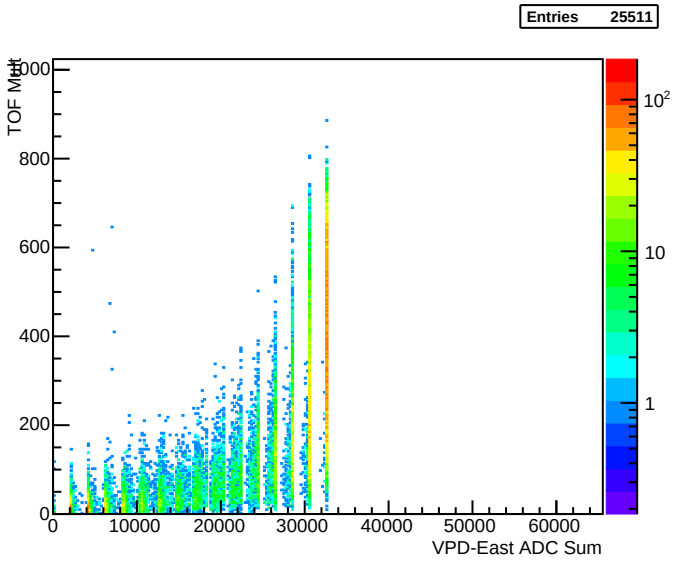


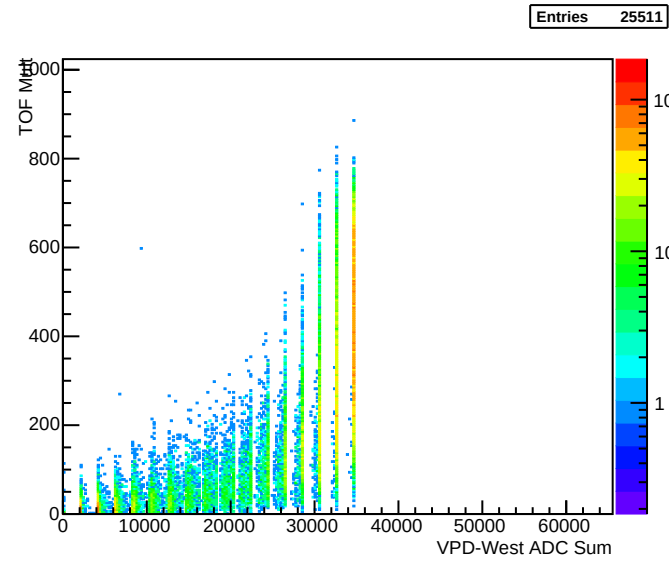
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

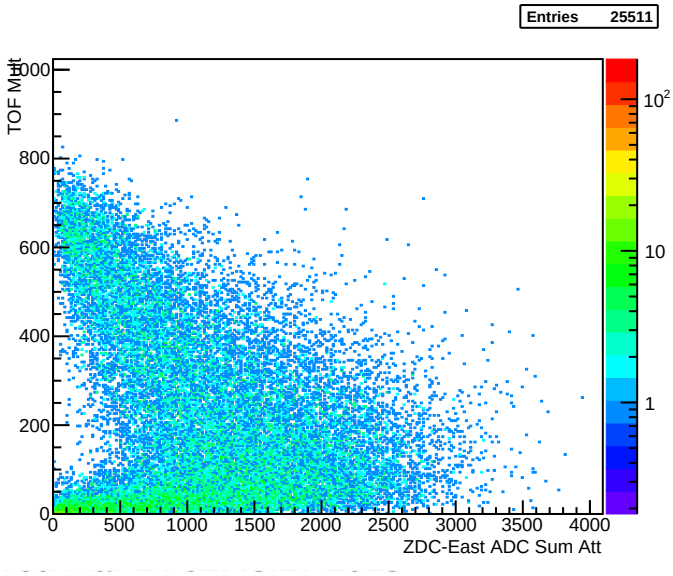


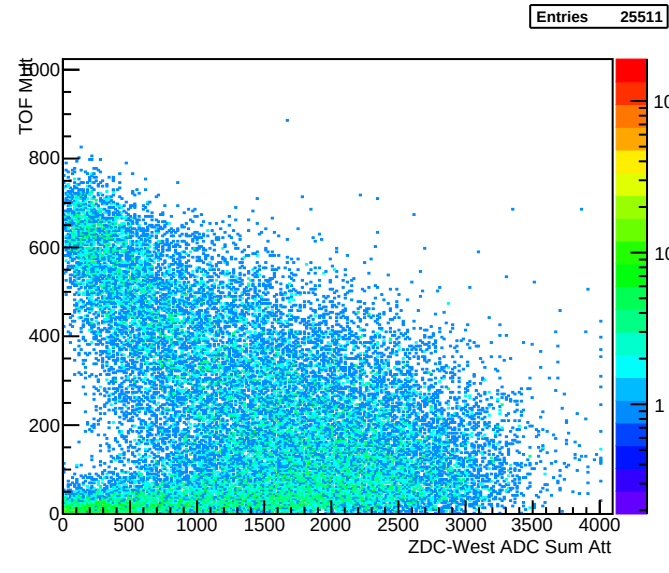

Entries 25511
TOF Mult
BBC-S-East ADC Sum

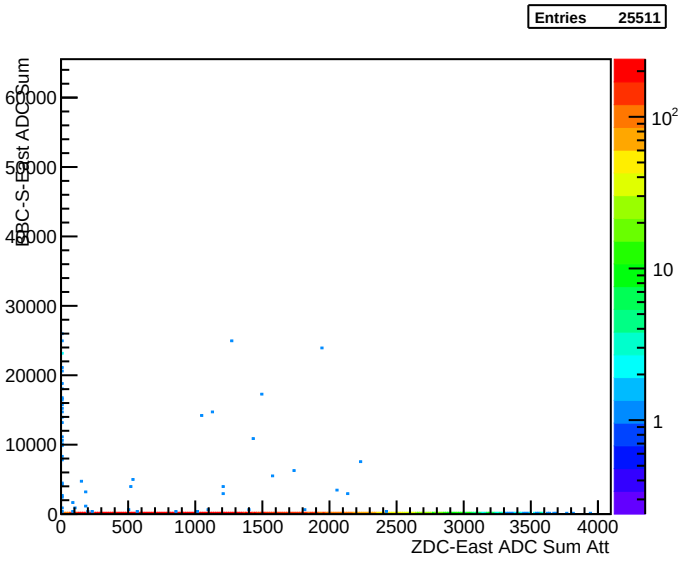

Entries 25511
TOF Mult
BBC-S-West ADC Sum

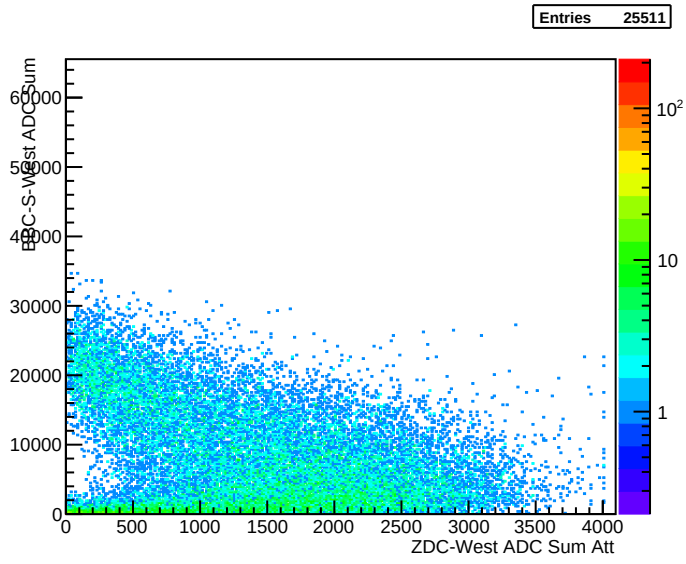

Entries 25511
TOF Mult
VPD-East ADC Sum

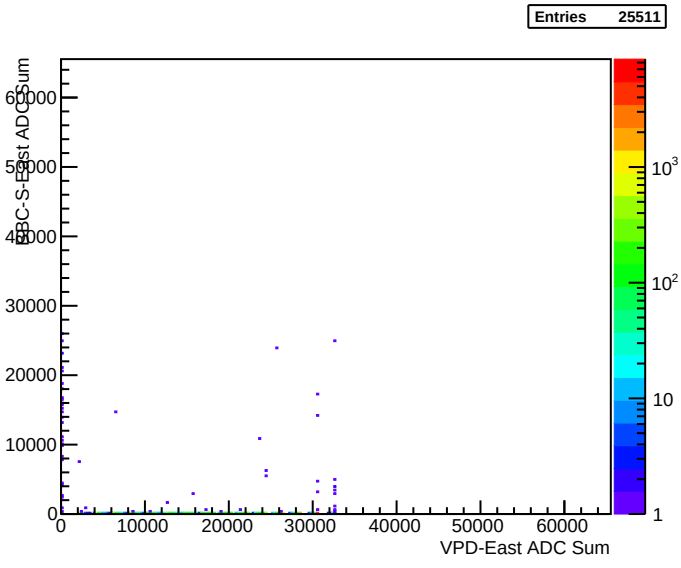

Entries 25511
TOF Mult
VPD-West ADC Sum

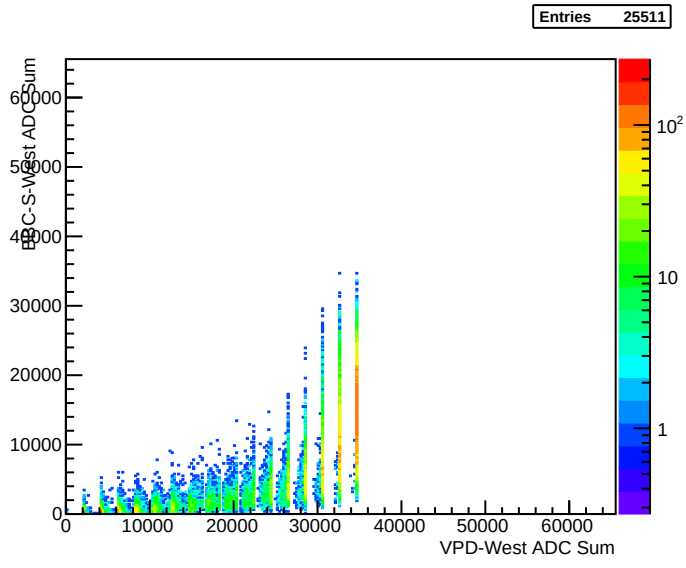

Entries 25511
TOF Mult
ZDC-East ADC Sum Att

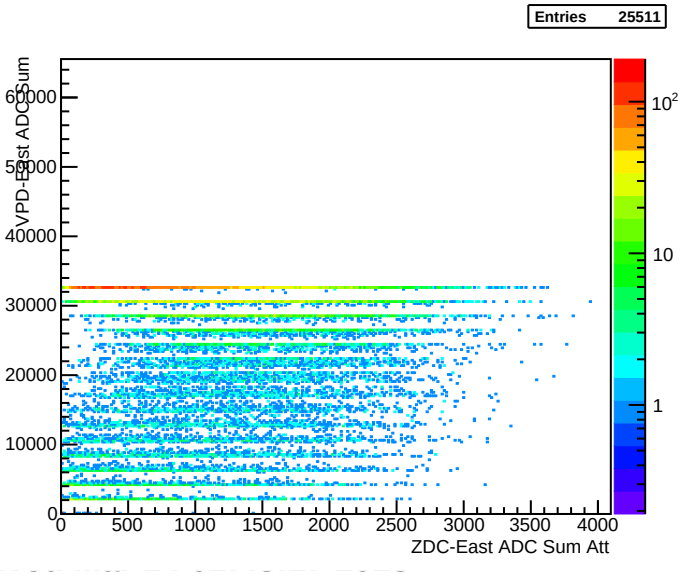

Entries 25511
TOF Mult
ZDC-West ADC Sum Att

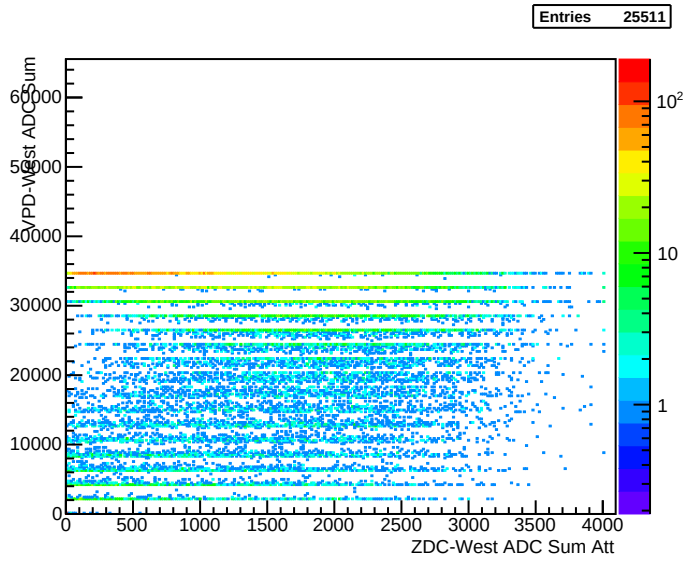

Entries 25511
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

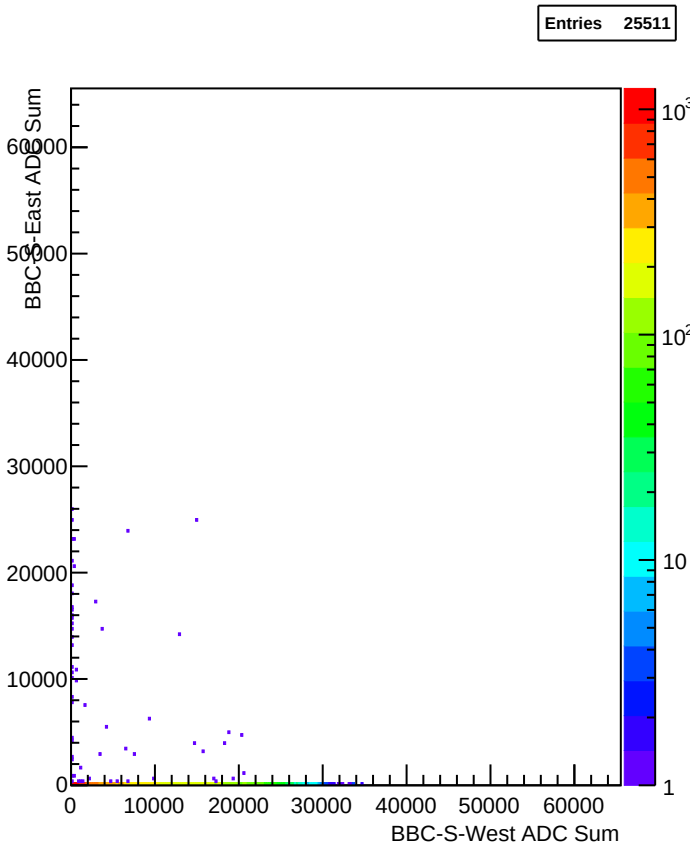

Entries 25511
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

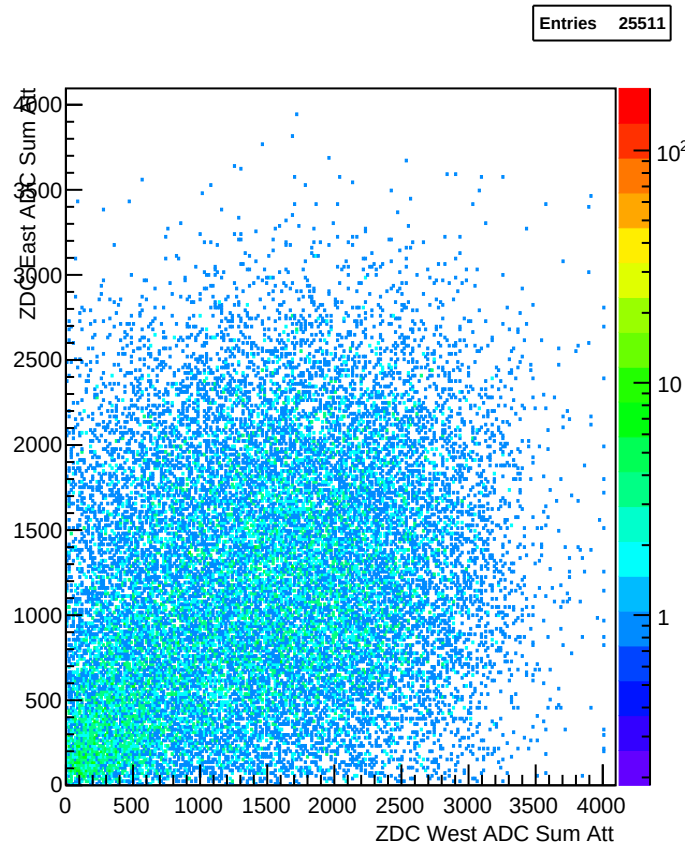

Entries 25511
BBC-S-East ADC Sum
VPD-East ADC Sum

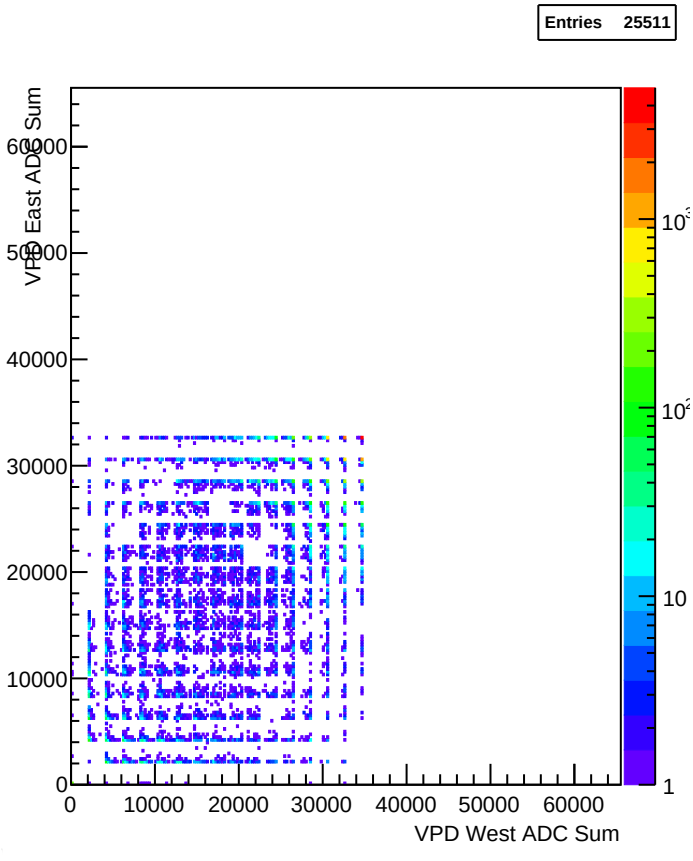

Entries 25511
BBC-S-West ADC Sum
VPD-West ADC Sum

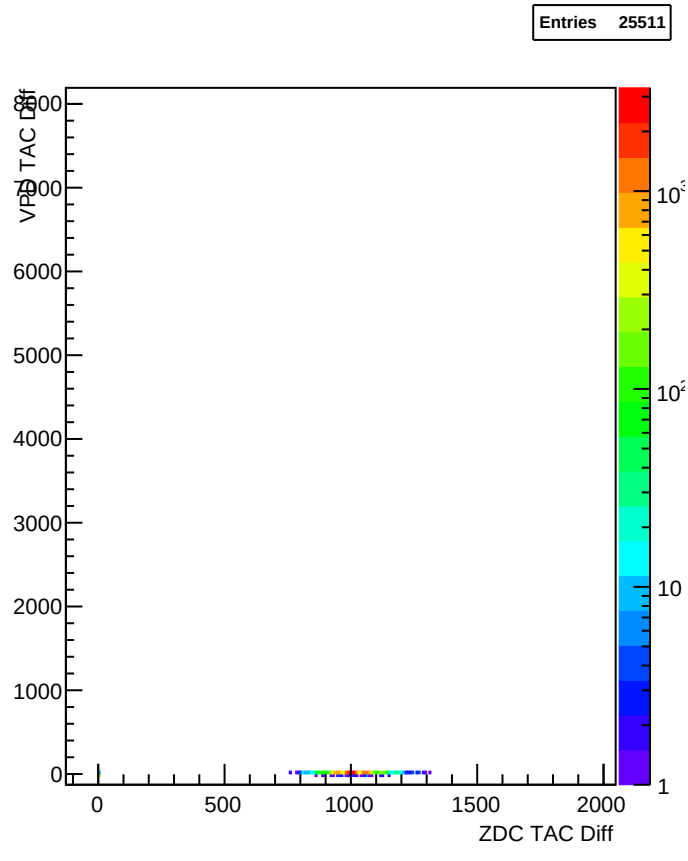

Entries 25511
VPD-East ADC Sum
ZDC-East ADC Sum Att

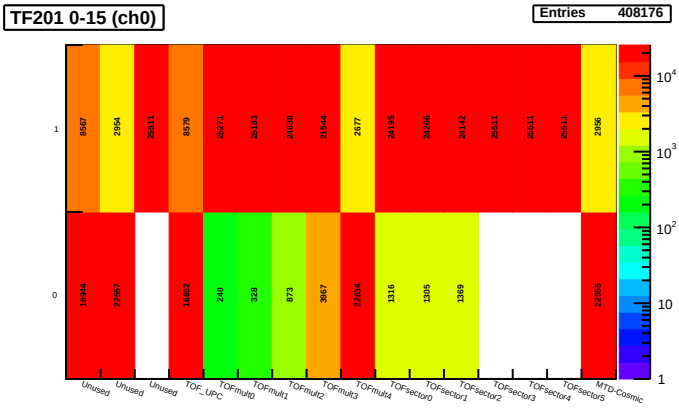

Entries 25511
VPD-West ADC Sum
ZDC-West ADC Sum Att

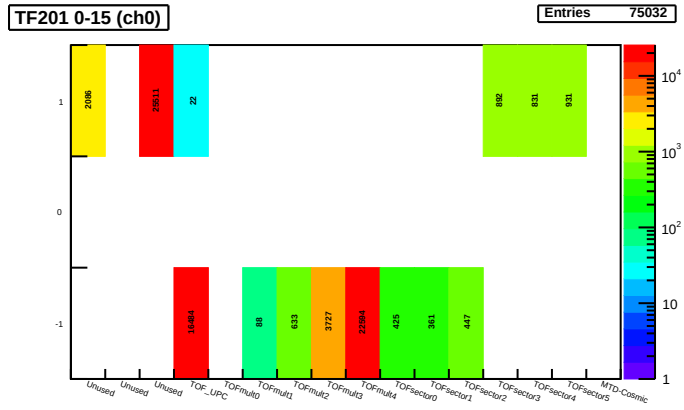

Entries 25511
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 25511
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 25511
VPD East ADC Sum
VPD West ADC Sum


Entries 25511
VPD TAC Diff
ZDC TAC Diff


TF201 0-15 (ch0)
Entries 408176
8567 2954 25511 8579 26271 25183 24638 21944 2677 24195 24206 24142 25511 25511 25511 2956
16944 22557 16932 240 328 873 3967 22834 1316 1305 1369 22555
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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Entries 75032
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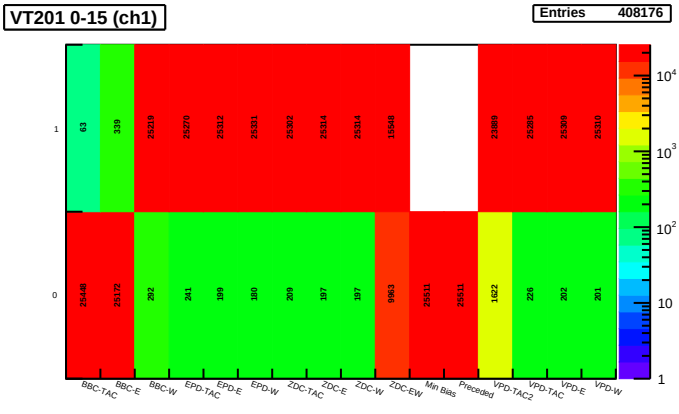
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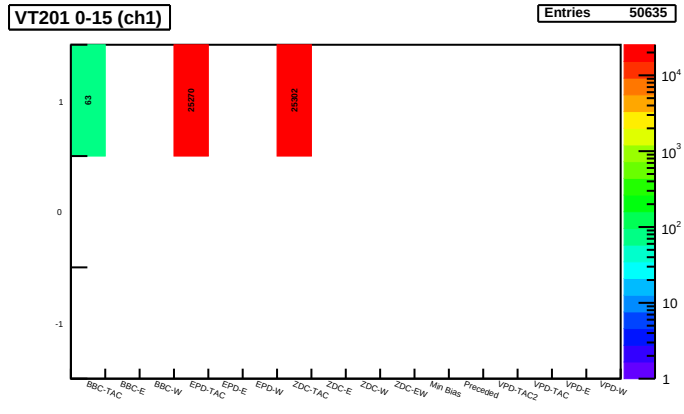
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W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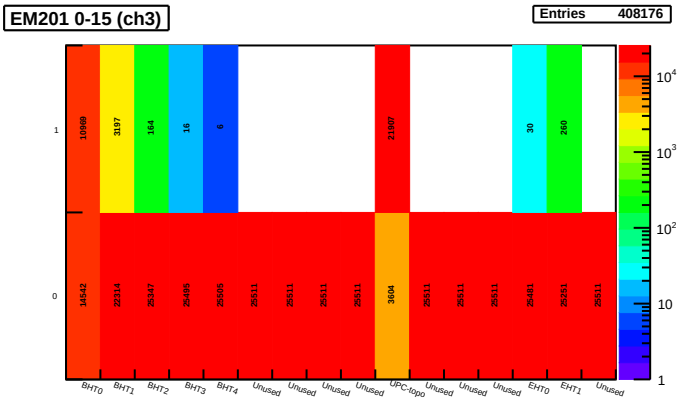
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Entries 408176
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14542 22314 25347 25499 25505 25511 25511 25511 25511 3604 25511 25511 25511 25481 25251 25511
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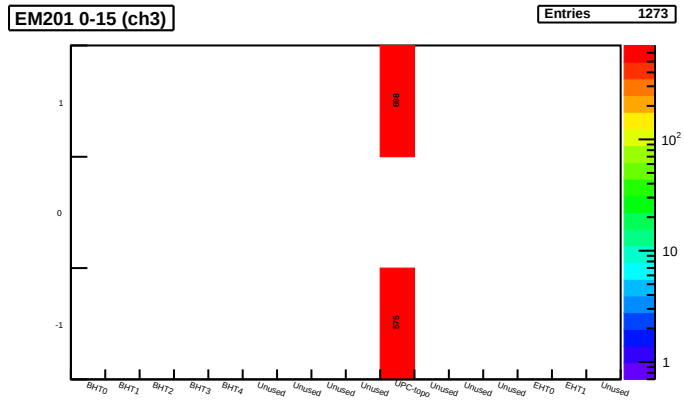
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Entries 1273
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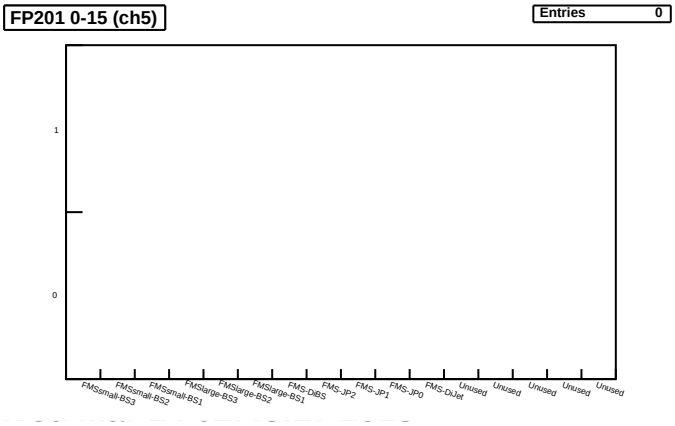
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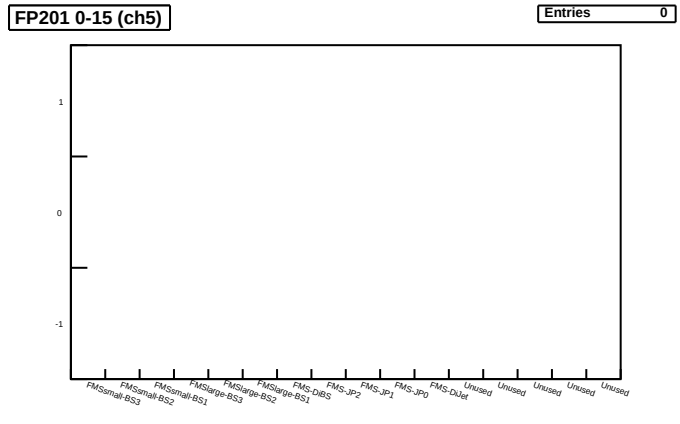
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