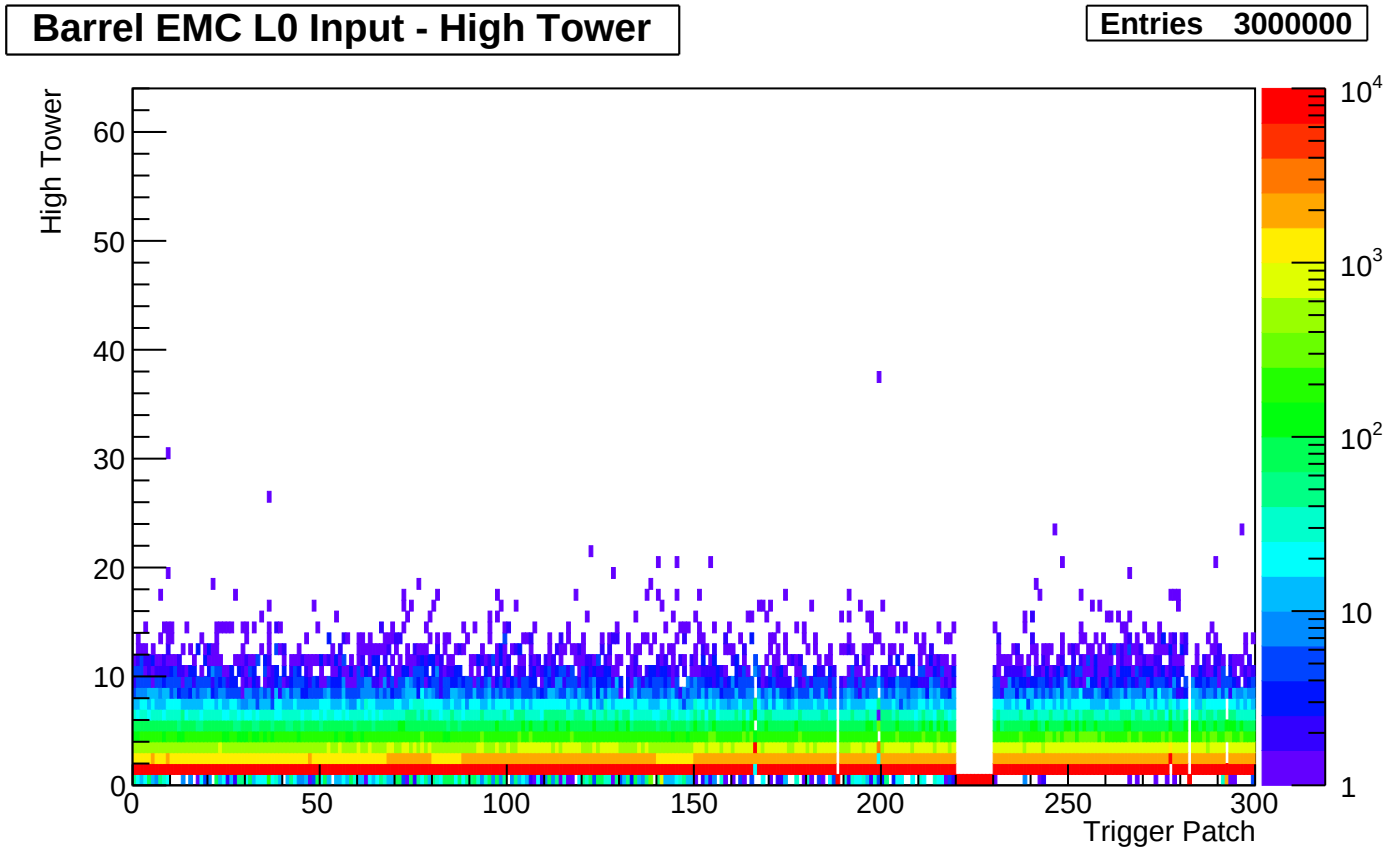
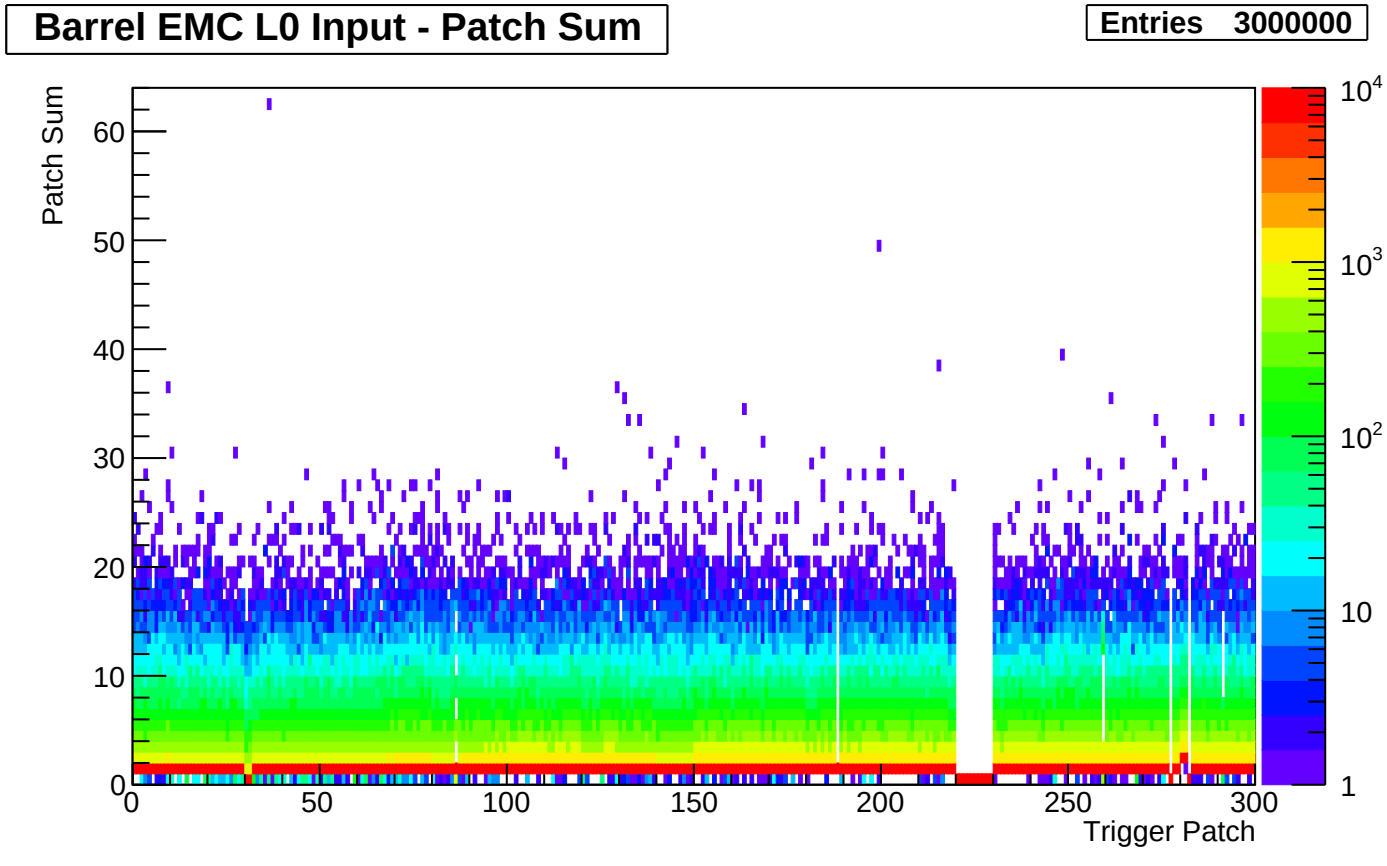
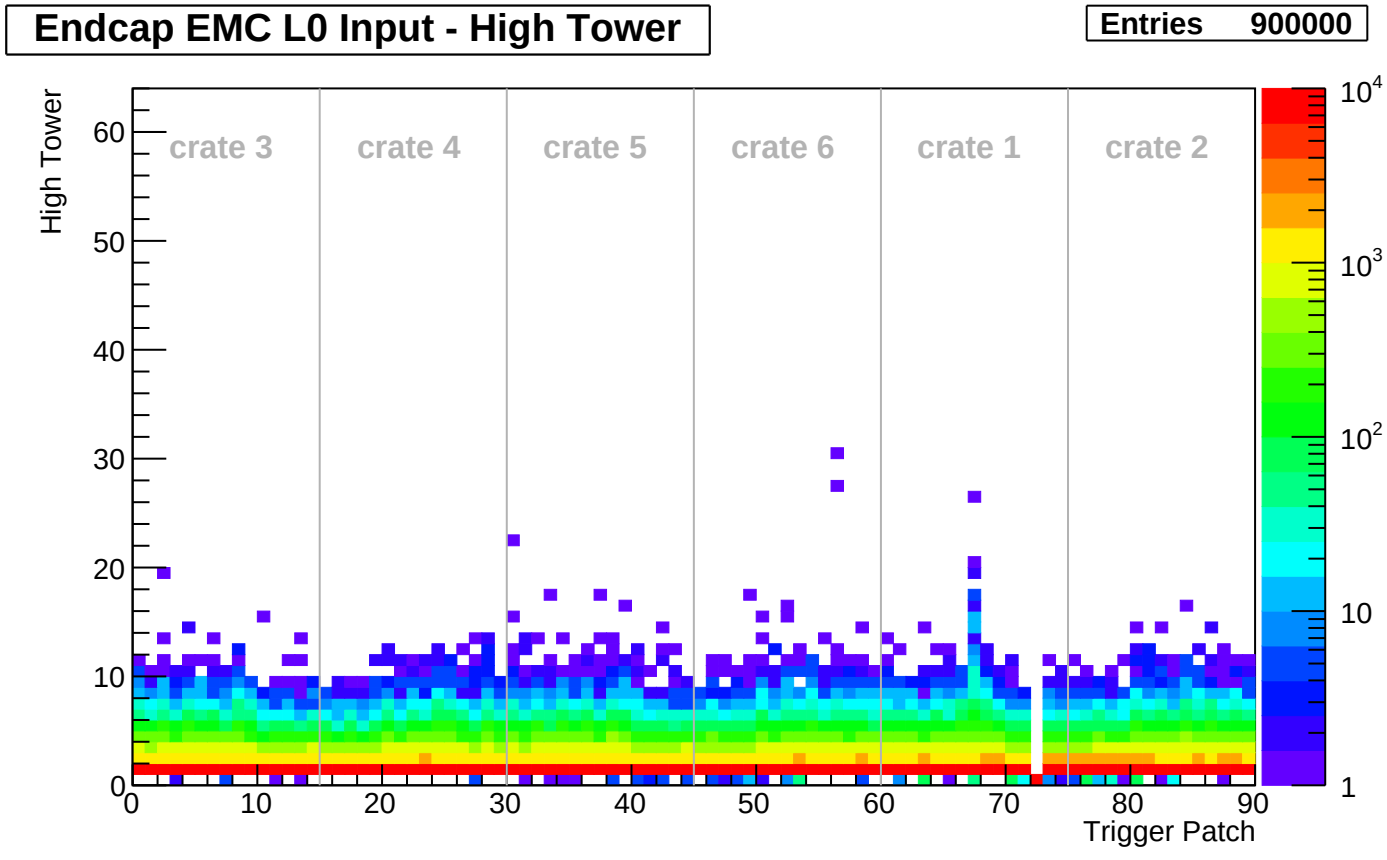
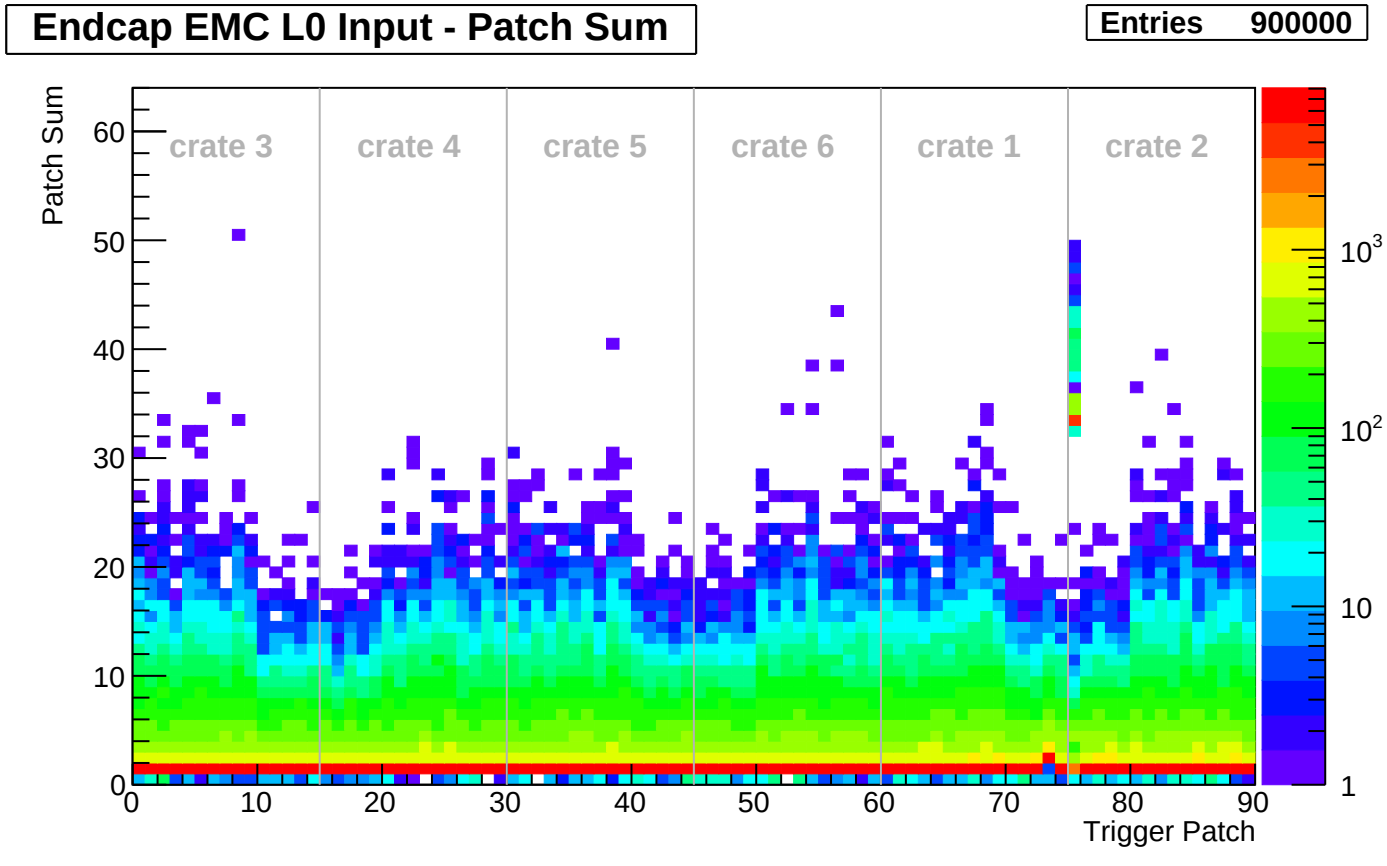
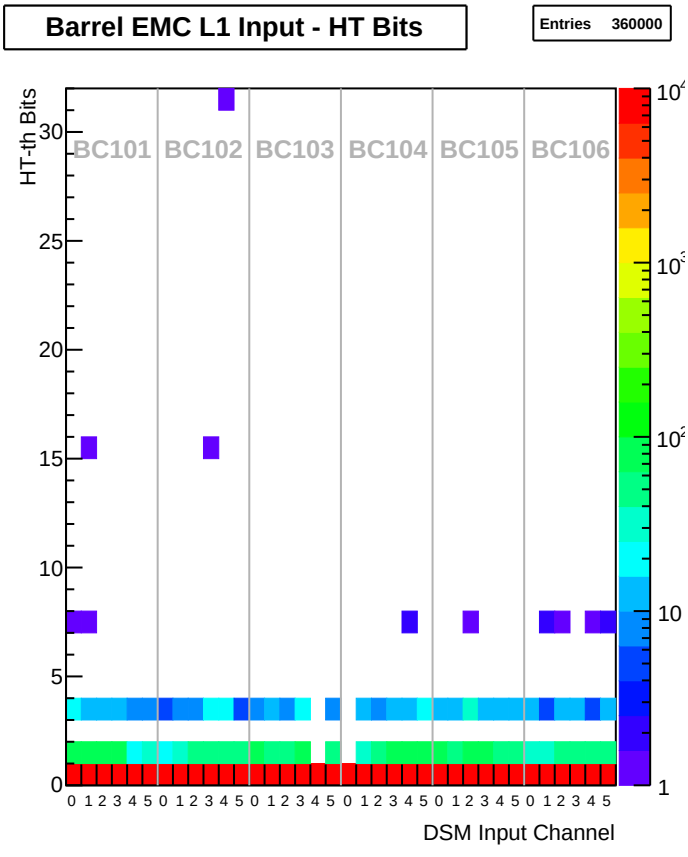
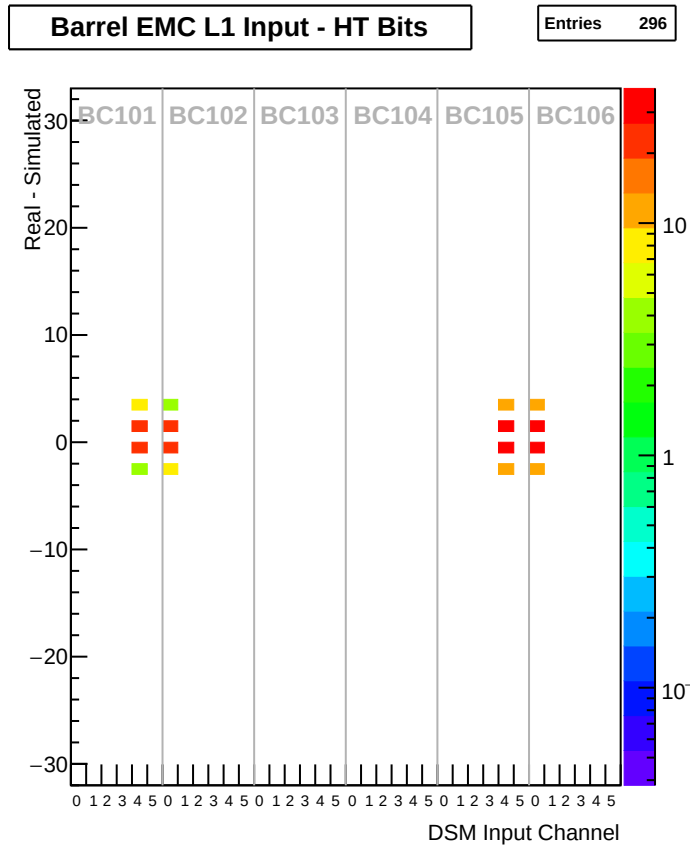

Barrel EMC L0 Input - High Tower
Entries 3000000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

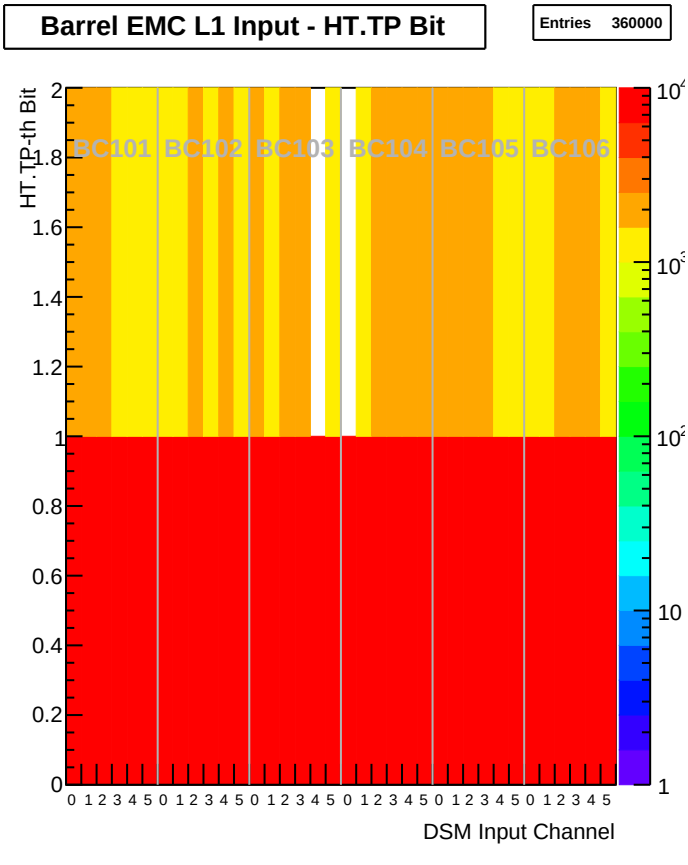

Barrel EMC L0 Input - Patch Sum
Entries 3000000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

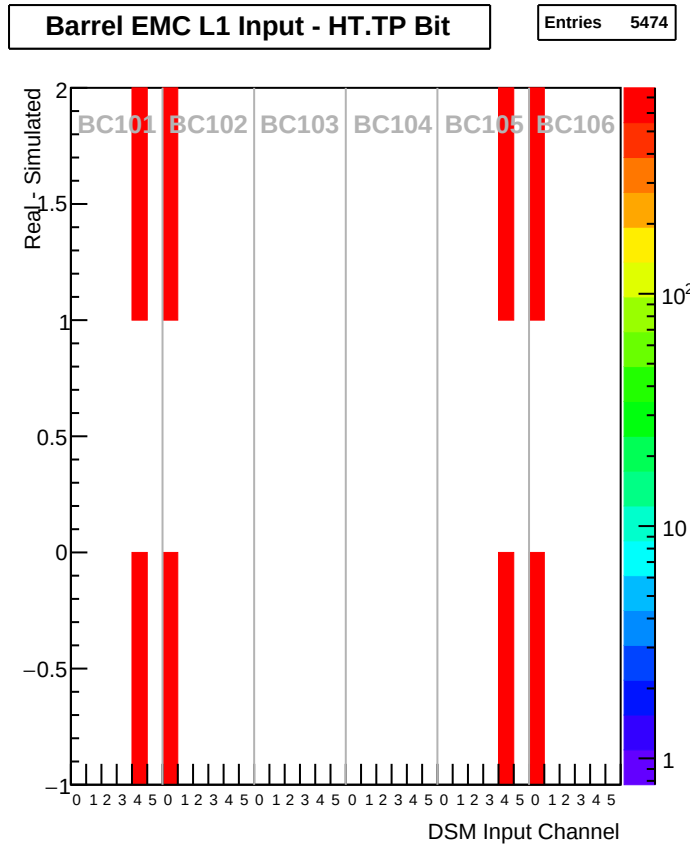

Endcap EMC L0 Input - High Tower
Entries 900000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

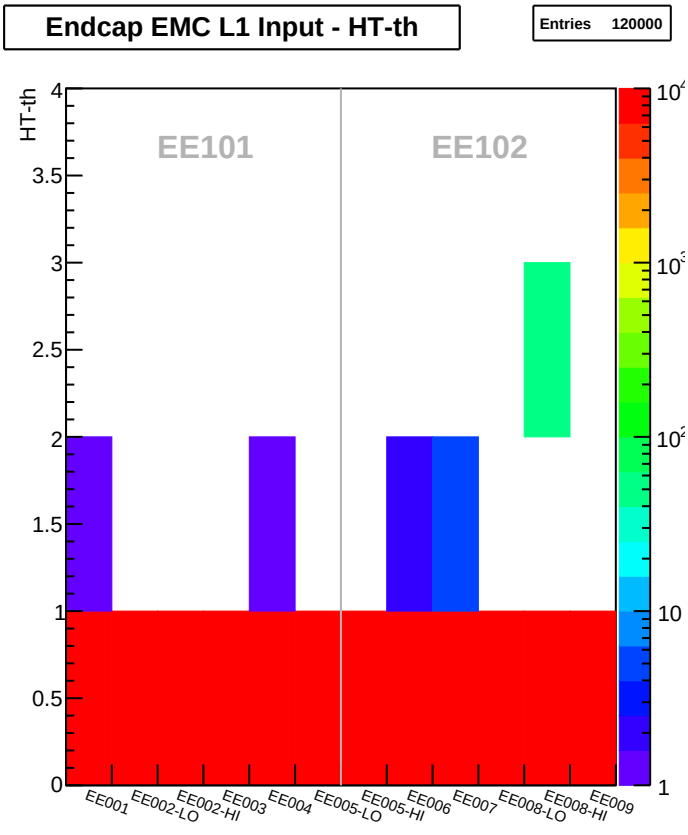

Endcap EMC L0 Input - Patch Sum
Entries 900000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

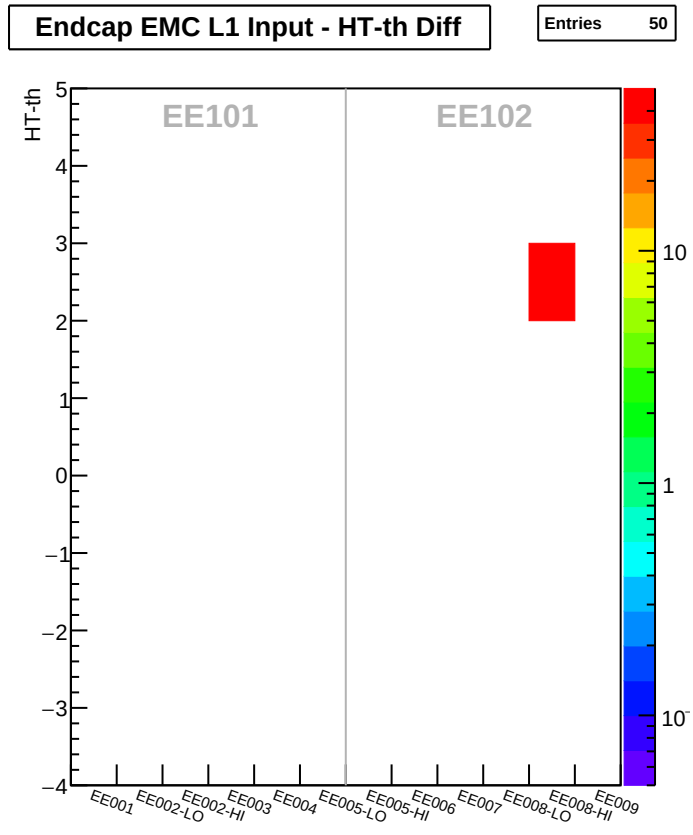

Barrel EMC L1 Input - HT Bits
Entries 360000
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

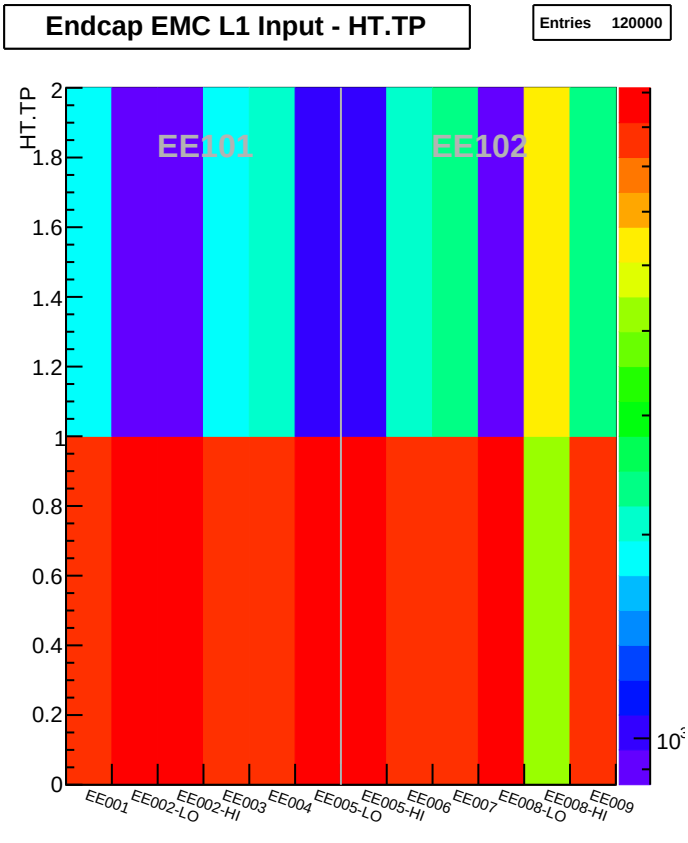

Barrel EMC L1 Input - HT Bits
Entries 296
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

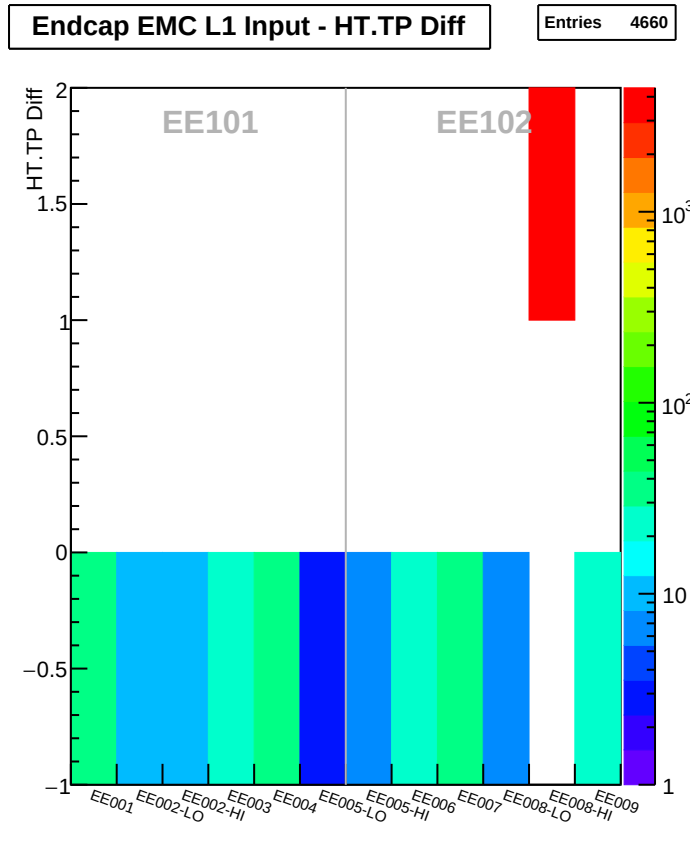

Barrel EMC L1 Input - HT.TP Bit
Entries 360000
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

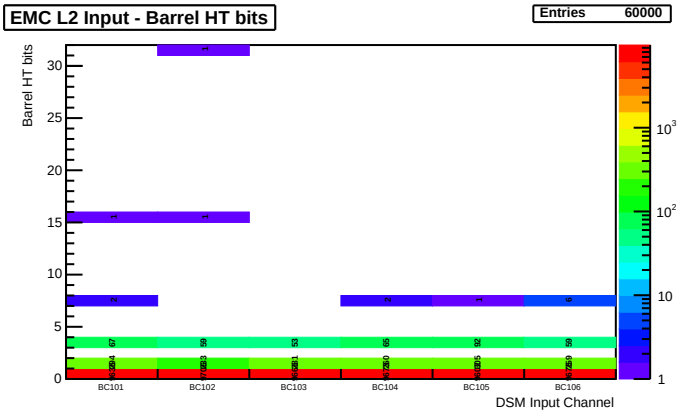

Barrel EMC L1 Input - HT.TP Bit
Entries 5474
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

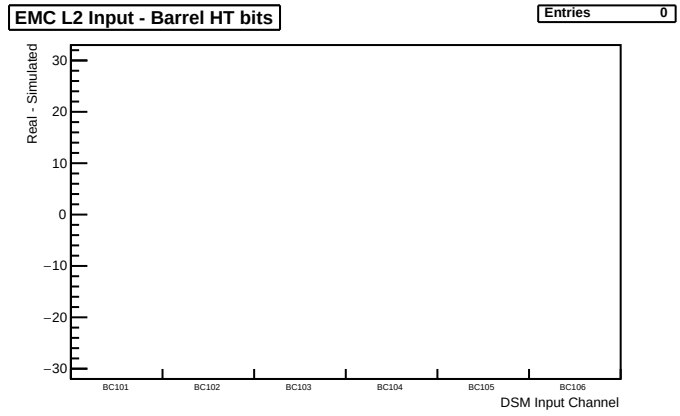

Endcap EMC L1 Input - HT-th
Entries 120000
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

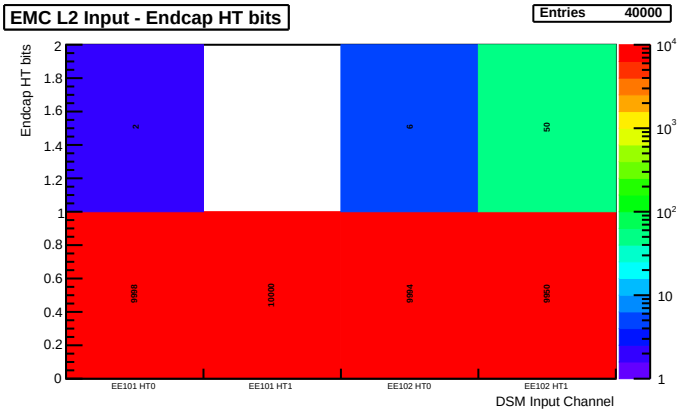

Endcap EMC L1 Input - HT-th Diff
Entries 50
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

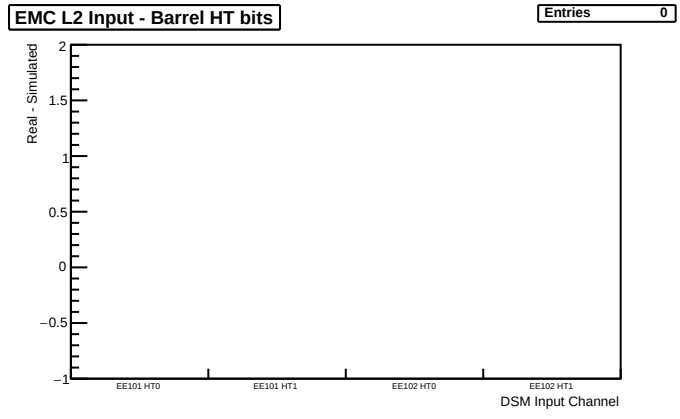

Endcap EMC L1 Input - HT.TP
Entries 120000
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

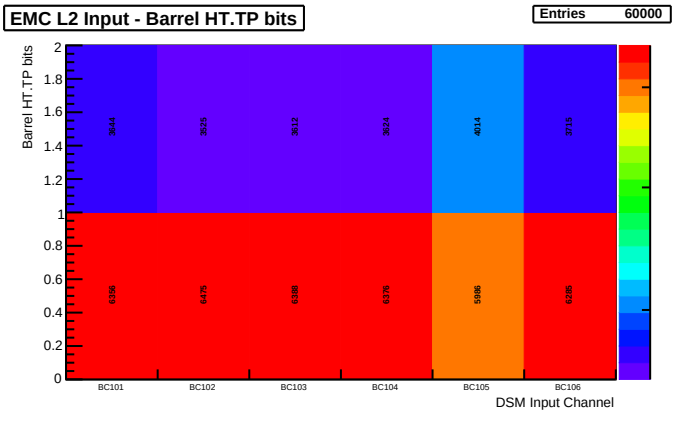

Endcap EMC L1 Input - HT.TP Diff
Entries 4660
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

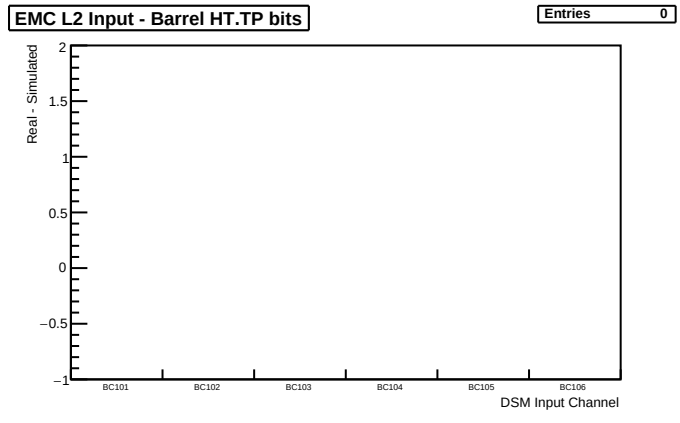

EMC L2 Input - Barrel HT bits
Entries 60000
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

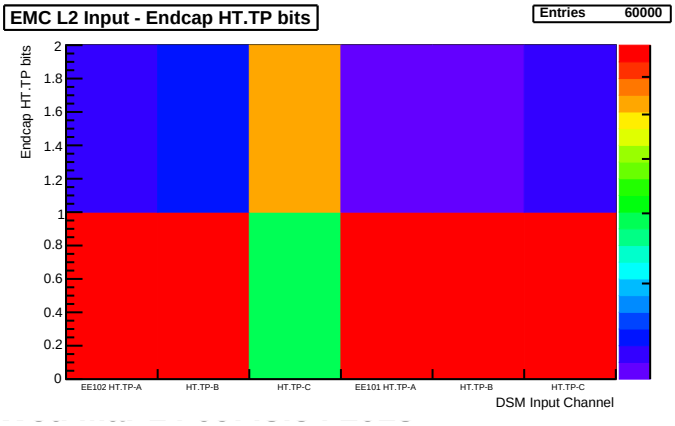

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

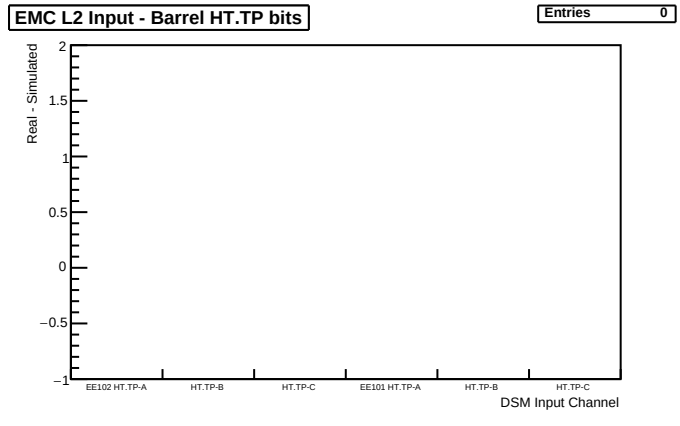

EMC L2 Input - Endcap HT bits
Entries 40000
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

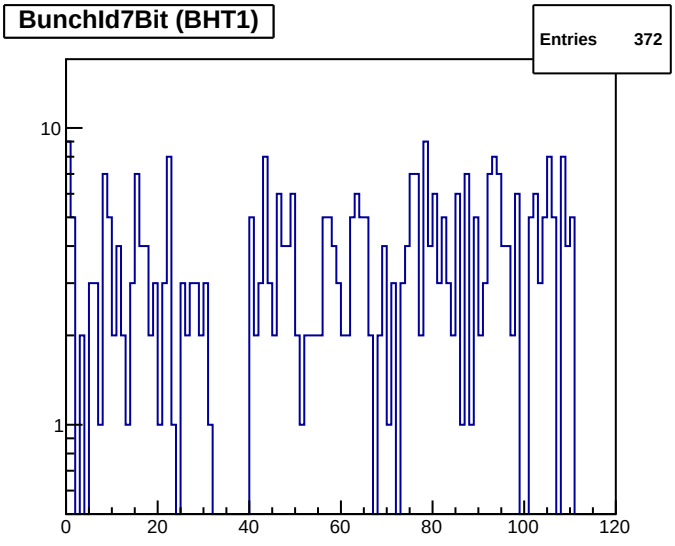

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

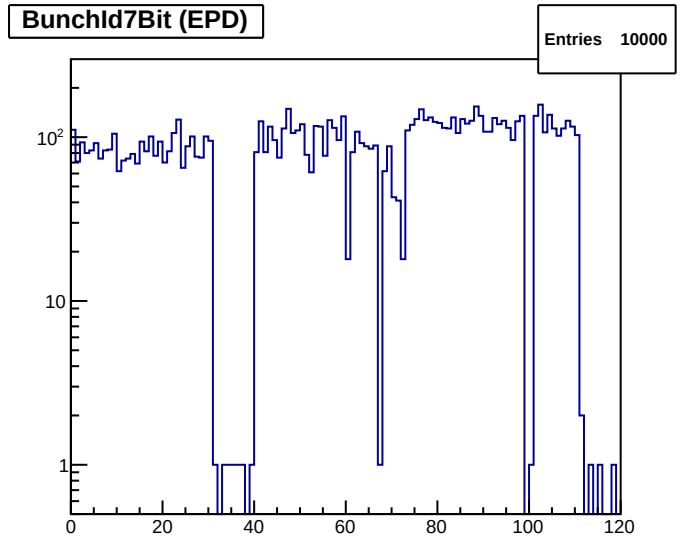

EMC L2 Input - Barrel HT.TP bits
Entries 60000
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

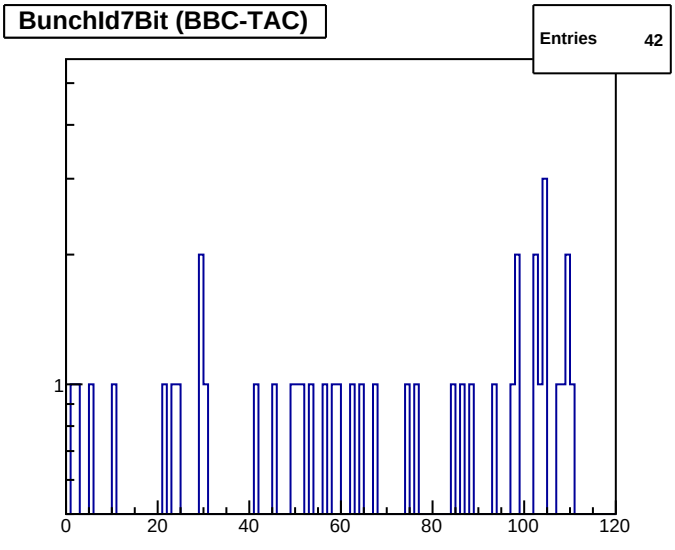

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

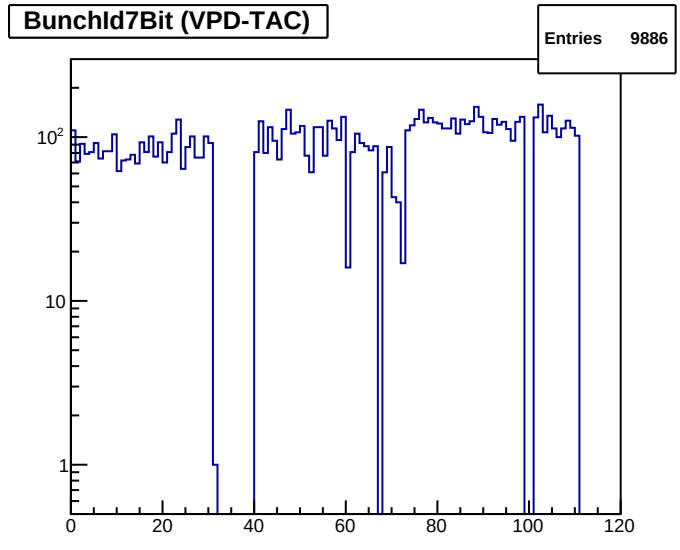

EMC L2 Input - Endcap HT.TP bits
Entries 60000
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

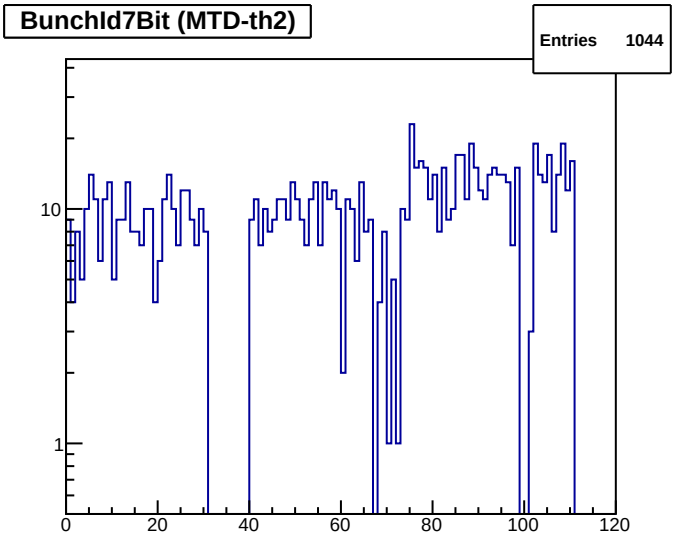

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

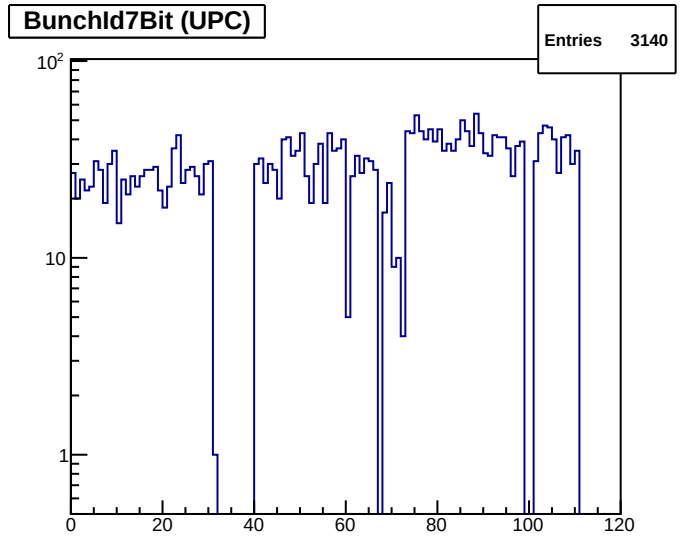

BunchId7Bit (BHT1)
Entries 372
10
1
0 20 40 60 80 100 120

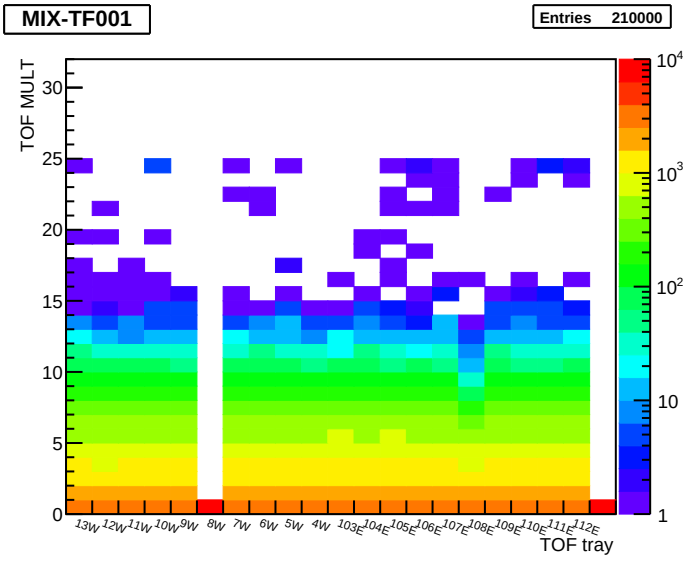

BunchId7Bit (EPD)
Entries 10000
10^2
10
1
0 20 40 60 80 100 120

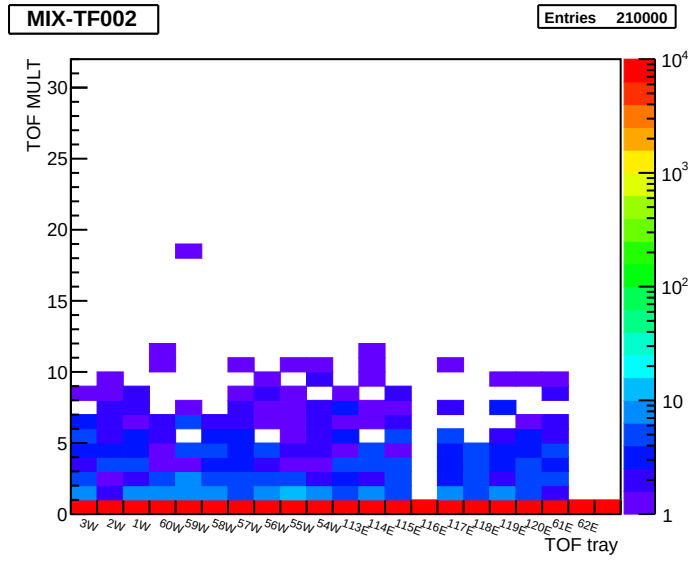

BunchId7Bit (BBC-TAC)
Entries 42
1
0 20 40 60 80 100 120

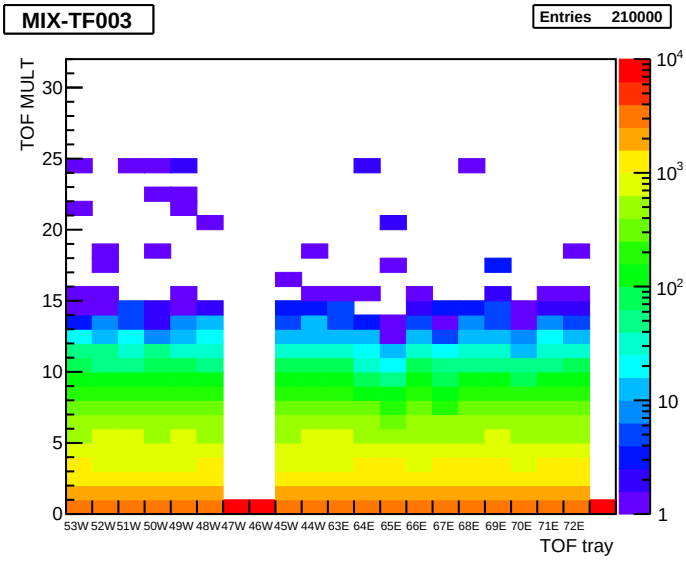

BunchId7Bit (VPD-TAC)
Entries 9886
10^2
10
1
0 20 40 60 80 100 120

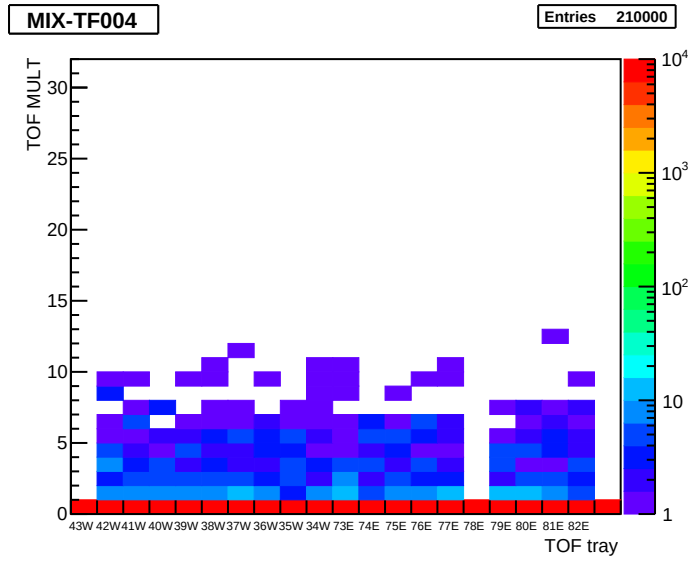

BunchId7Bit (MTD-th2)
Entries 1044
10
1
0 20 40 60 80 100 120

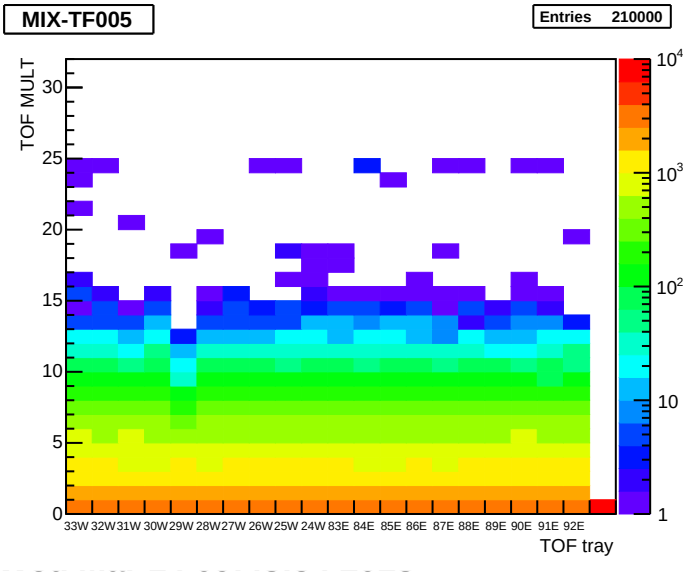

BunchId7Bit (UPC)
Entries 3140
10^2
10
1
0 20 40 60 80 100 120

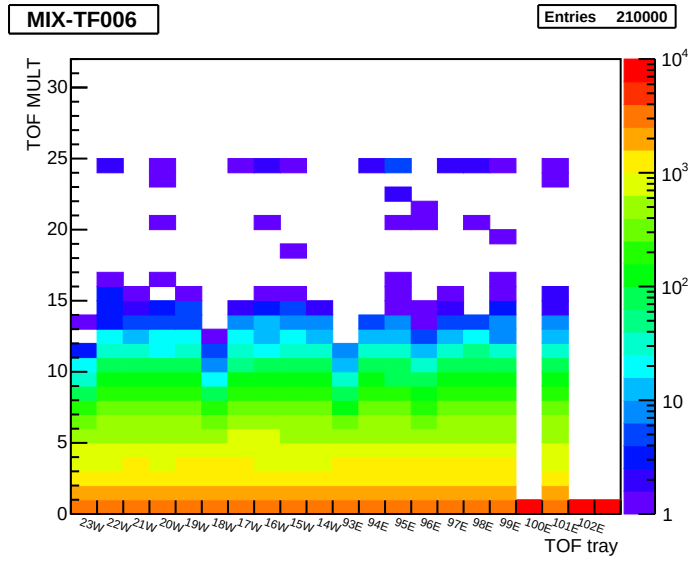

MIX-TF001
Entries 210000
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

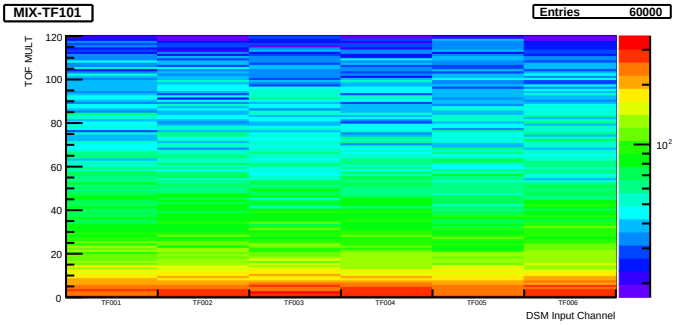

MIX-TF002
Entries 210000
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

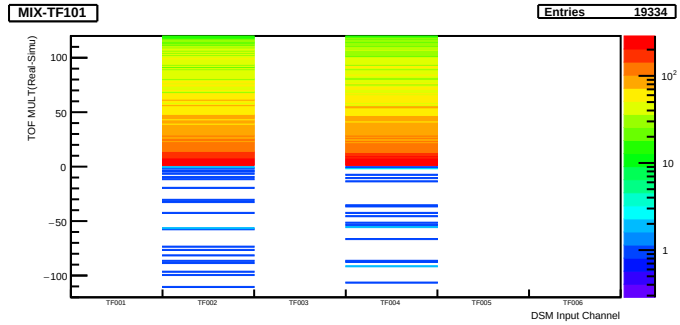

MIX-TF003
Entries 210000
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

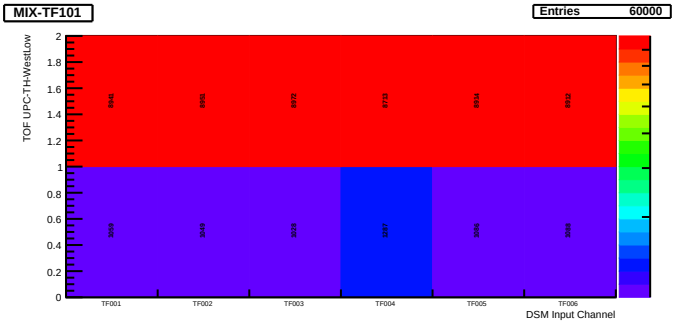

MIX-TF004
Entries 210000
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

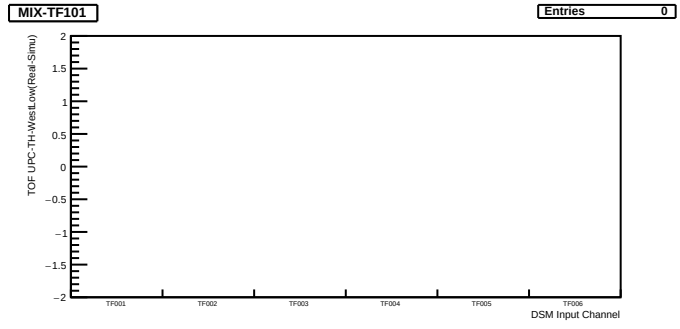

MIX-TF005
Entries 210000
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

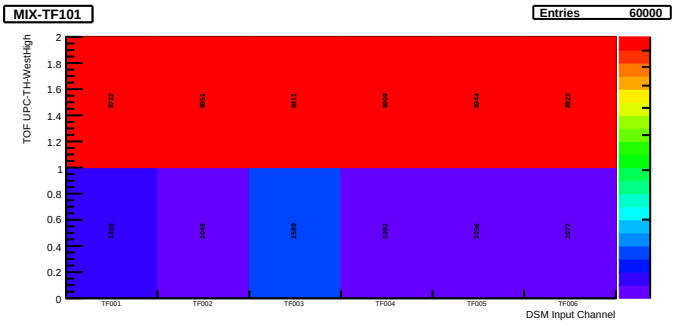

MIX-TF006
Entries 210000
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

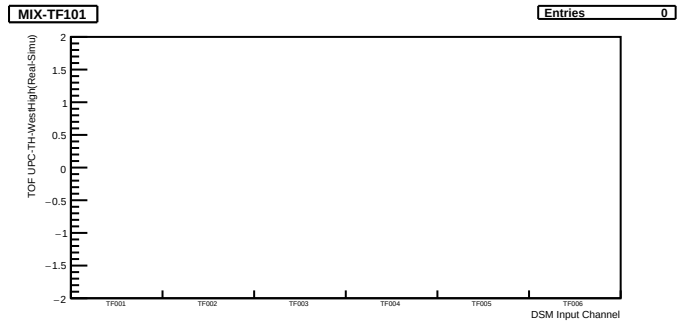

MIX-TF101
Entries 60000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

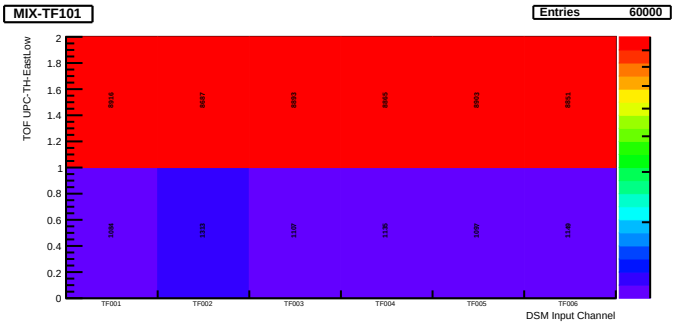

MIX-TF101
Entries 19334
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

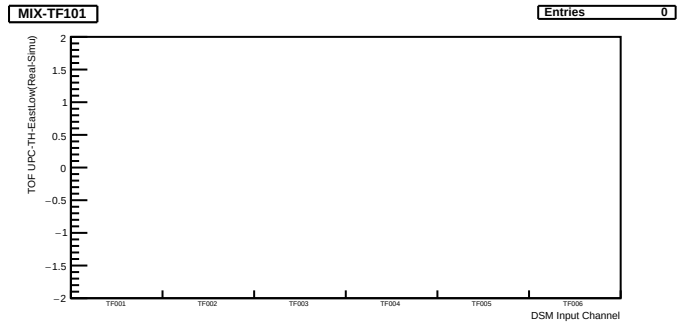

MIX-TF101
Entries 60000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

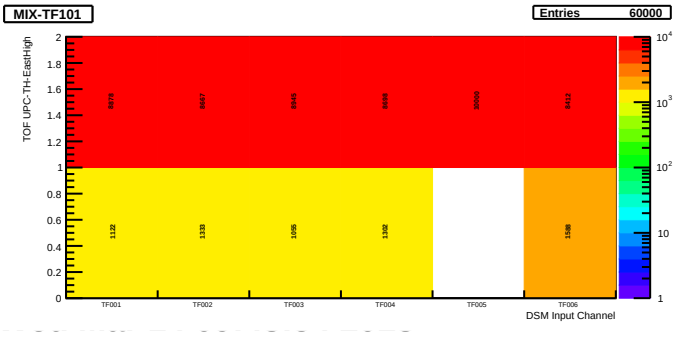

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

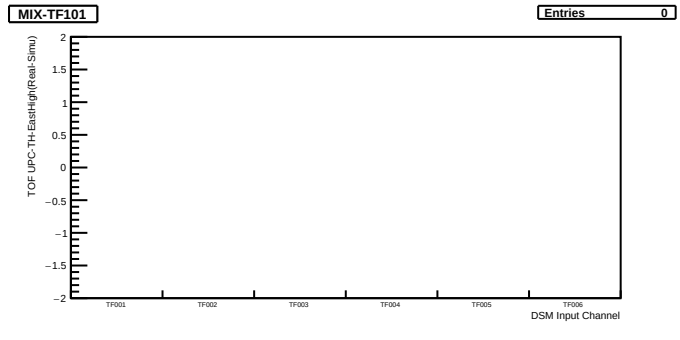

MIX-TF101
Entries 60000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

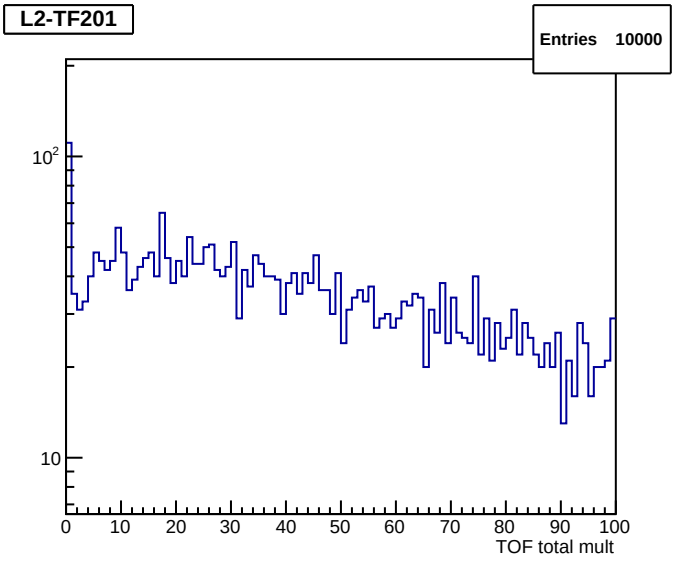

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

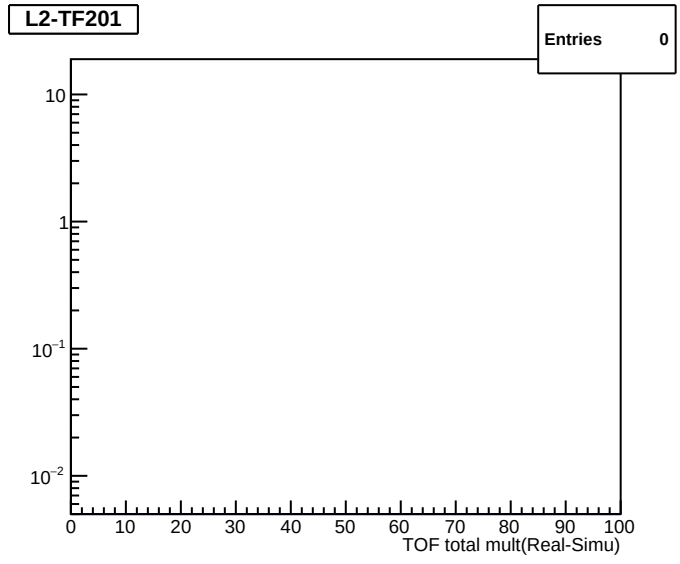

MIX-TF101
Entries 60000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

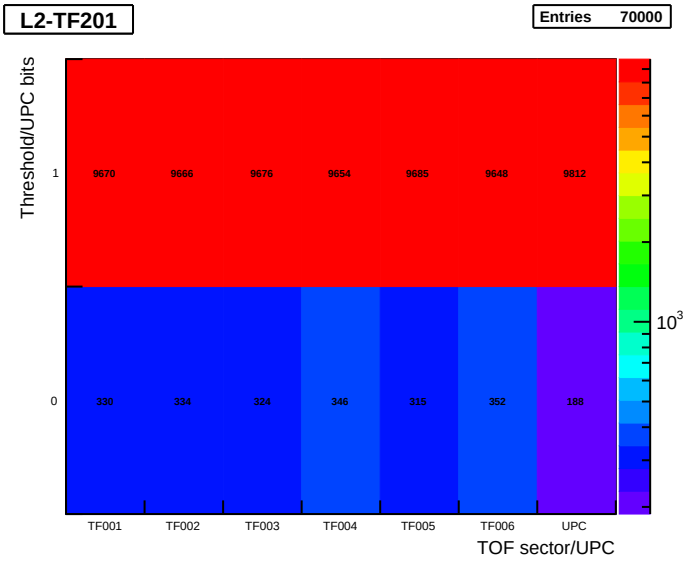

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

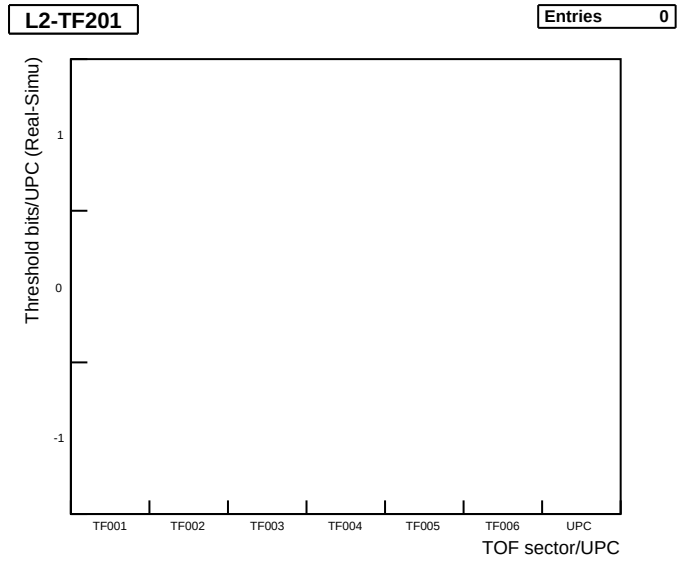

MIX-TF101
Entries 60000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

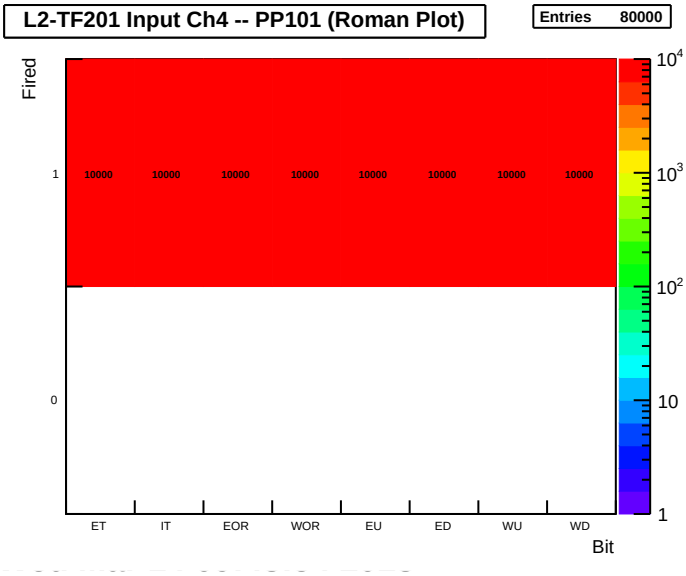

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

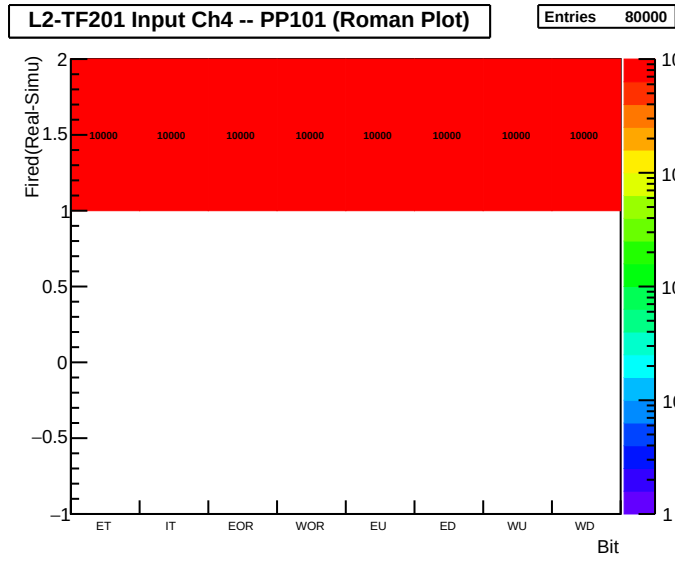

L2-TF201
Entries 10000
TOF total mult

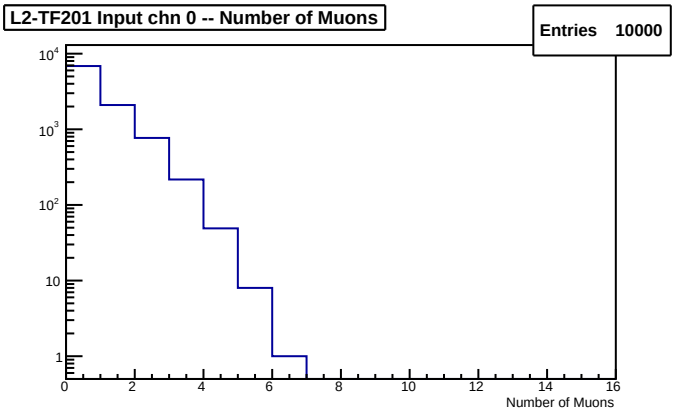

L2-TF201
Entries 0
TOF total mult(Real-Simu)

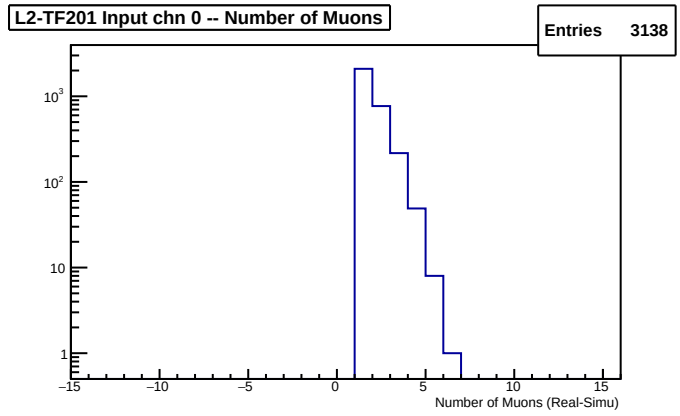

L2-TF201
Entries 70000
Threshold/UPC bits
9670 9666 9676 9654 9685 9648 9812
330 334 324 346 315 352 188
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

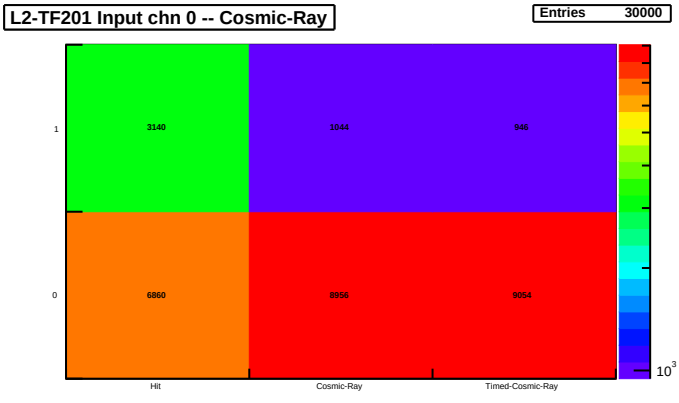

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

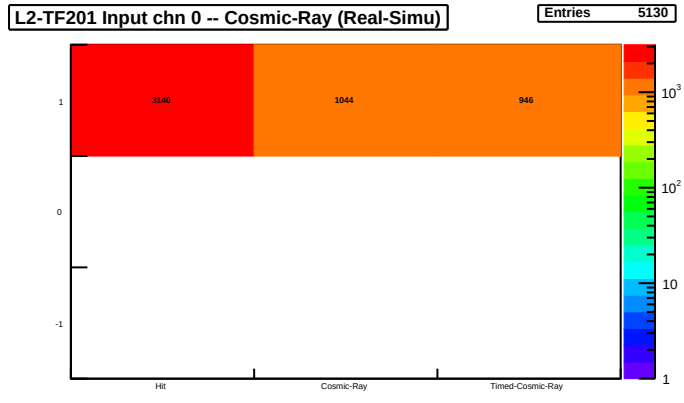

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 80000
Fired
10000 10000 10000 10000 10000 10000 10000 10000
ET IT EOR WOR EU ED WU WD
Bit

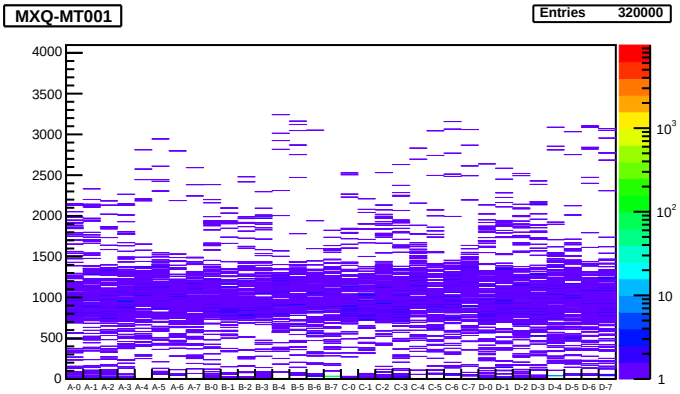

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 80000
Fired(Real-Simu)
10000 10000 10000 10000 10000 10000 10000 10000
ET IT EOR WOR EU ED WU WD
Bit

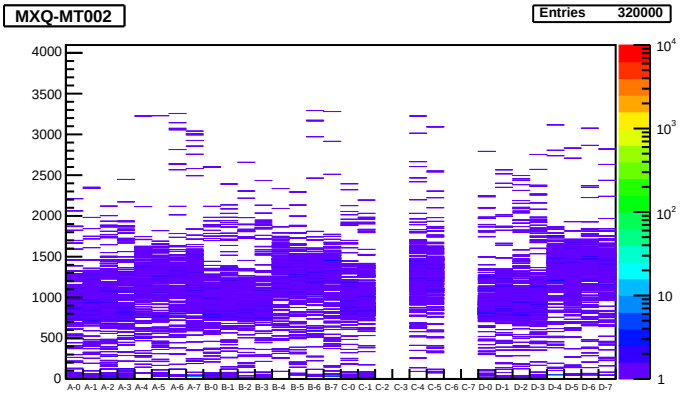

L2-TF201 Input chn 0 -- Number of Muons
Entries 10000
Number of Muons

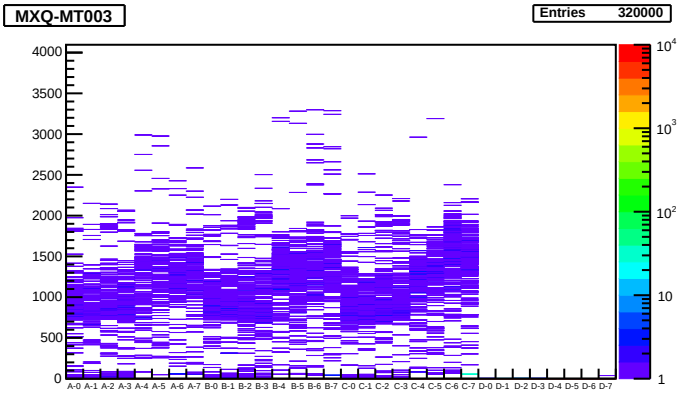

L2-TF201 Input chn 0 -- Number of Muons
Entries 3138
Number of Muons (Real-Simu)

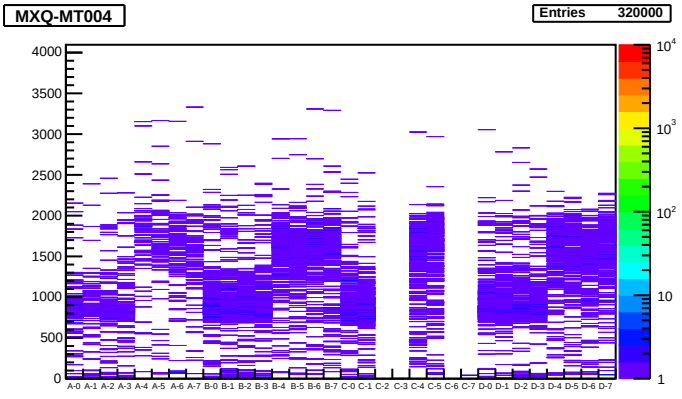

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 30000
3140
1044
946
6860
8956
9054
Hit
Cosmic-Ray
Timed-Cosmic-Ray

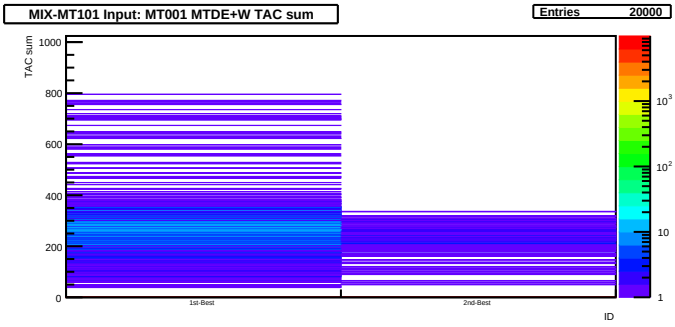

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 5130
3140
1044
946
Hit
Cosmic-Ray
Timed-Cosmic-Ray

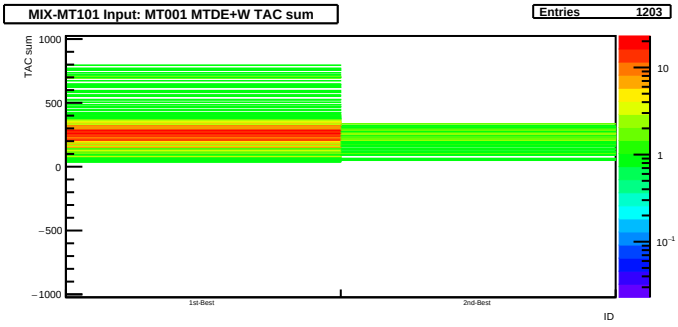

MXQ-MT001
Entries 320000

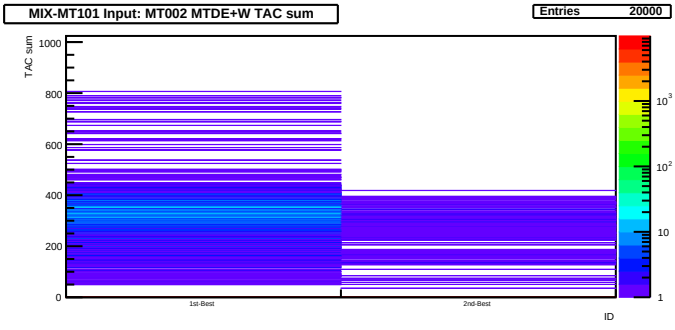

MXQ-MT002
Entries 320000

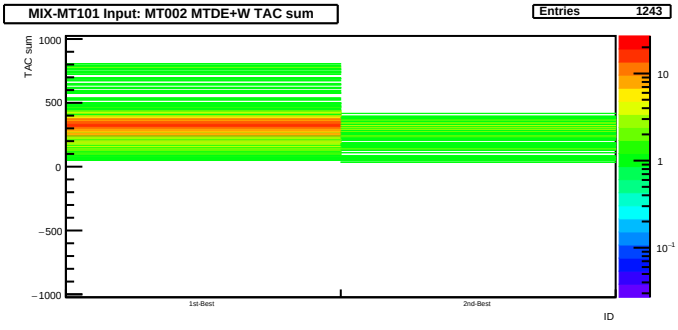

MXQ-MT003
Entries 320000

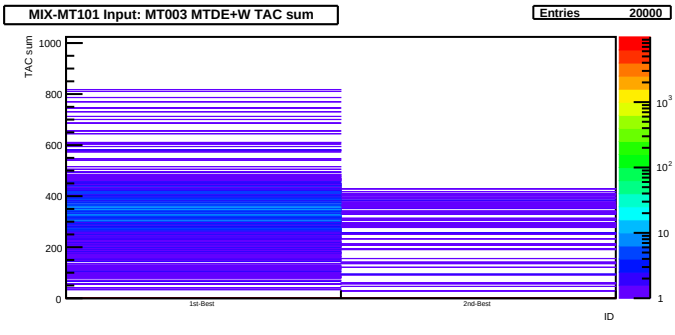

MXQ-MT004
Entries 320000

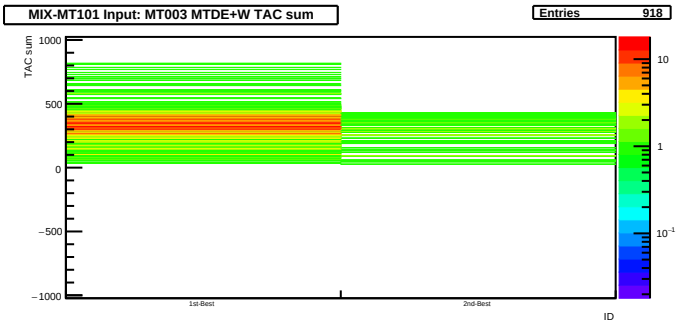

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

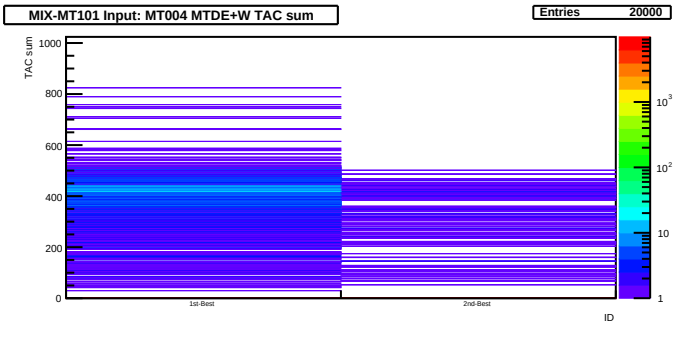

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 1203
TAC sum
1st-Best
2nd-Best
ID

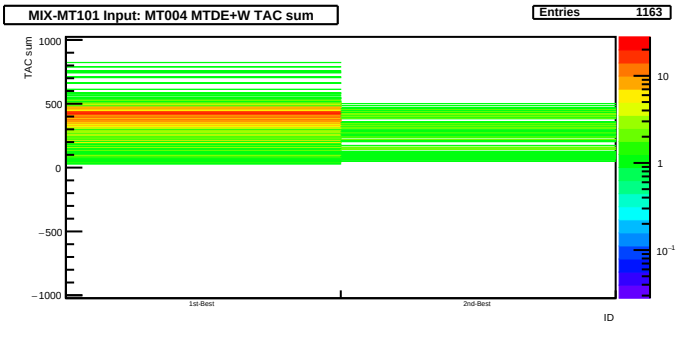

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

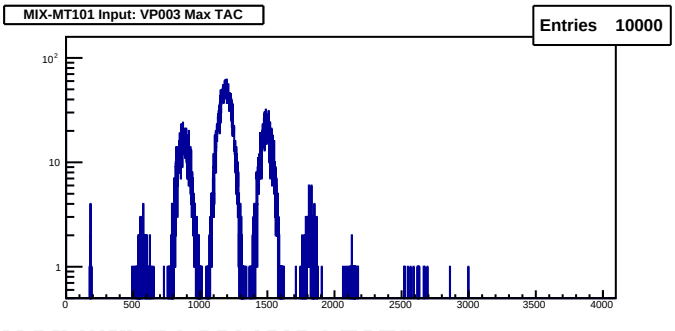

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 1243
TAC sum
1st-Best
2nd-Best
ID

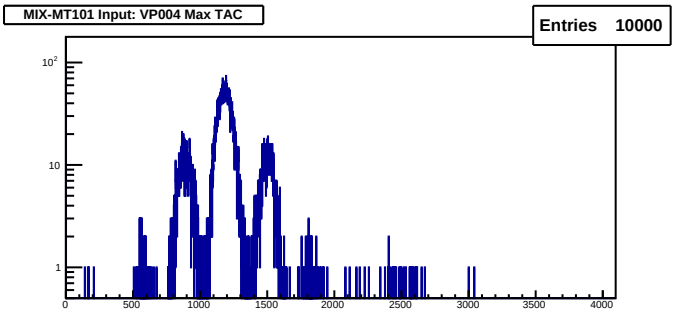

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

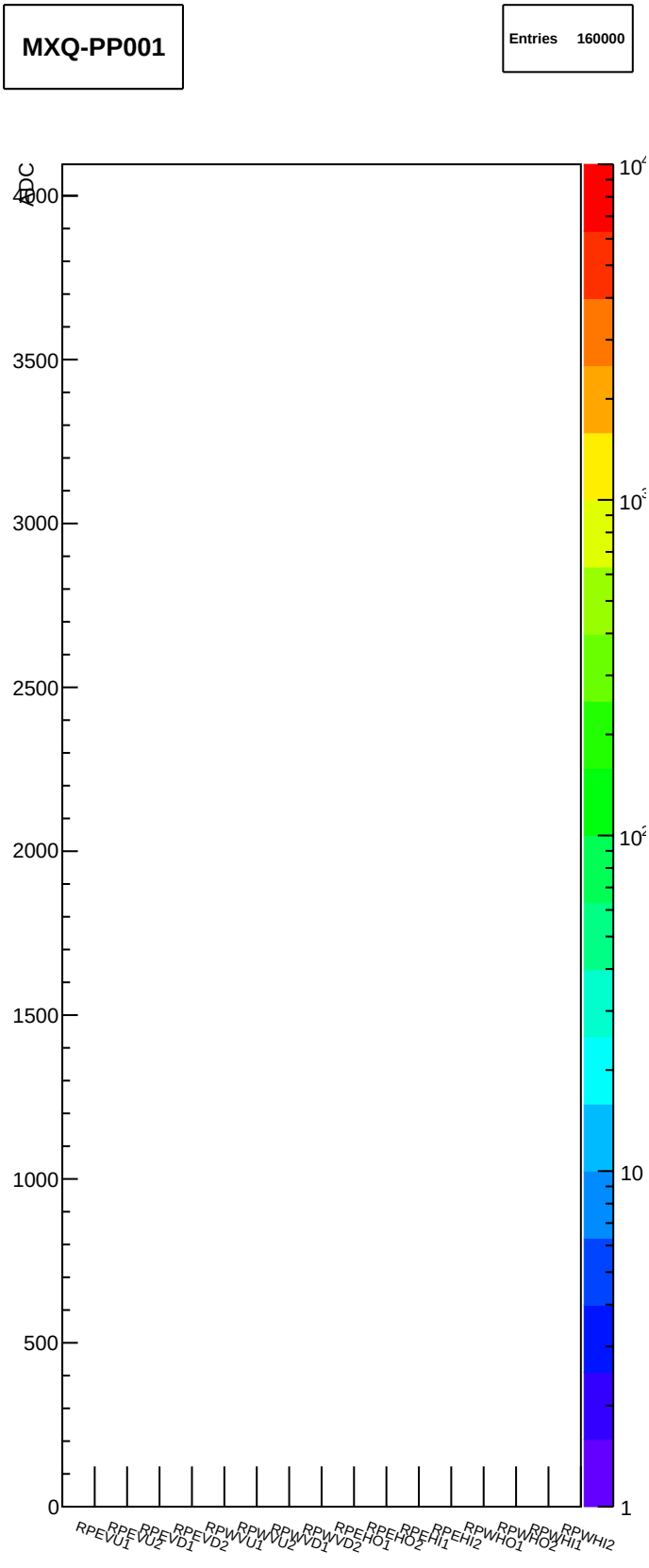

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 918
TAC sum
1st-Best
2nd-Best
ID

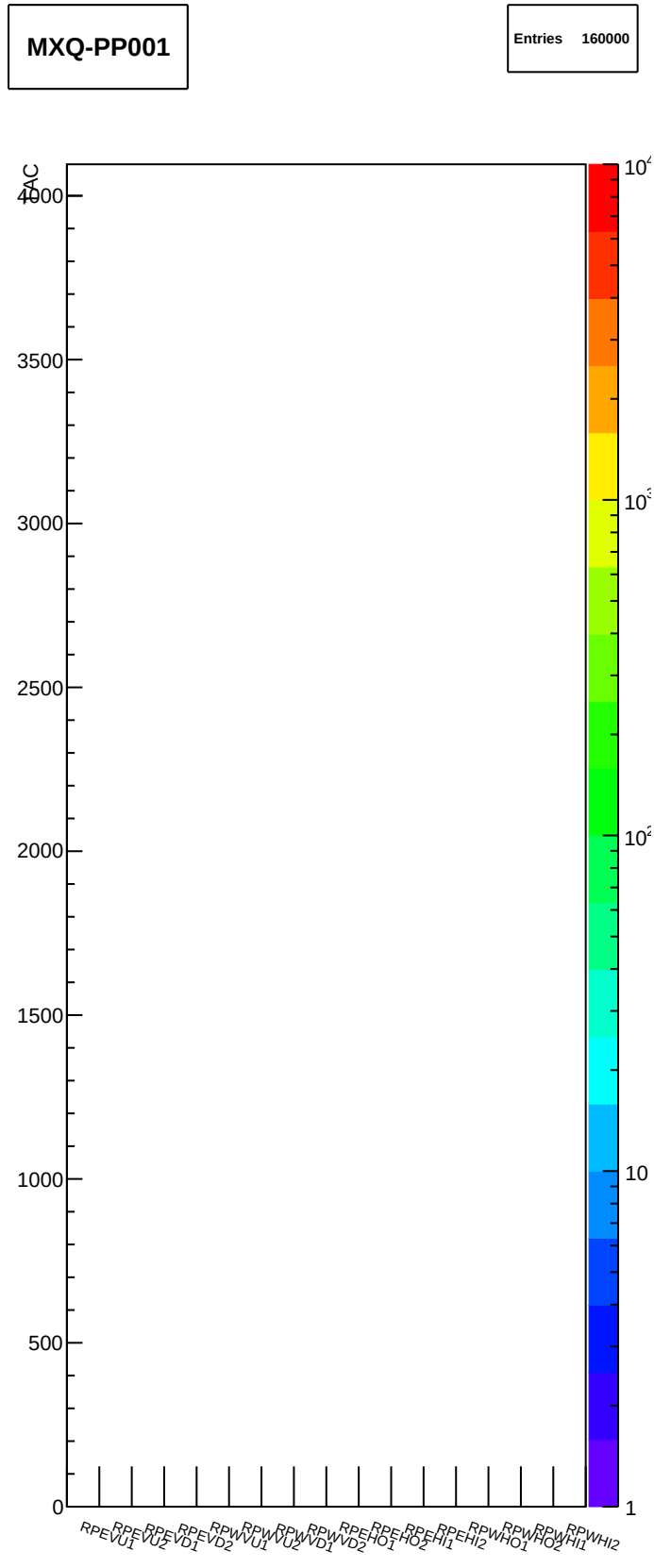

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

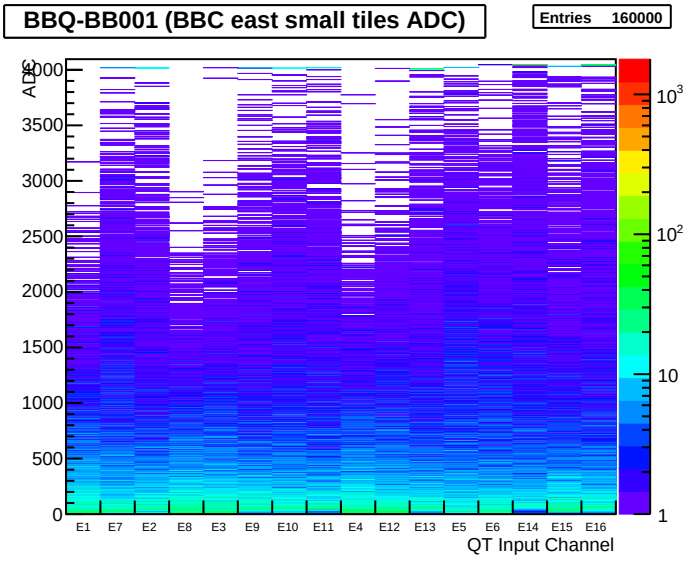

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 1163
TAC sum
1st-Best
2nd-Best
ID

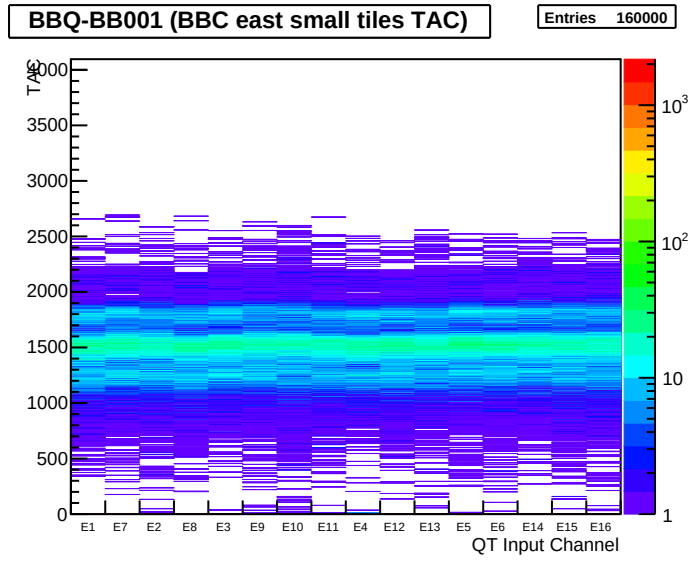

MIX-MT101 Input: VP003 Max TAC
Entries 10000

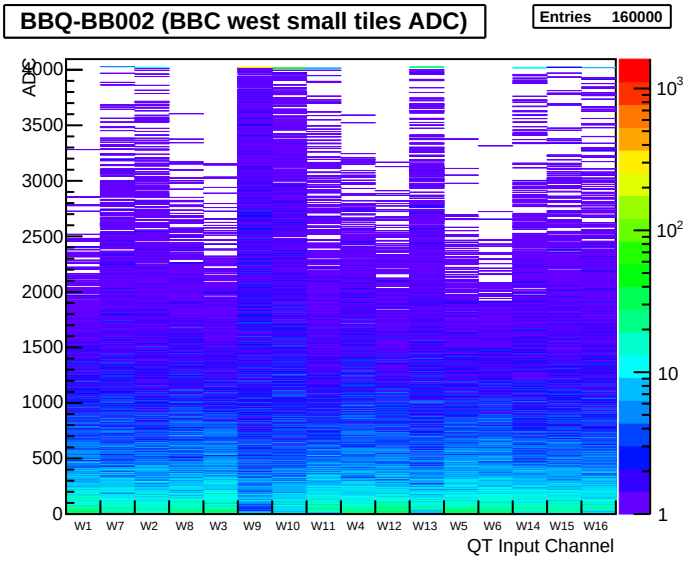

MIX-MT101 Input: VP004 Max TAC
Entries 10000

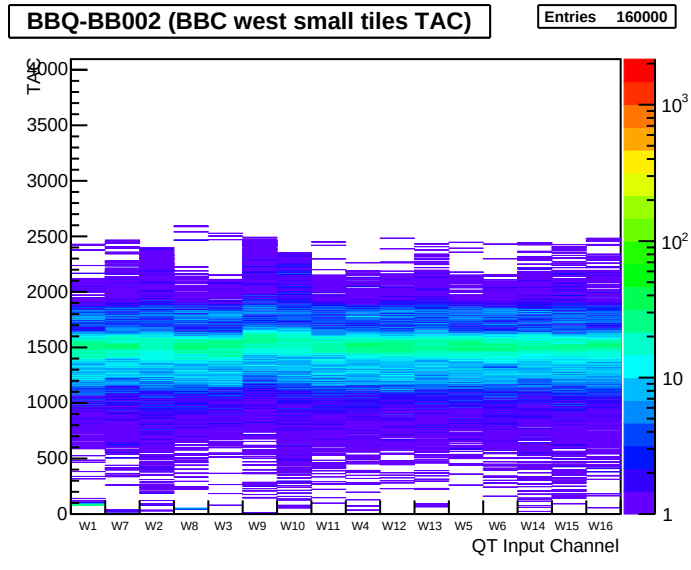

MXQ-PP001
Entries 160000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

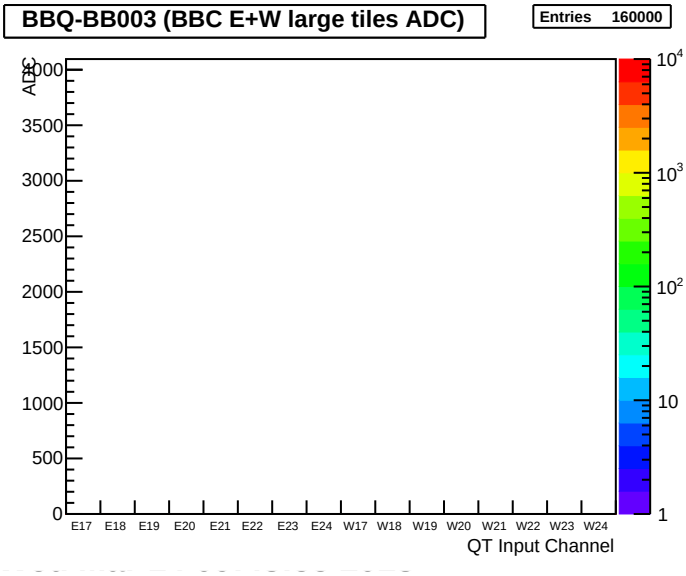

MXQ-PP001
Entries 160000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

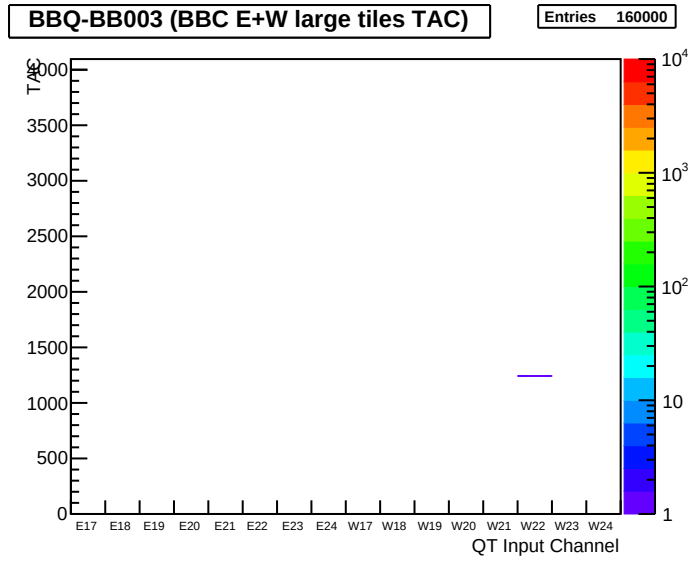

BBQ-BB001 (BBC east small tiles ADC)
Entries 160000
ADC
QT Input Channel

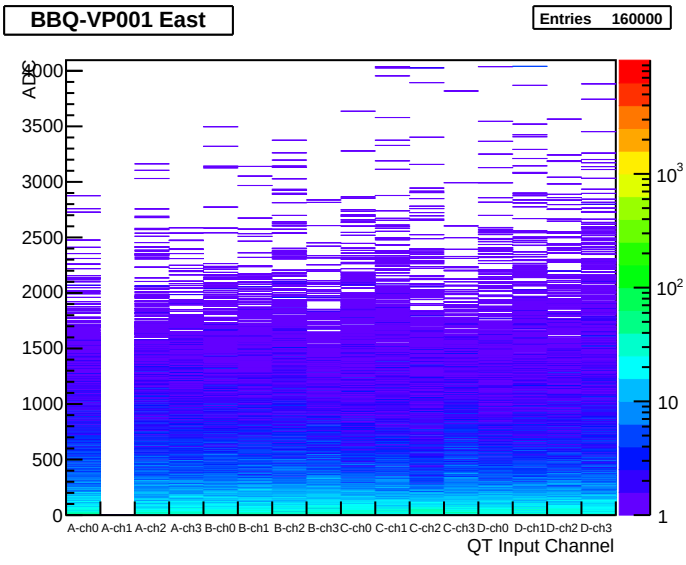

BBQ-BB001 (BBC east small tiles TAC)
Entries 160000
TAC
QT Input Channel

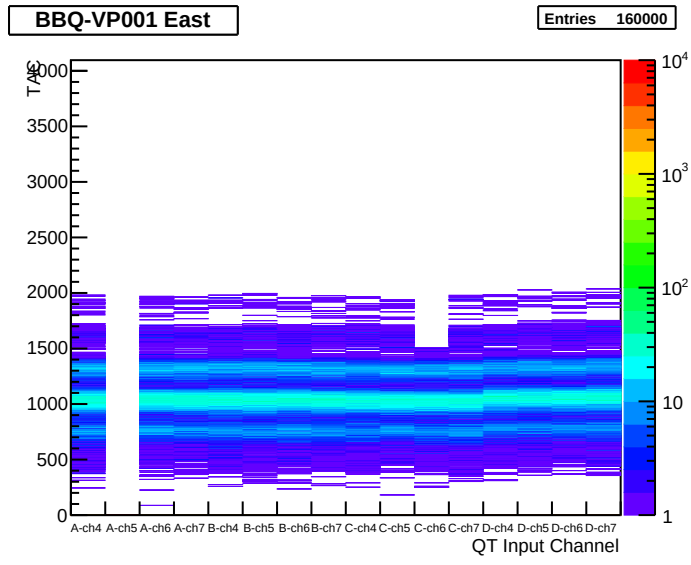

BBQ-BB002 (BBC west small tiles ADC)
Entries 160000
ADC
QT Input Channel

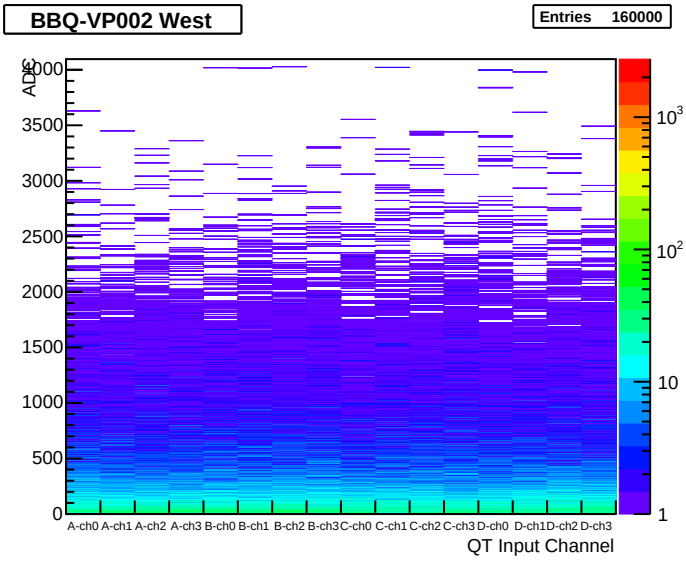

BBQ-BB002 (BBC west small tiles TAC)
Entries 160000
TAC
QT Input Channel

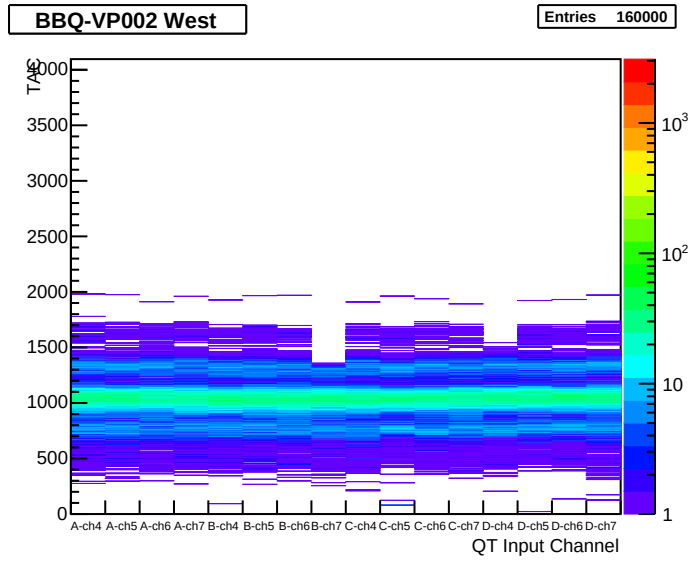

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 160000
ADC
QT Input Channel

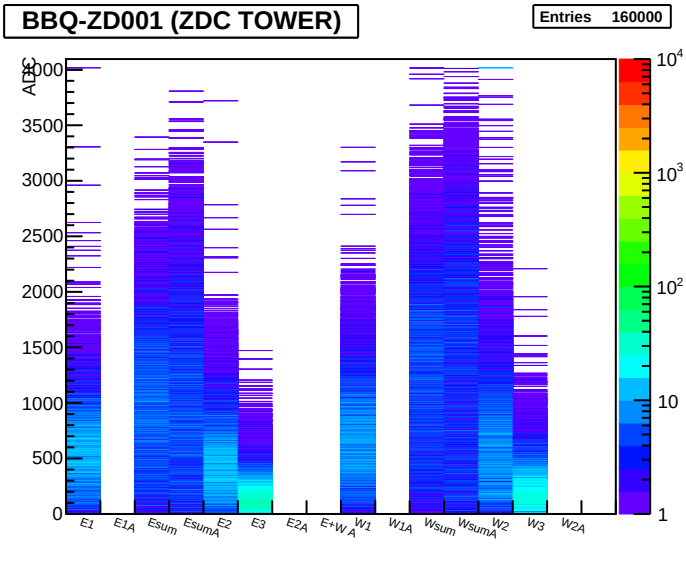

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 160000
TAC
QT Input Channel

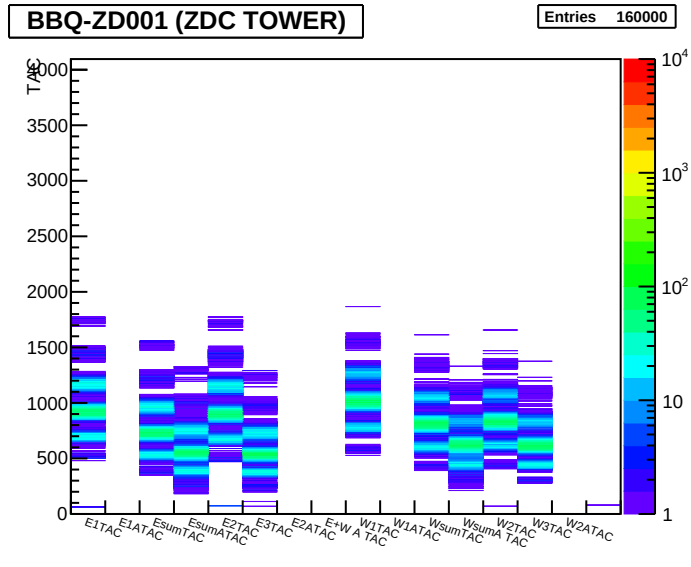

BBQ-VP001 East
Entries 160000
ADC
QT Input Channel

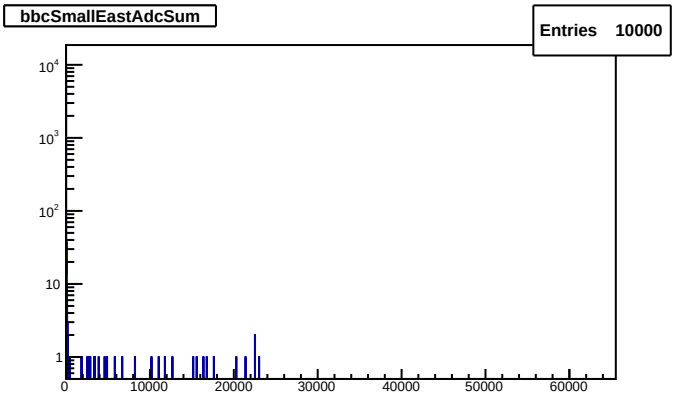

BBQ-VP001 East
Entries 160000
TAC
QT Input Channel

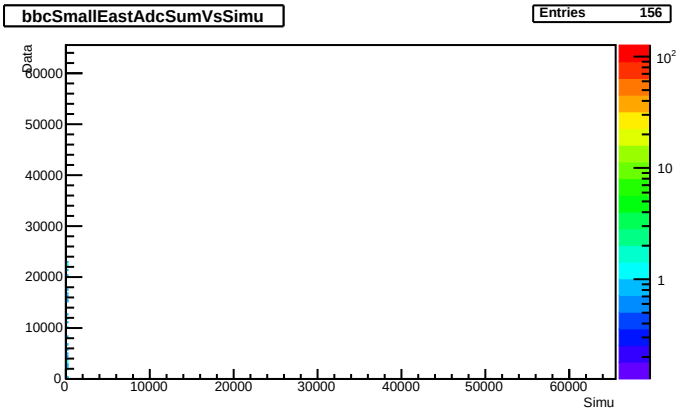

BBQ-VP002 West
Entries 160000
ADC
QT Input Channel

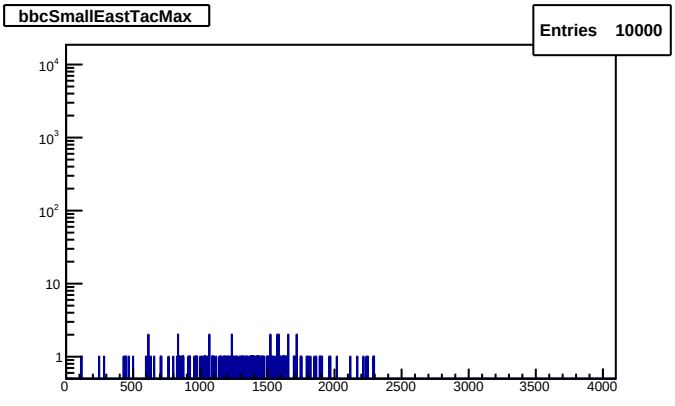

BBQ-VP002 West
Entries 160000
TAC
QT Input Channel

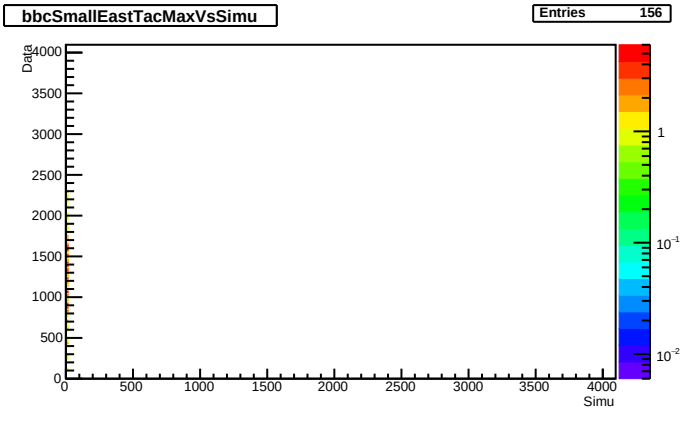

BBQ-ZD001 (ZDC TOWER)
Entries 160000
ADC

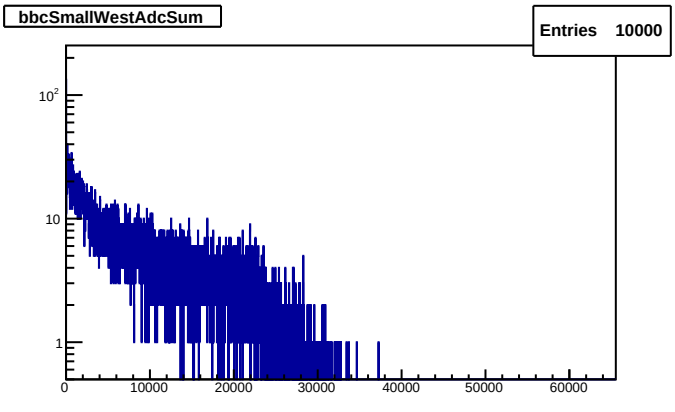

BBQ-ZD001 (ZDC TOWER)
Entries 160000
TAC

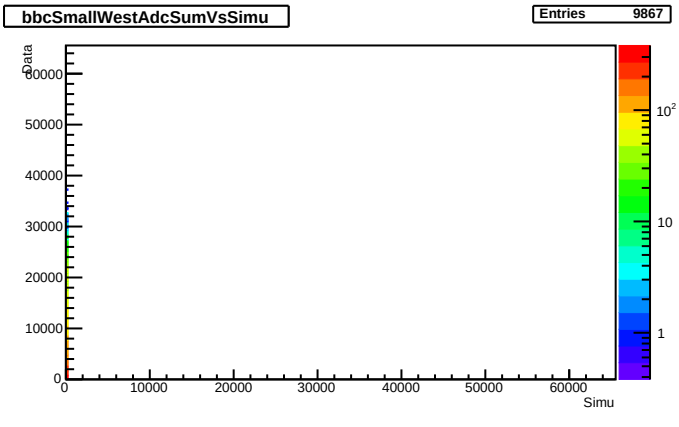

bbcSmallEastAdcSum
Entries 10000

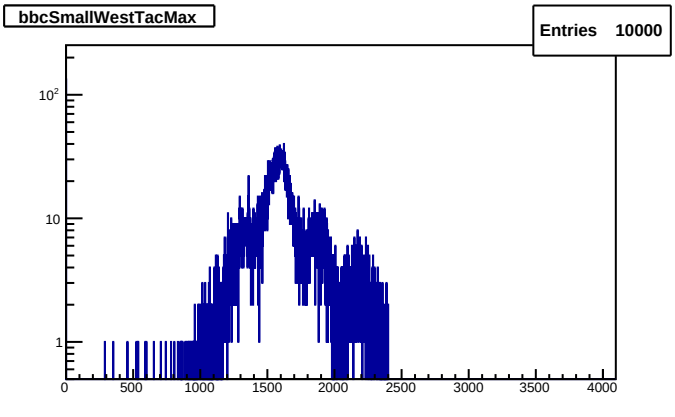

bbcSmallEastAdcSumVsSimu
Entries 156
Data
Simu

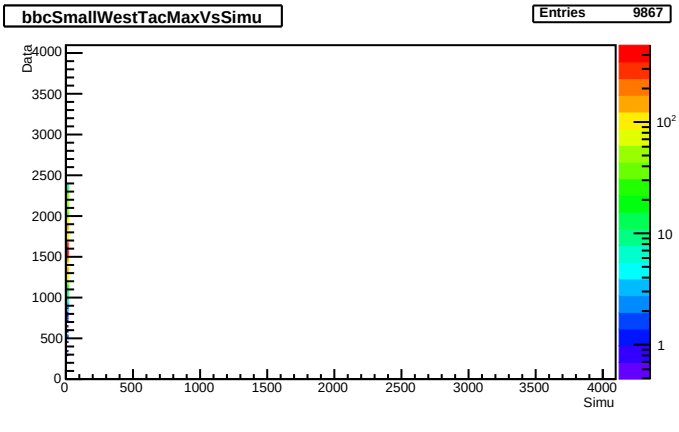

bbcSmallEastTacMax
Entries 10000

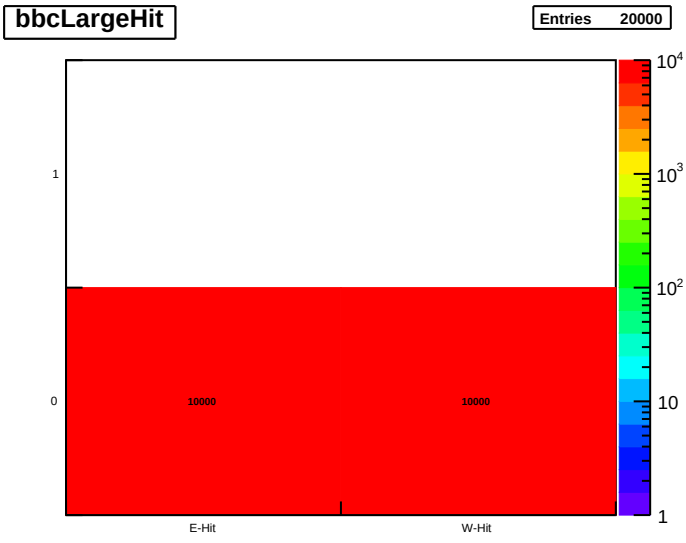

bbcSmallEastTacMaxVsSimu
Entries 156
Data
Simu

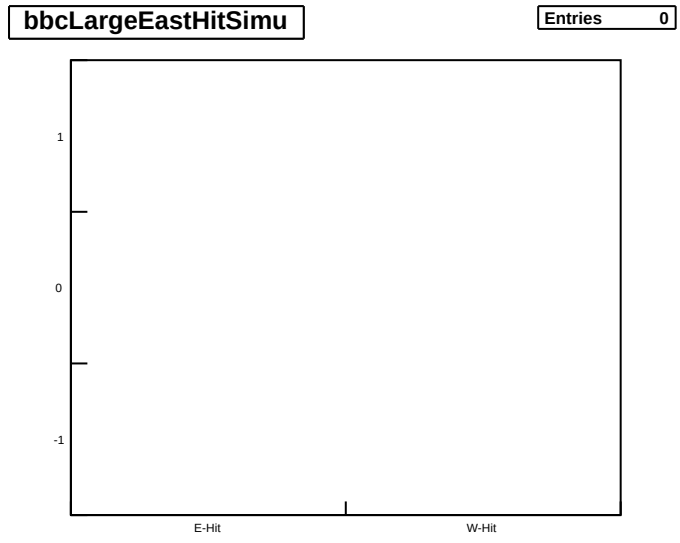

bbcSmallWestAdcSum
Entries 10000

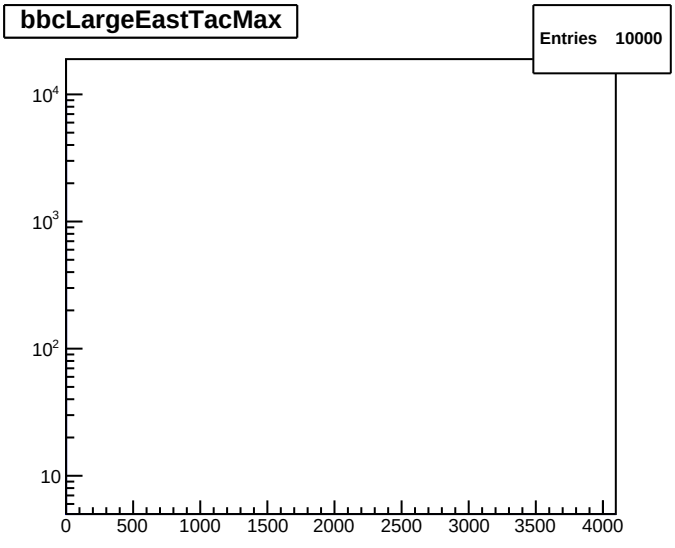

bbcSmallWestAdcSumVsSimu
Entries 9867
Data
Simu

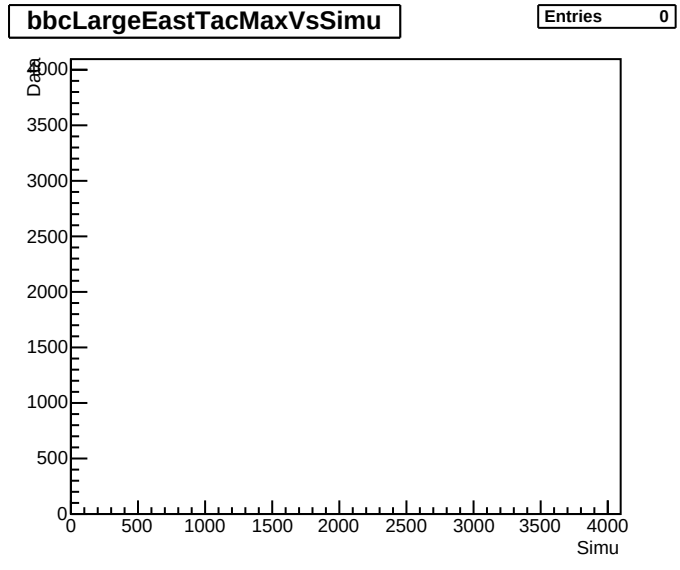

bbcSmallWestTacMax
Entries 10000

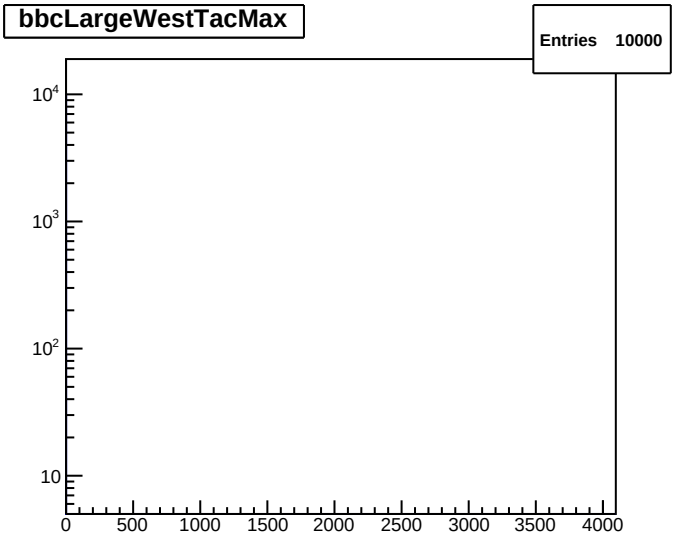

bbcSmallWestTacMaxVsSimu
Entries 9867
Data
Simu

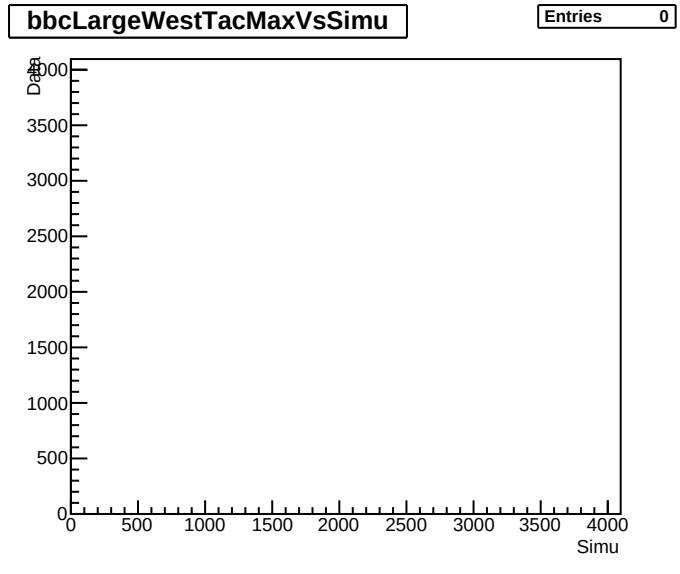

bbcLargeHit
Entries 20000
10000
10000
E-Hit
W-Hit

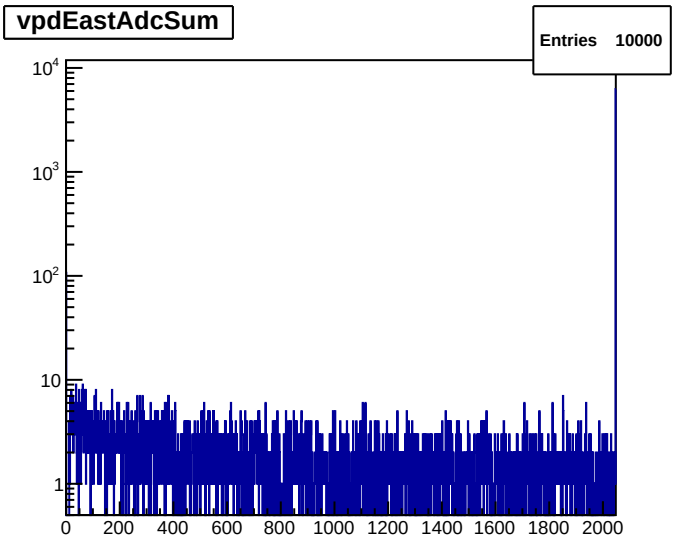

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

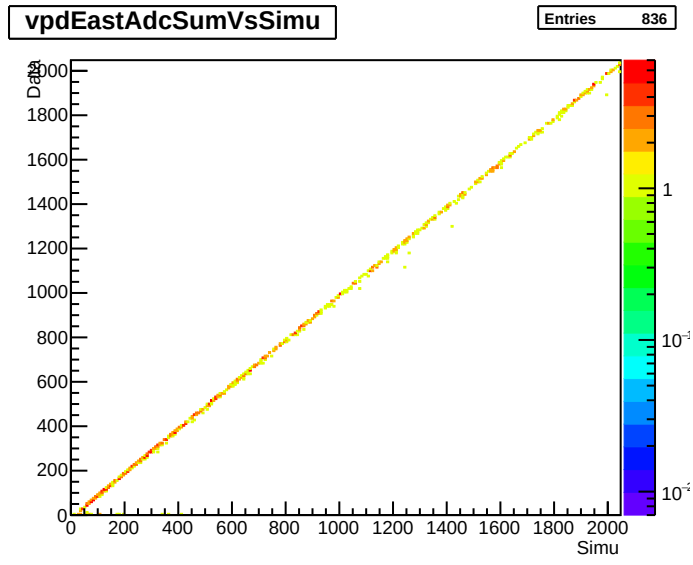

bbcLargeEastTacMax
Entries 10000

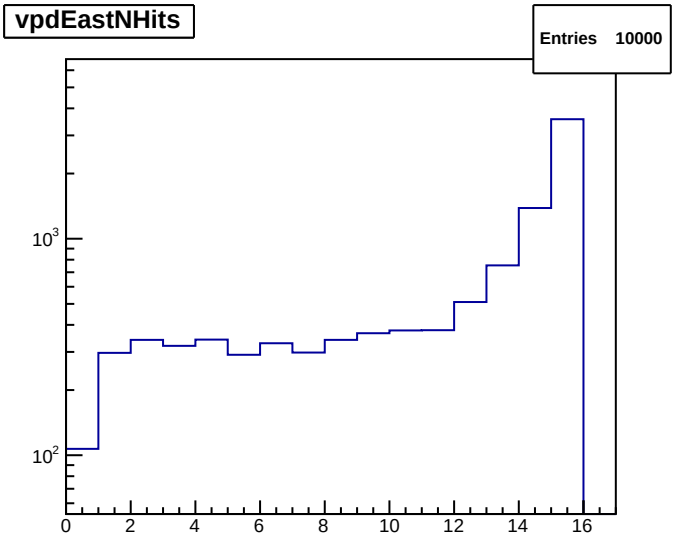

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

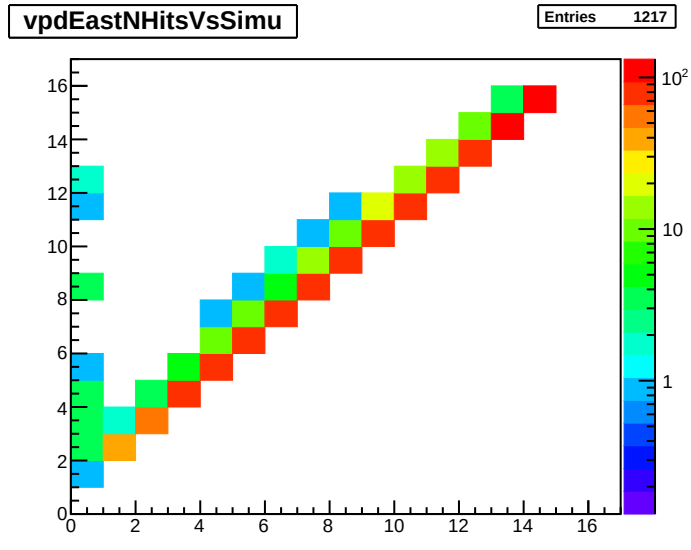

bbcLargeWestTacMax
Entries 10000

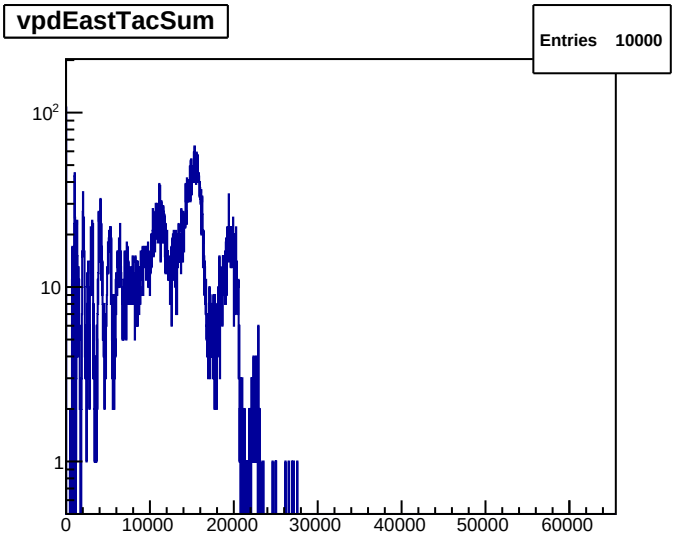

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

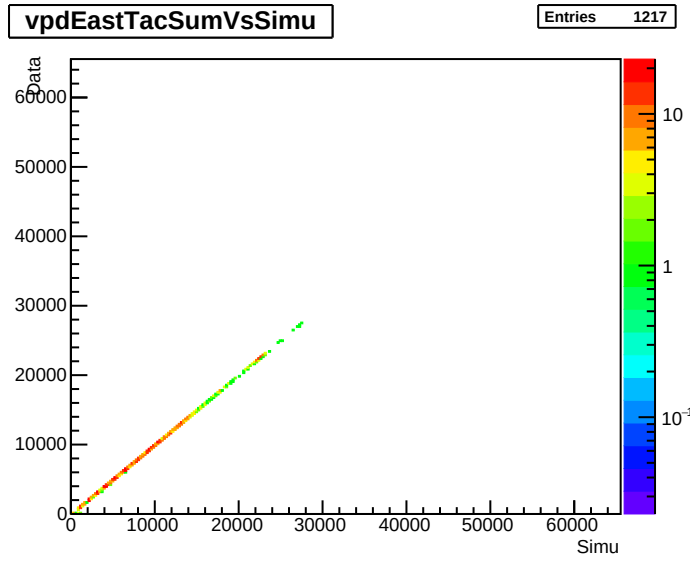

vpdEastAdcSum
Entries 10000

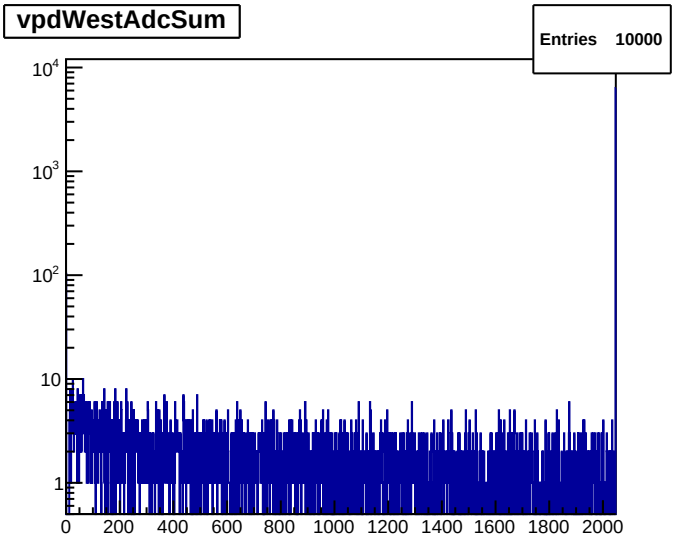

vpdEastAdcSumVsSimu
Entries 836
Data
Simu

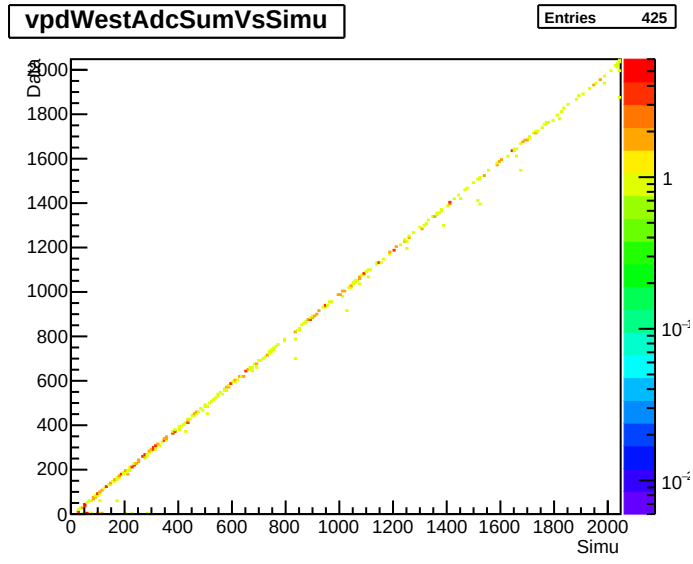

vpdEastNHits
Entries 10000

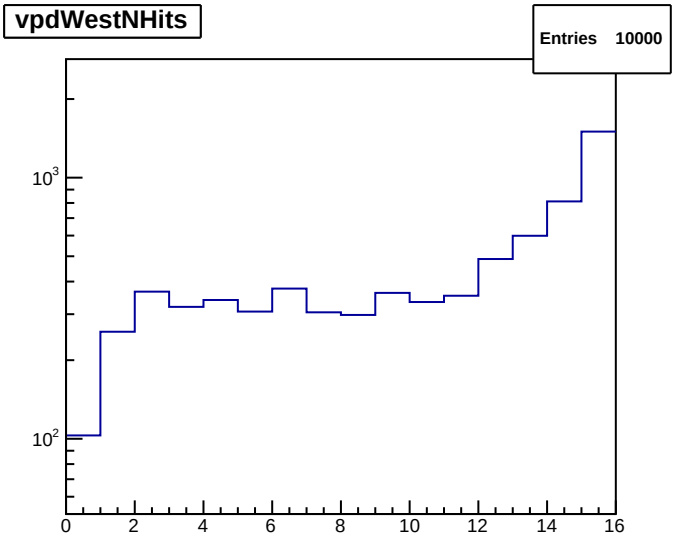

vpdEastNHitsVsSimu
Entries 1217

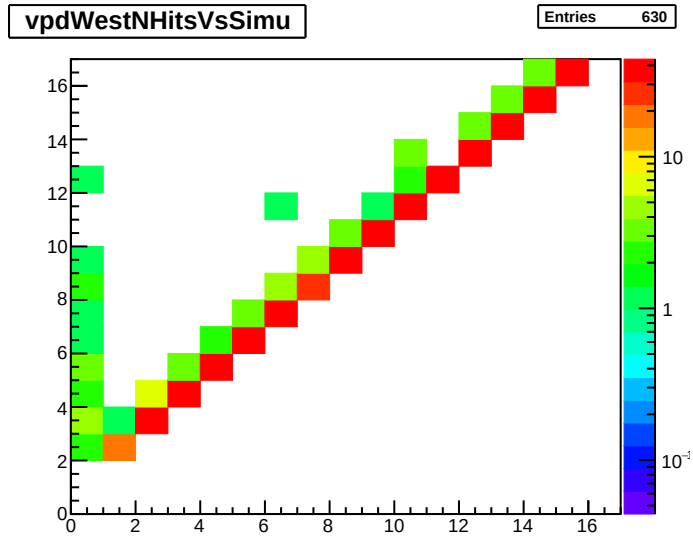

vpdEastTacSum
Entries 10000

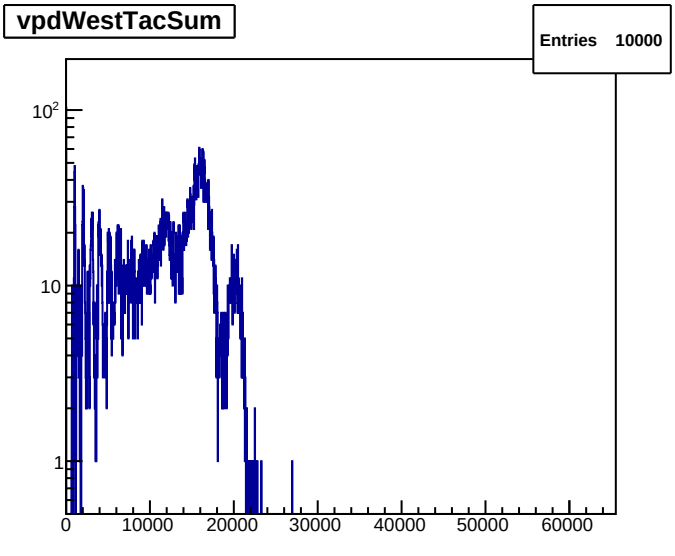

vpdEastTacSumVsSimu
Entries 1217
Data
Simu

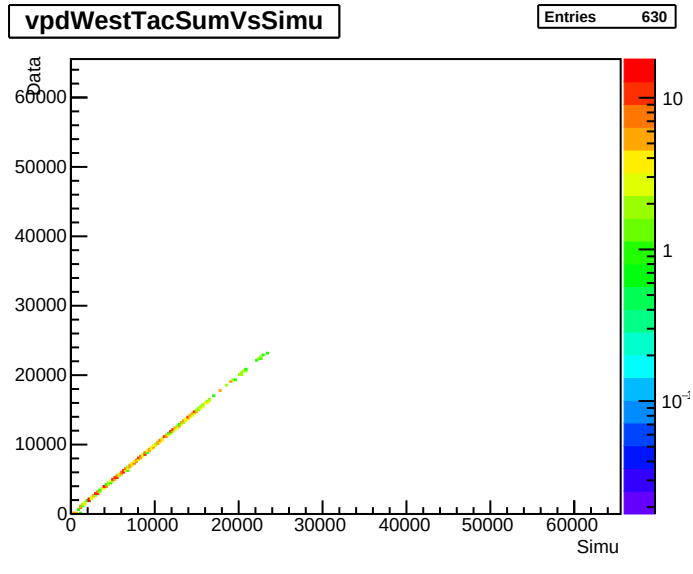

vpdWestAdcSum
Entries 10000

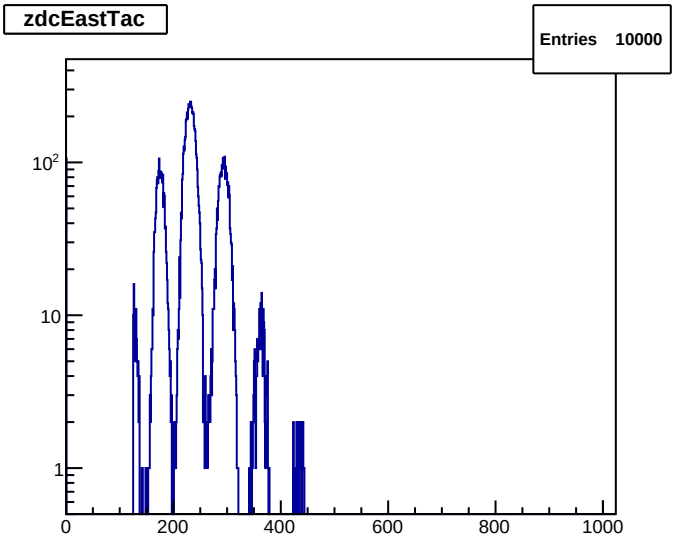

vpdWestAdcSumVsSimu
Entries 425
Data
Simu

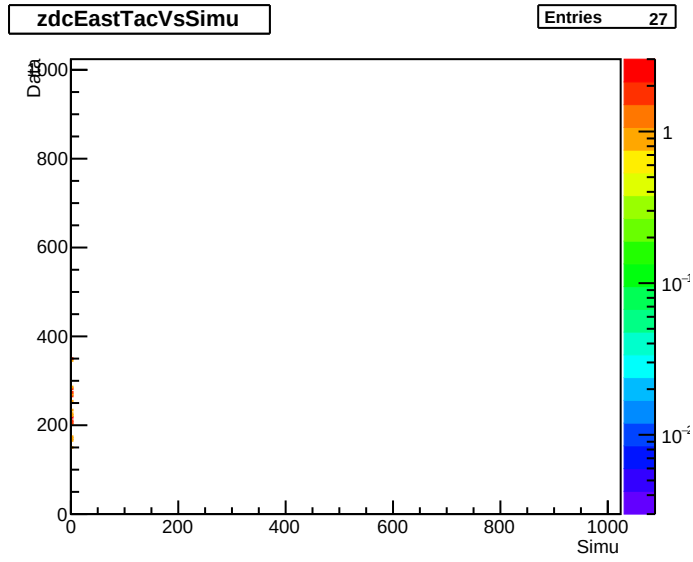

vpdWestNHits
Entries 10000

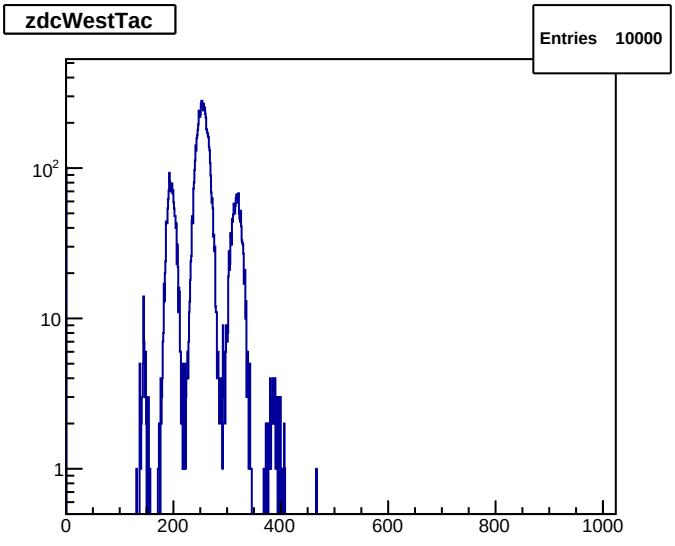

vpdWestNHitsVsSimu
Entries 630

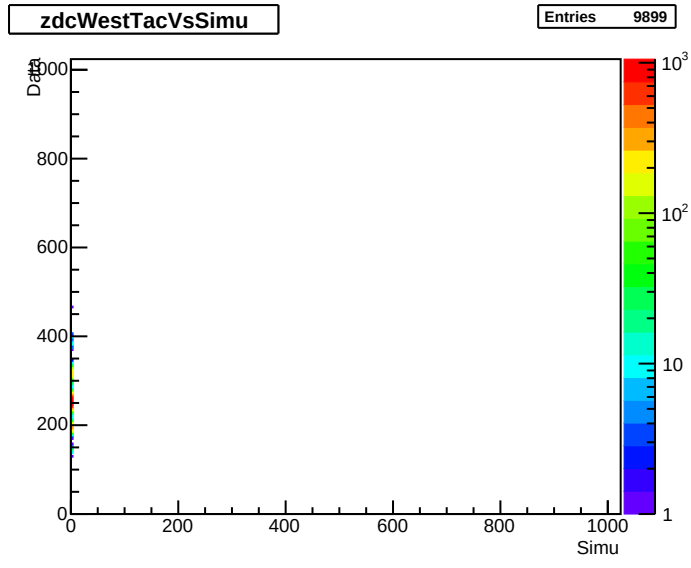

vpdWestTacSum
Entries 10000

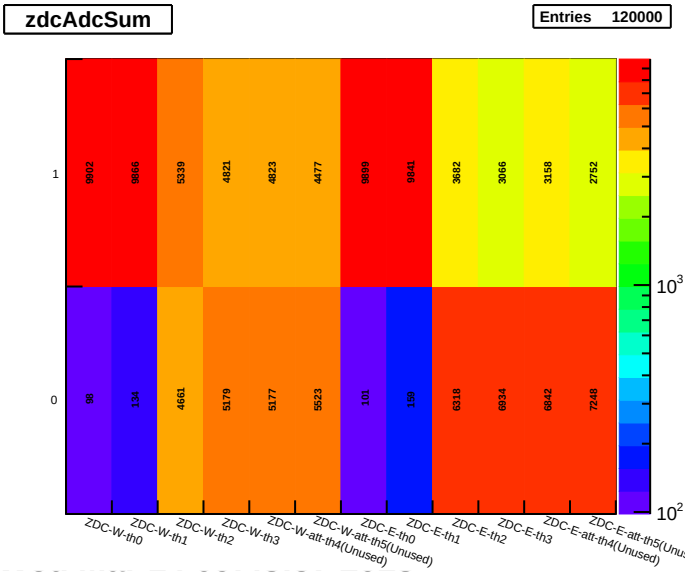

vpdWestTacSumVsSimu
Entries 630
Data
Simu

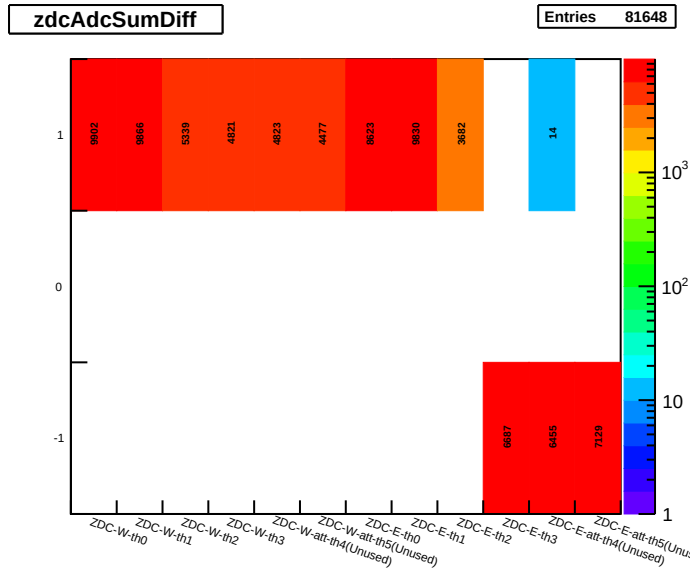

zdcEastTac
Entries 10000

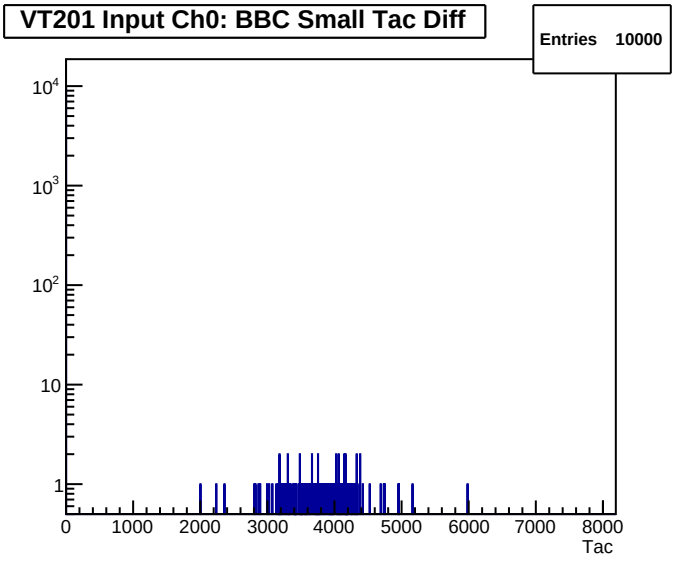

zdcEastTacVsSimu
Entries 27
Data
Simu

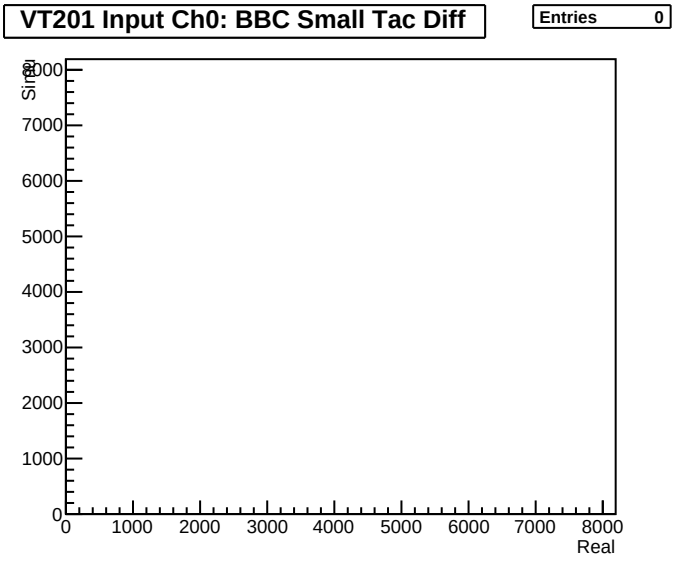

zdcWestTac
Entries 10000

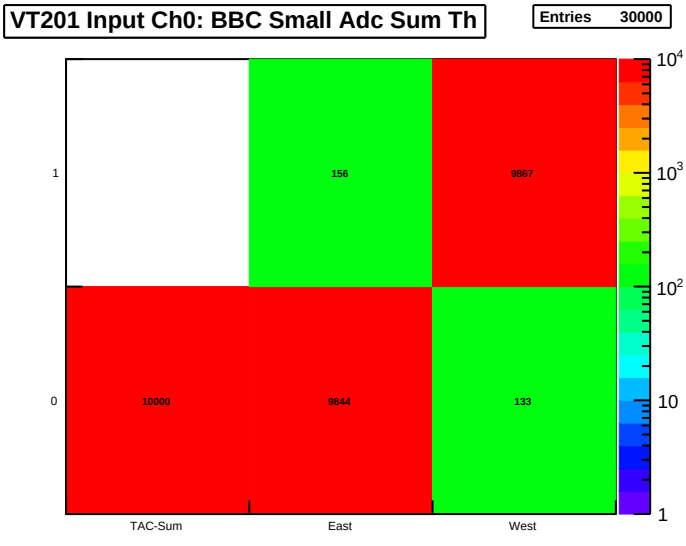

zdcWestTacVsSimu
Entries 9899
Data
Simu

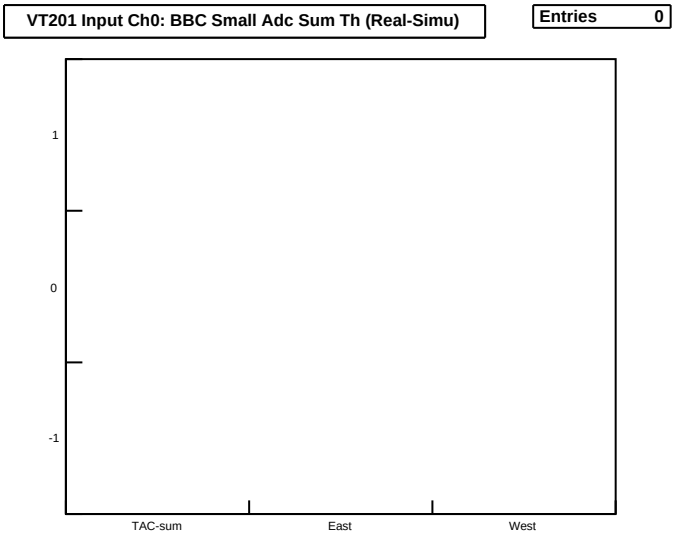

zdcAdcSum
Entries 120000
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

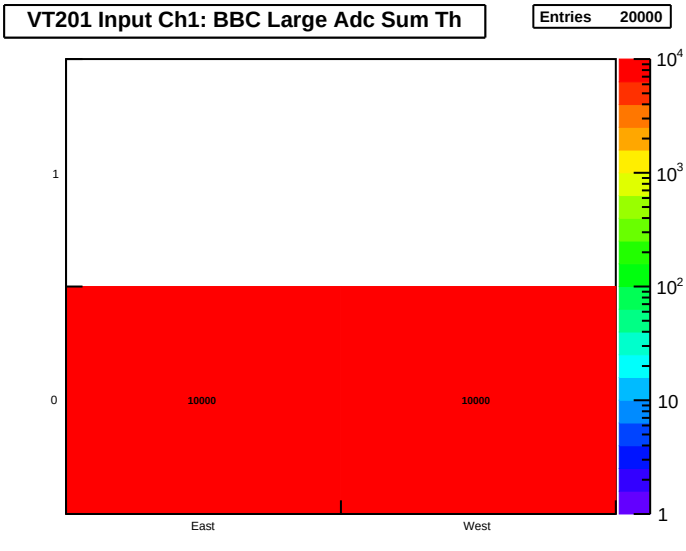

zdcAdcSumDiff
Entries 81648
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

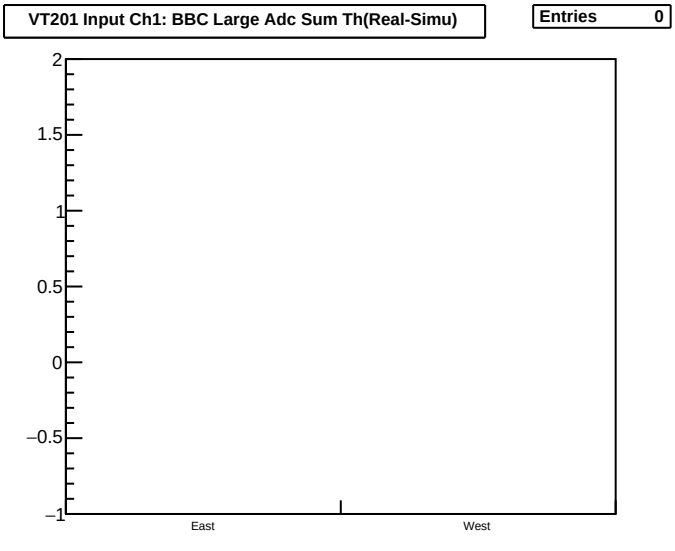

VT201 Input Ch0: BBC Small Tac Diff
Entries 10000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

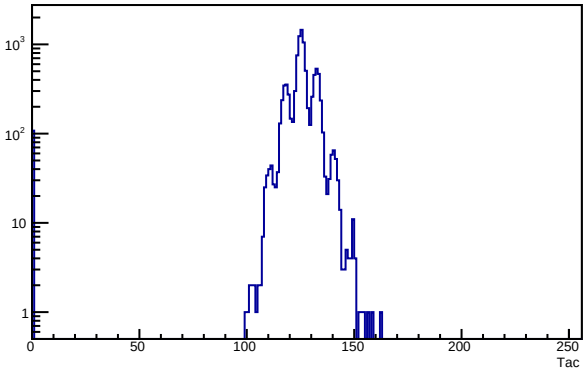

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 30000
156
9867
10000
9844
133
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

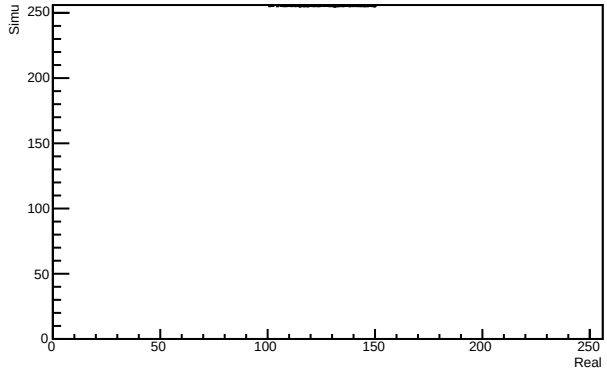

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 20000
10000
10000
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

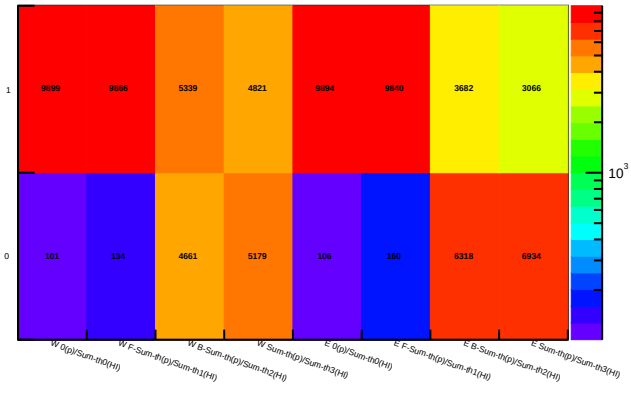
VT201 Input Ch2: ZDC Tac Diff



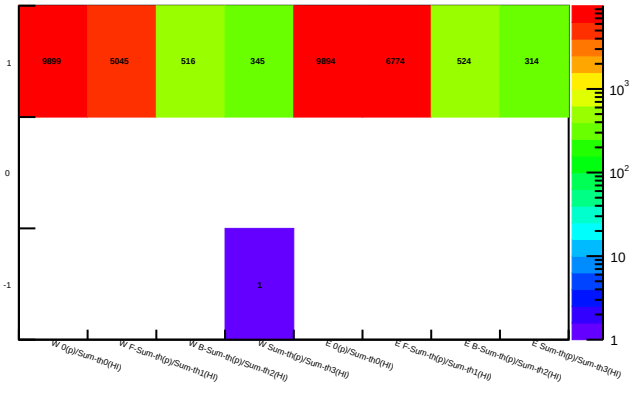
VT201 Input Ch2: ZDC Tac Diff



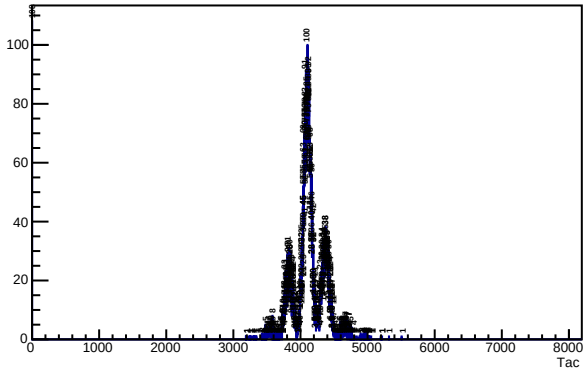
VT201 Input Ch2: ZDC Adc Sum Th



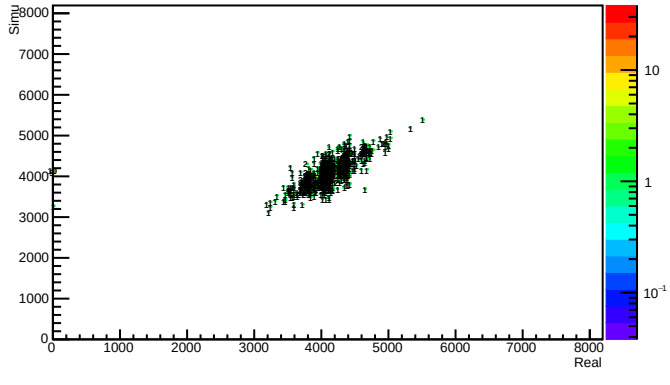
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



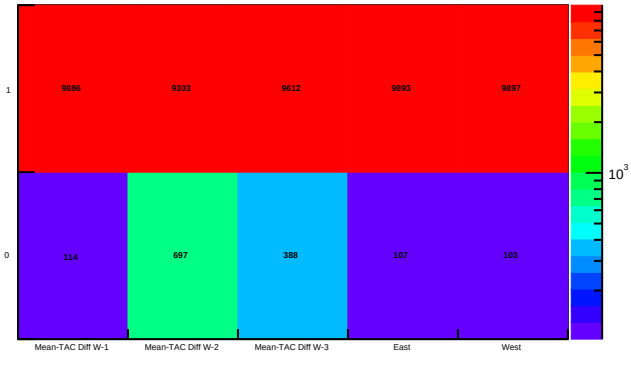
VT201 Input Ch4: VPD Tac Diff



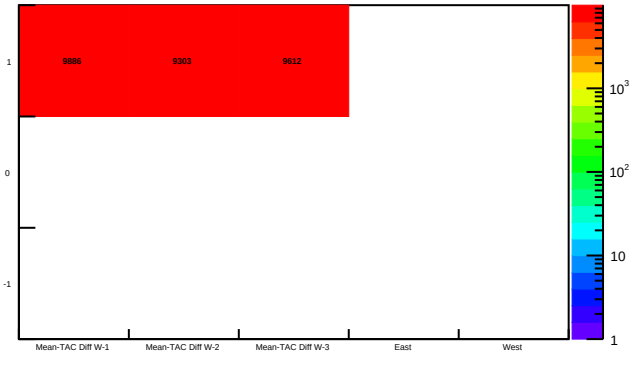
VT201 Input Ch4: VPD Tac Diff

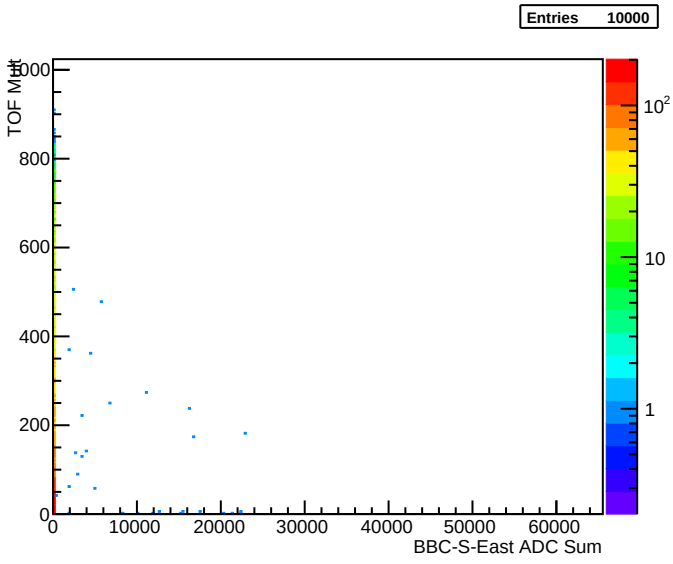


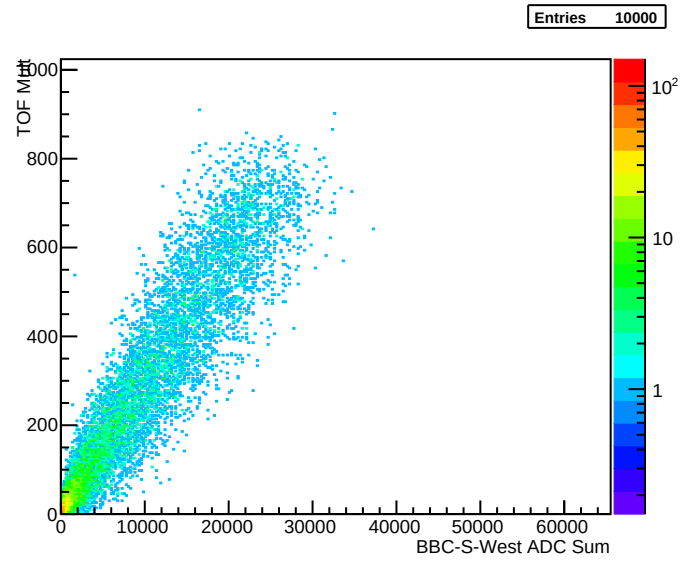
VT201 Input Ch4: VPD Adc Sum Th

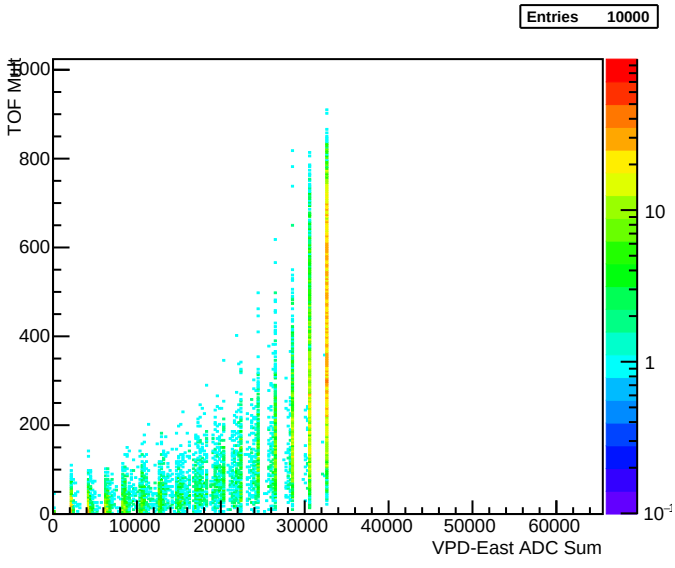


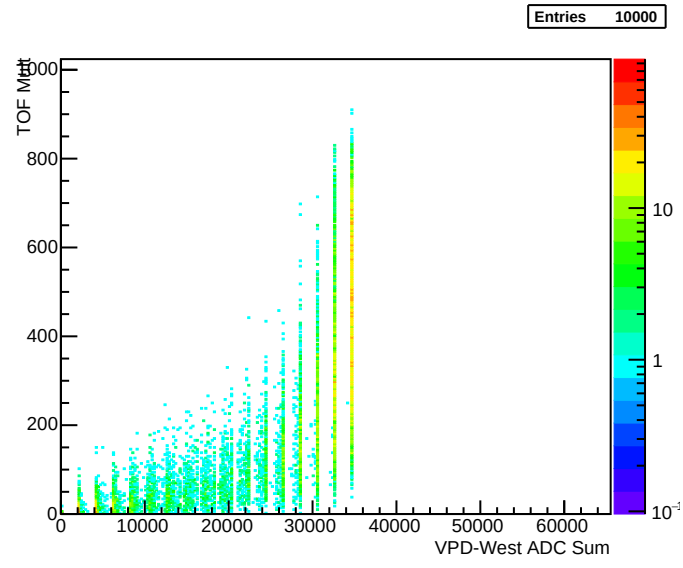
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

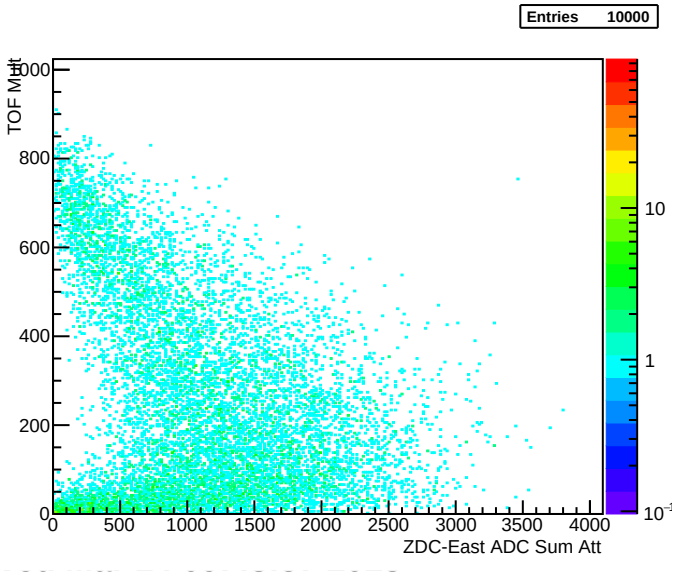


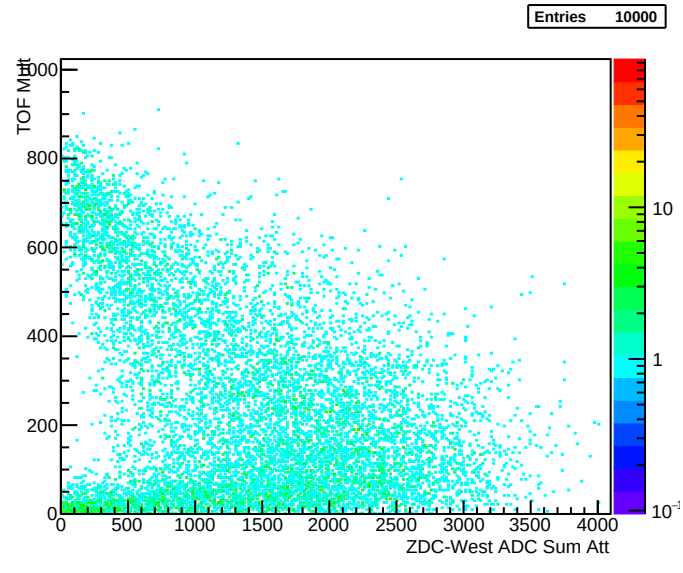

Entries 10000
TOF Mult
BBC-S-East ADC Sum

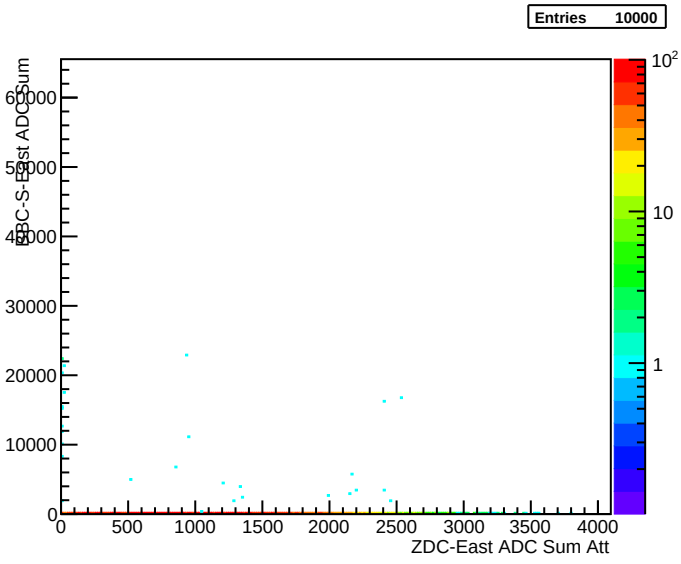

Entries 10000
TOF Mult
BBC-S-West ADC Sum

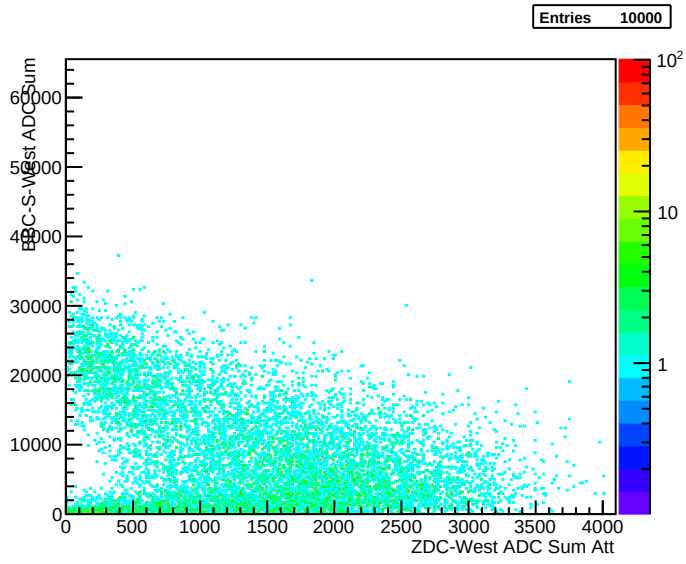

Entries 10000
TOF Mult
VPD-East ADC Sum

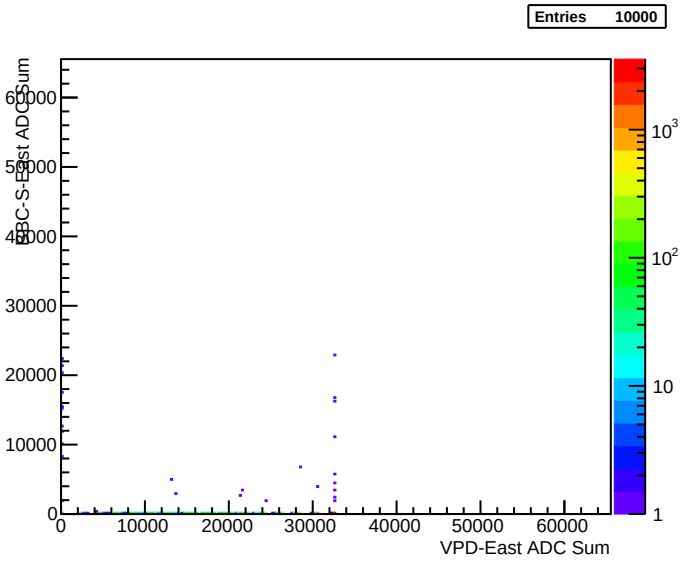

Entries 10000
TOF Mult
VPD-West ADC Sum

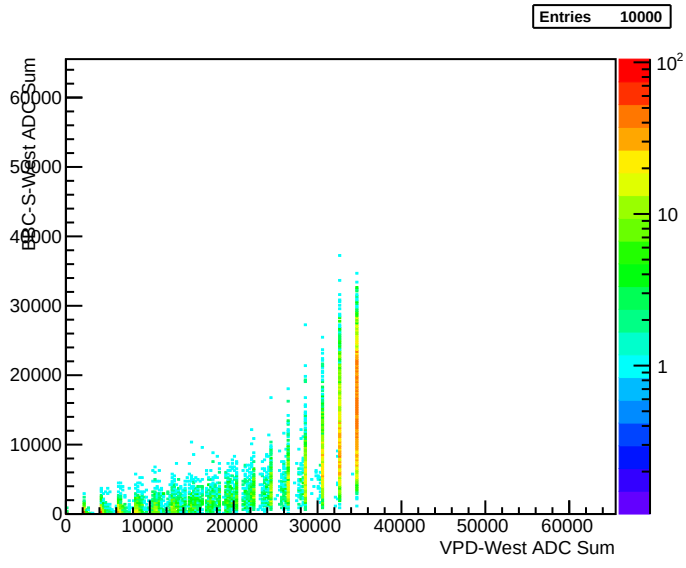

Entries 10000
TOF Mult
ZDC-East ADC Sum Att

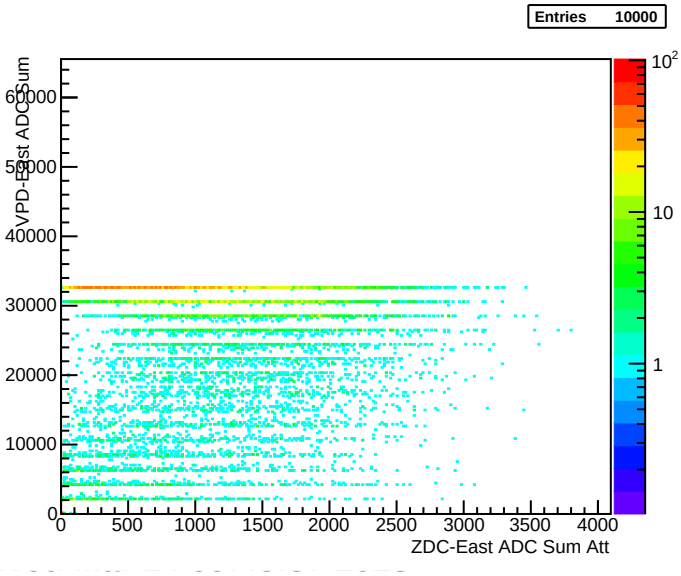

Entries 10000
TOF Mult
ZDC-West ADC Sum Att

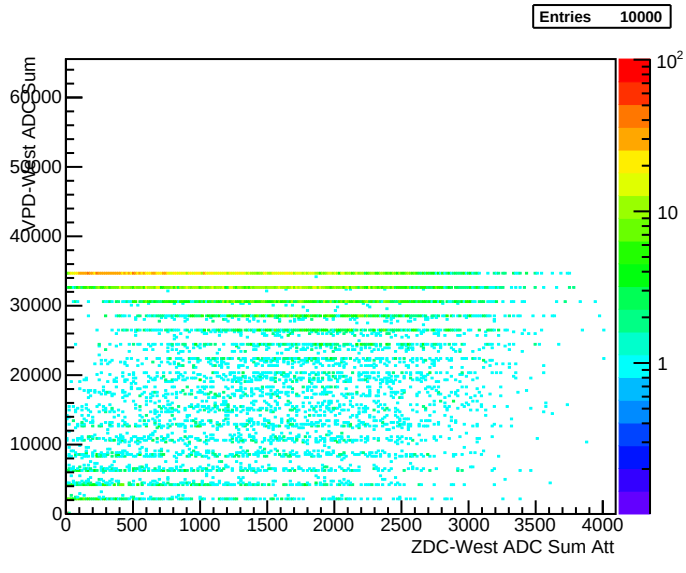

Entries 10000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

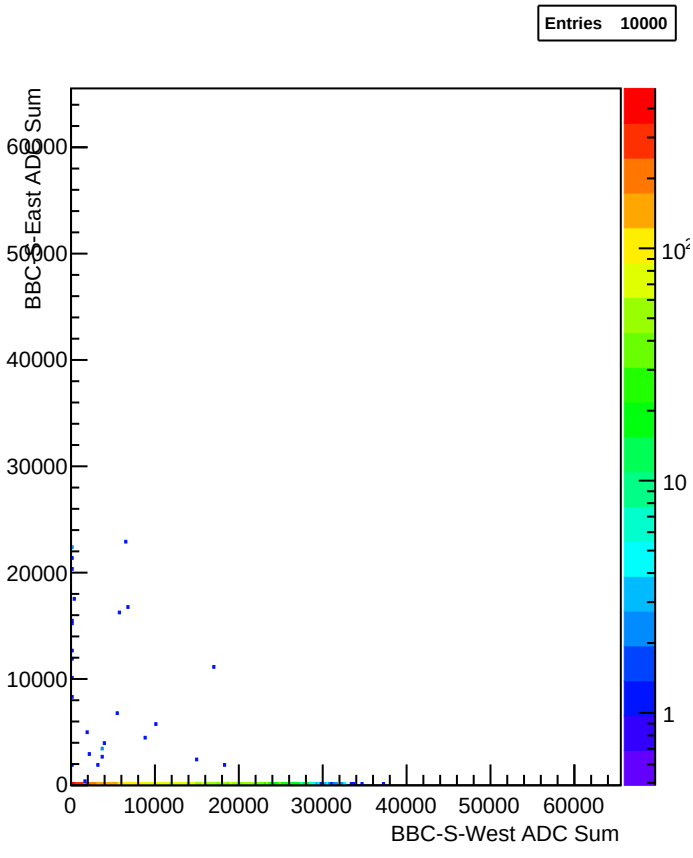

Entries 10000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

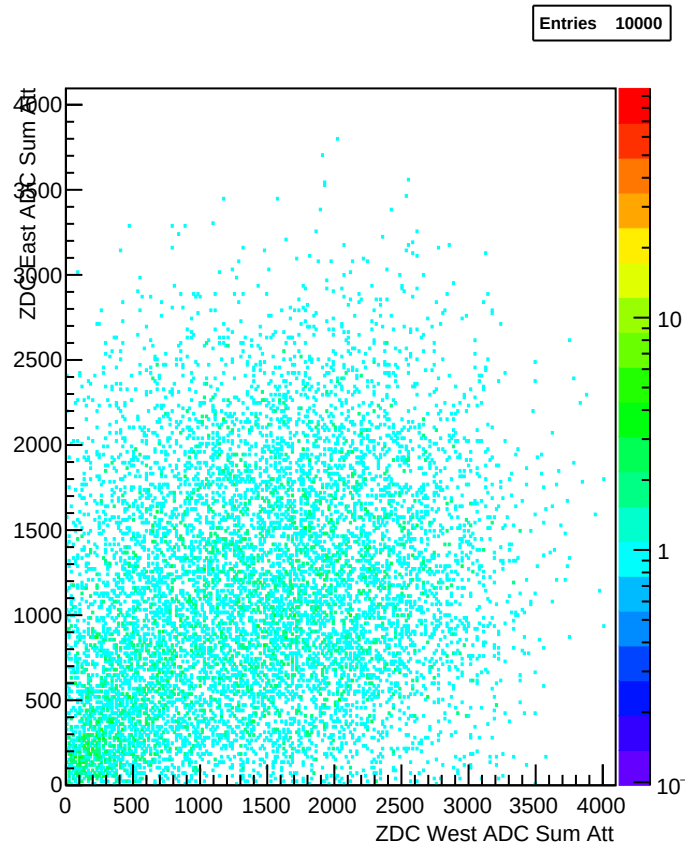

Entries 10000
BBC-S-East ADC Sum
VPD-East ADC Sum

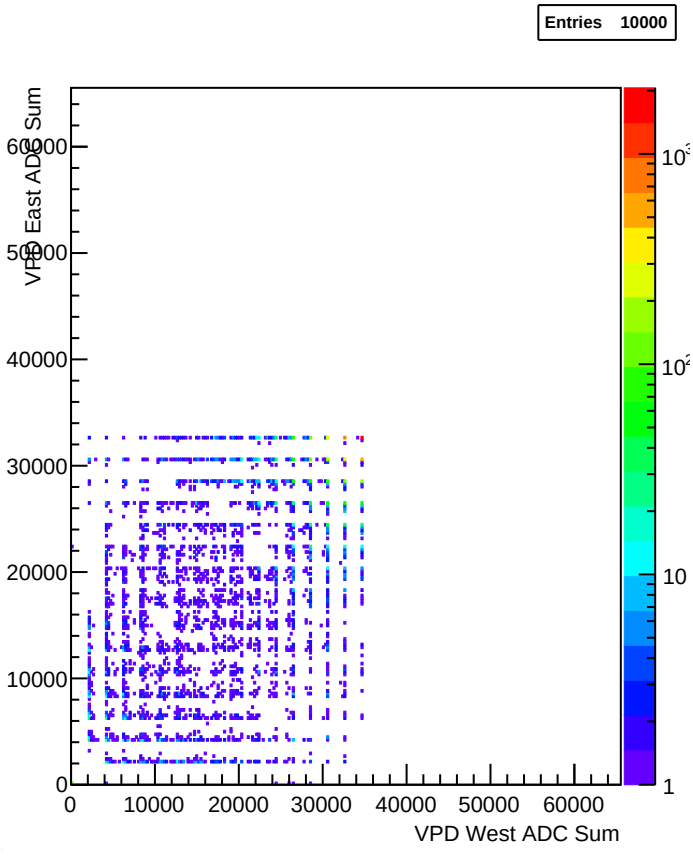

Entries 10000
BBC-S-West ADC Sum
VPD-West ADC Sum

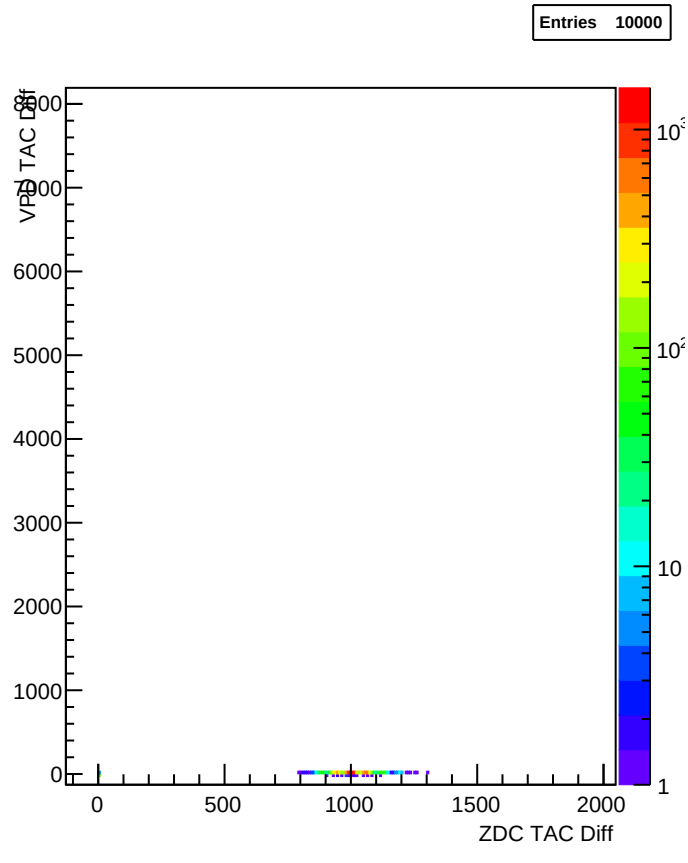

Entries 10000
VPD-East ADC Sum
ZDC-East ADC Sum Att

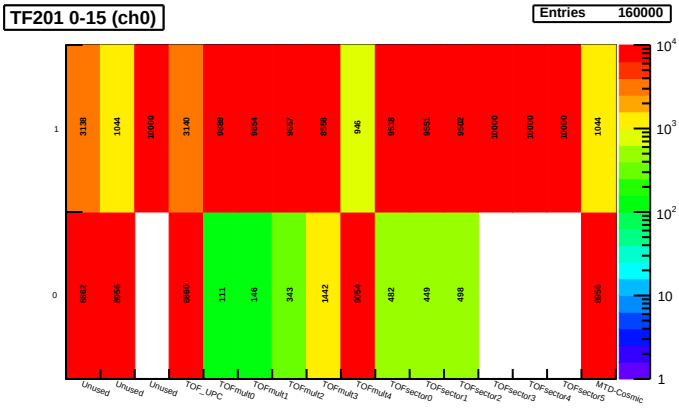

Entries 10000
VPD-West ADC Sum
ZDC-West ADC Sum Att

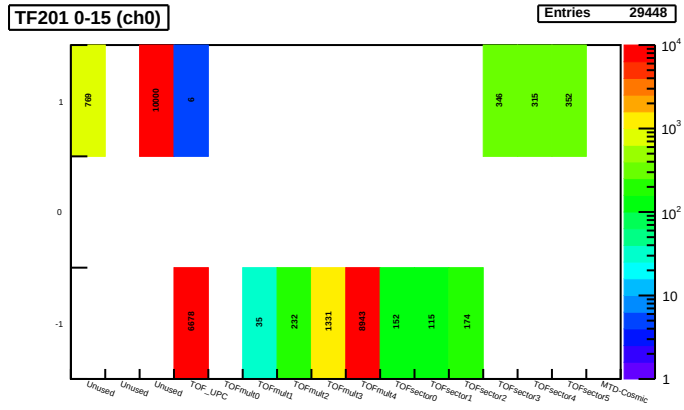

Entries 10000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

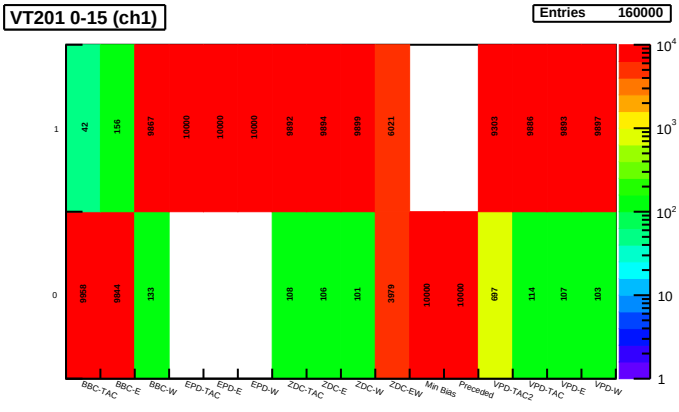

Entries 10000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

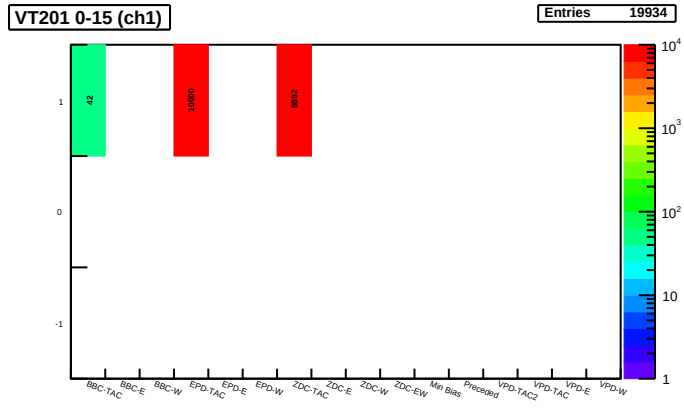

Entries 10000
VPD East ADC Sum
VPD West ADC Sum


Entries 10000
VPD TAC Diff
ZDC TAC Diff


TF201 0-15 (ch0)
Entries 160000
3138 1044 10000 3140 9889 9854 9657 8538 946 9518 9551 9502 10000 10000 10000 1044
6652 8956 6850 111 146 343 1442 9054 482 449 498 8856
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 29448
769 10000 6 346 315 352
6678 35 232 1331 8943 152 115 174
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 160000
42 156 9857 10000 10000 10000 9892 9894 9899 6021 9303 9886 9893 9897
9953 9844 133 108 106 101 3979 10000 10000 697 114 107 103
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W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Entries 19934
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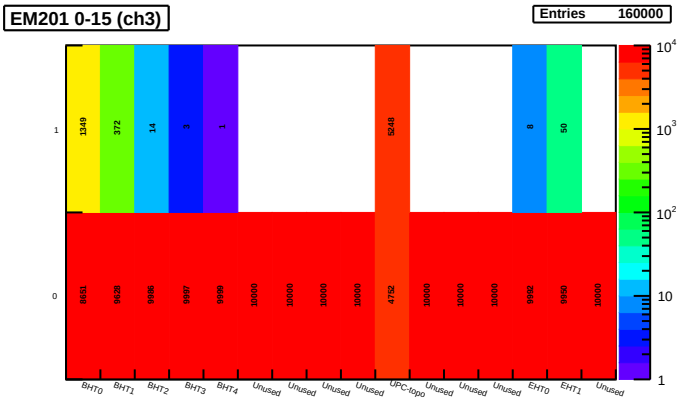
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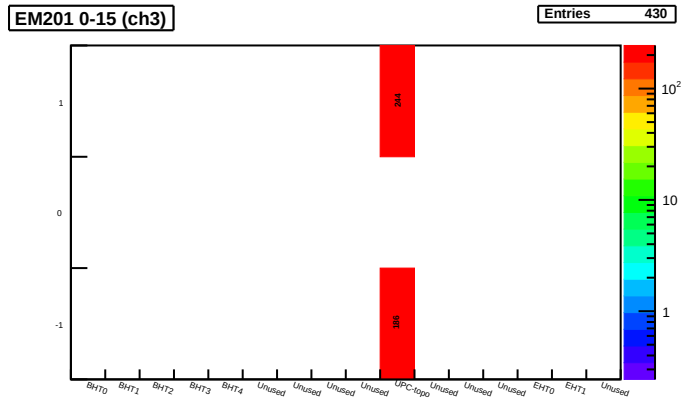
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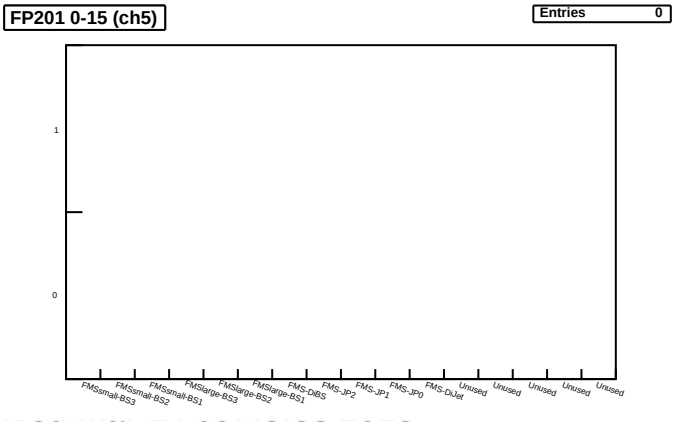
FP201 0-15 (ch5)
Entries 0
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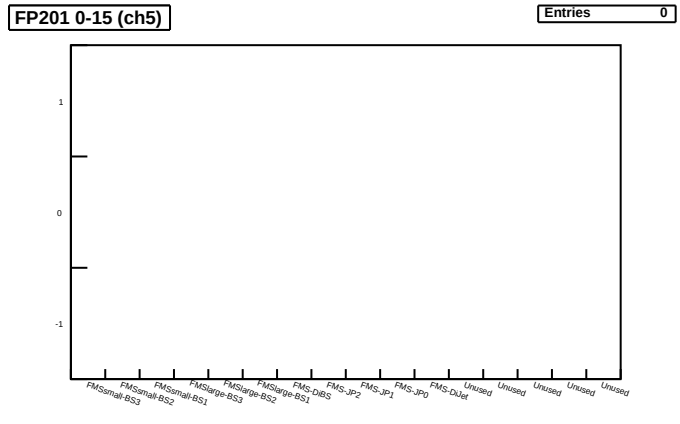
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