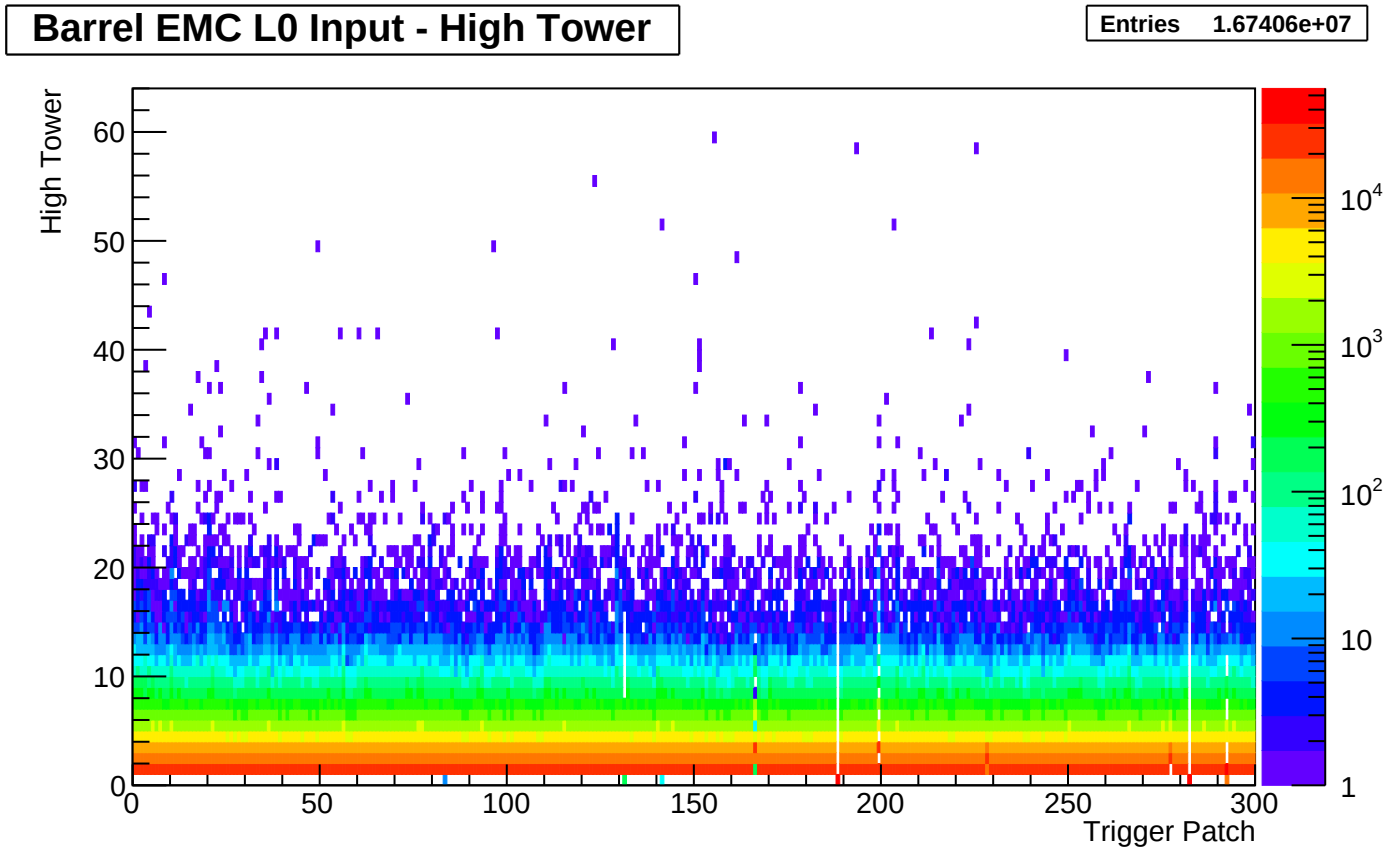
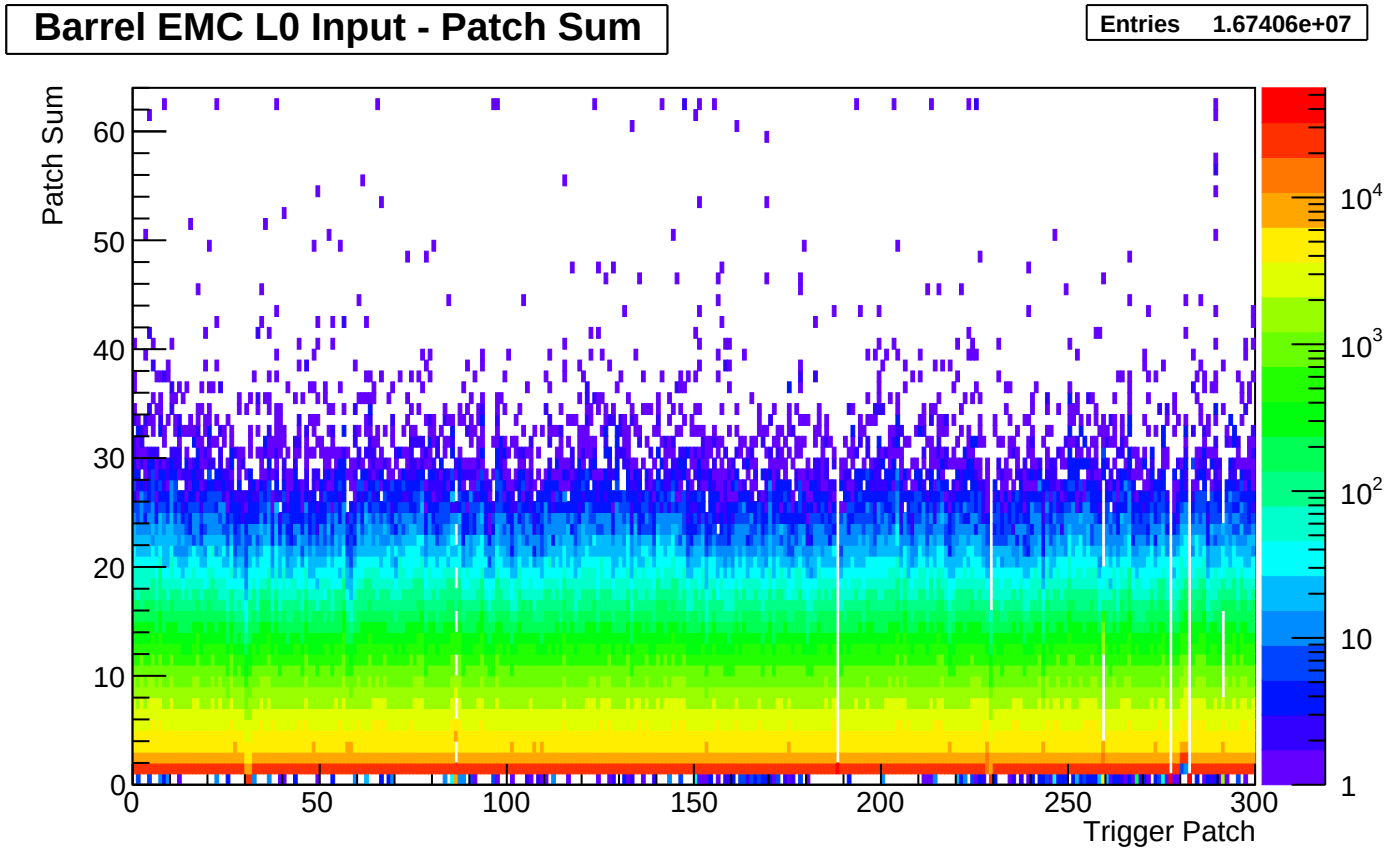
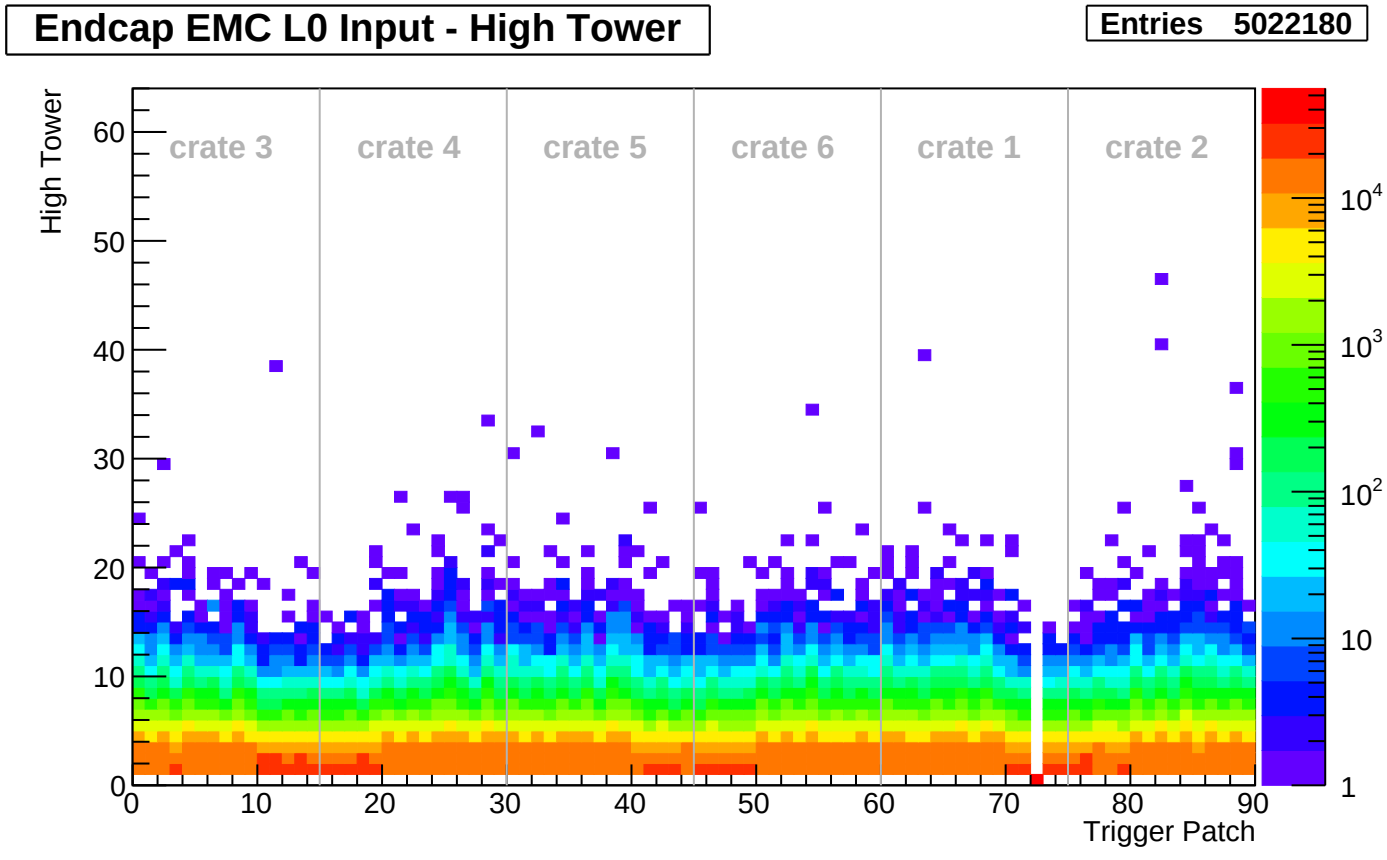
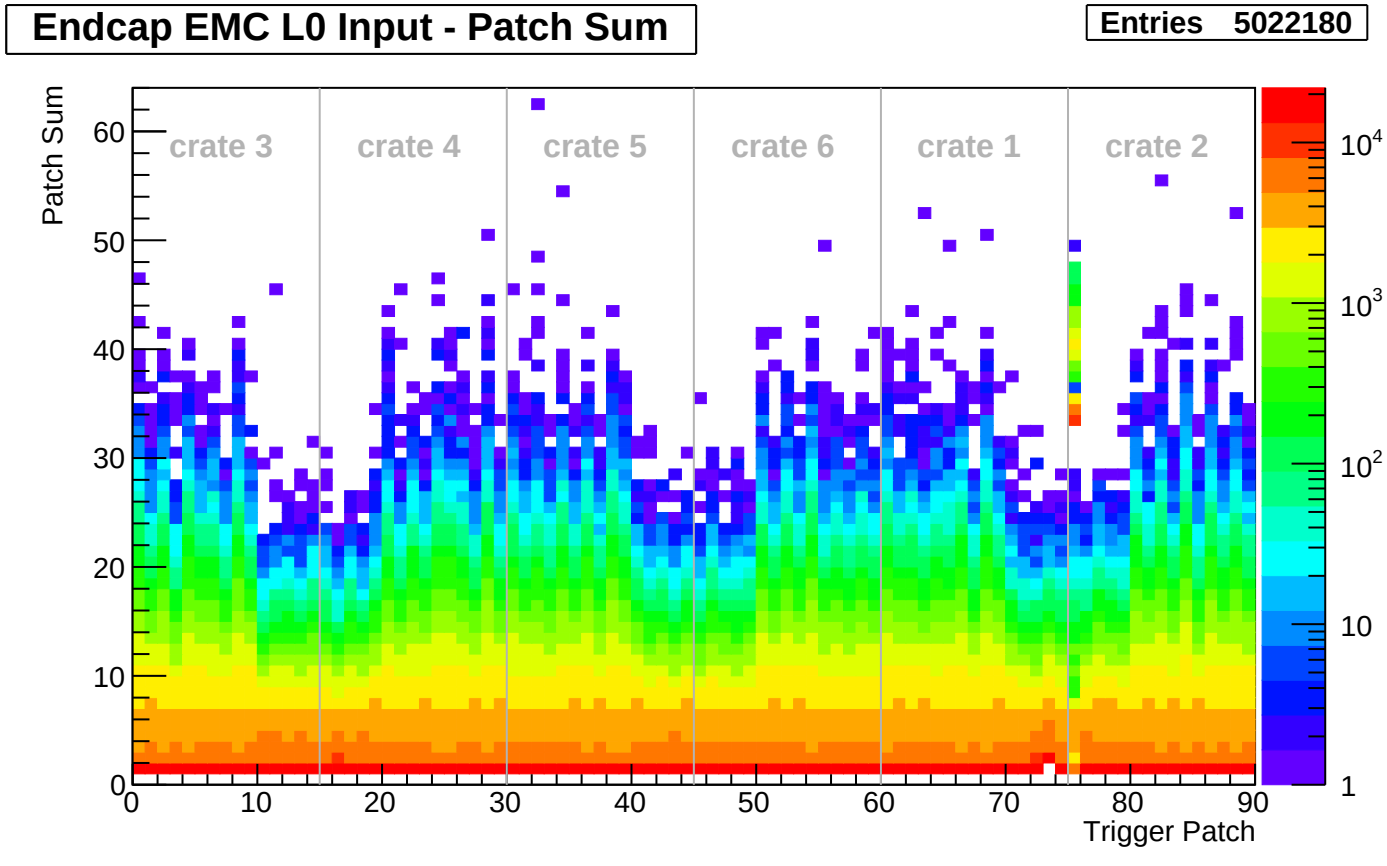
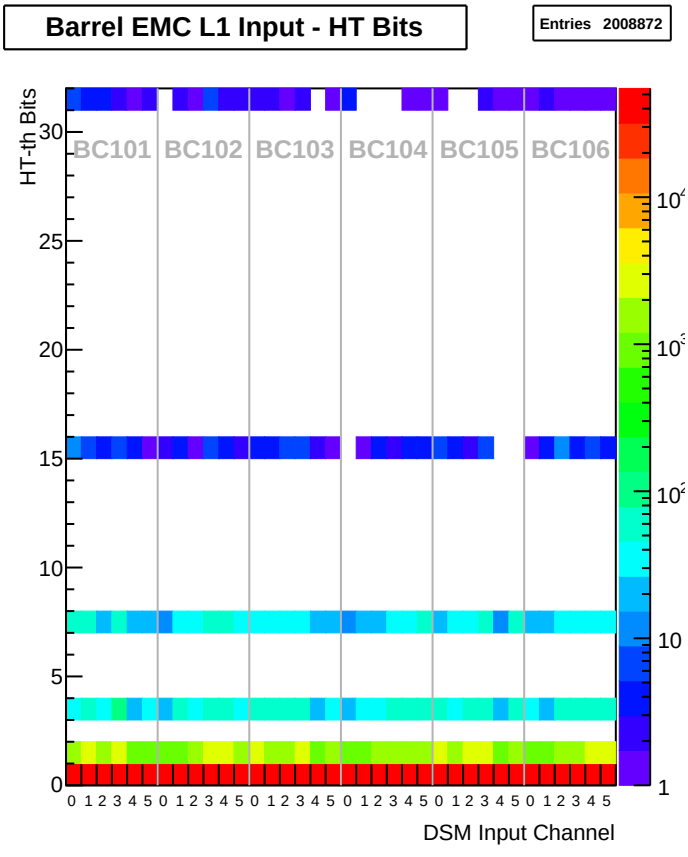
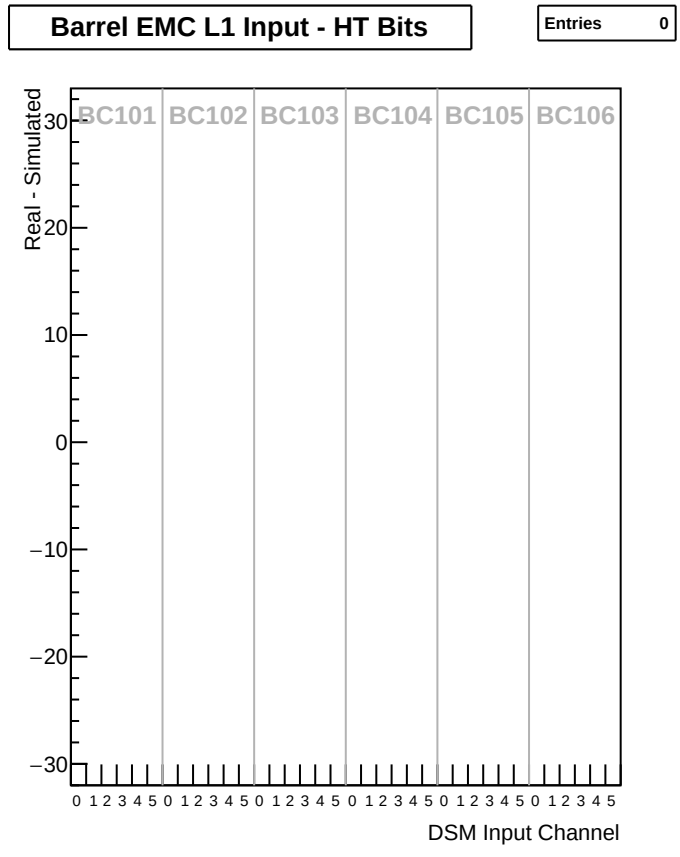

Barrel EMC L0 Input - High Tower
Entries 1.67406e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

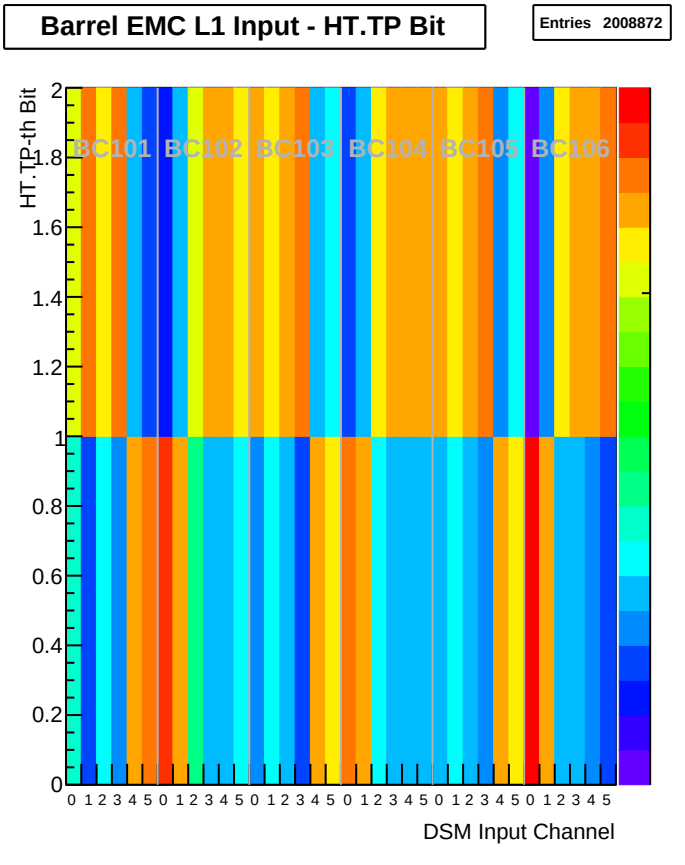

Barrel EMC L0 Input - Patch Sum
Entries 1.67406e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

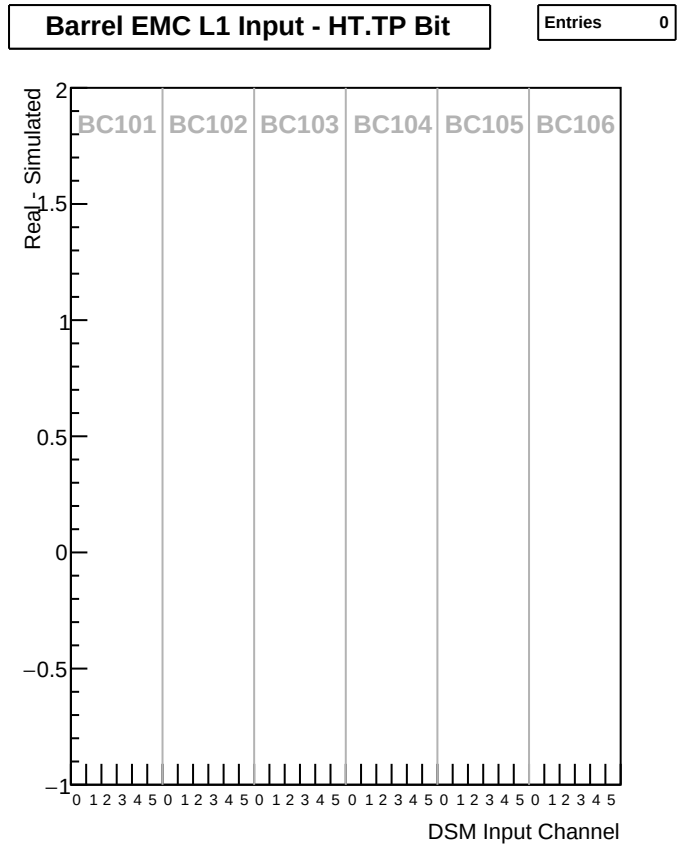

Endcap EMC L0 Input - High Tower
Entries 5022180
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

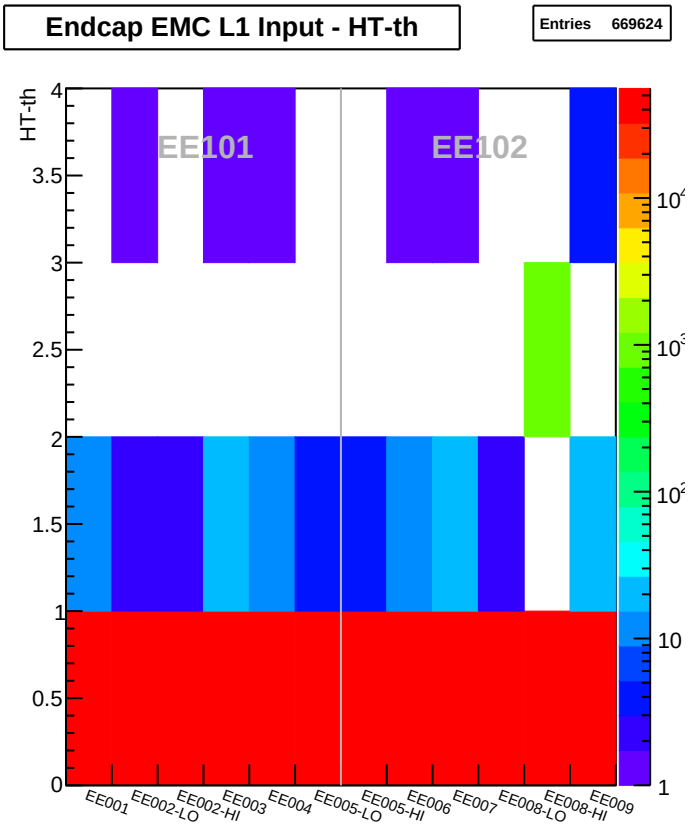

Endcap EMC L0 Input - Patch Sum
Entries 5022180
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

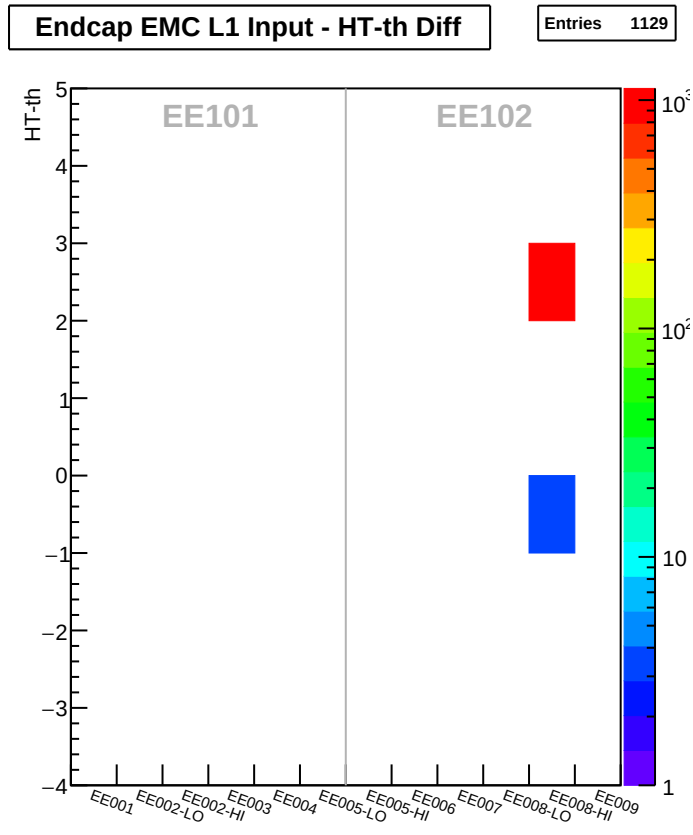

Barrel EMC L1 Input - HT Bits
Entries 2008872
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

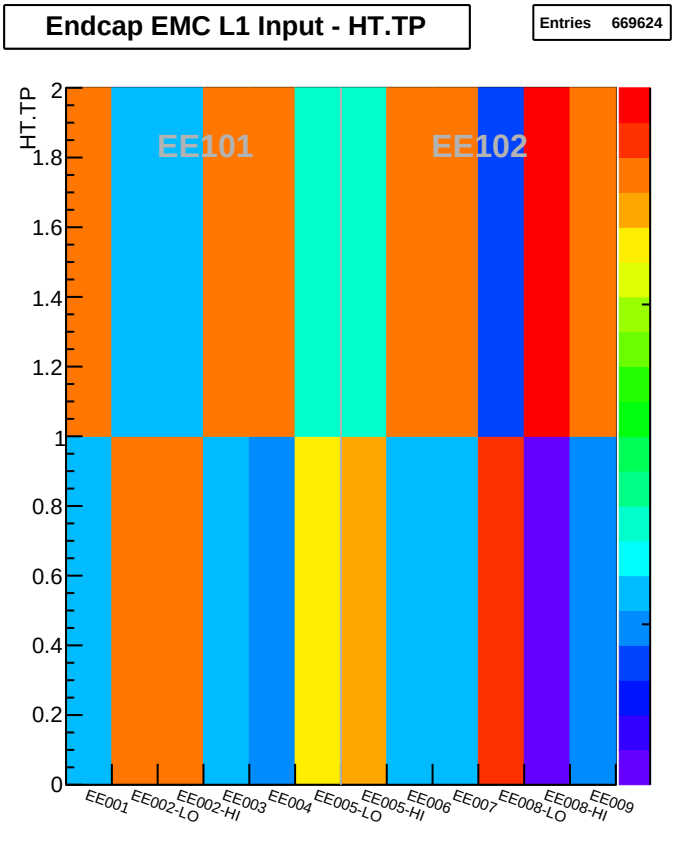

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

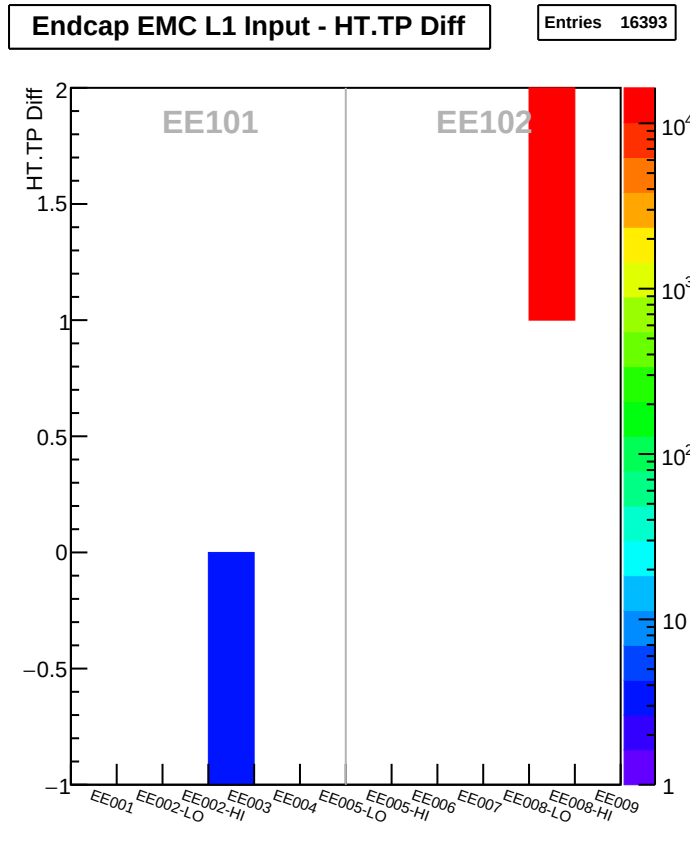

Barrel EMC L1 Input - HT.TP Bit
Entries 2008872
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

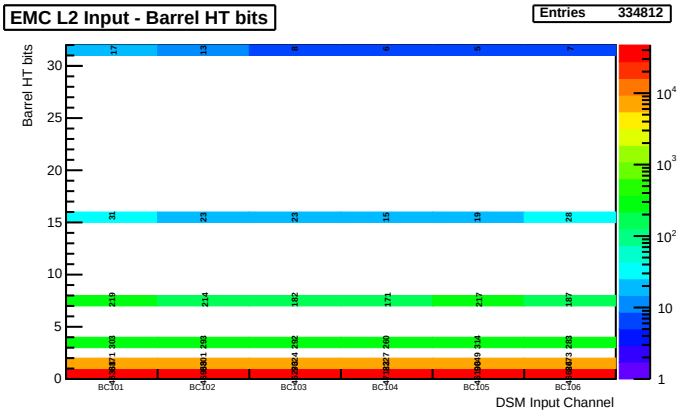

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

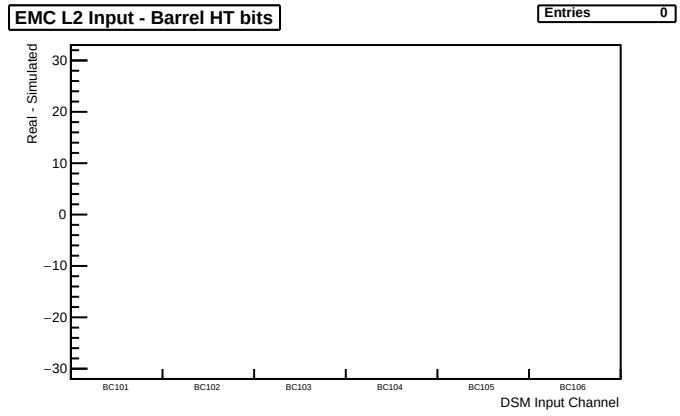

Endcap EMC L1 Input - HT-th
Entries 669624
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

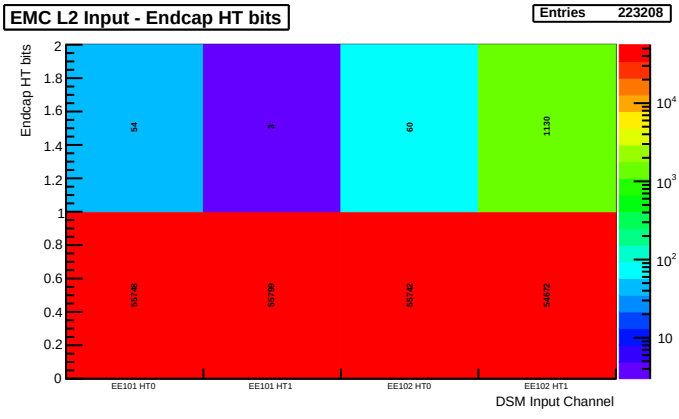

Endcap EMC L1 Input - HT-th Diff
Entries 1129
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

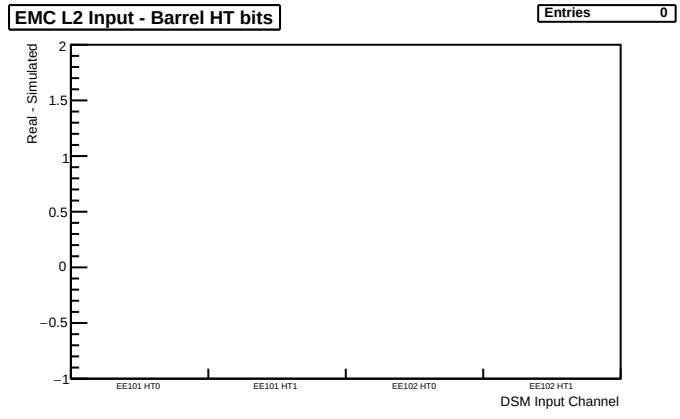

Endcap EMC L1 Input - HT.TP
Entries 669624
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

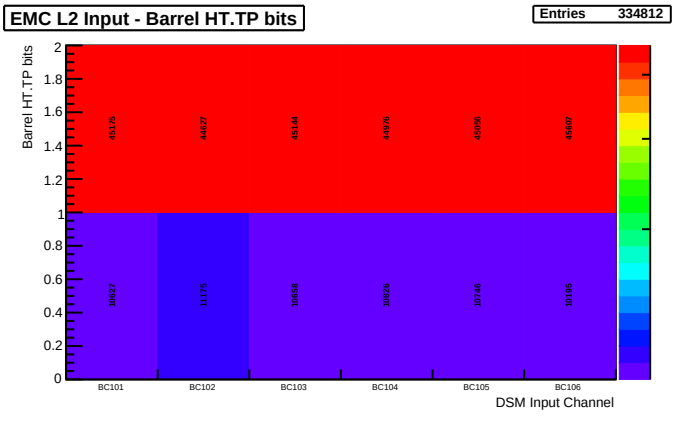

Endcap EMC L1 Input - HT.TP Diff
Entries 16393
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

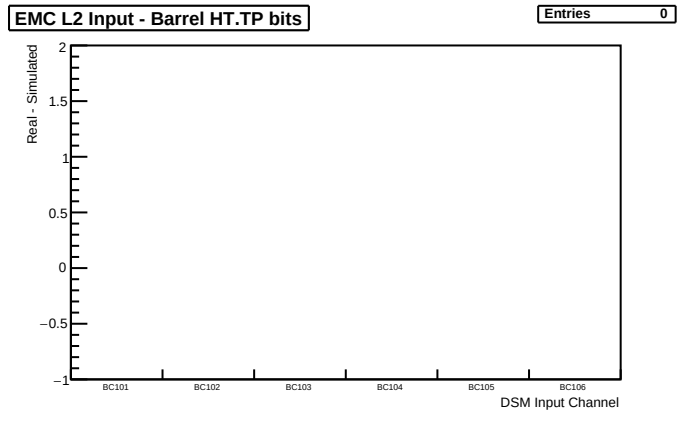

EMC L2 Input - Barrel HT bits
Entries 334812
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

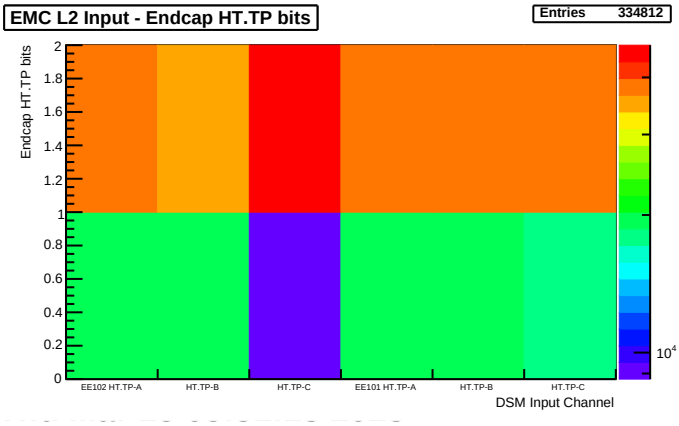

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

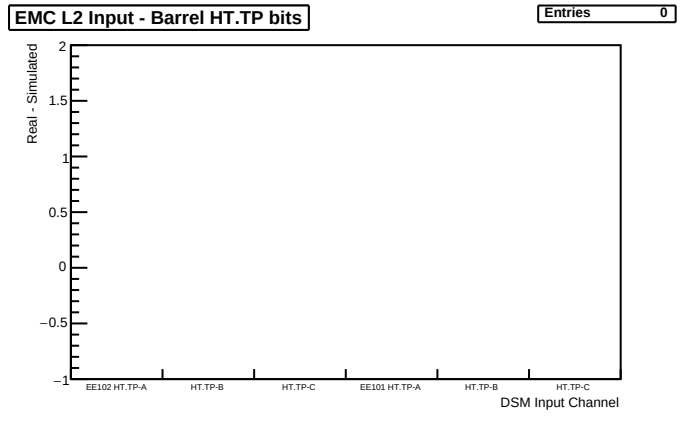

EMC L2 Input - Endcap HT bits
Entries 223208
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

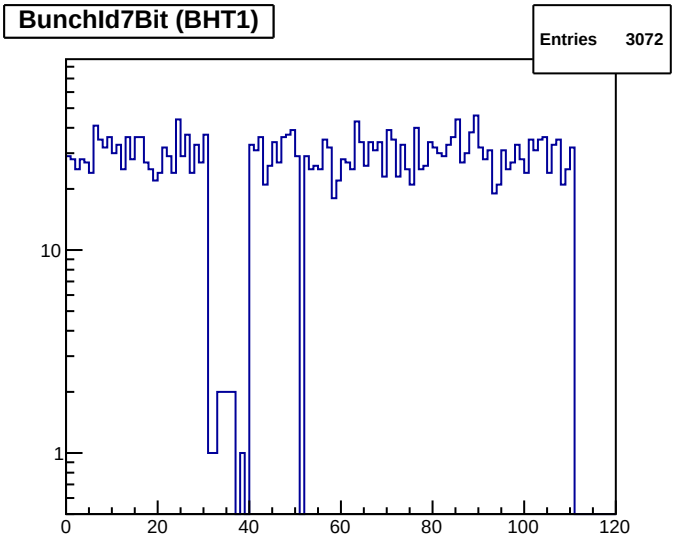

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

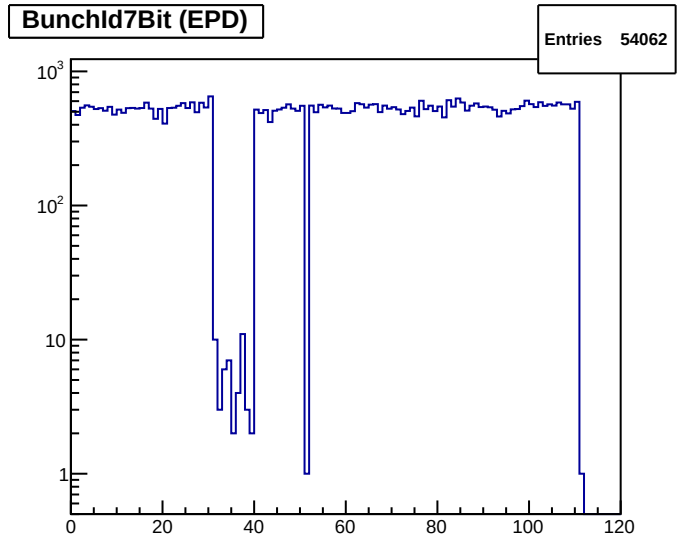

EMC L2 Input - Barrel HT.TP bits
Entries 334812
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

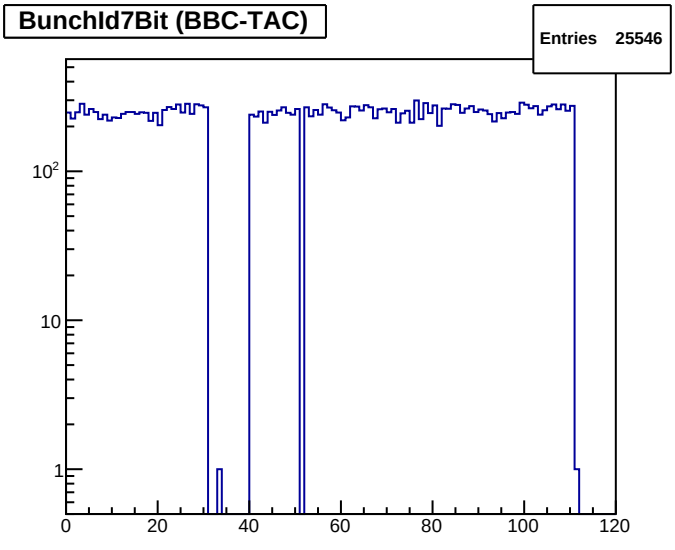

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

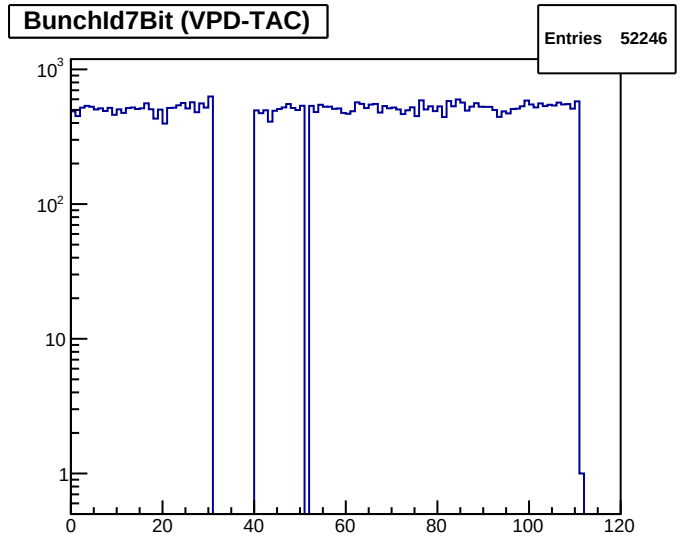

EMC L2 Input - Endcap HT.TP bits
Entries 334812
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

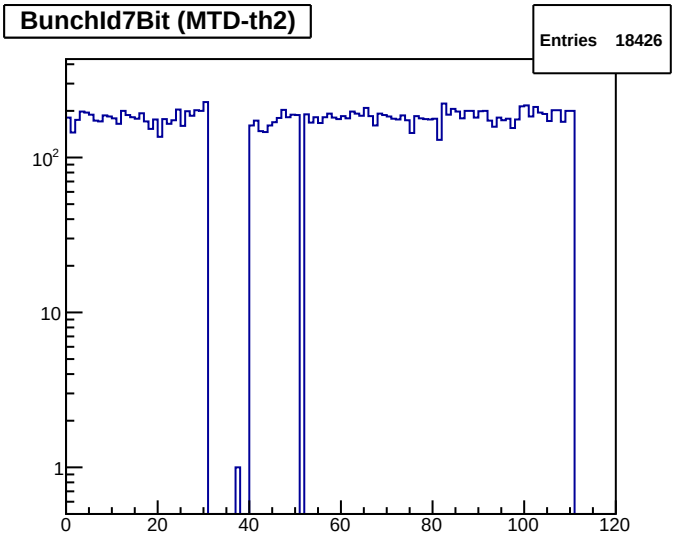

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

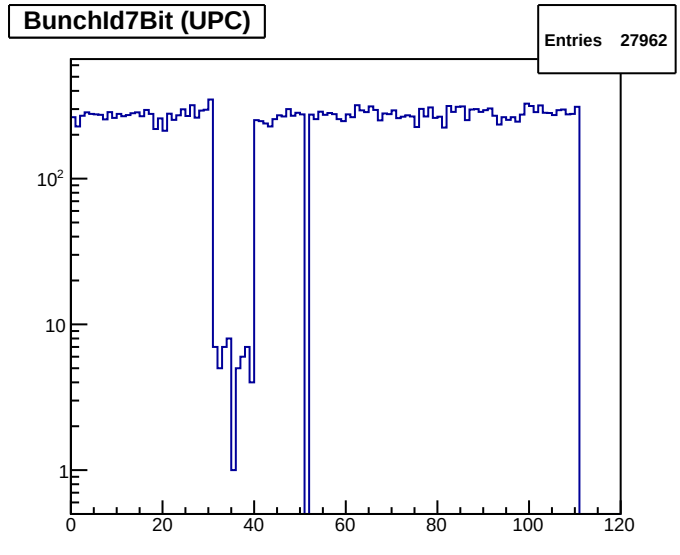

BunchId7Bit (BHT1)
Entries 3072
10
1
0
20
40
60
80
100
120

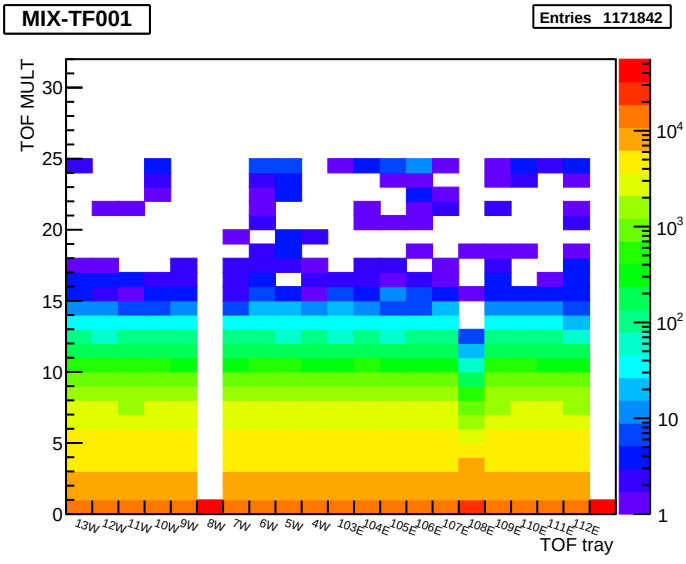

BunchId7Bit (EPD)
Entries 54062
10^3
10^2
10
1
0
20
40
60
80
100
120

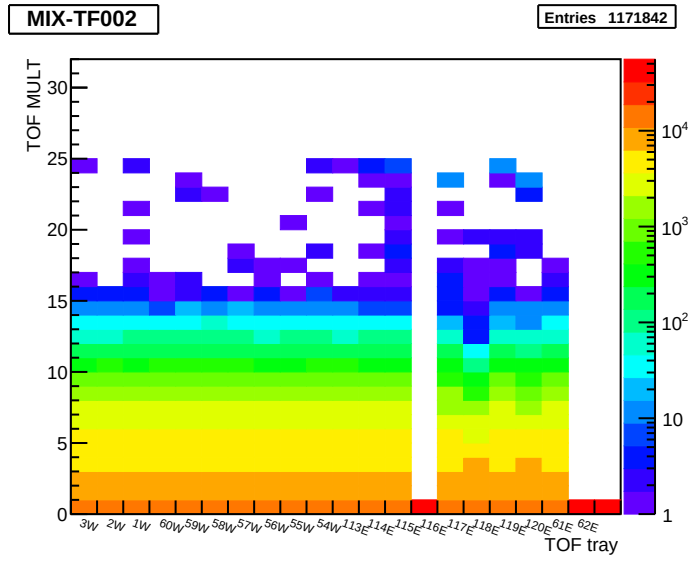

BunchId7Bit (BBC-TAC)
Entries 25546
10^2
10
1
0
20
40
60
80
100
120

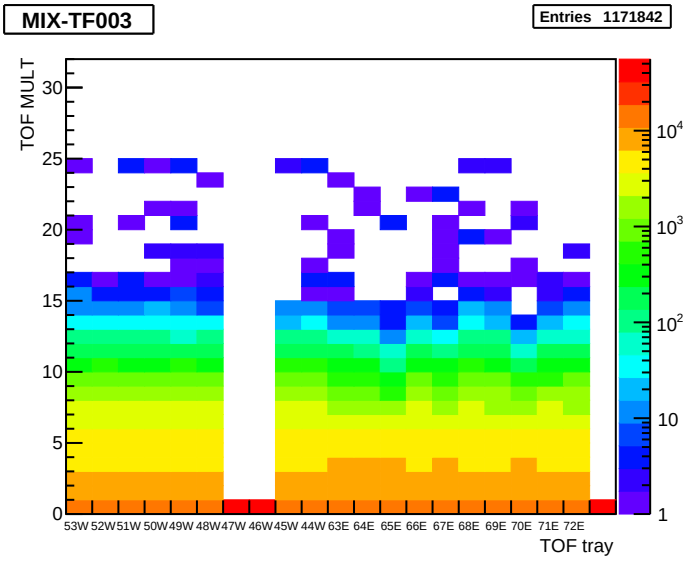

BunchId7Bit (VPD-TAC)
Entries 52246
10^3
10^2
10
1
0
20
40
60
80
100
120

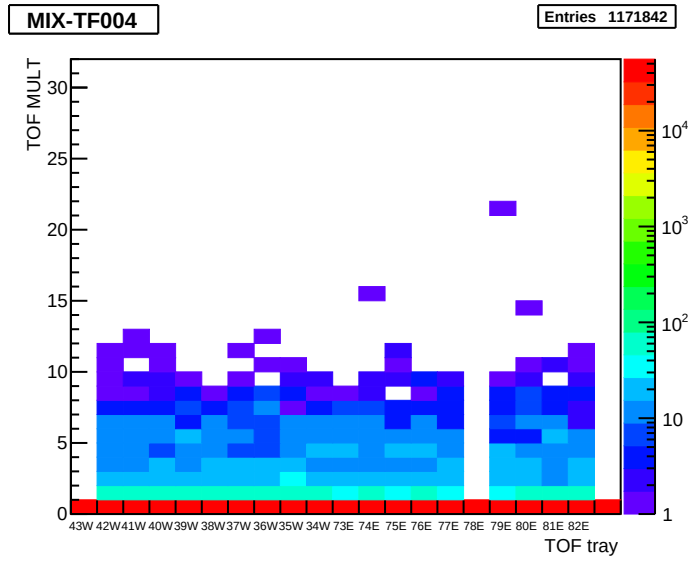

BunchId7Bit (MTD-th2)
Entries 18426
10^2
10
1
0
20
40
60
80
100
120

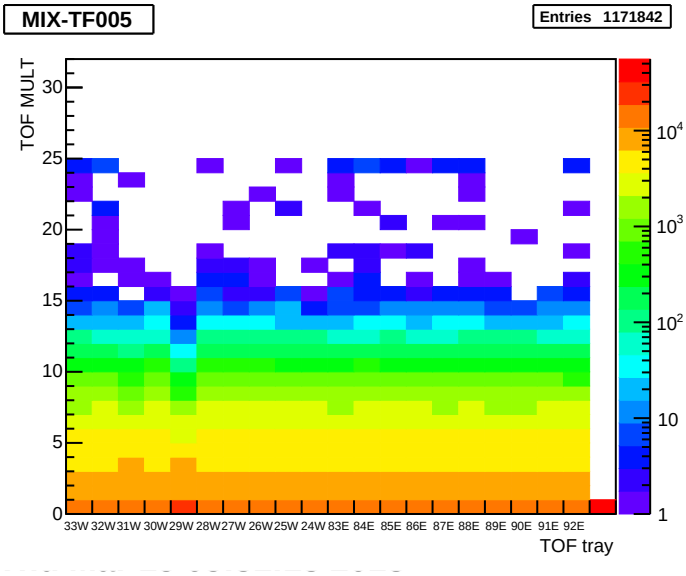

BunchId7Bit (UPC)
Entries 27962
10^2
10
1
0
20
40
60
80
100
120

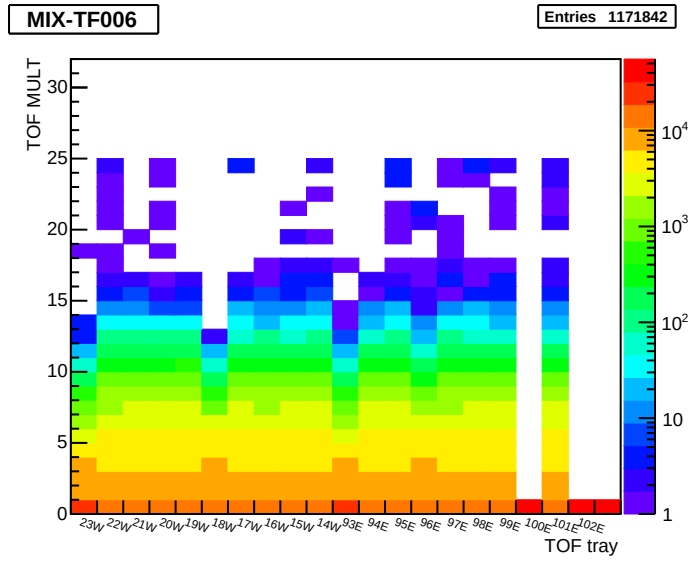

MIX-TF001
Entries 1171842
TOF MULT
TOF tray

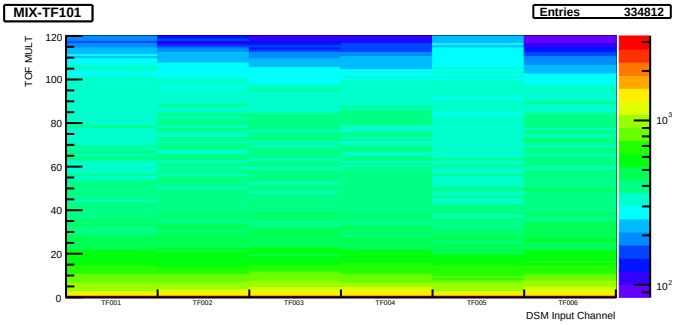

MIX-TF002
Entries 1171842
TOF MULT
TOF tray

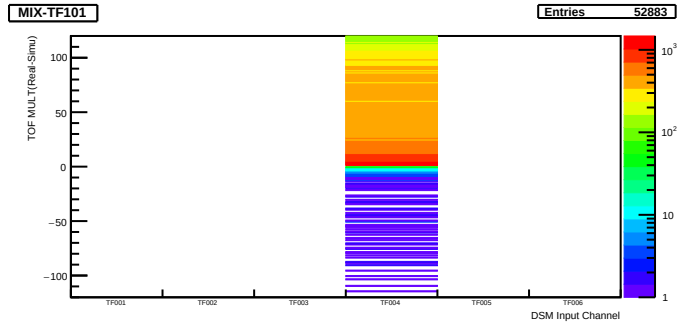

MIX-TF003
Entries 1171842
TOF MULT
TOF tray

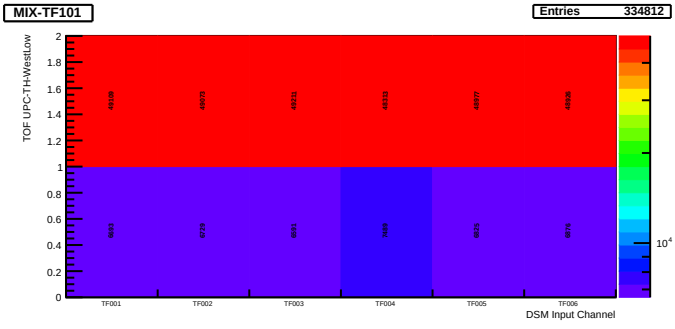

MIX-TF004
Entries 1171842
TOF MULT
TOF tray

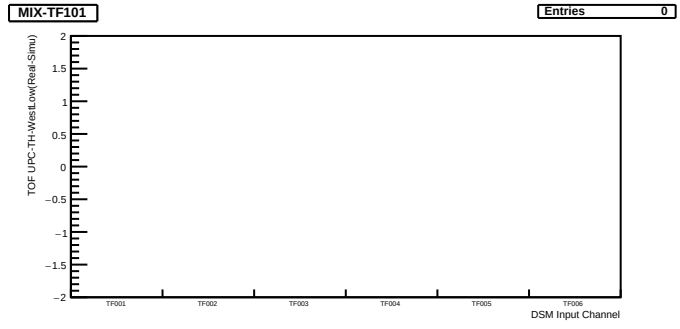

MIX-TF005
Entries 1171842
TOF MULT
TOF tray

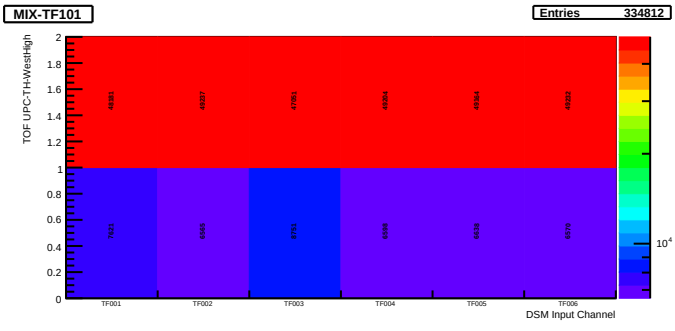

MIX-TF006
Entries 1171842
TOF MULT
TOF tray

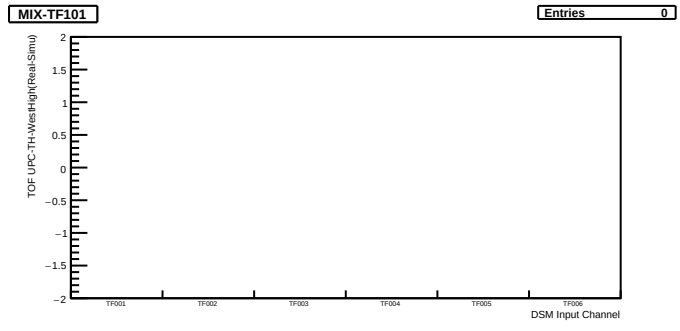

MIX-TF101
Entries 334812
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

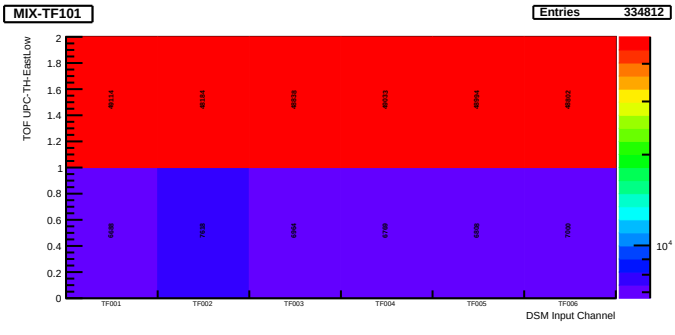

MIX-TF101
Entries 52883
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

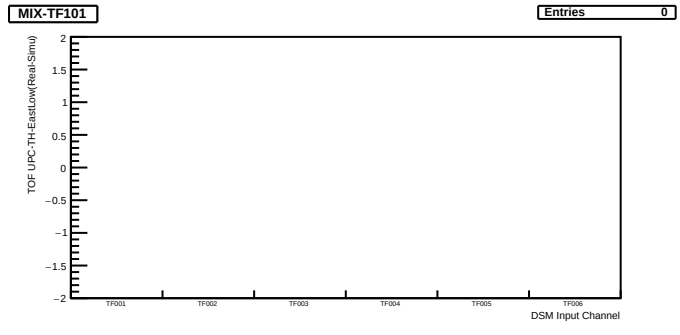

MIX-TF101
Entries 334812
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

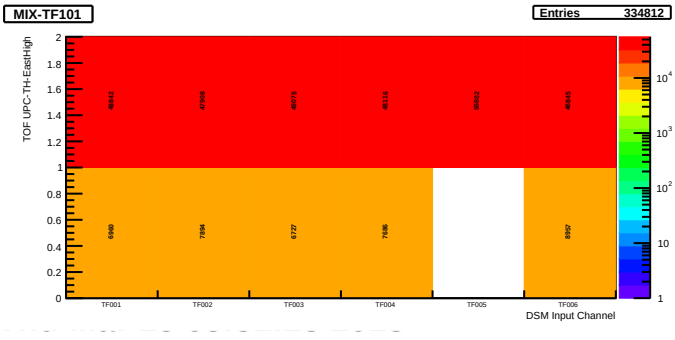

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

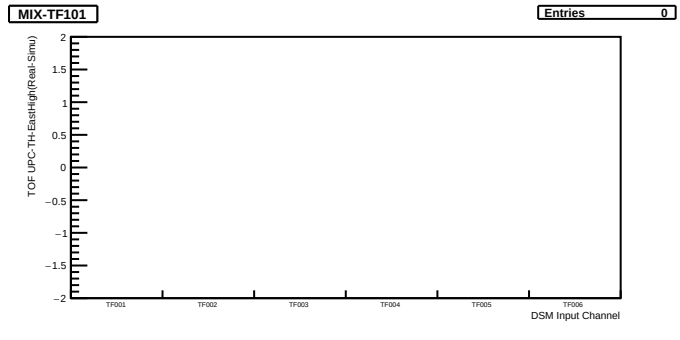

MIX-TF101
Entries 334812
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

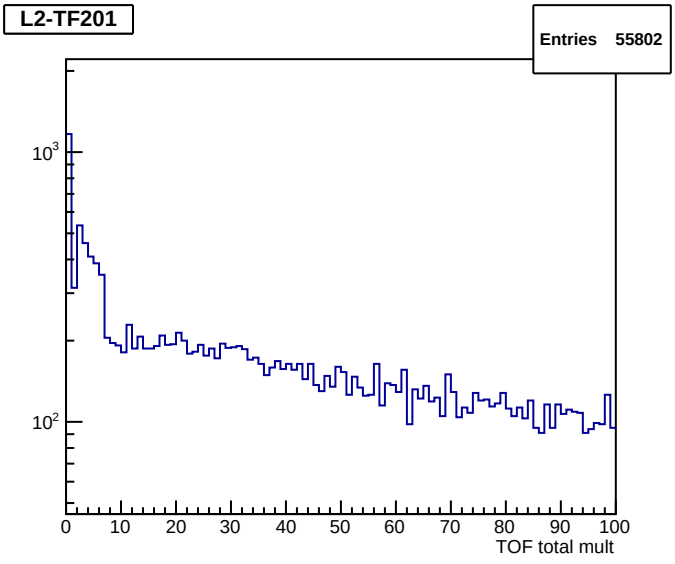

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

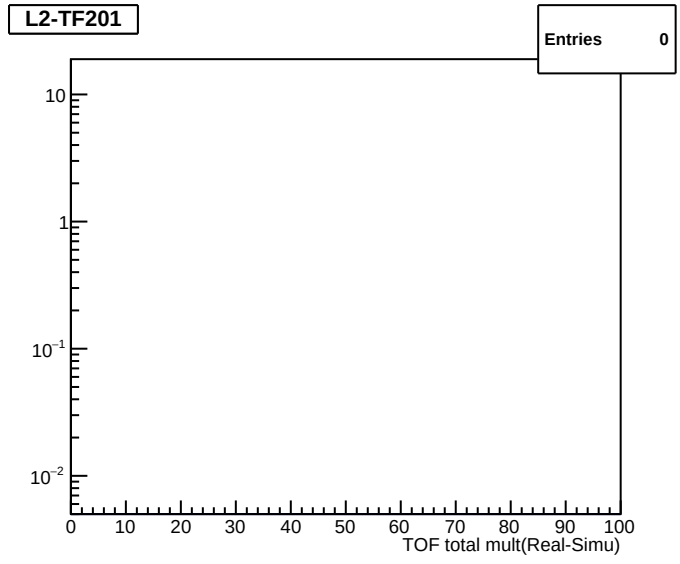

MIX-TF101
Entries 334812
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

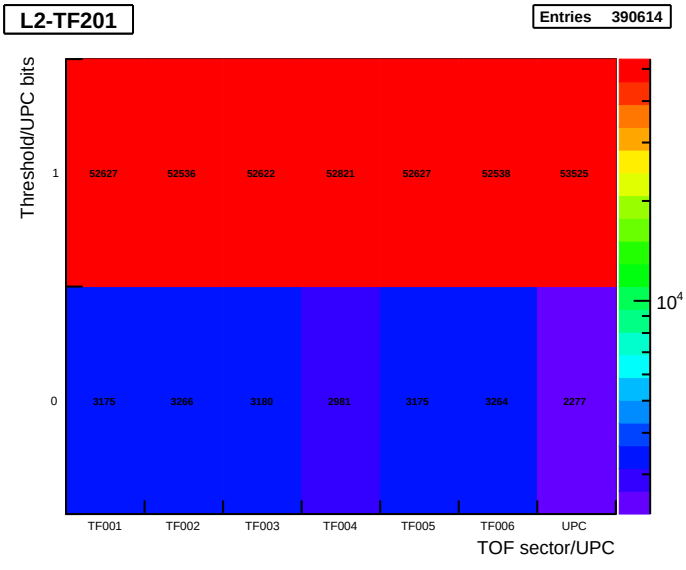

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

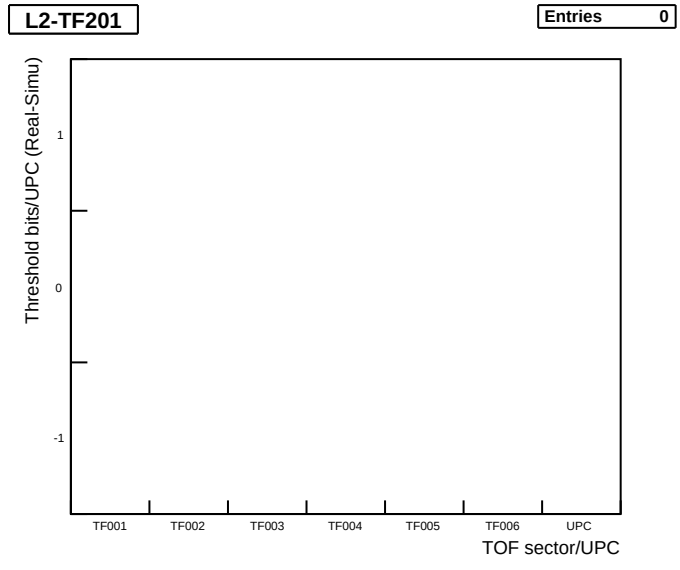

MIX-TF101
Entries 334812
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

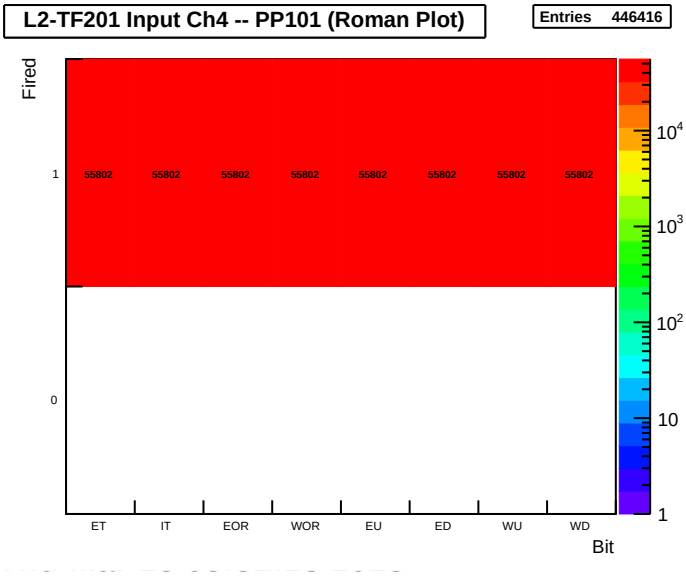

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

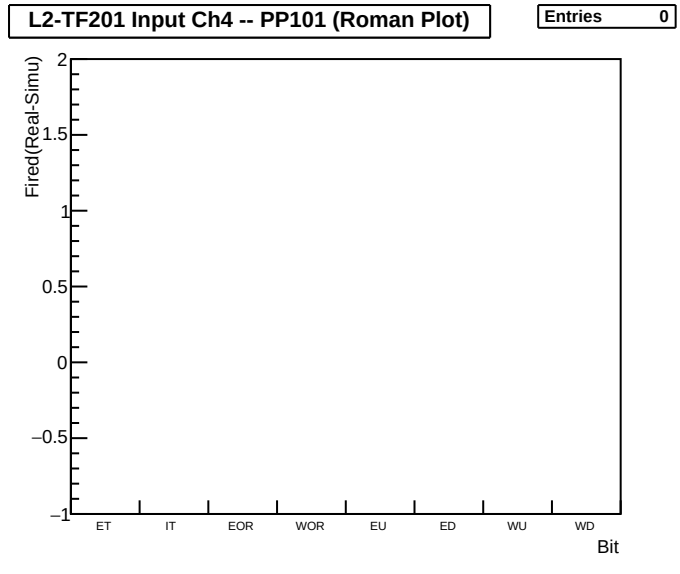

L2-TF201
Entries 55802
TOF total mult

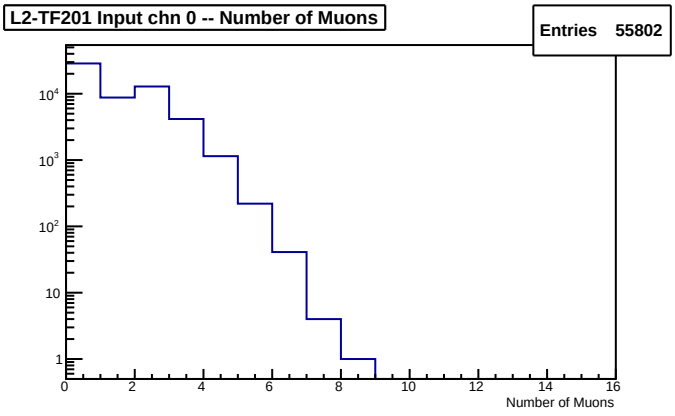

L2-TF201
Entries 0
TOF total mult(Real-Simu)

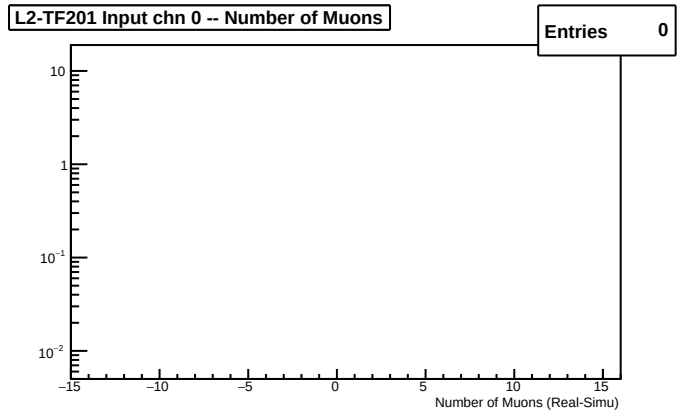

L2-TF201
Entries 390614
Threshold/UPC bits
52627 52536 52622 52821 52627 52538 53525
3175 3266 3180 2981 3175 3264 2277
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

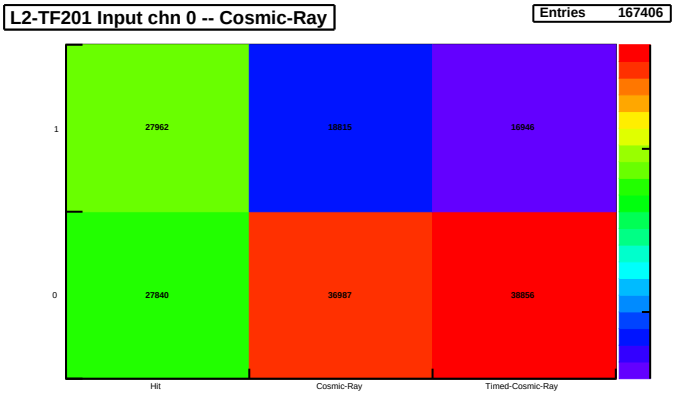

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

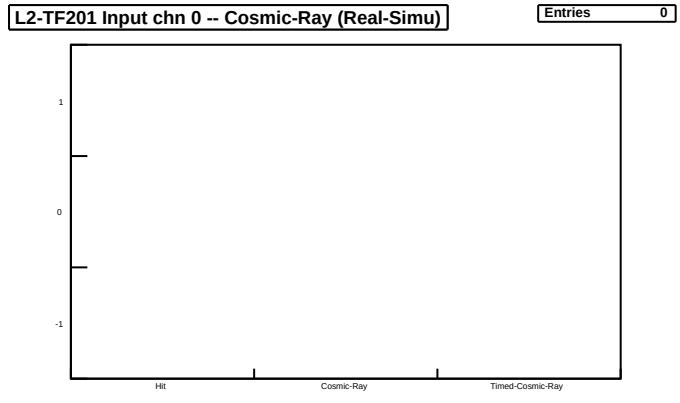

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 446416
Fired
55802 55802 55802 55802 55802 55802 55802 55802
ET IT EOR WOR EU ED WU WD
Bit

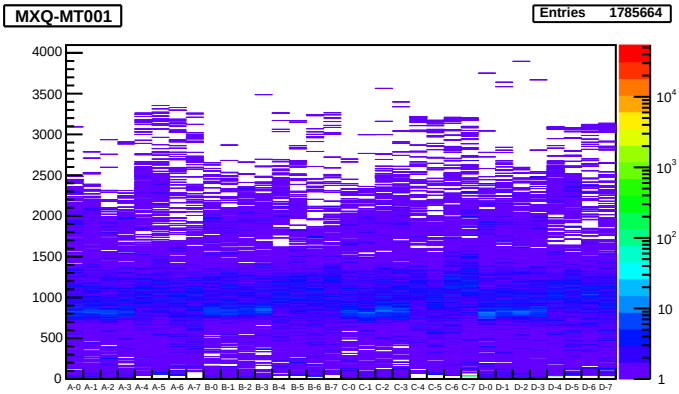

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET IT EOR WOR EU ED WU WD
Bit

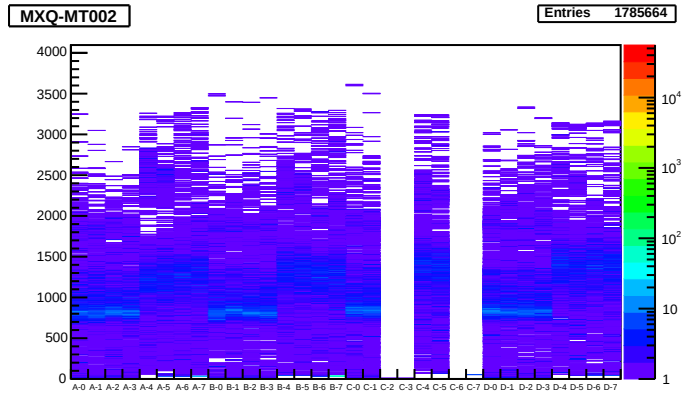

L2-TF201 Input chn 0 -- Number of Muons
Entries 55802
Number of Muons

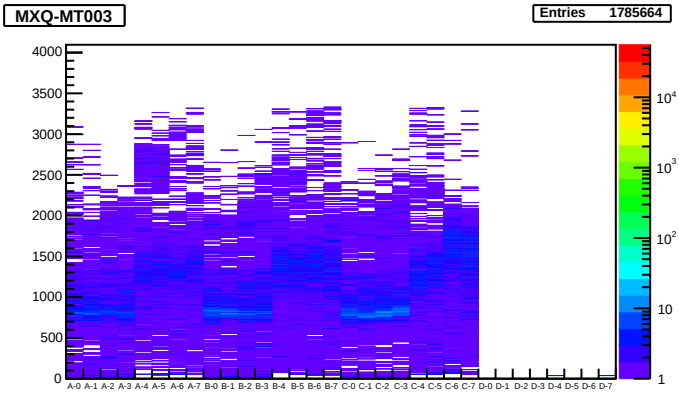

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

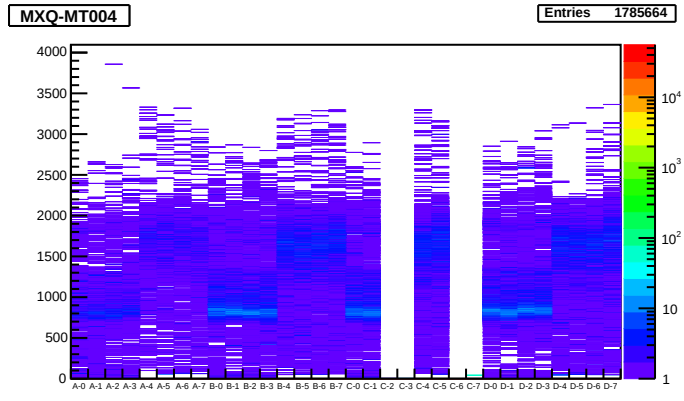

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 167406
27962
18815
16946
27840
36987
38856
Hit
Cosmic-Ray
Timed-Cosmic-Ray

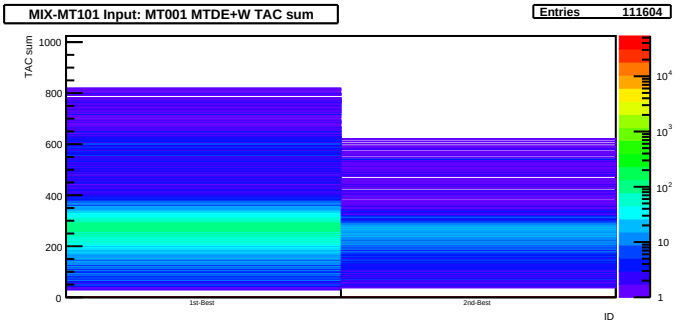

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

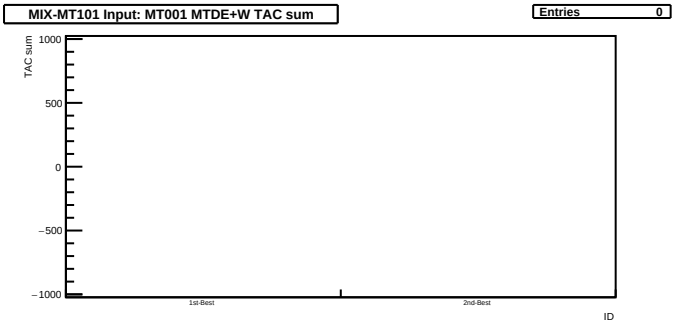

MXQ-MT001
Entries 1785664
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

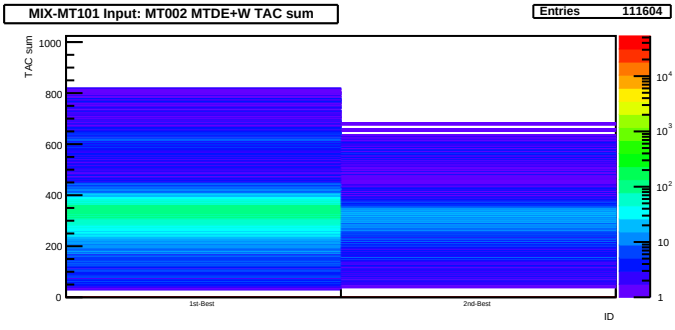

MXQ-MT002
Entries 1785664
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

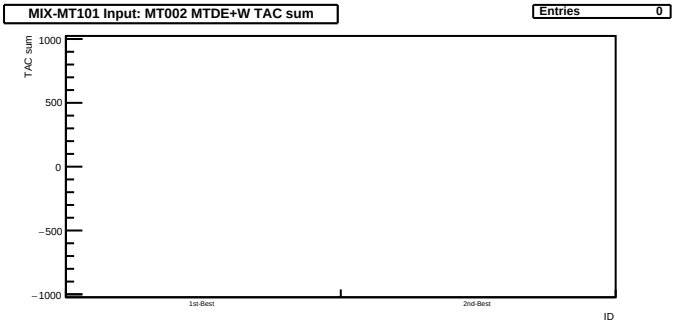

MXQ-MT003
Entries 1785664
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

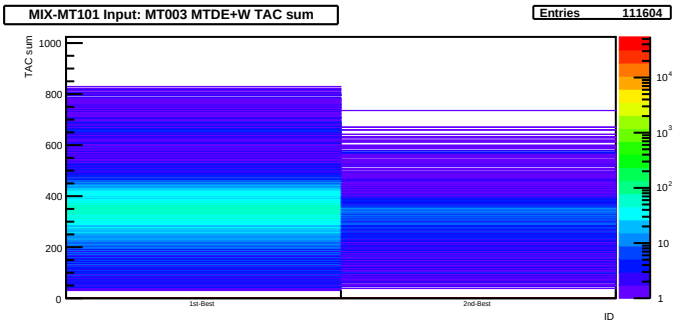

MXQ-MT004
Entries 1785664
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

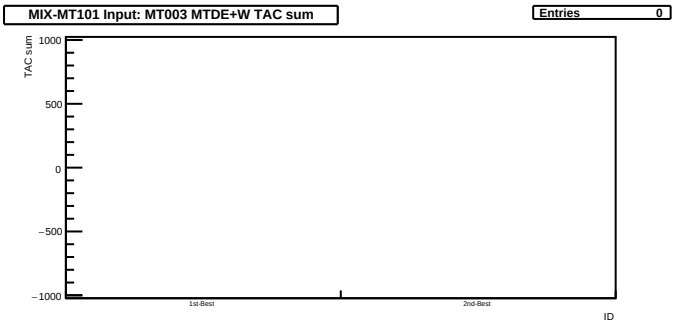

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 111604
TAC sum
1st-Best
2nd-Best
ID

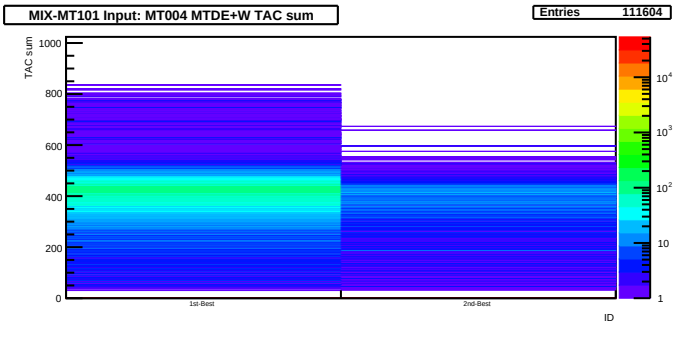

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 0
TAC sum
1st-Best
2nd-Best
ID

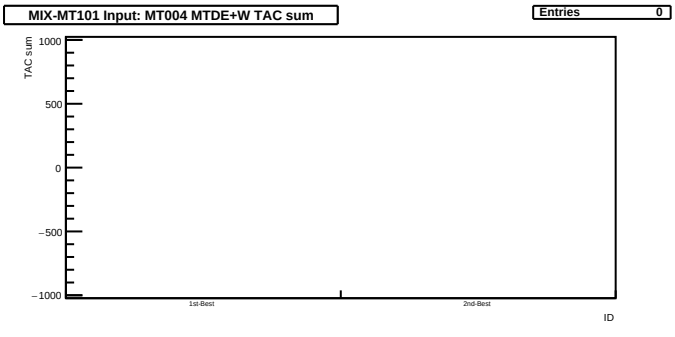

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 111604
TAC sum
1st-Best
2nd-Best
ID

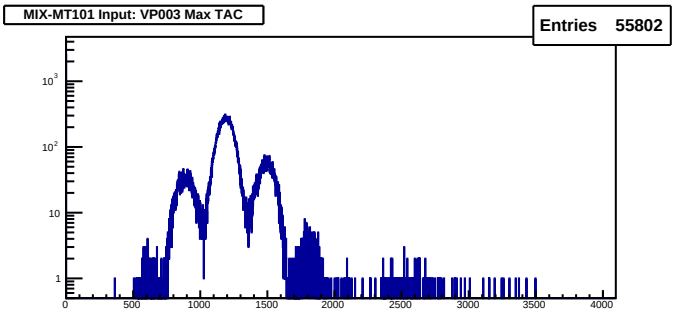

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 0
TAC sum
1st-Best
2nd-Best
ID

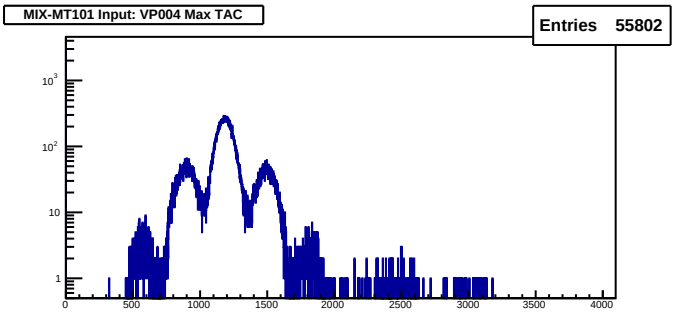

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 111604
TAC sum
1st-Best
2nd-Best
ID

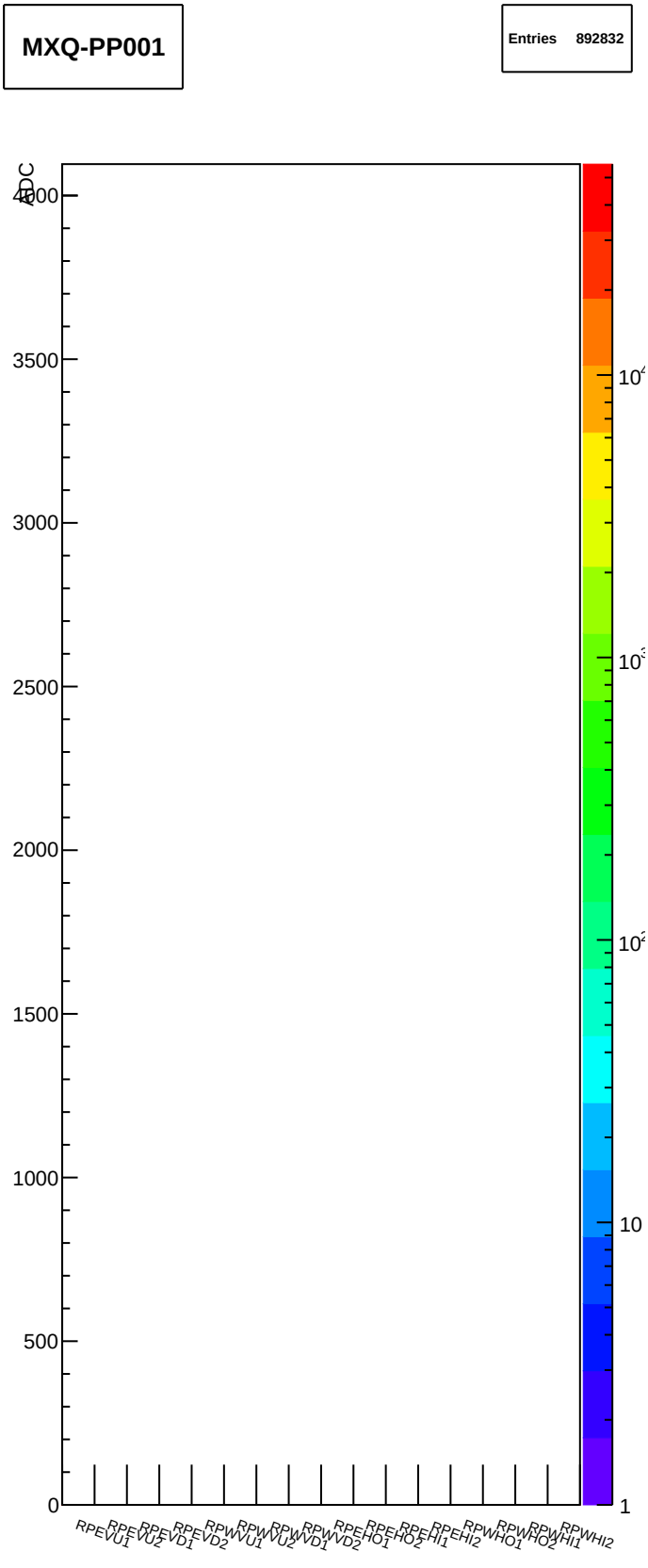

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 0
TAC sum
1st-Best
2nd-Best
ID

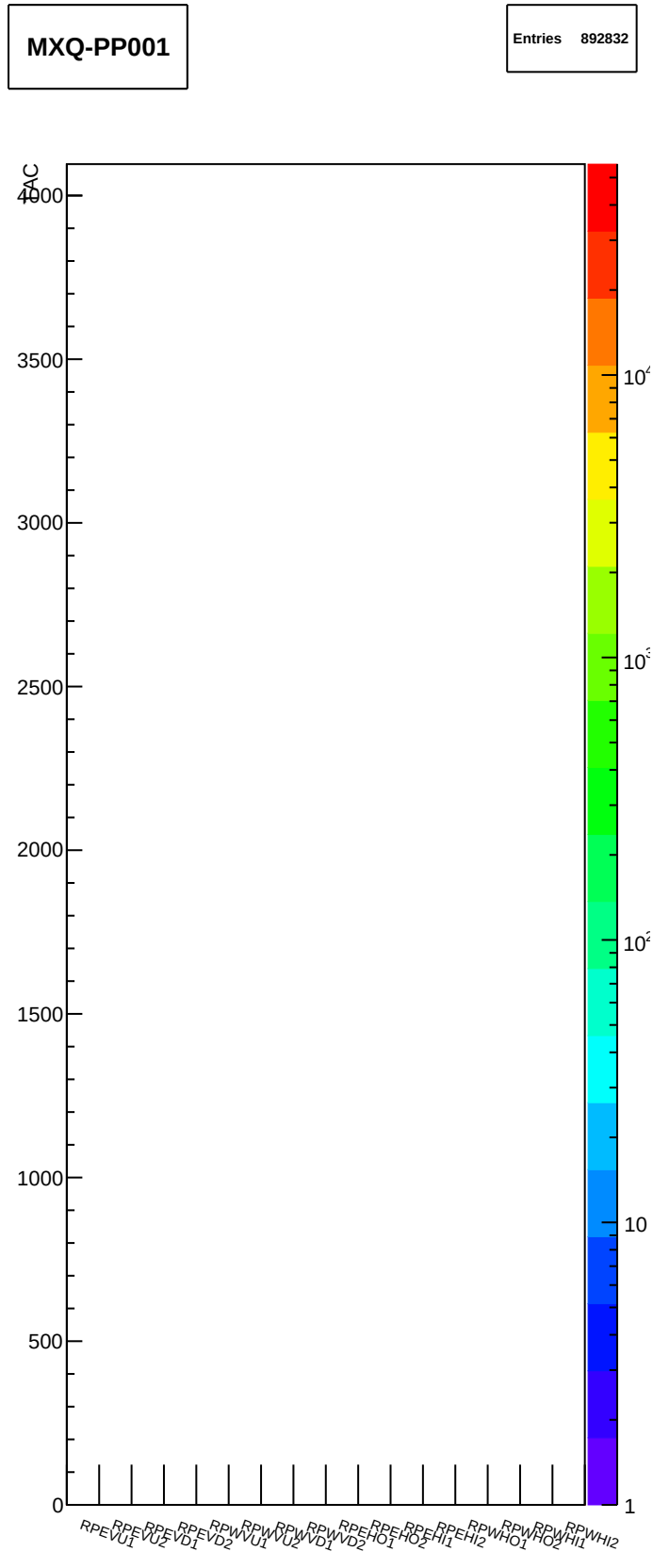

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 111604
TAC sum
1st-Best
2nd-Best
ID

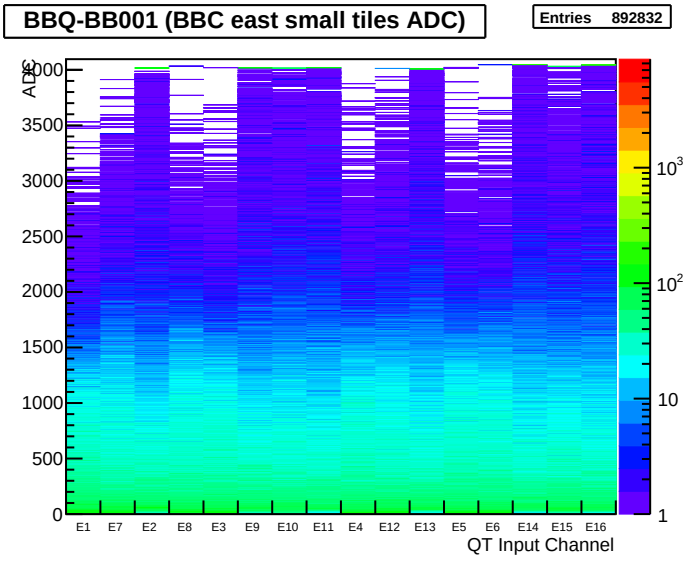

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 0
TAC sum
1st-Best
2nd-Best
ID

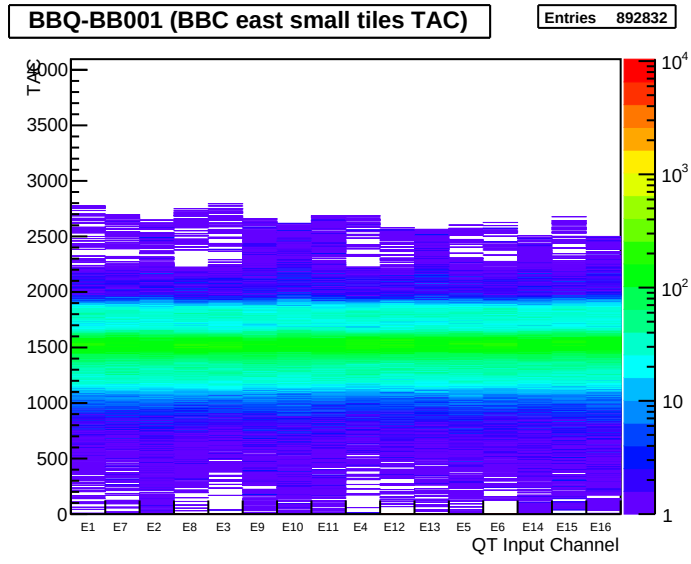

MIX-MT101 Input: VP003 Max TAC
Entries 55802

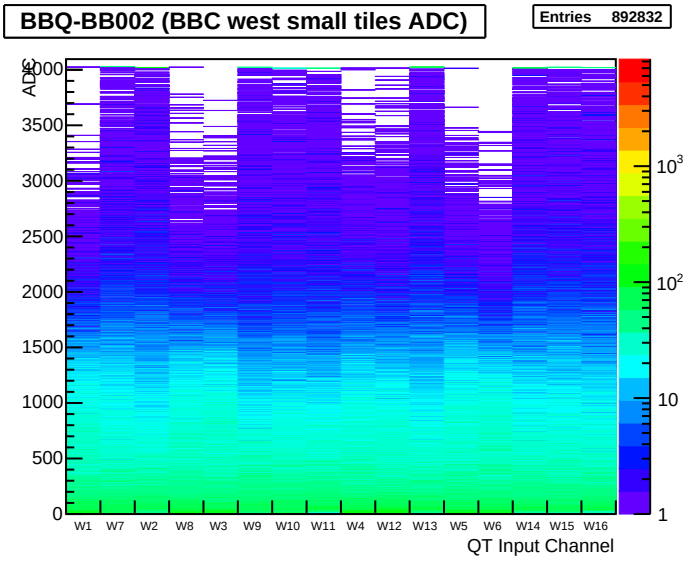

MIX-MT101 Input: VP004 Max TAC
Entries 55802

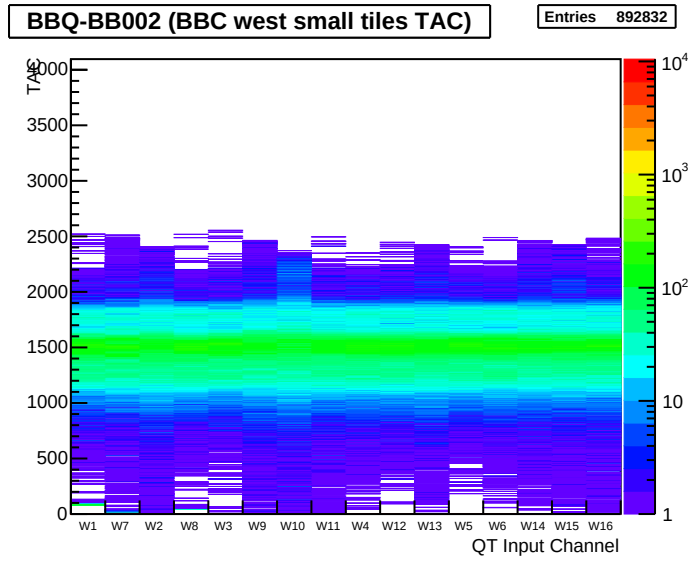

MXQ-PP001
Entries 892832
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

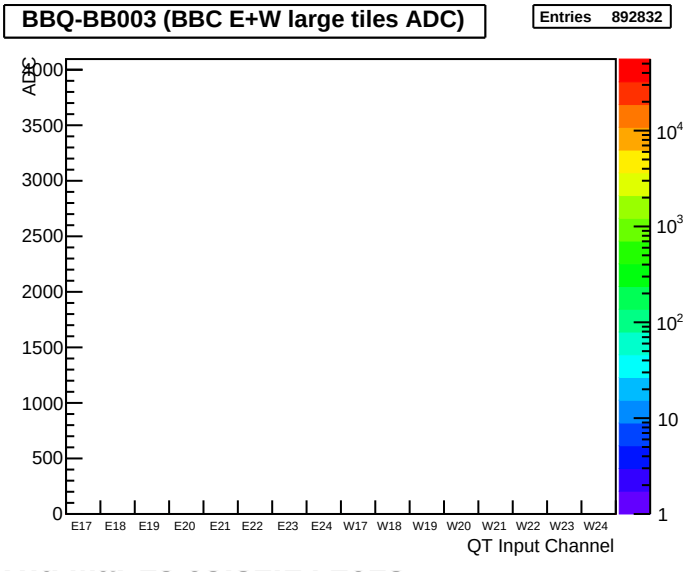

MXQ-PP001
Entries 892832
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

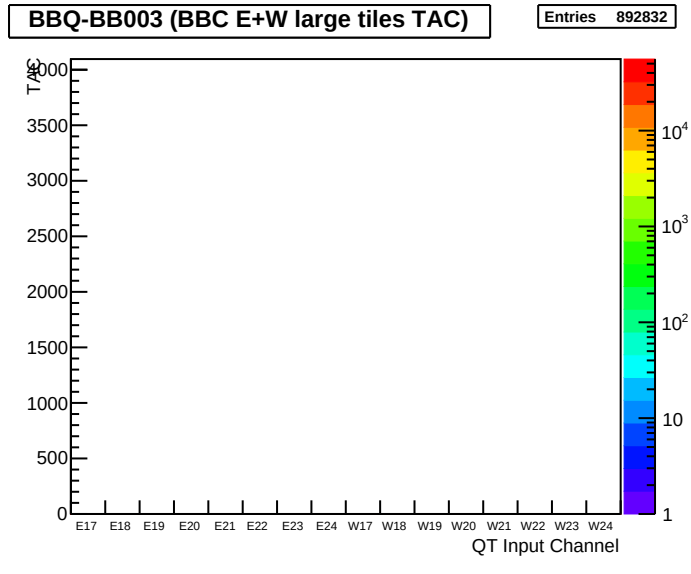

BBQ-BB001 (BBC east small tiles ADC)
Entries 892832
ADC
QT Input Channel

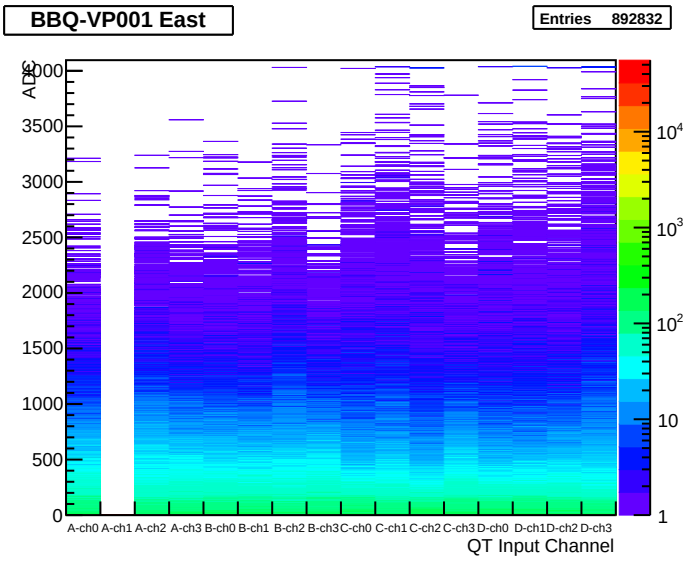

BBQ-BB001 (BBC east small tiles TAC)
Entries 892832
TAC
QT Input Channel

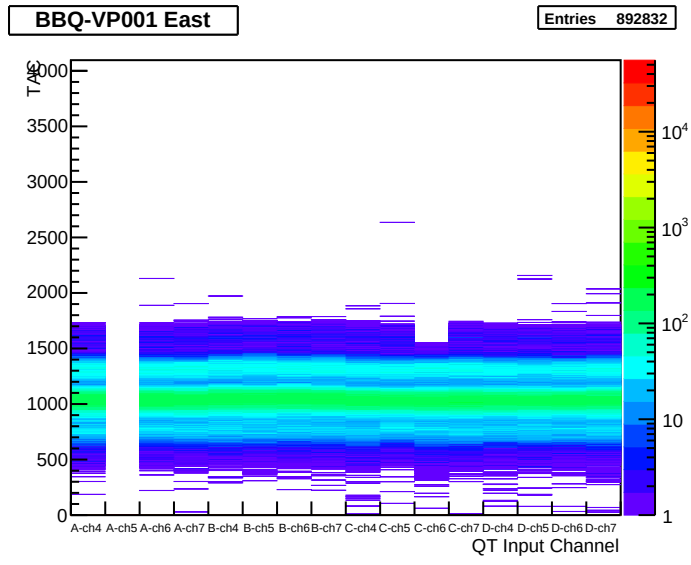

BBQ-BB002 (BBC west small tiles ADC)
Entries 892832
ADC
QT Input Channel

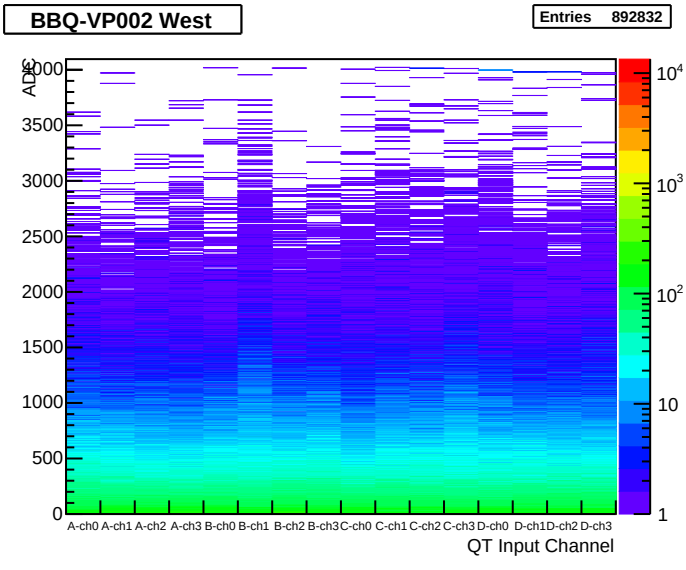

BBQ-BB002 (BBC west small tiles TAC)
Entries 892832
TAC
QT Input Channel

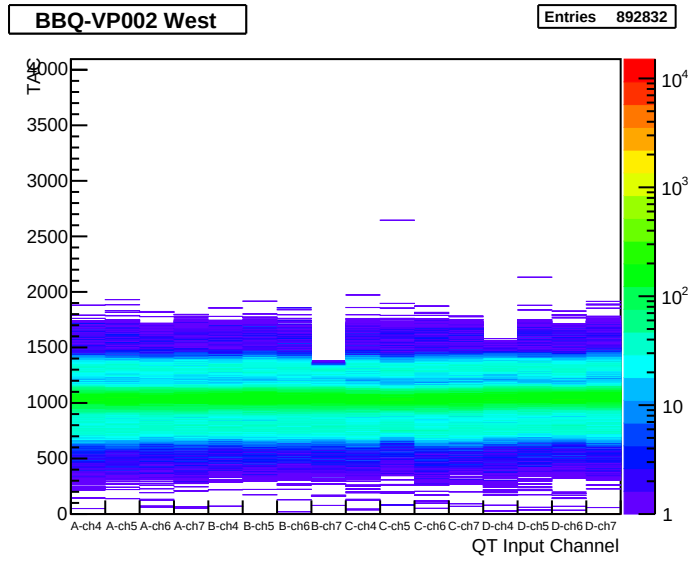

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 892832
ADC
QT Input Channel

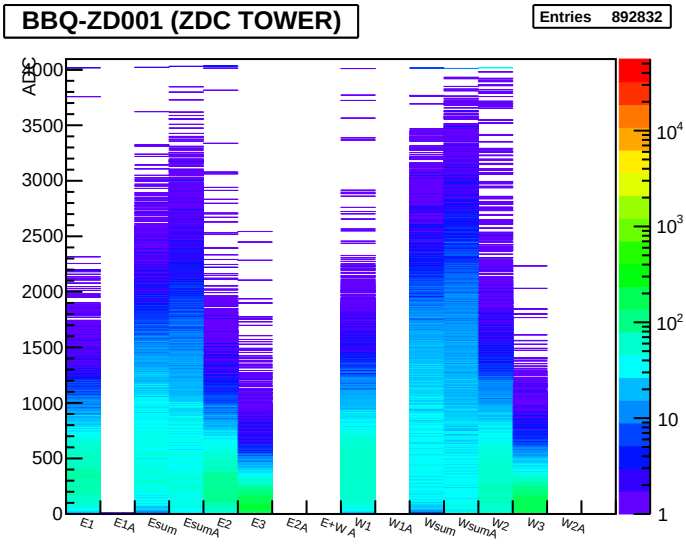

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 892832
TAC
QT Input Channel

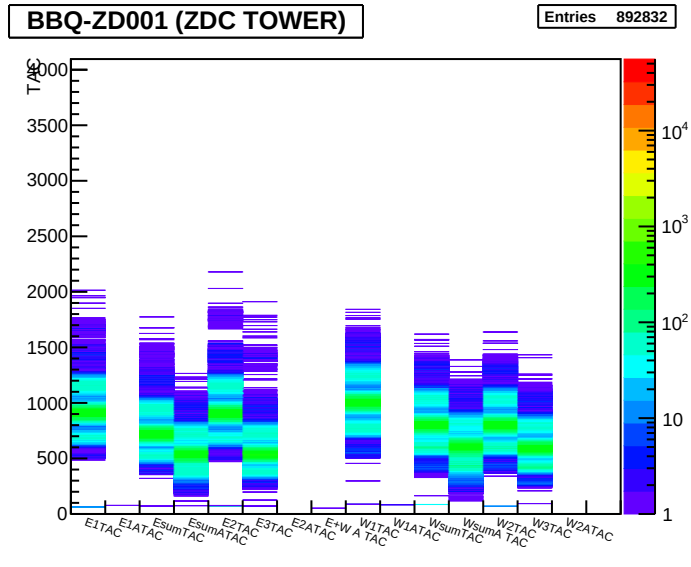

BBQ-VP001 East
Entries 892832
ADC
QT Input Channel

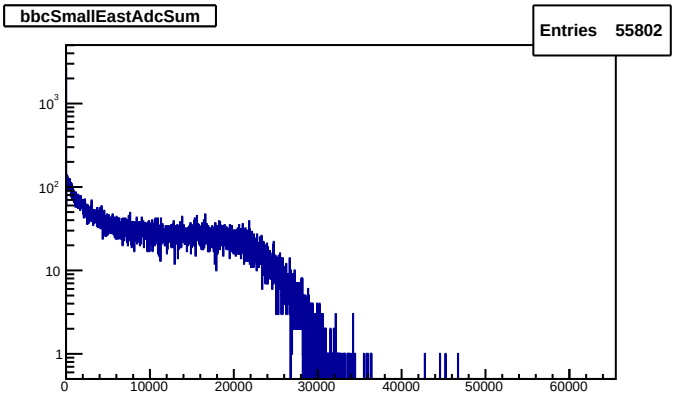

BBQ-VP001 East
Entries 892832
TAC
QT Input Channel

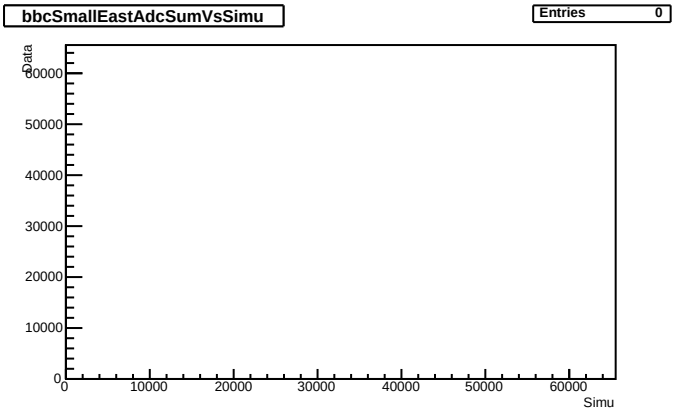

BBQ-VP002 West
Entries 892832
ADC
QT Input Channel

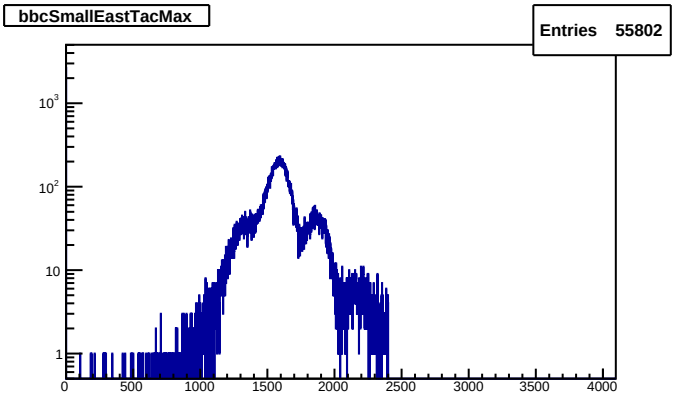

BBQ-VP002 West
Entries 892832
TAC
QT Input Channel

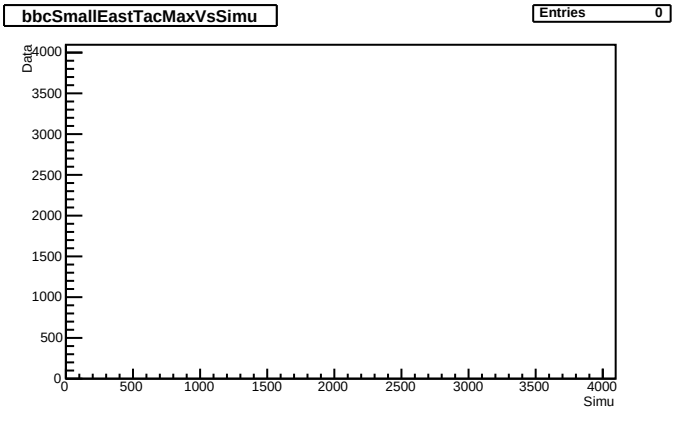

BBQ-ZD001 (ZDC TOWER)
Entries 892832
ADC

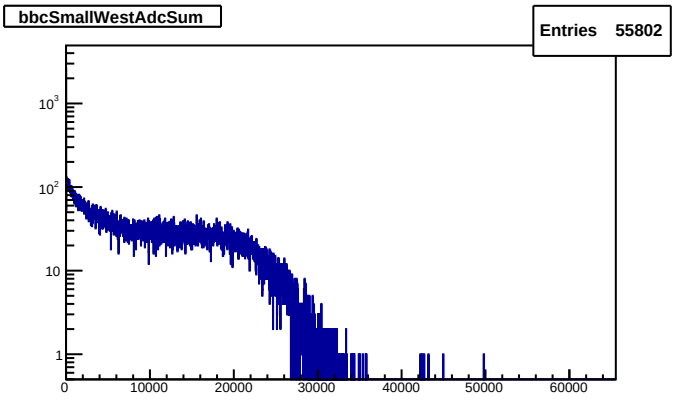

BBQ-ZD001 (ZDC TOWER)
Entries 892832
TAC

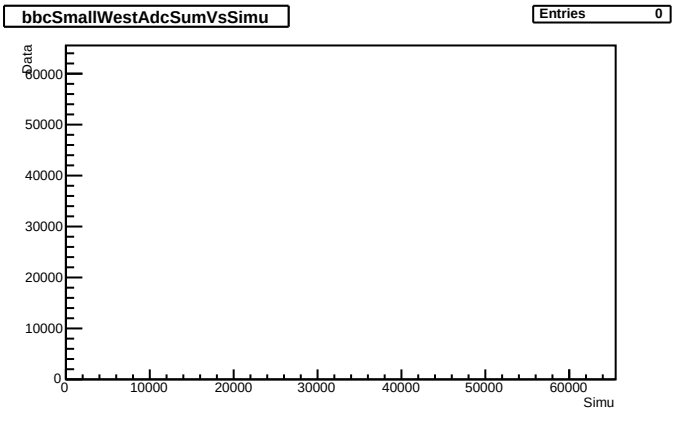

bbcSmallEastAdcSum
Entries 55802

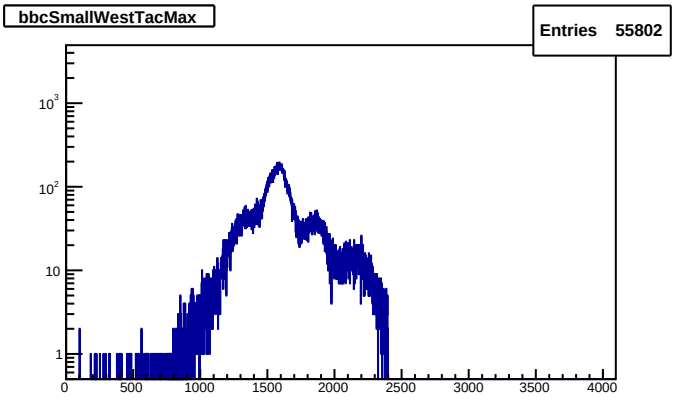

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

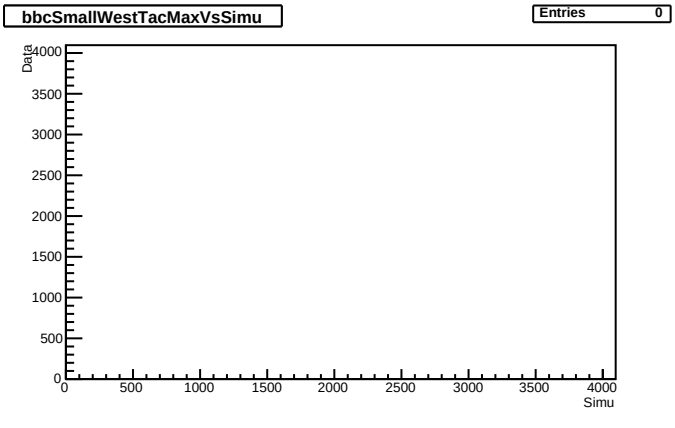

bbcSmallEastTacMax
Entries 55802

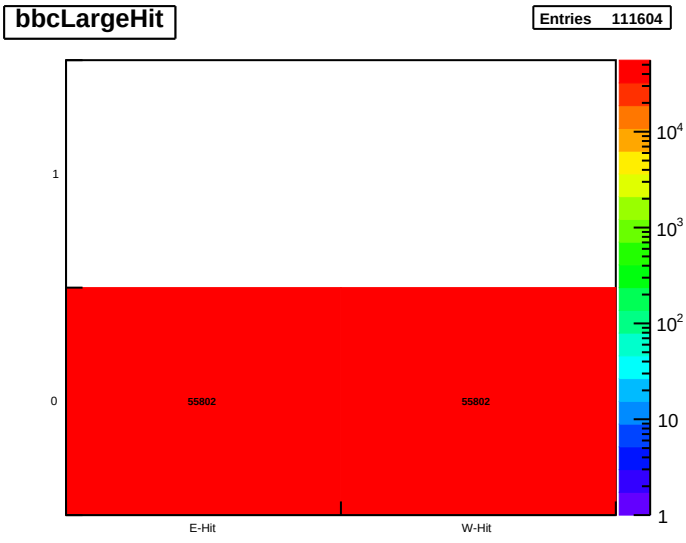

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

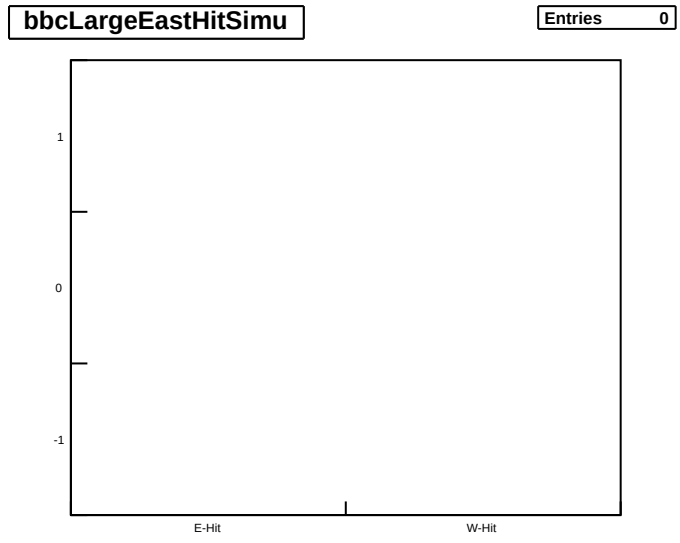

bbcSmallWestAdcSum
Entries 55802

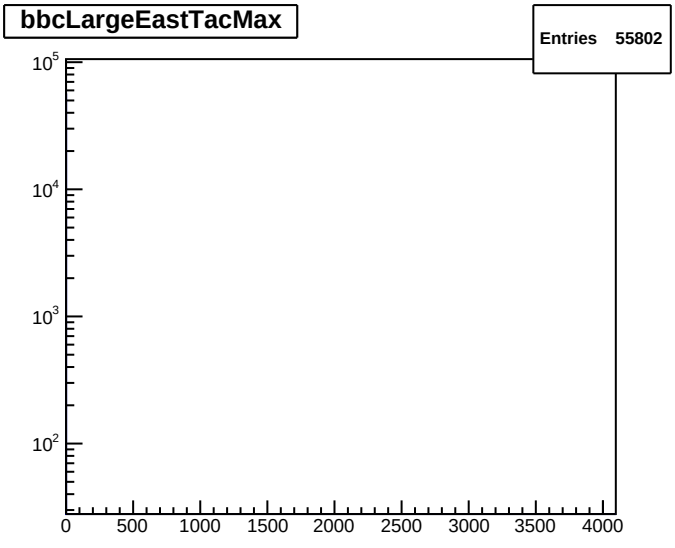

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

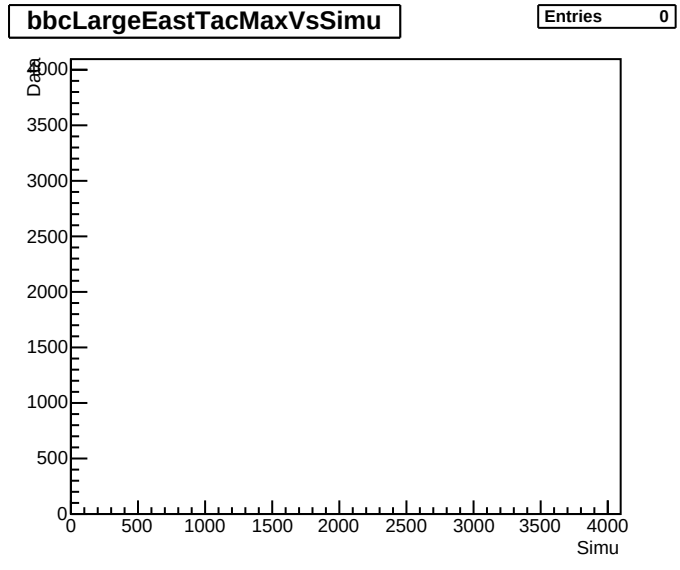

bbcSmallWestTacMax
Entries 55802

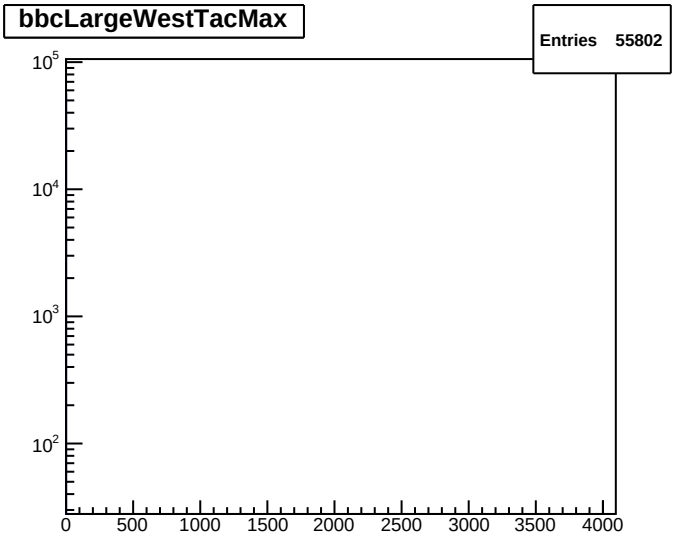

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

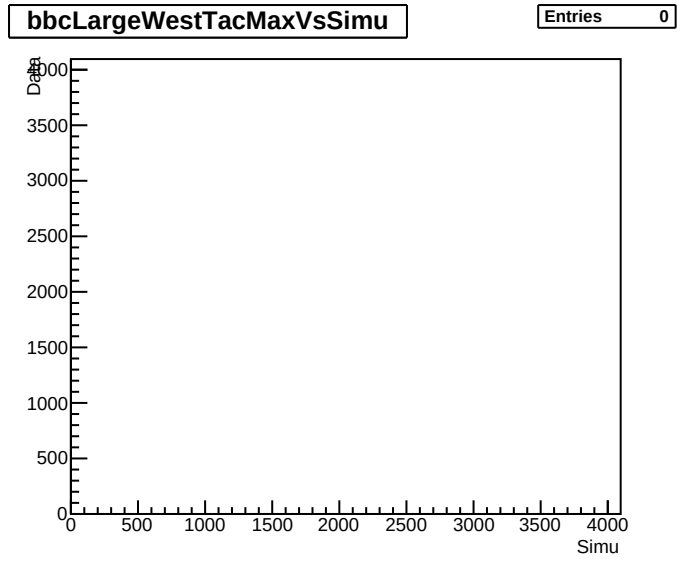

bbcLargeHit
Entries 111604
55802
55802
E-Hit
W-Hit

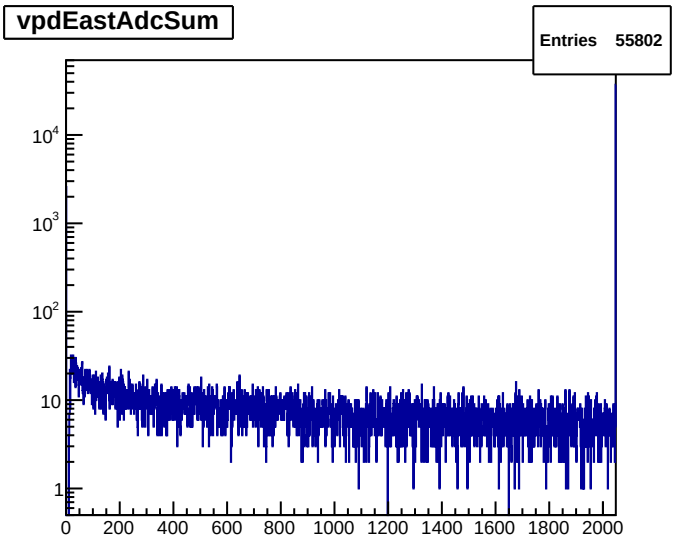

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

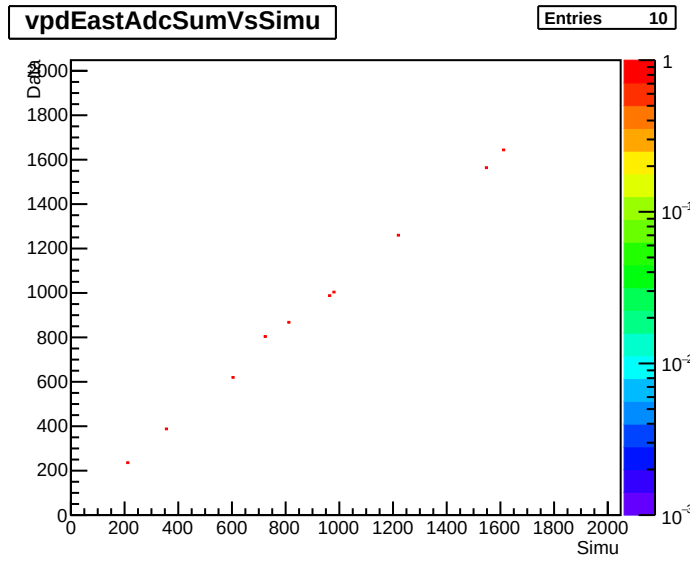

bbcLargeEastTacMax
Entries 55802

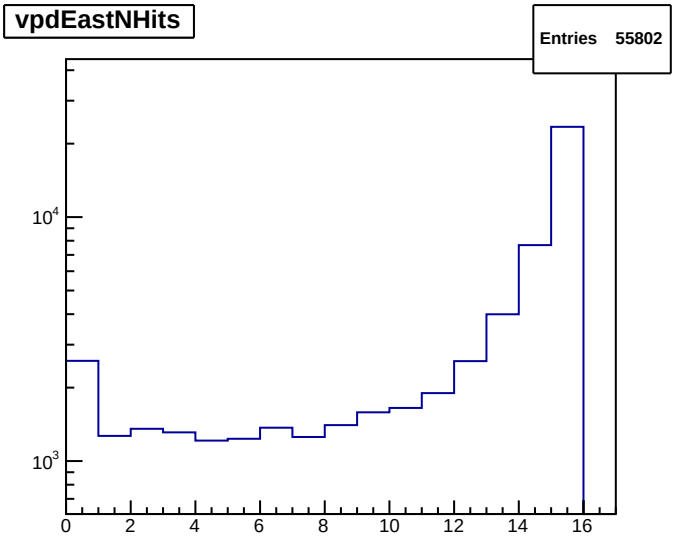

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

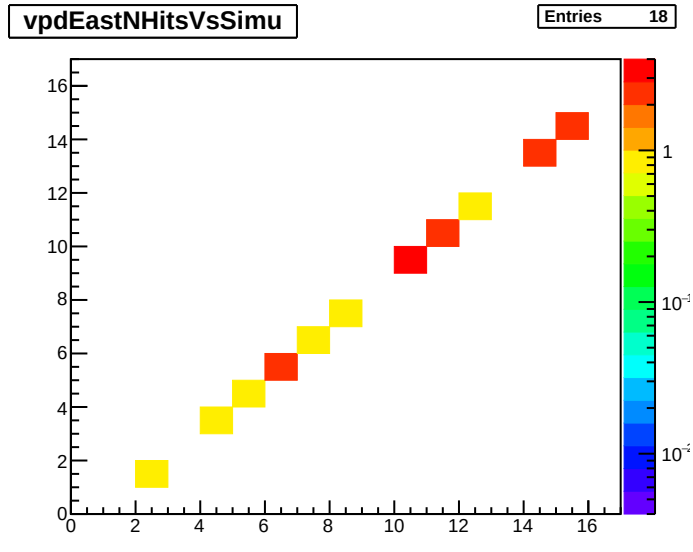

bbcLargeWestTacMax
Entries 55802

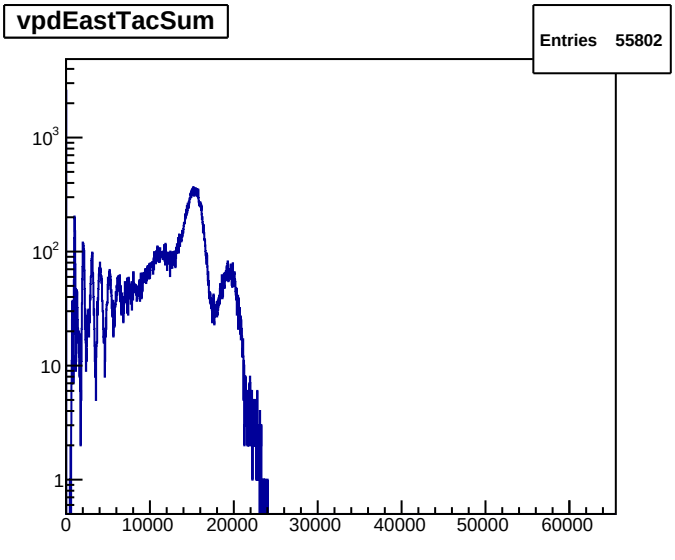

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

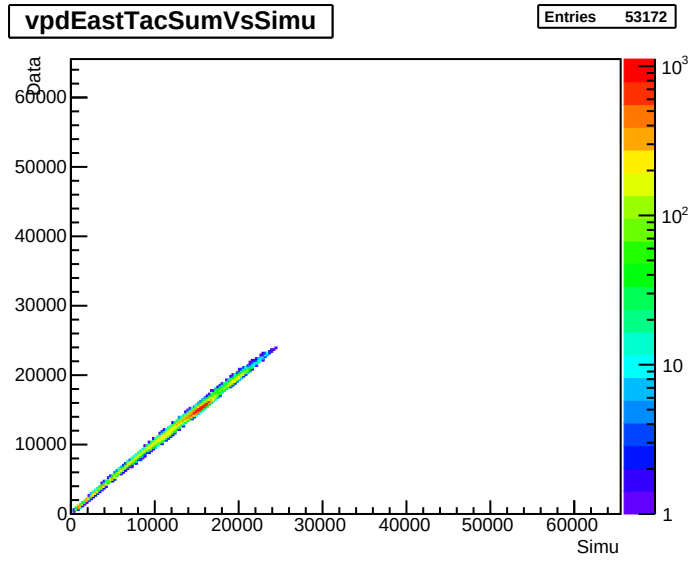

vpdEastAdcSum
Entries 55802

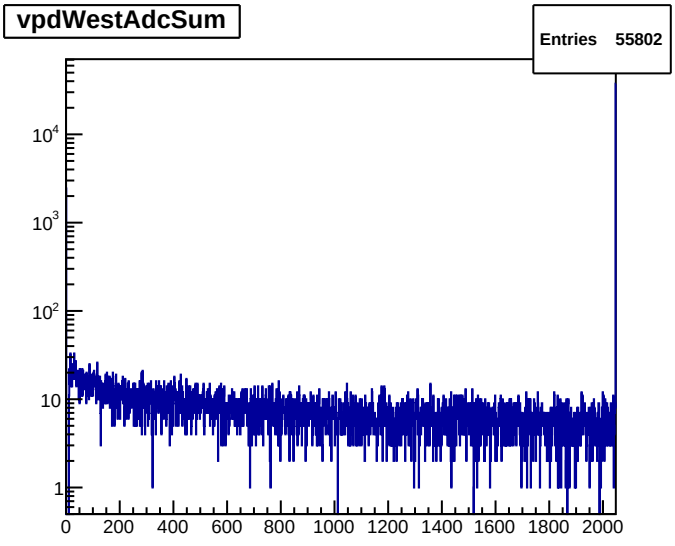

vpdEastAdcSumVsSimu
Entries 10
Data
Simu

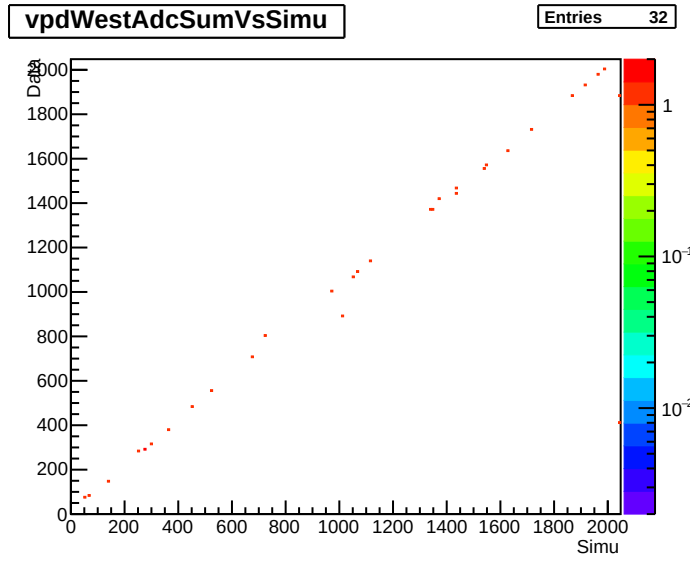

vpdEastNHits
Entries 55802

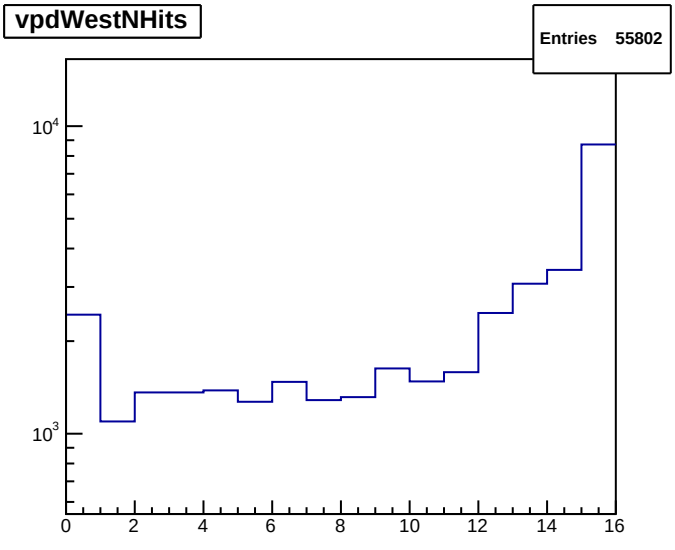

vpdEastNHitsVsSimu
Entries 18

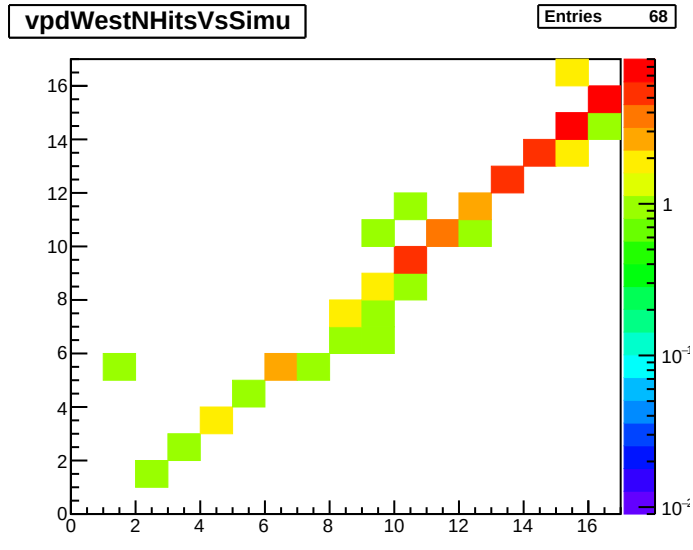

vpdEastTacSum
Entries 55802

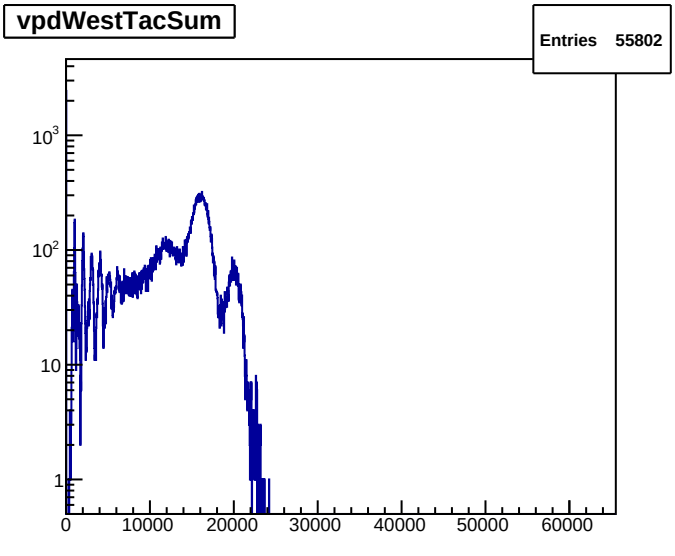

vpdEastTacSumVsSimu
Entries 53172
Data
Simu

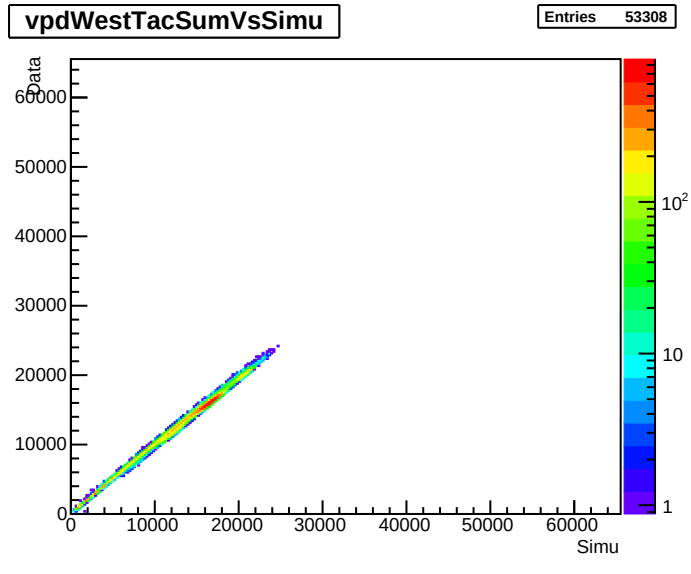

vpdWestAdcSum
Entries 55802

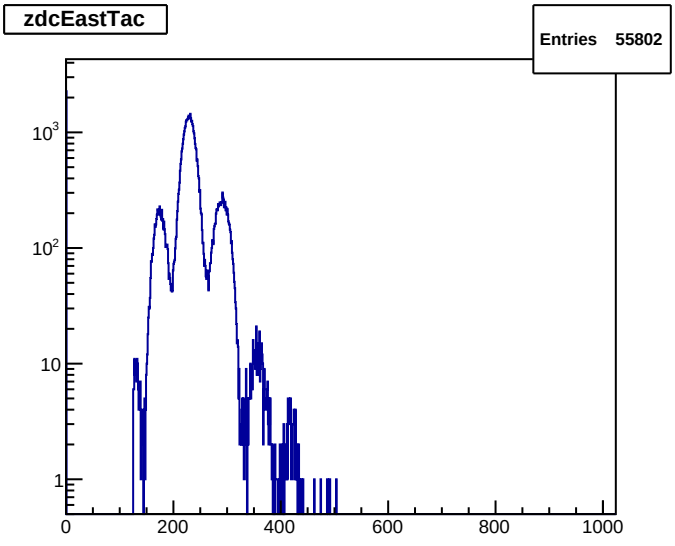

vpdWestAdcSumVsSimu
Entries 32
Data
Simu

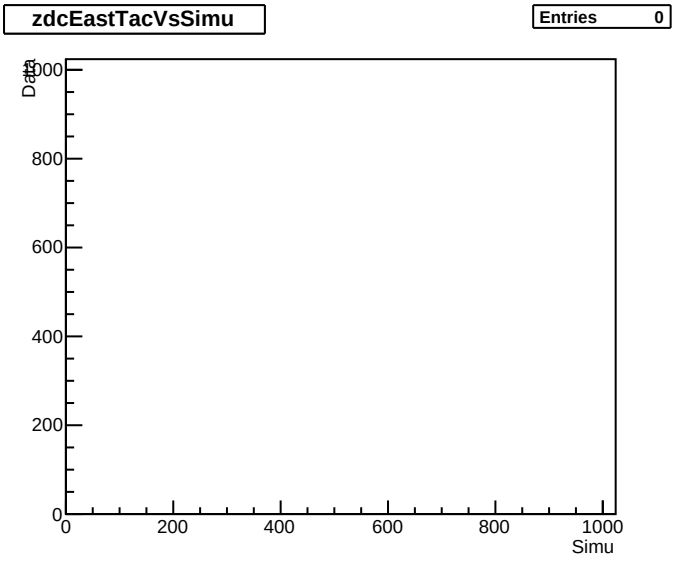

vpdWestNHits
Entries 55802

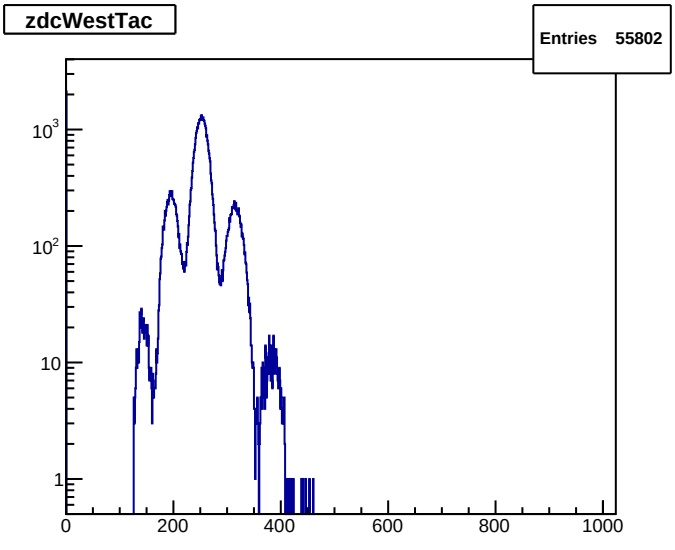

vpdWestNHitsVsSimu
Entries 68

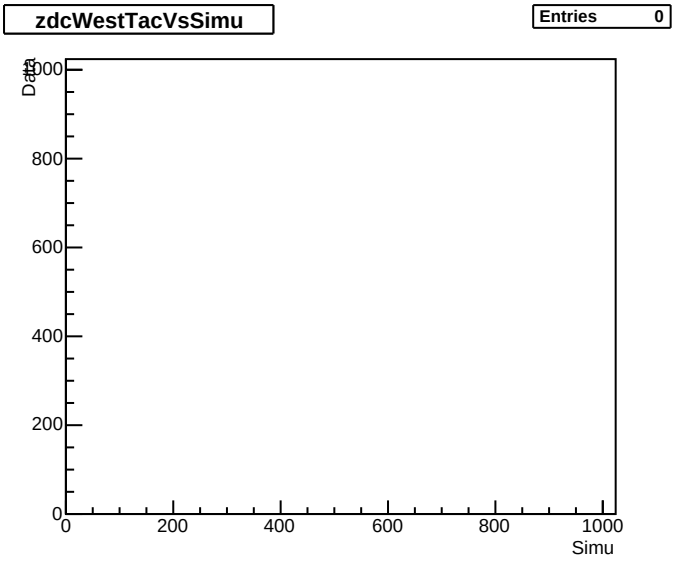

vpdWestTacSum
Entries 55802

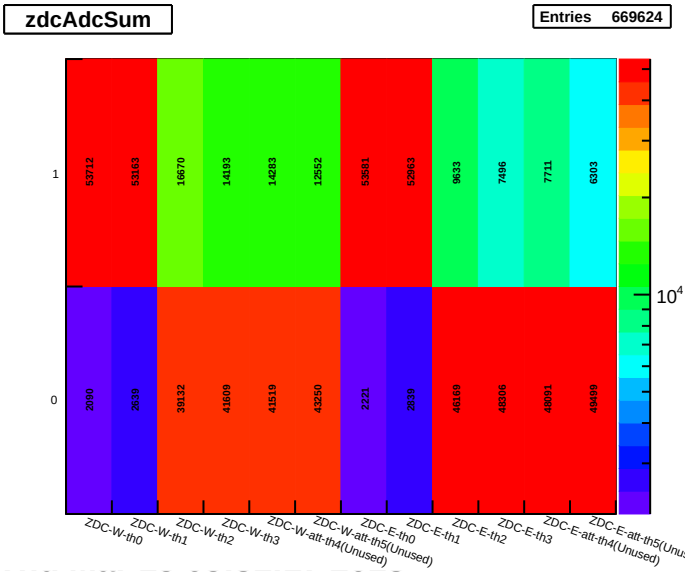

vpdWestTacSumVsSimu
Entries 53308
Data
Simu

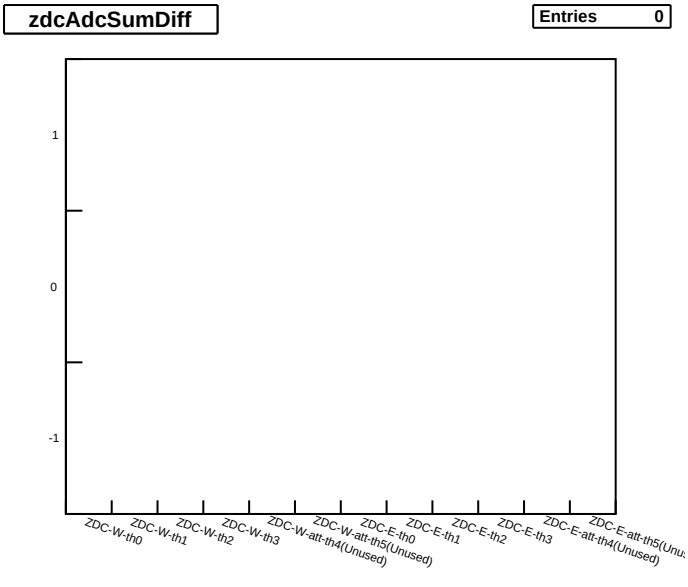

zdcEastTac
Entries 55802
10^3
10^2
10
1
0
200
400
600
800
1000

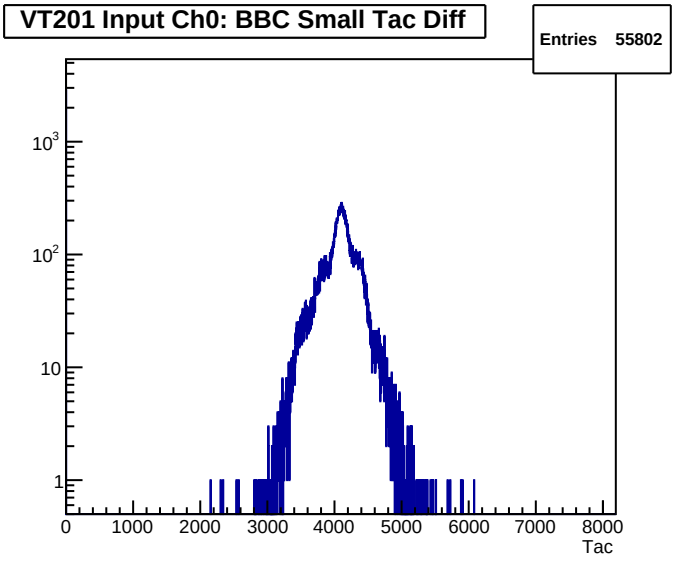

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

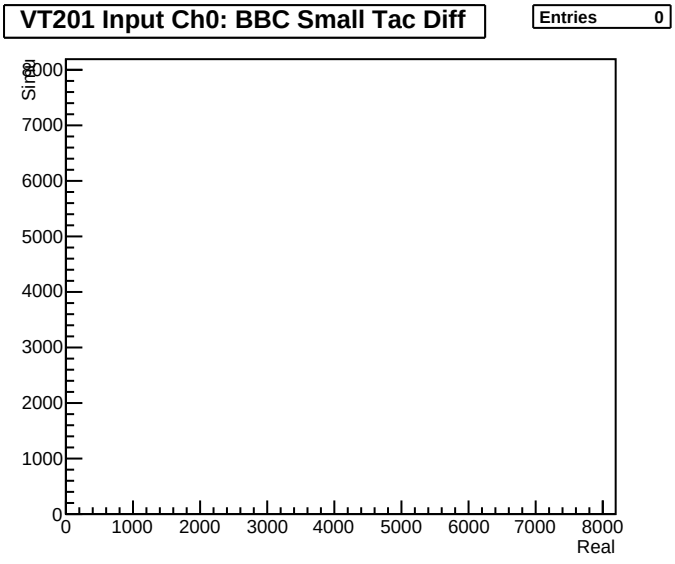

zdcWestTac
Entries 55802
10^3
10^2
10
1
0
200
400
600
800
1000

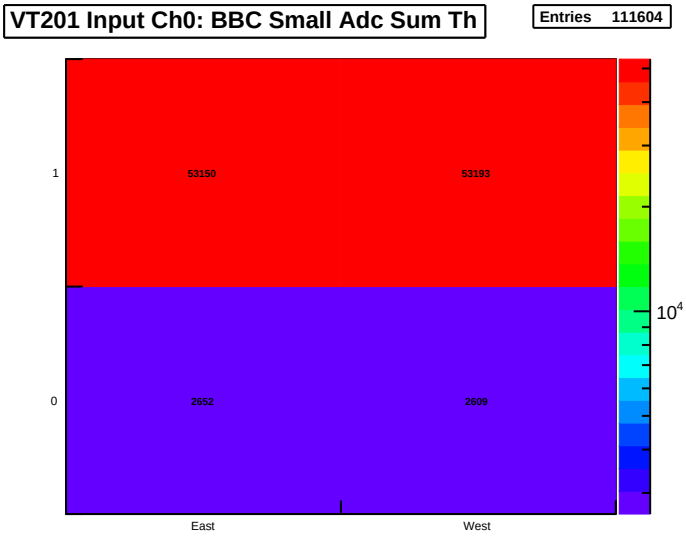

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

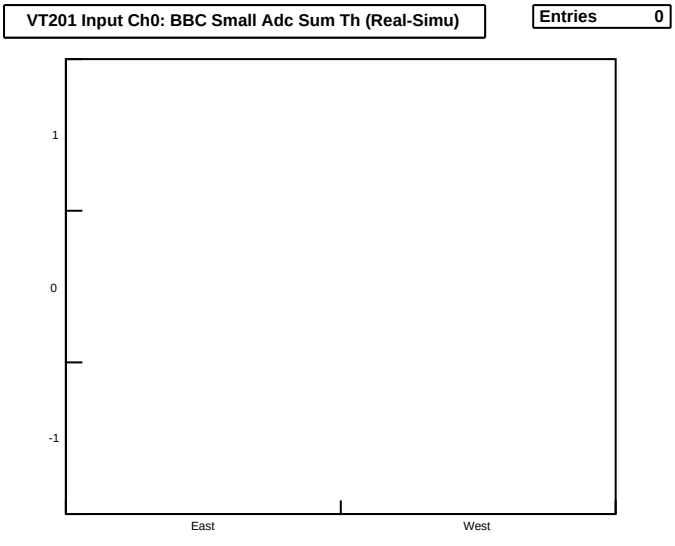

zdcAdcSum
Entries 669624
1
0
53712
53163
16670
14193
14283
12552
53581
52863
9633
7496
7711
6303
2090
2639
39132
41609
41519
43250
2221
2839
46169
48306
48991
49499
10^4
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

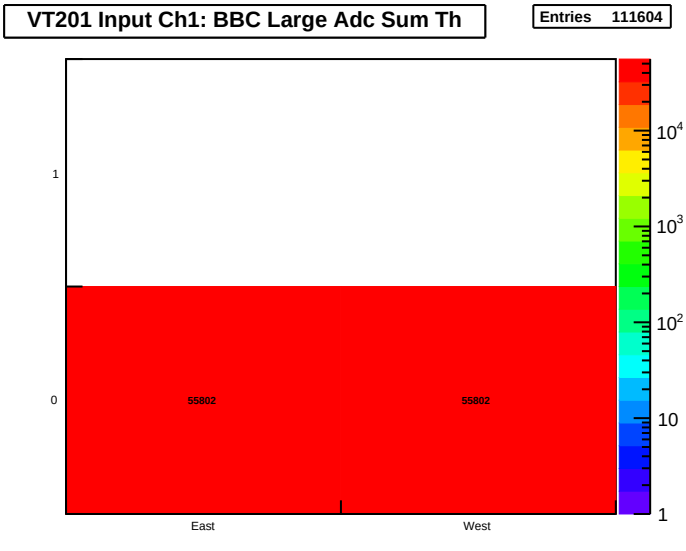

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

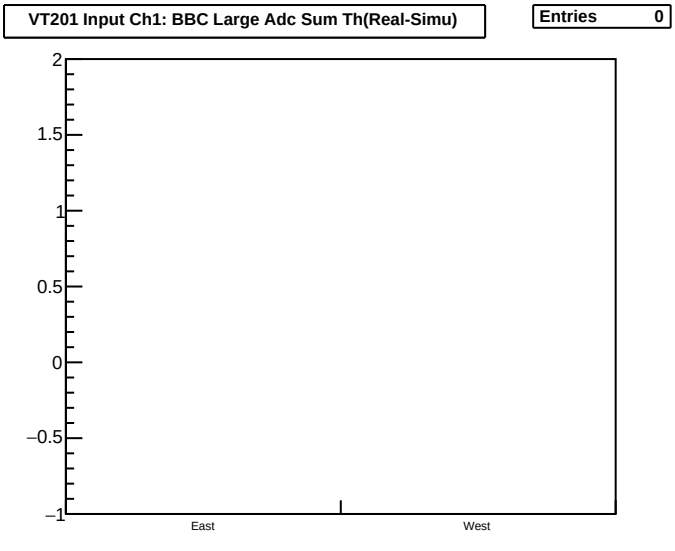

VT201 Input Ch0: BBC Small Tac Diff
Entries 55802
Tac

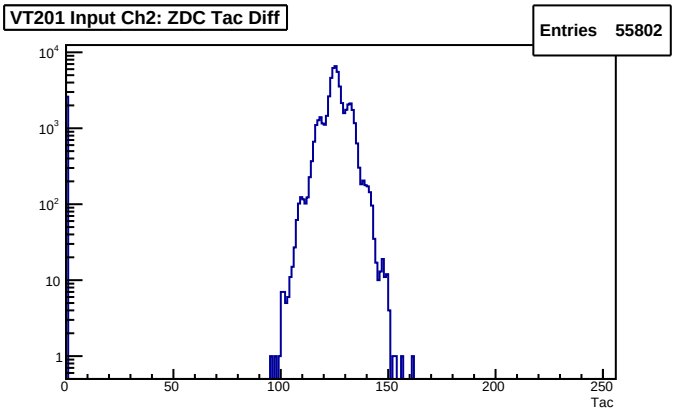

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

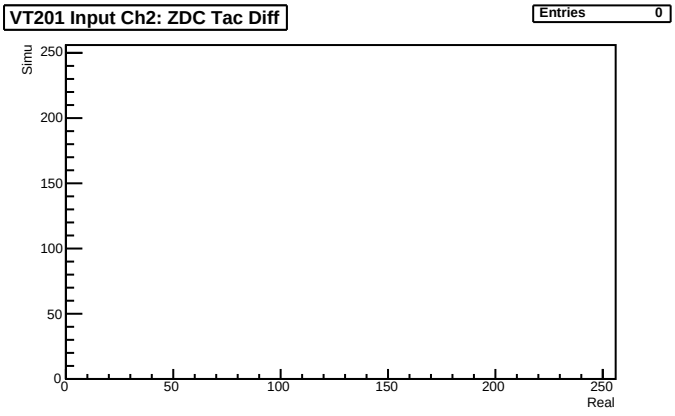

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 111604
53150
53193
2652
2609
East
West

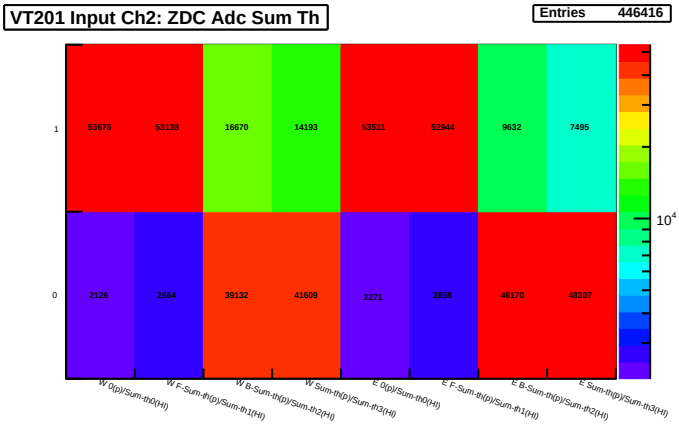

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

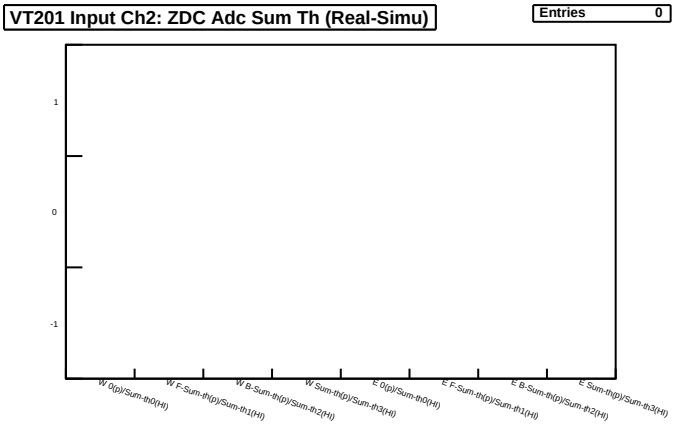

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 111604
55802
55802
East
West

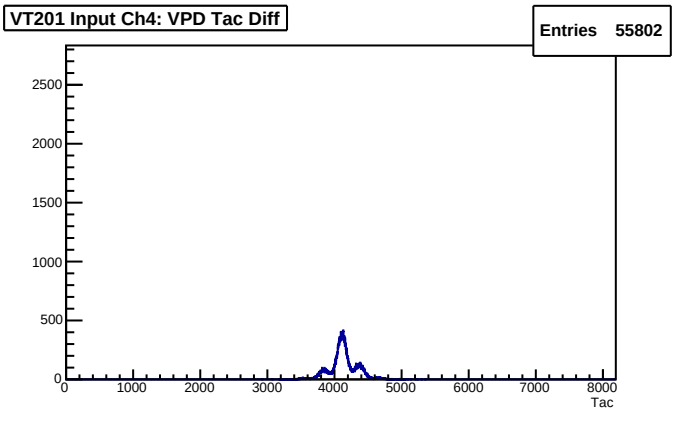

VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

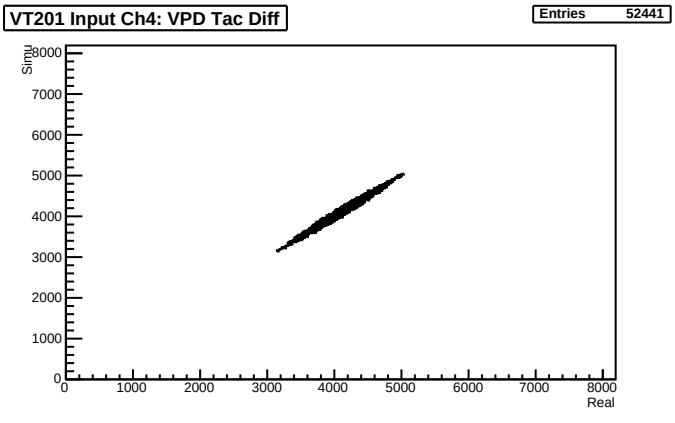

VT201 Input Ch2: ZDC Tac Diff
Entries 55802
Tac

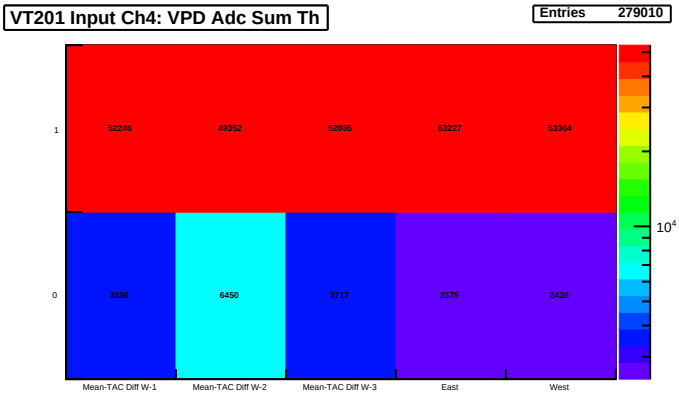

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

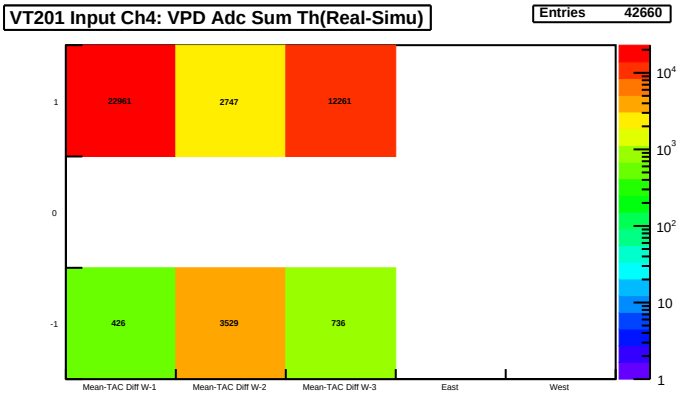

VT201 Input Ch2: ZDC Adc Sum Th
Entries 446416
53676
53138
16670
14193
53531
52944
9632
7495
2126
2664
39132
41609
2271
2858
46170
48307
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

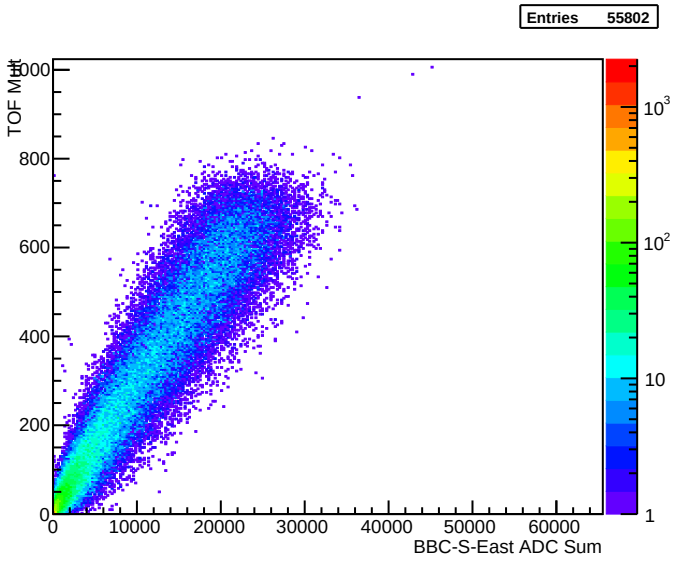

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

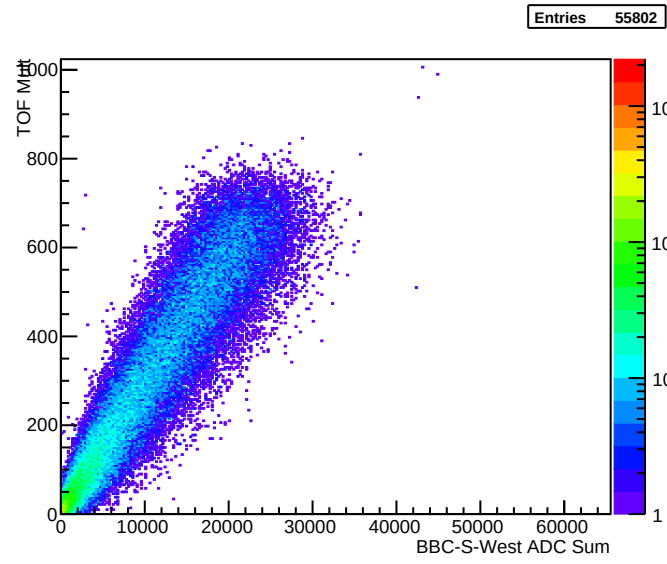

VT201 Input Ch4: VPD Tac Diff
Entries 55802
Tac

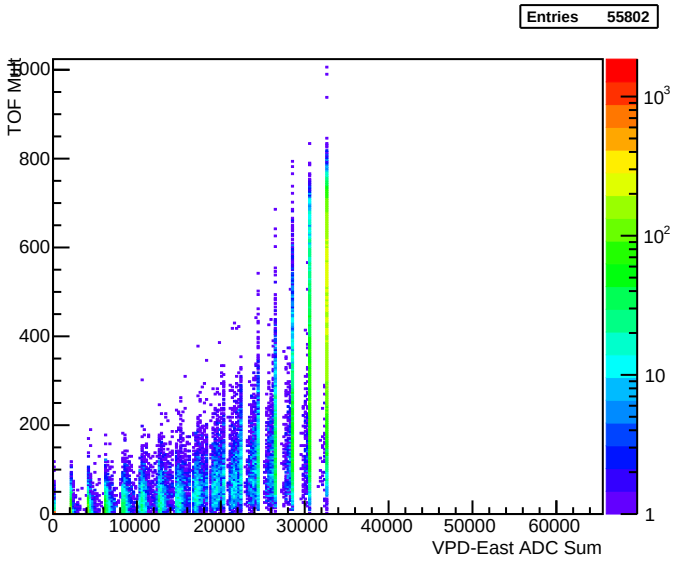

VT201 Input Ch4: VPD Tac Diff
Entries 52441
Simu
Real

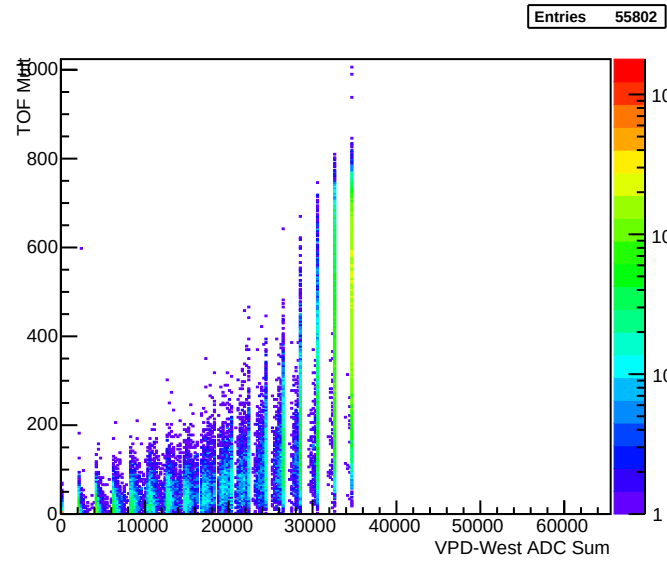

VT201 Input Ch4: VPD Adc Sum Th
Entries 279010
52246
49352
52085
53227
53364
3556
6450
3717
2575
2438
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

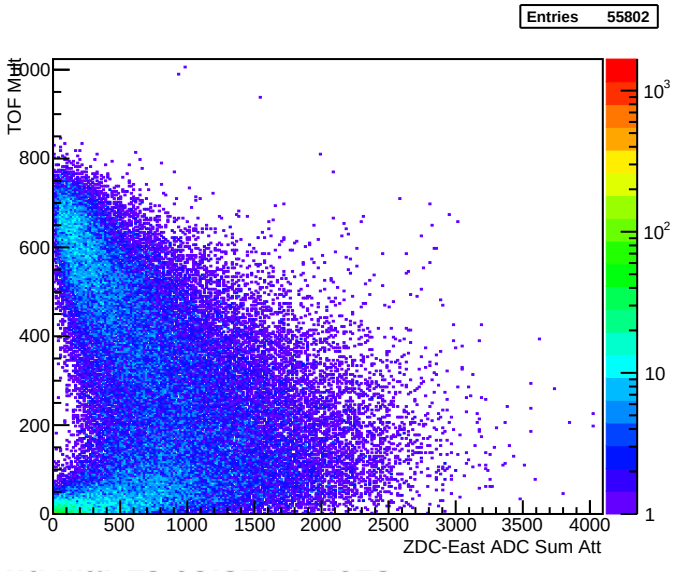

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 42660
22961
2747
12261
426
3529
736
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

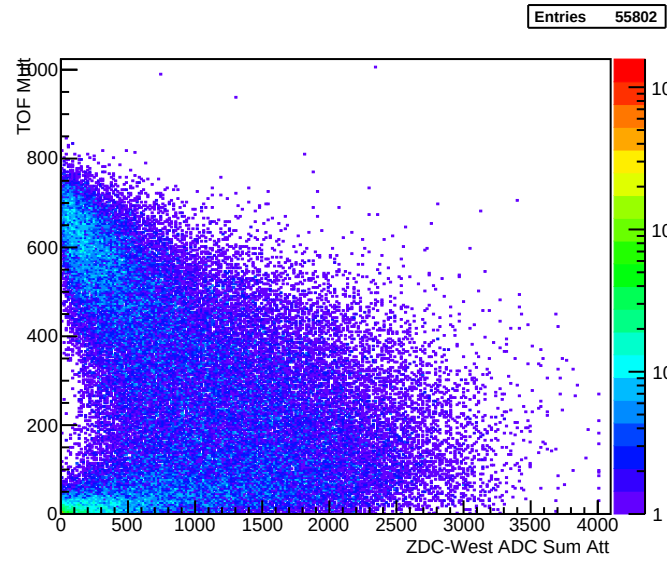

Entries 55802
TOF Mult
BBC-S-East ADC Sum

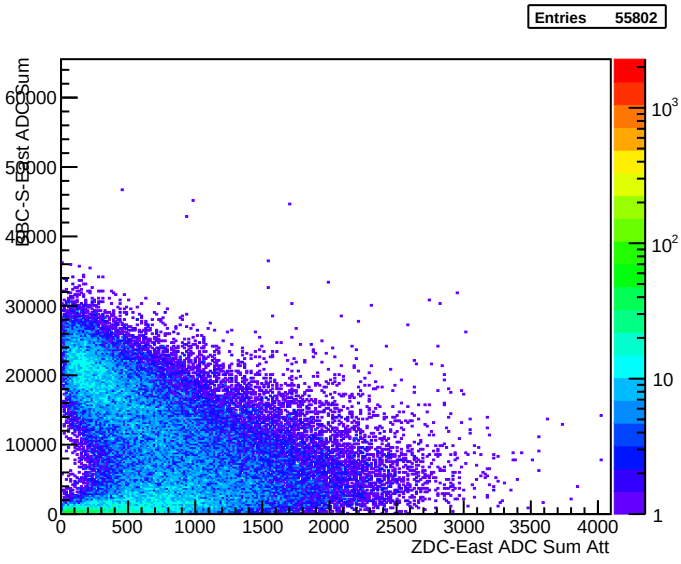

Entries 55802
TOF Mult
BBC-S-West ADC Sum

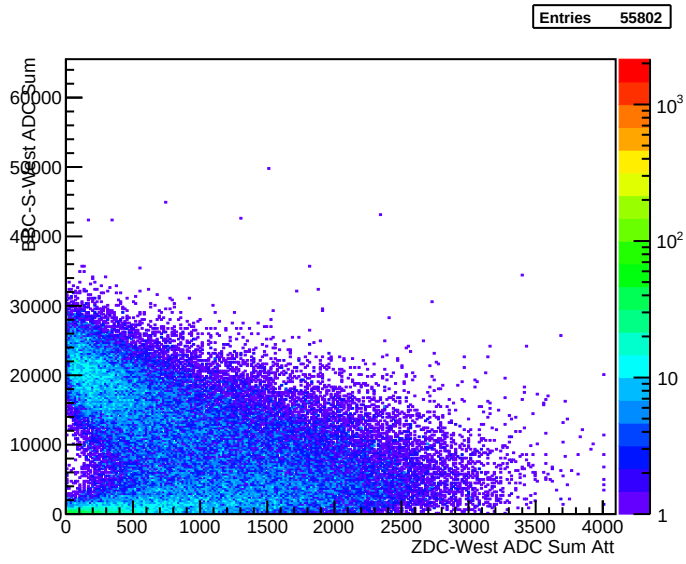

Entries 55802
TOF Mult
VPD-East ADC Sum

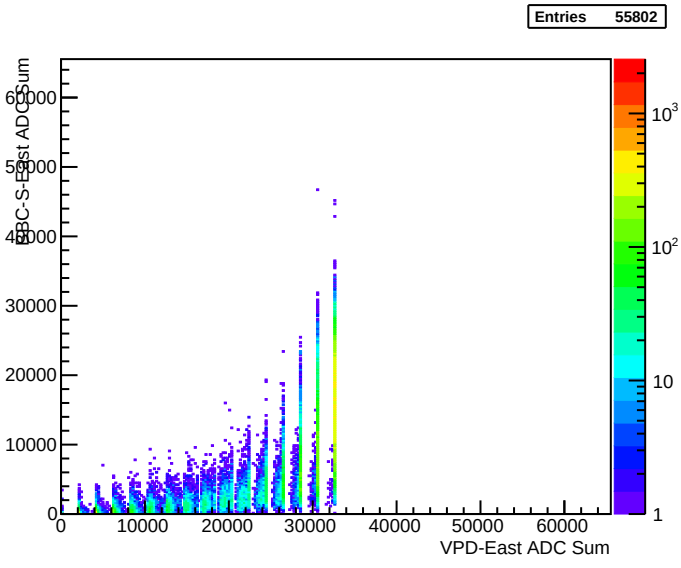

Entries 55802
TOF Mult
VPD-West ADC Sum

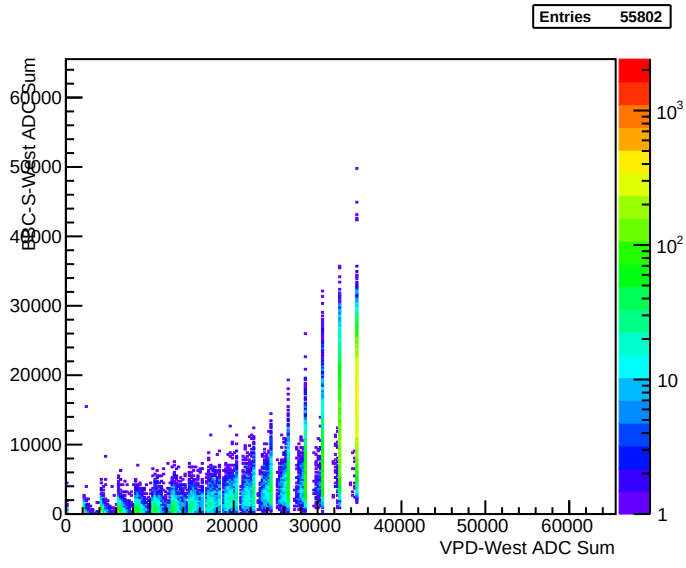

Entries 55802
TOF Mult
ZDC-East ADC Sum Att

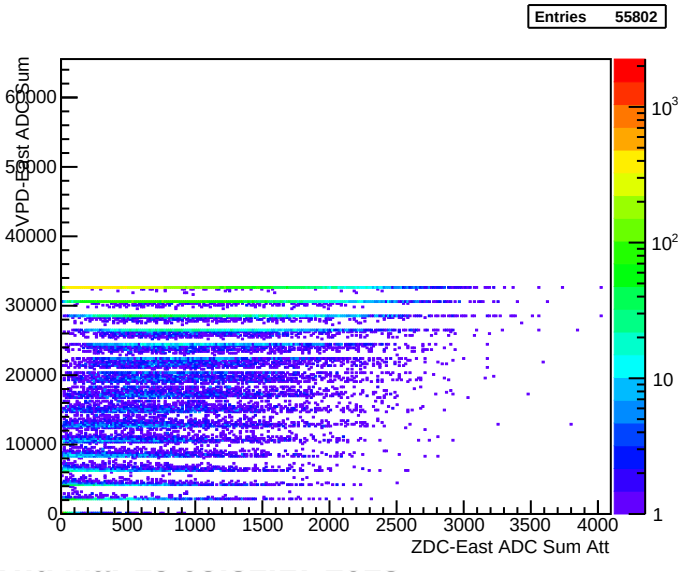

Entries 55802
TOF Mult
ZDC-West ADC Sum Att

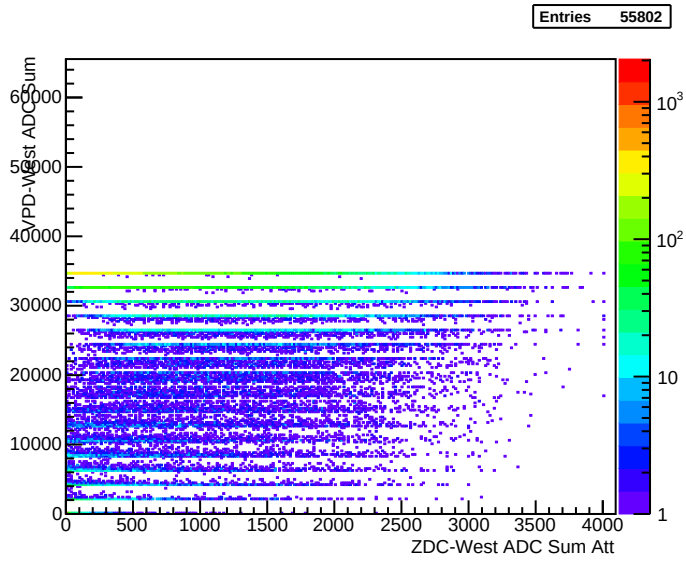

Entries 55802
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

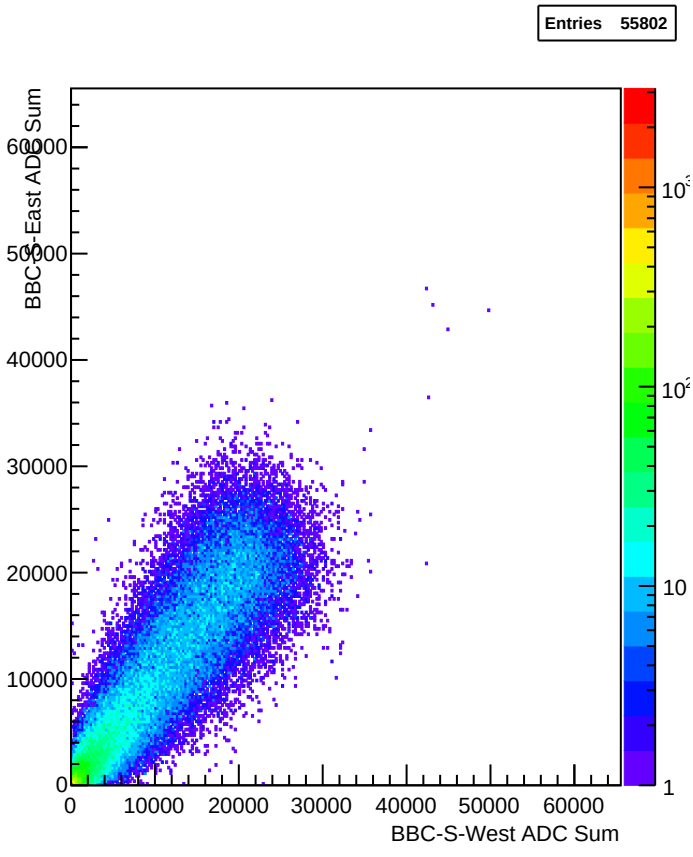

Entries 55802
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

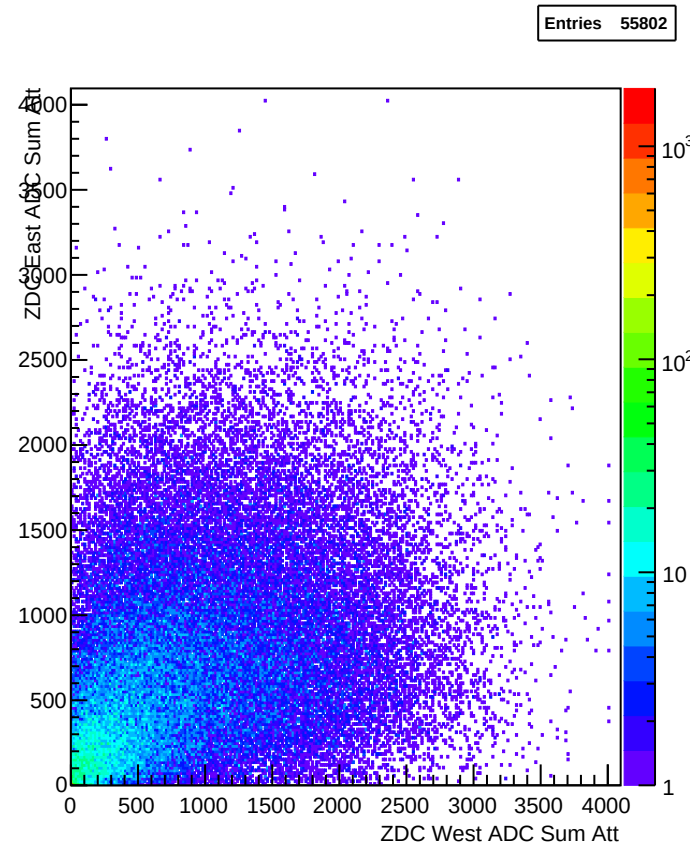

Entries 55802
BBC-S-East ADC Sum
VPD-East ADC Sum

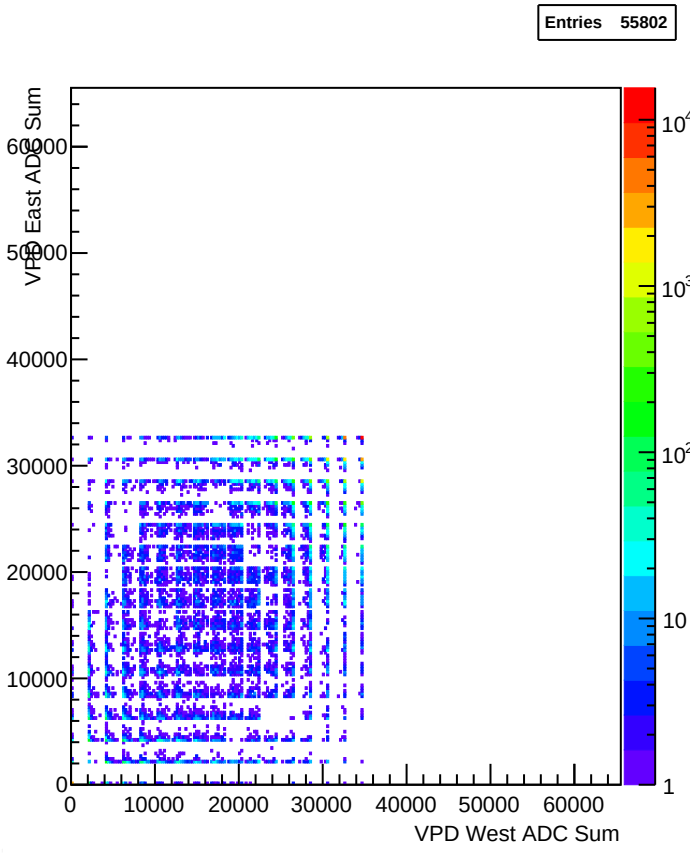

Entries 55802
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 55802
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 55802
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 55802
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 55802
ZDC East ADC Sum Att
ZDC West ADC Sum Att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VPD East ADC Sum
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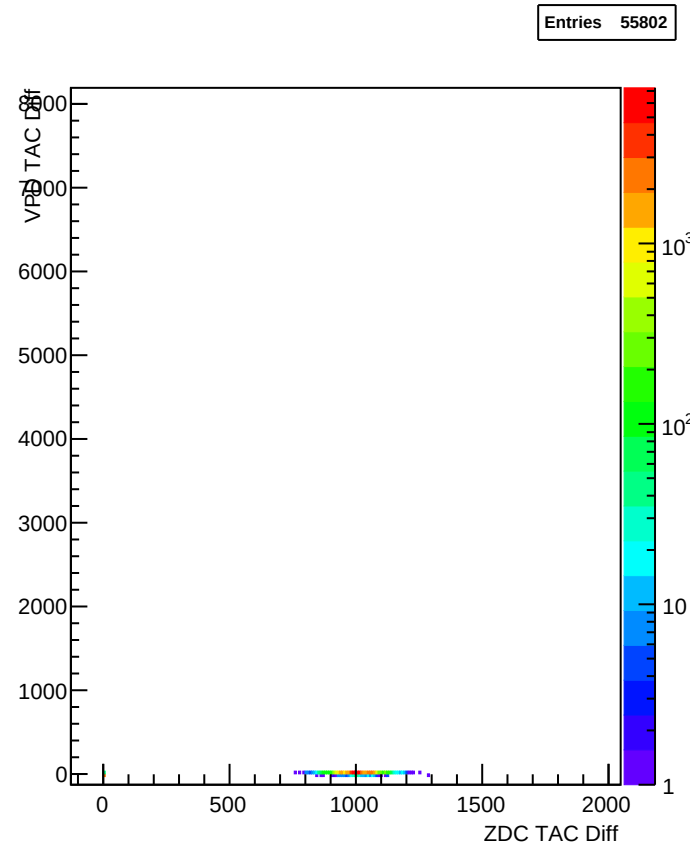
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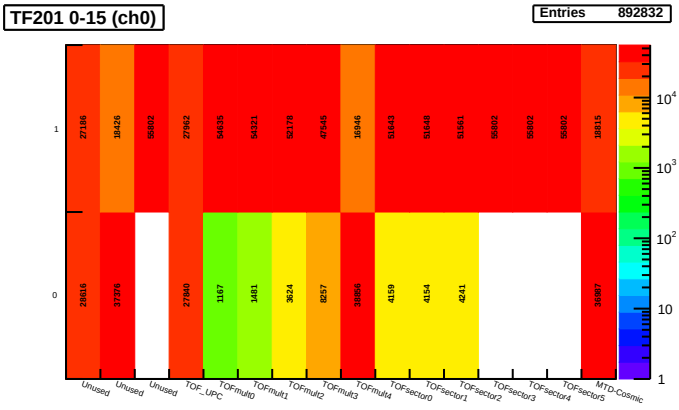
VPD TAC Diff
ZDC TAC Diff


TF201 0-15 (ch0)
Entries 892832
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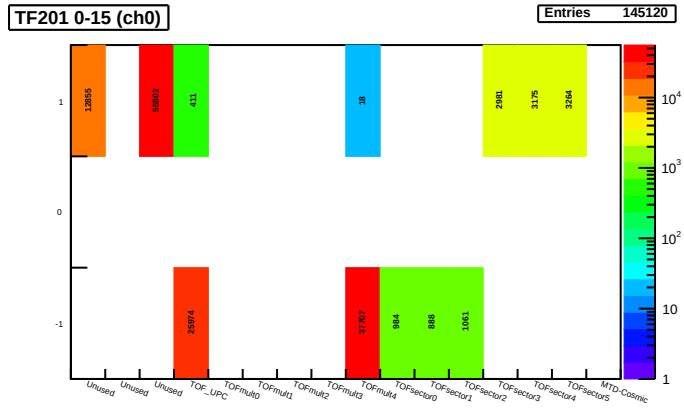
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Entries 145120
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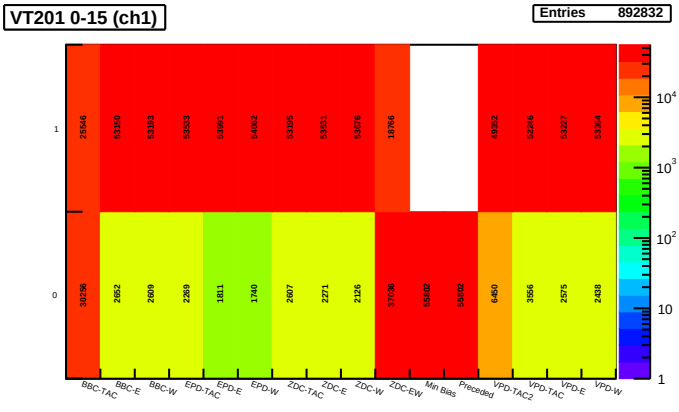
VT201 0-15 (ch1)
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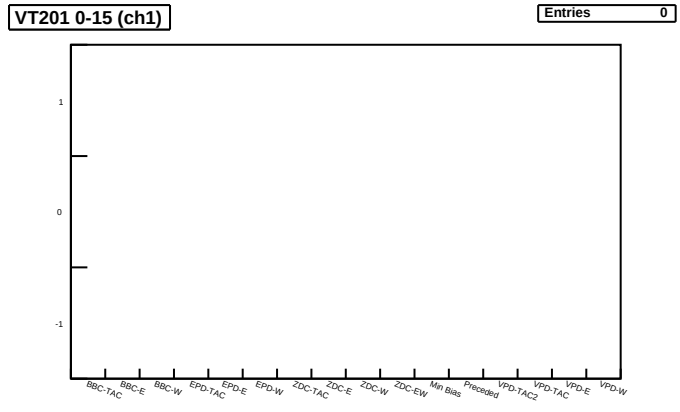
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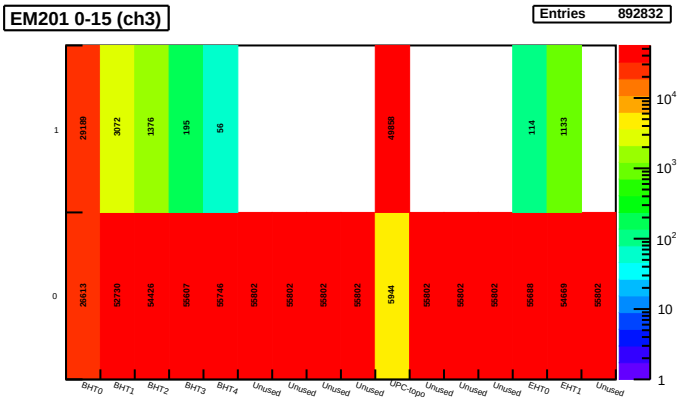
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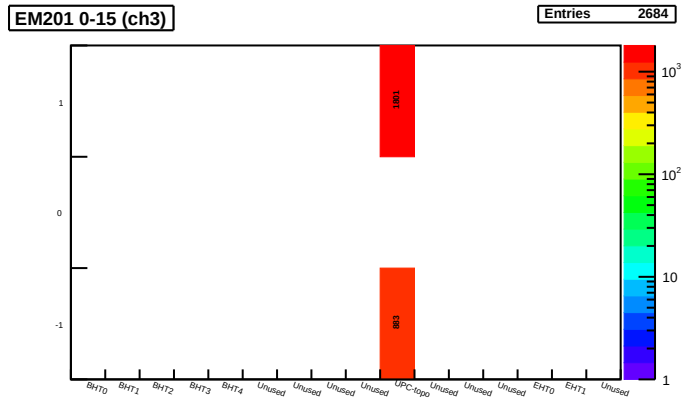
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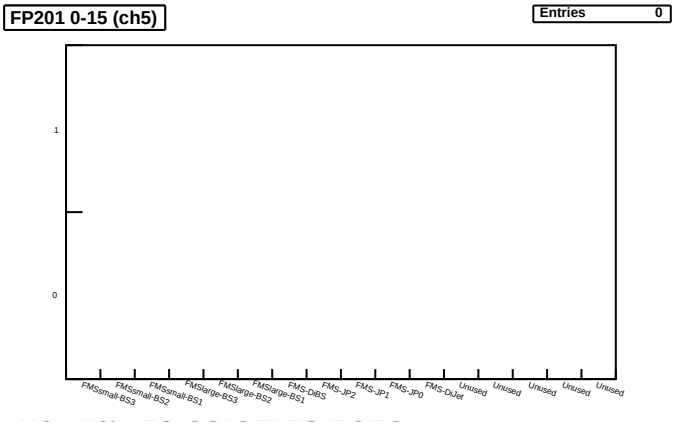
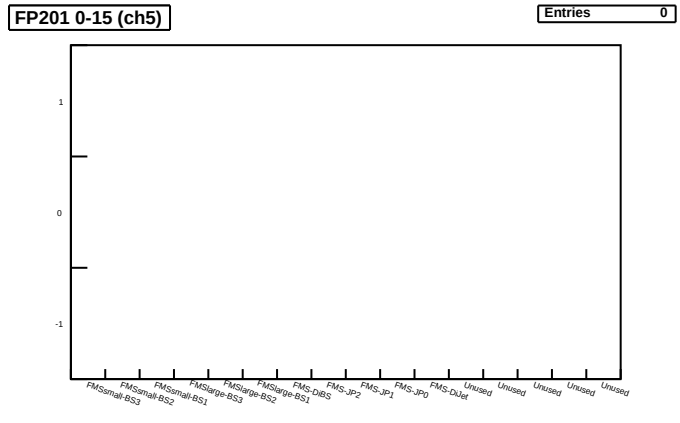

FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused


FP201 0-15 (ch5)
Entries 0
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