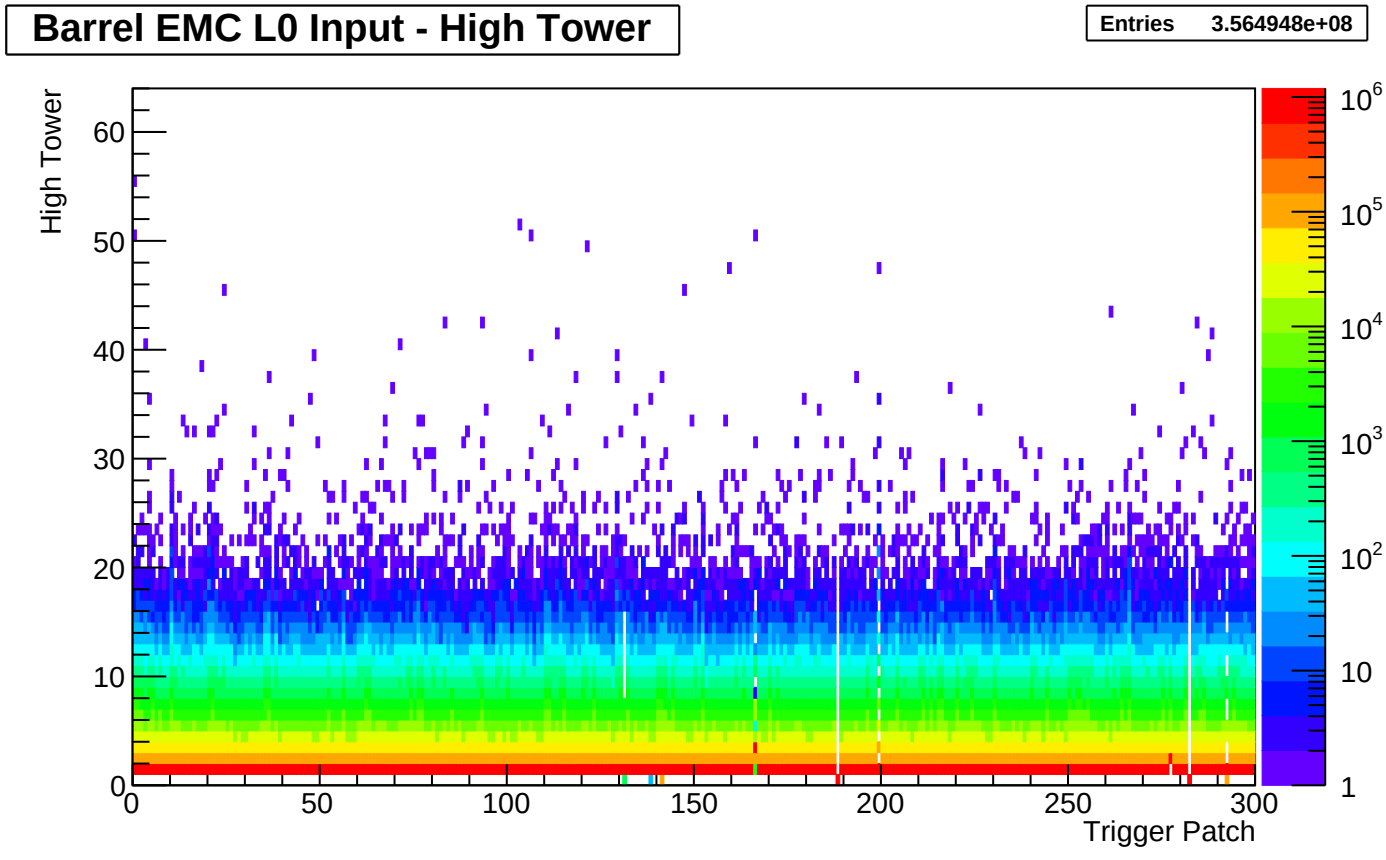
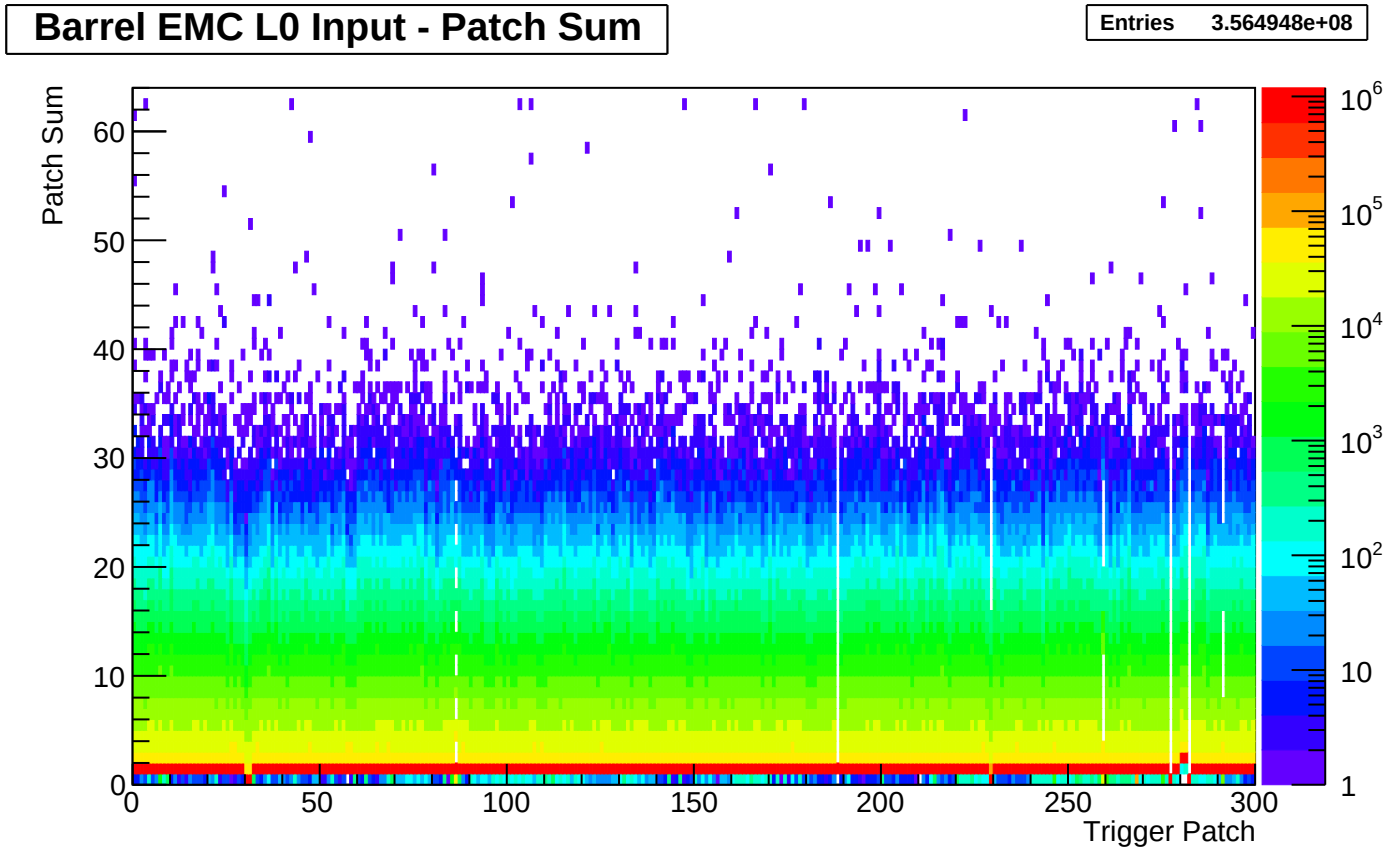
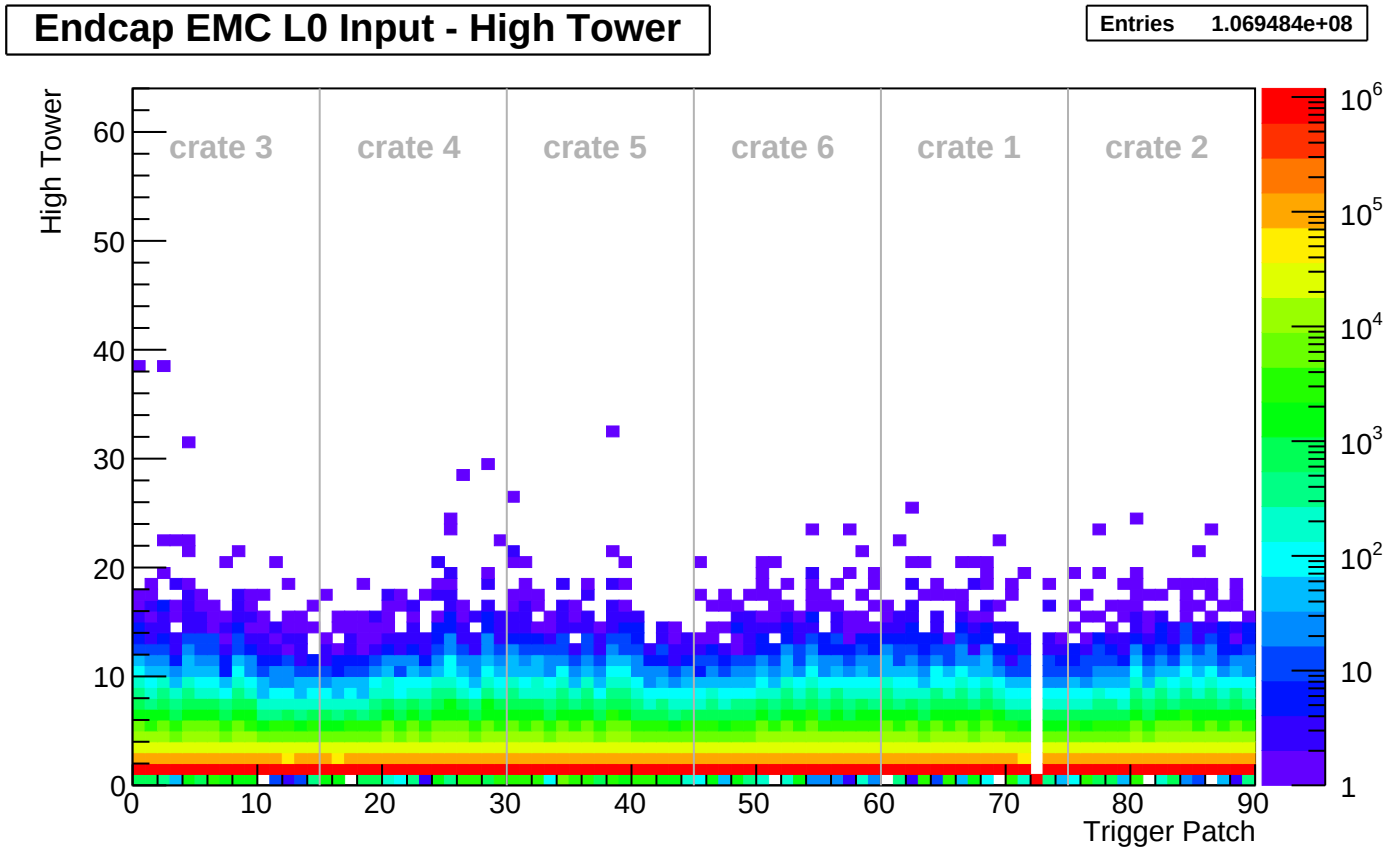
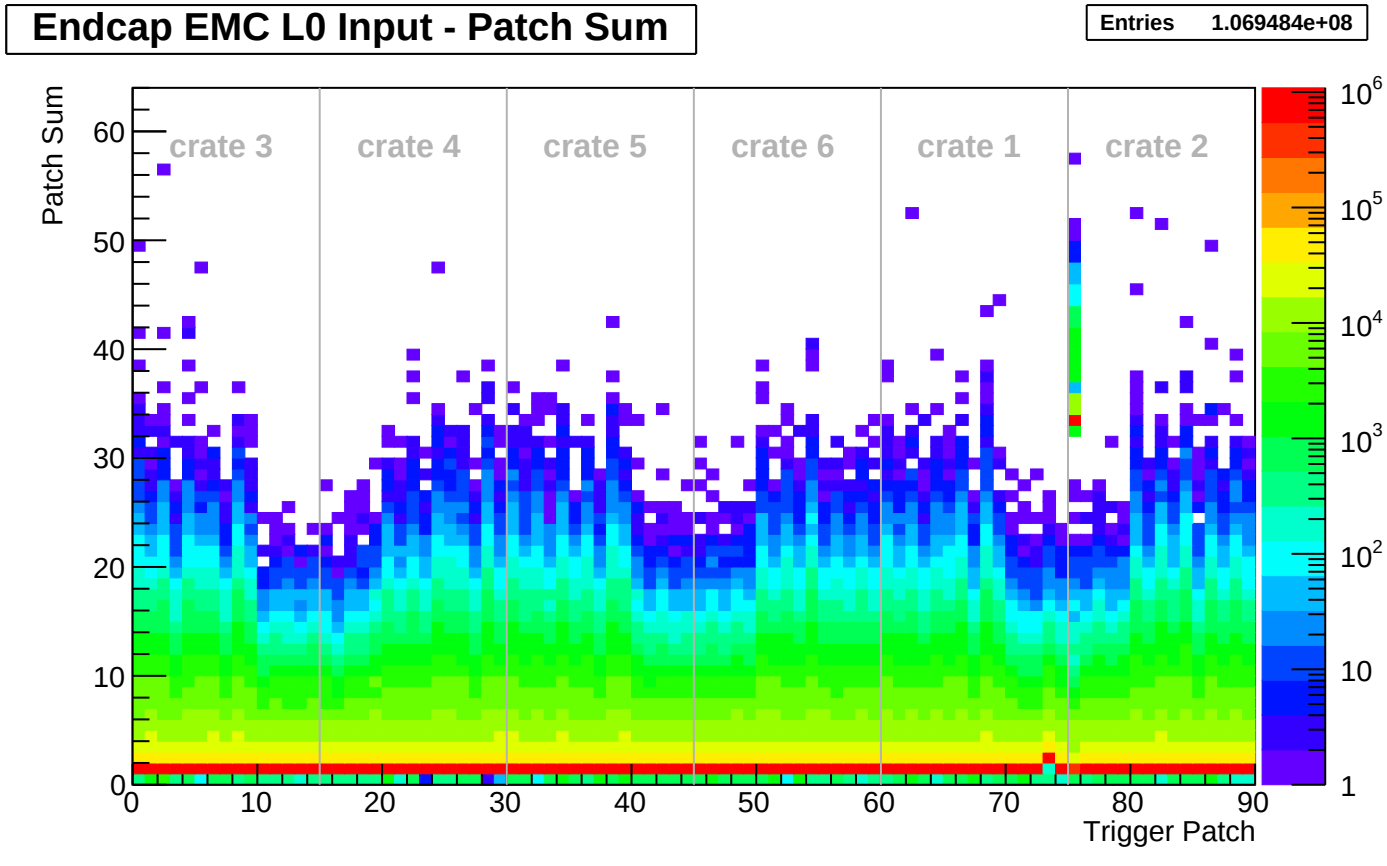
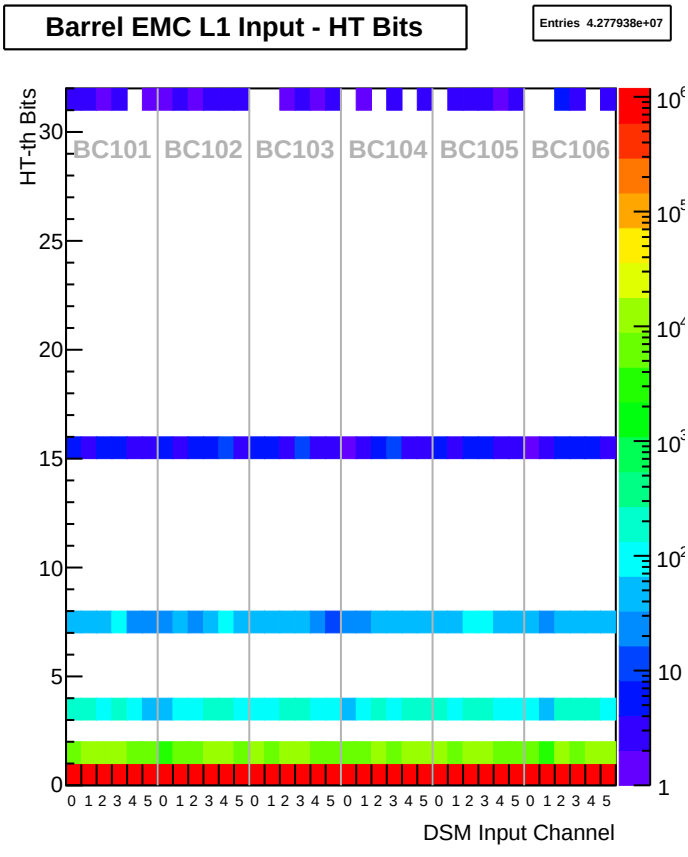
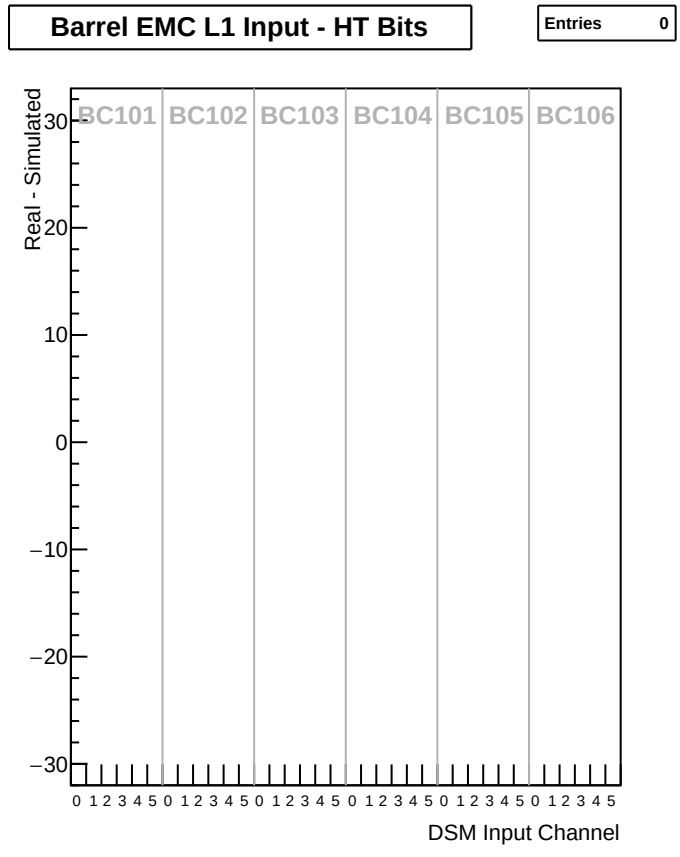

Barrel EMC L0 Input - High Tower
Entries 3.564948e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

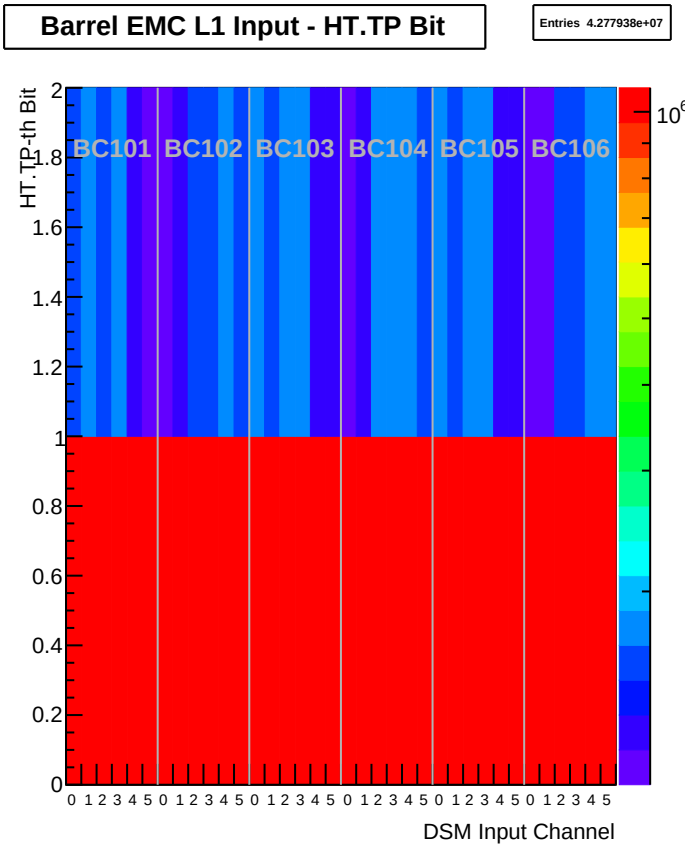

Barrel EMC L0 Input - Patch Sum
Entries 3.564948e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

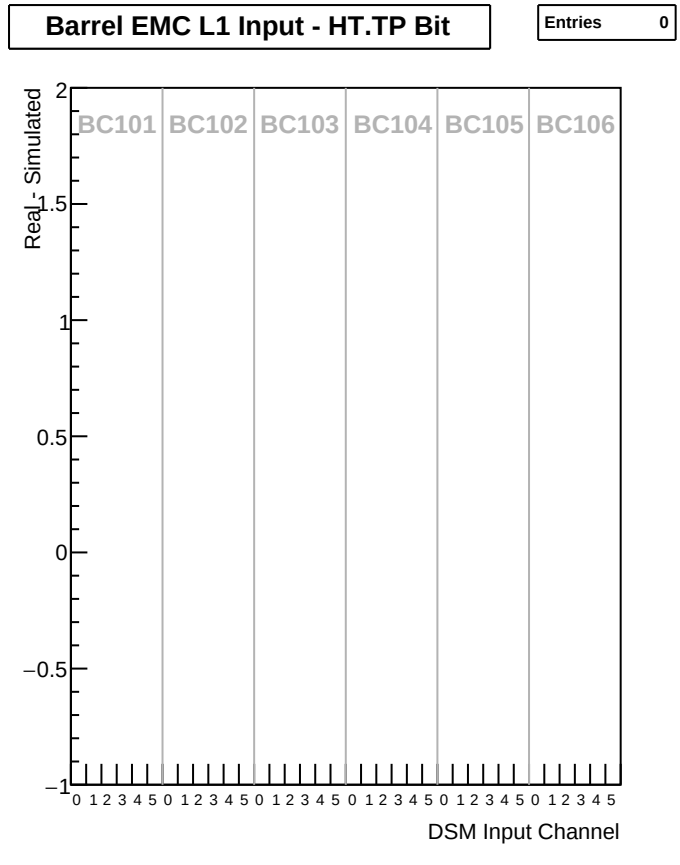

Endcap EMC L0 Input - High Tower
Entries 1.069484e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

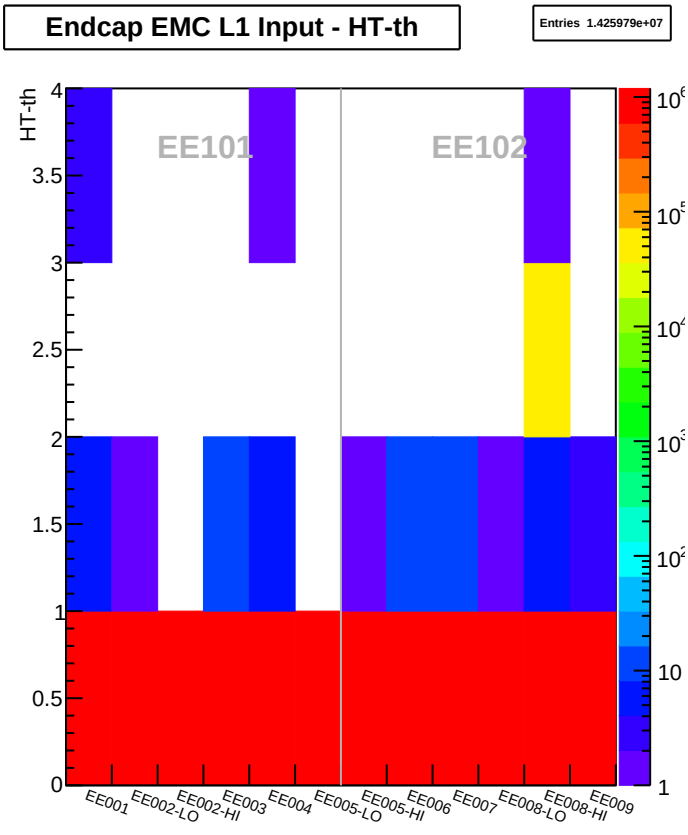

Endcap EMC L0 Input - Patch Sum
Entries 1.069484e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

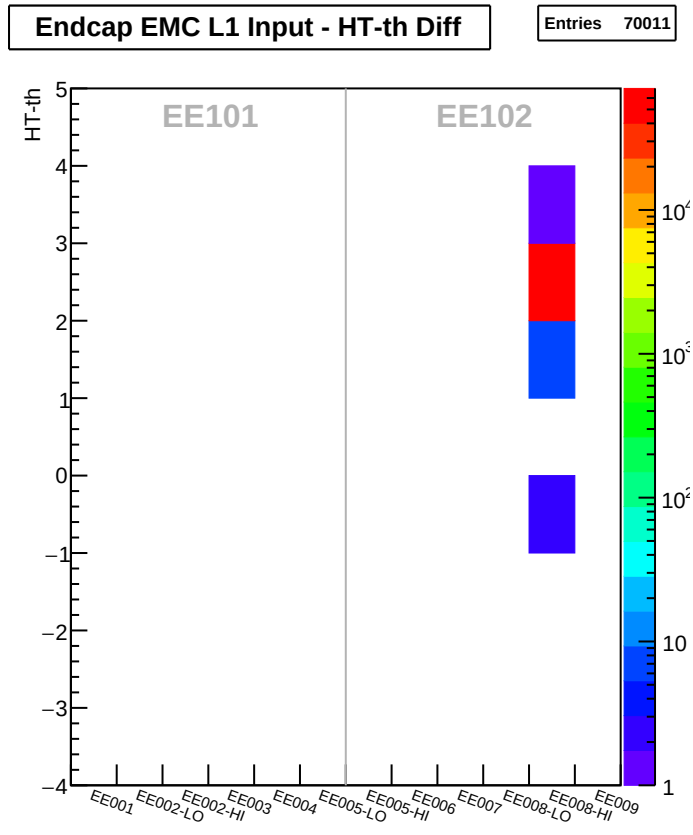

Barrel EMC L1 Input - HT Bits
Entries 4.277938e+07
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

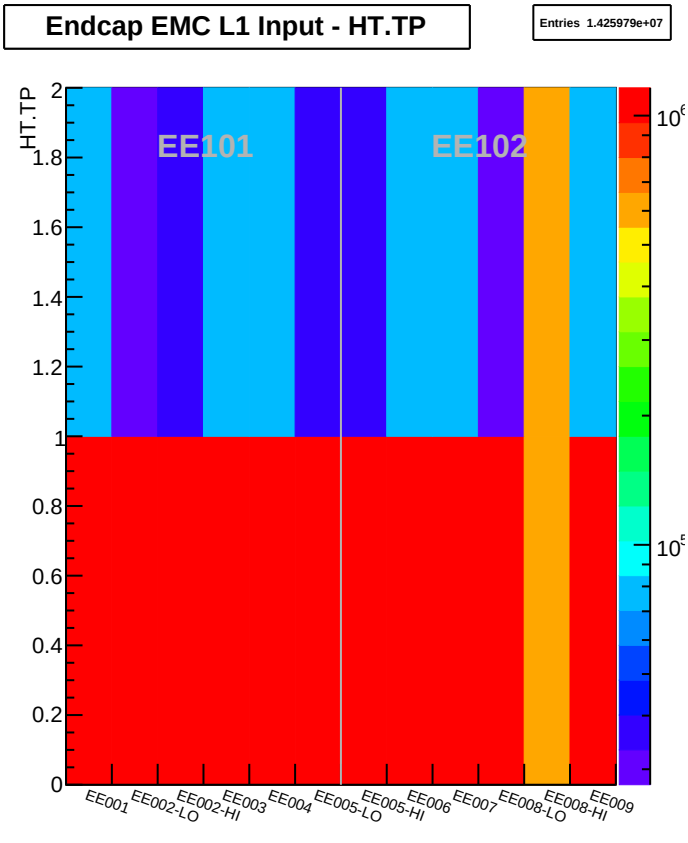

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

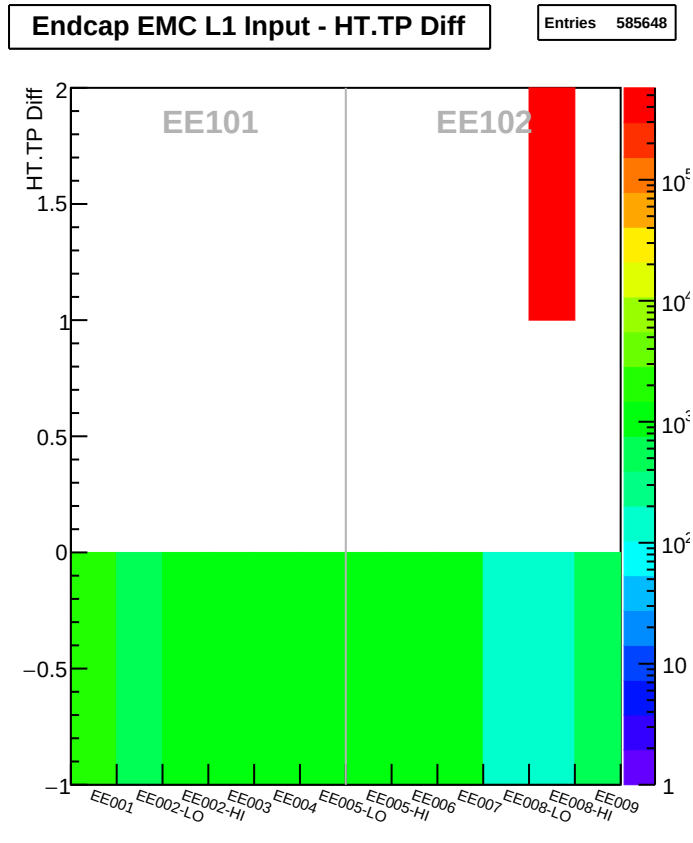

Barrel EMC L1 Input - HT.TP Bit
Entries 4.277938e+07
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

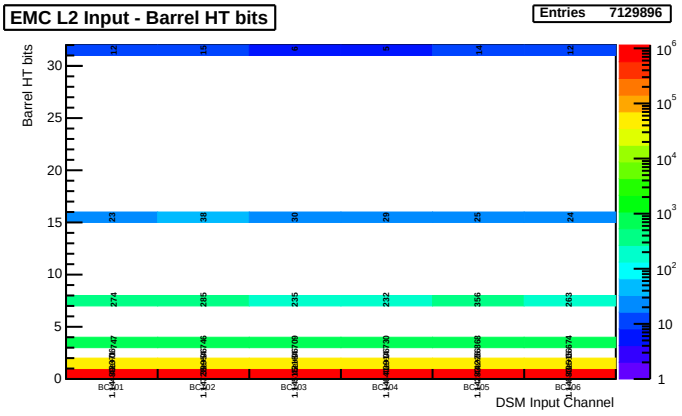

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

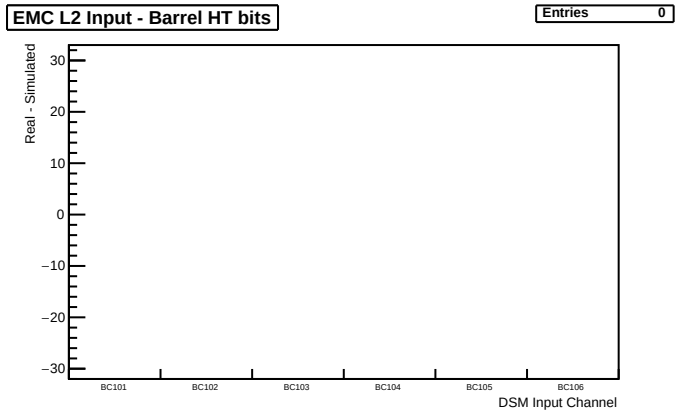

Endcap EMC L1 Input - HT-th
Entries 1.425979e+07
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

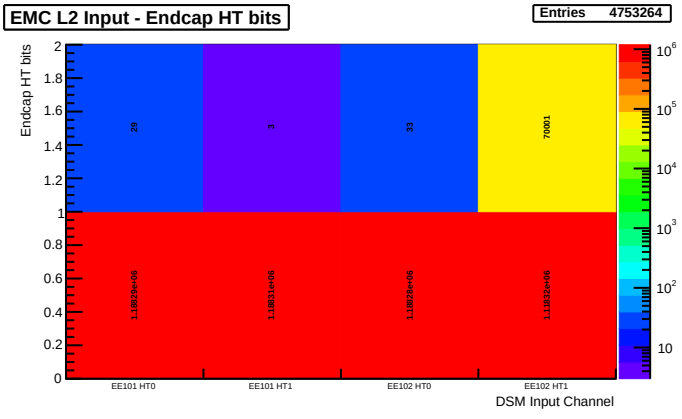

Endcap EMC L1 Input - HT-th Diff
Entries 70011
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

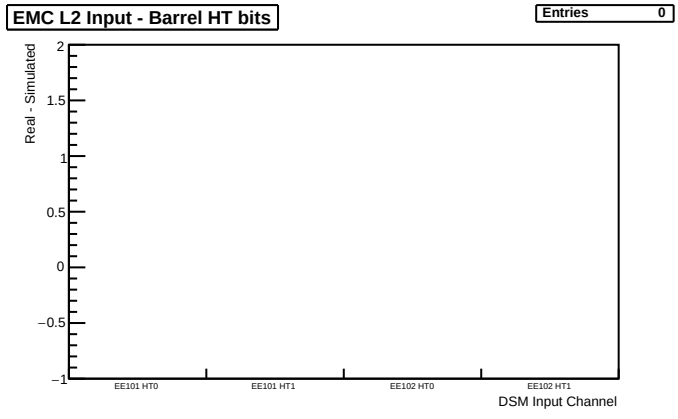

Endcap EMC L1 Input - HT.TP
Entries 1.425979e+07
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

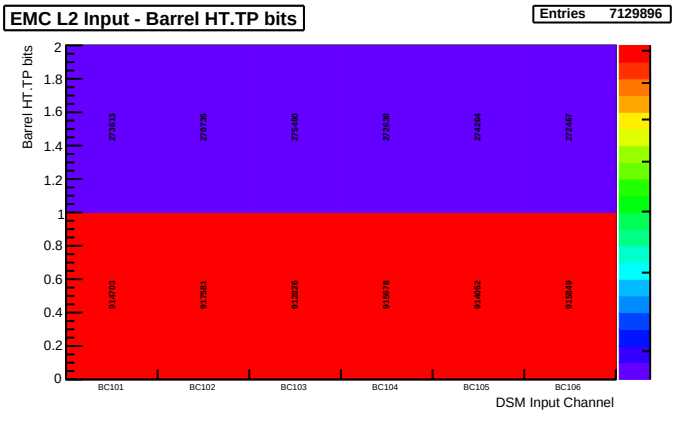

Endcap EMC L1 Input - HT.TP Diff
Entries 585648
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

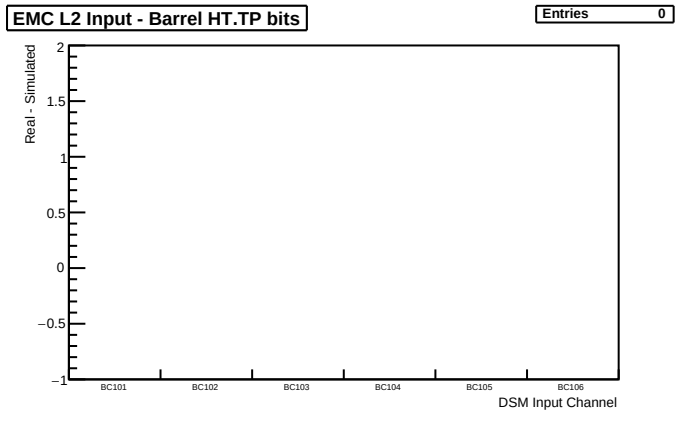

EMC L2 Input - Barrel HT bits
Entries 7129896
Barrel HT bits
DSM Input Channel

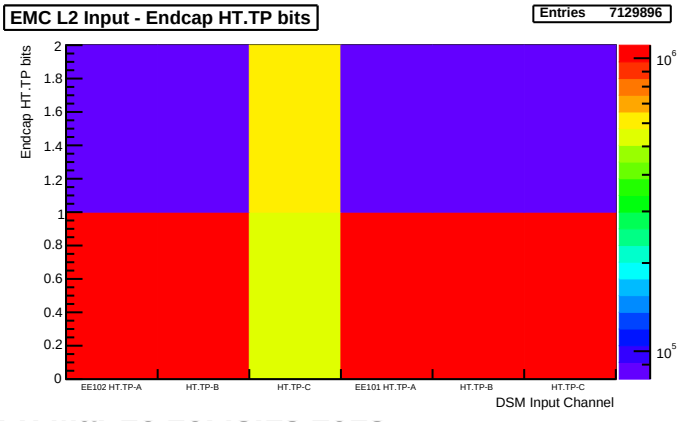

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel

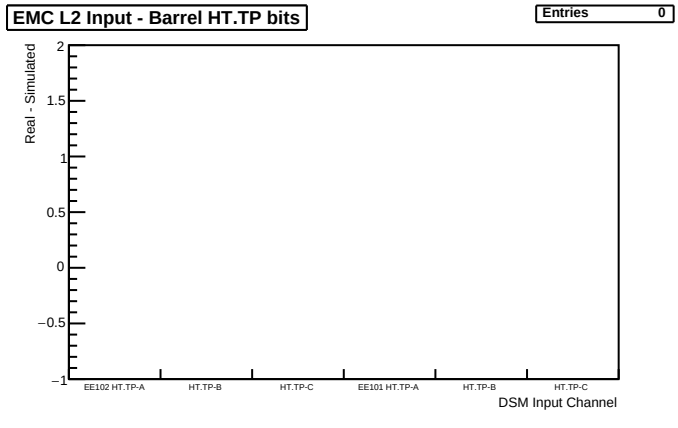

EMC L2 Input - Endcap HT bits
Entries 4753264
Endcap HT bits
DSM Input Channel

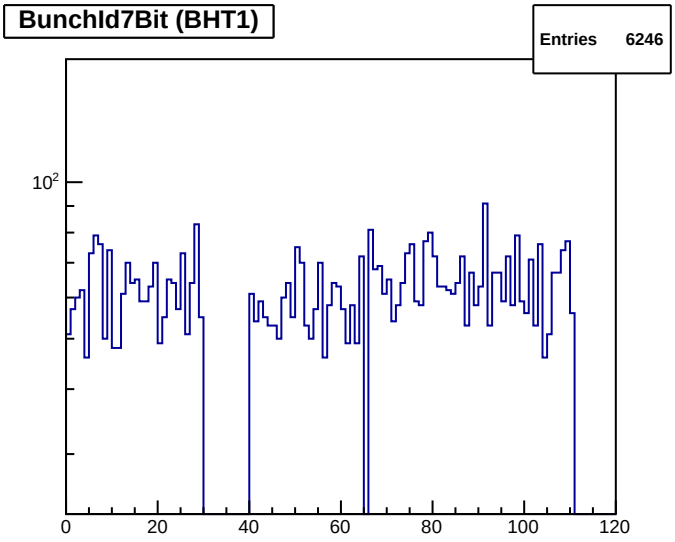

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel

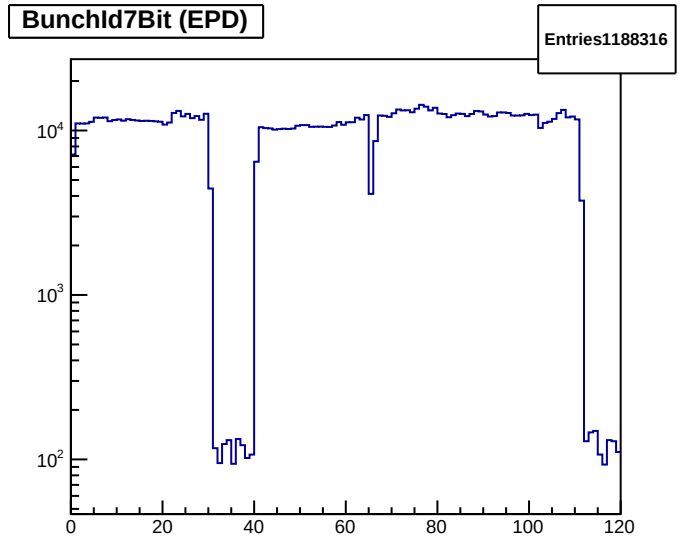

EMC L2 Input - Barrel HT.TP bits
Entries 7129896
Barrel HT.TP bits
DSM Input Channel

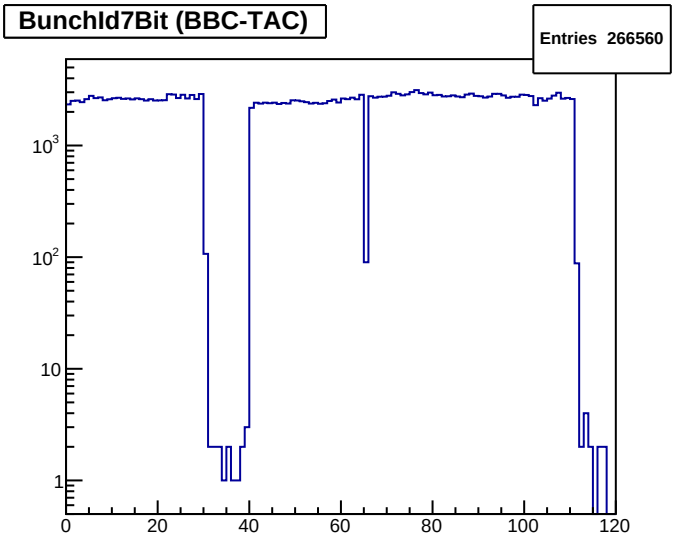

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel

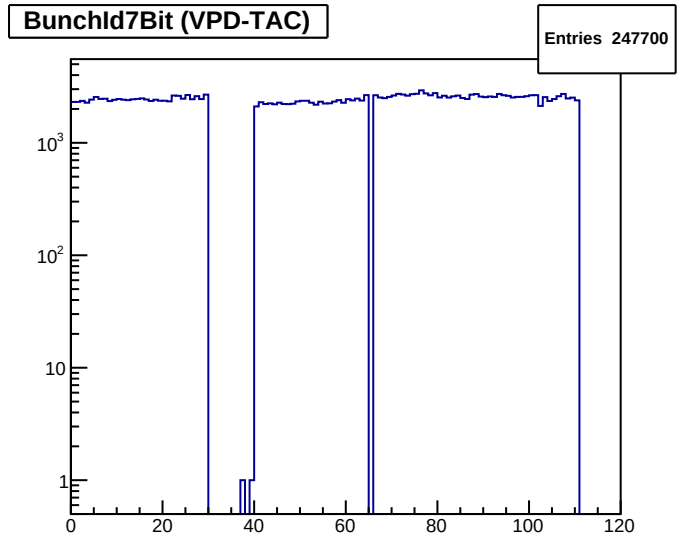

EMC L2 Input - Endcap HT.TP bits
Entries 7129896
Endcap HT.TP bits
DSM Input Channel

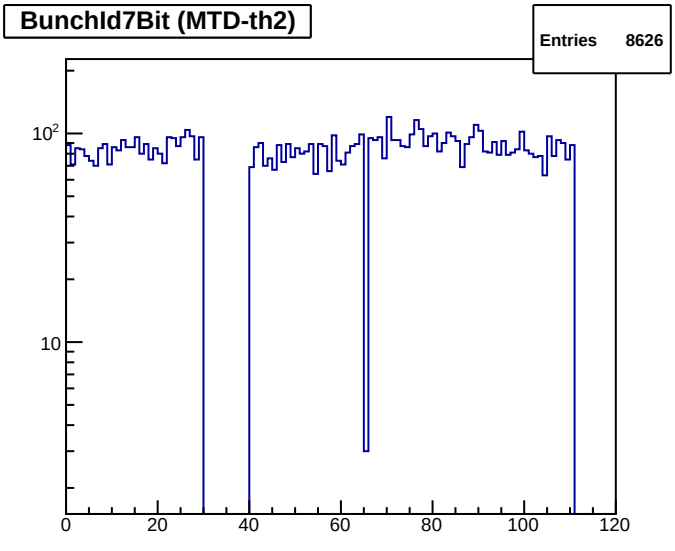

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel

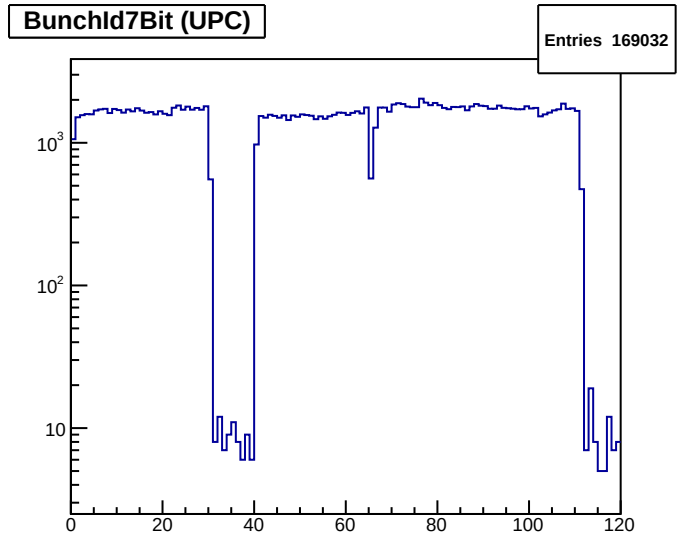

BunchId7Bit (BHT1)
Entries 6246
10^2
0
20
40
60
80
100
120

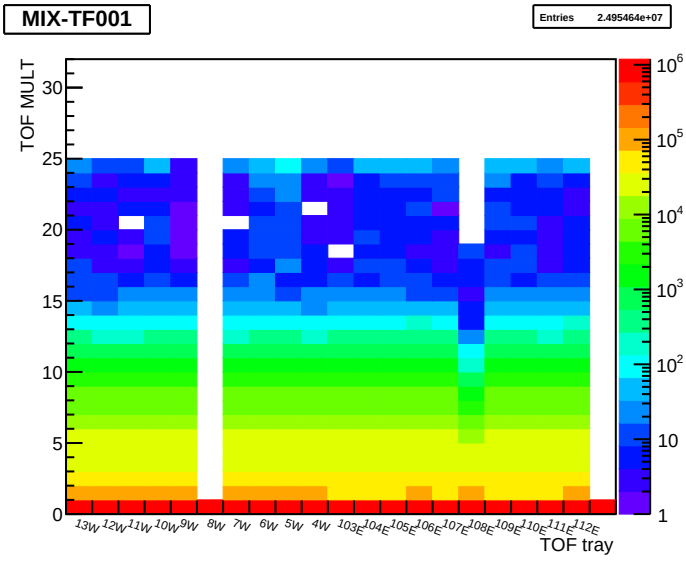

BunchId7Bit (EPD)
Entries1188316
10^4
10^3
10^2
0
20
40
60
80
100
120

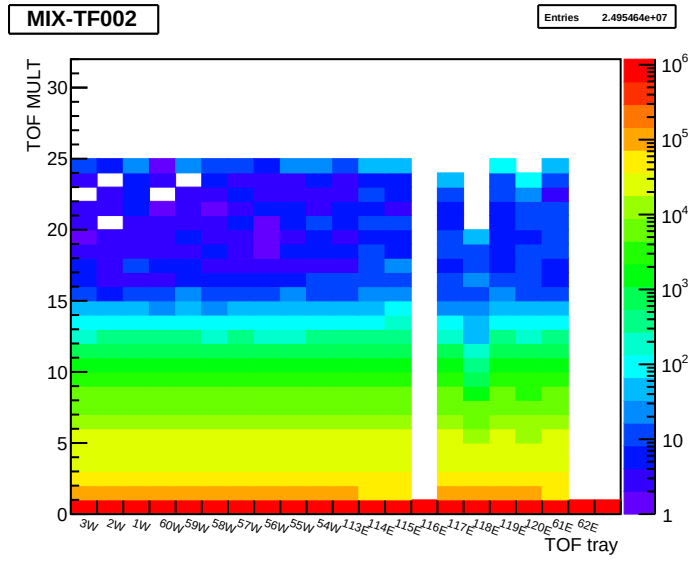

BunchId7Bit (BBC-TAC)
Entries 266560
10^3
10^2
10
1
0
20
40
60
80
100
120

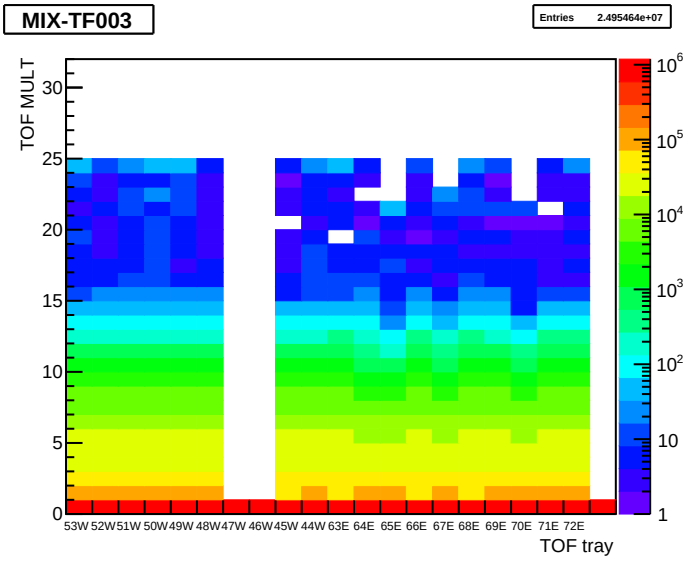

BunchId7Bit (VPD-TAC)
Entries 247700
10^3
10^2
10
1
0
20
40
60
80
100
120

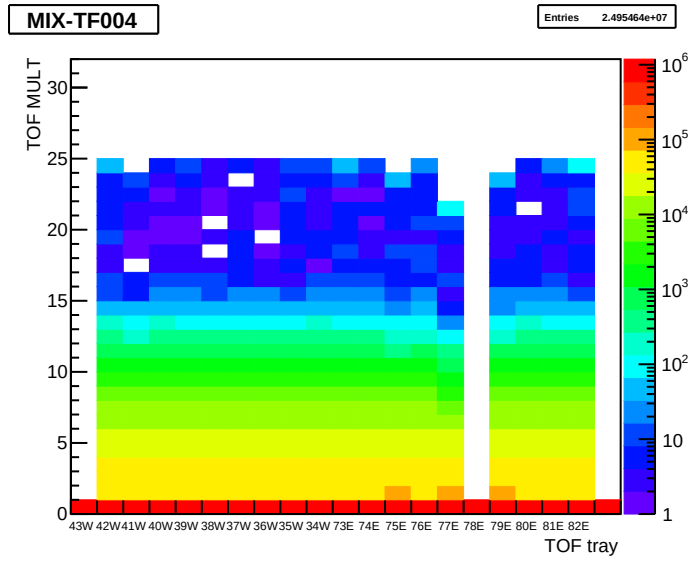

BunchId7Bit (MTD-th2)
Entries 8626
10^2
10
0
20
40
60
80
100
120

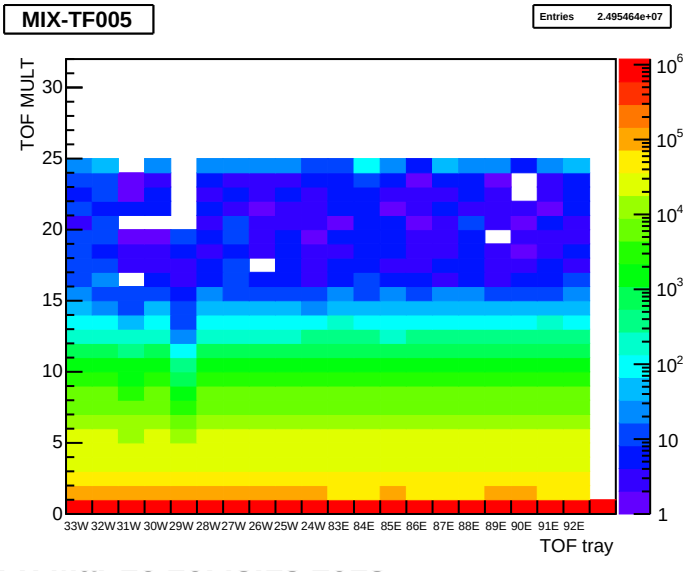

BunchId7Bit (UPC)
Entries 169032
10^3
10^2
10
0
20
40
60
80
100
120

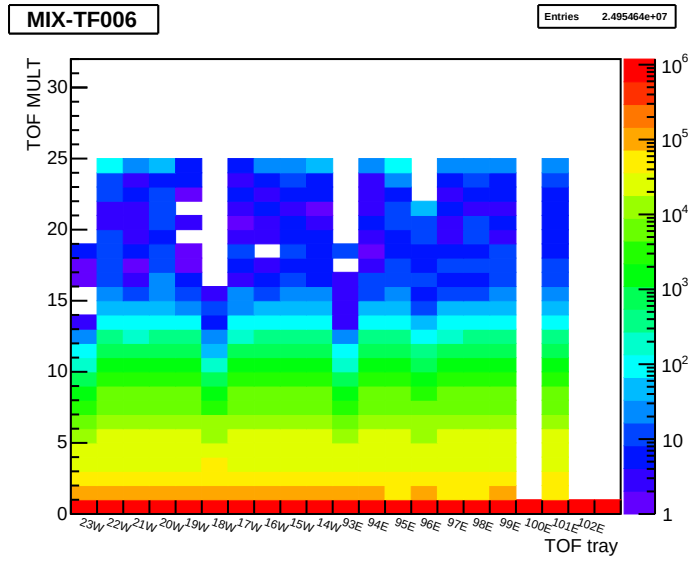

MIX-TF001
Entries 2.495464e+07
TOF MULT
TOF tray

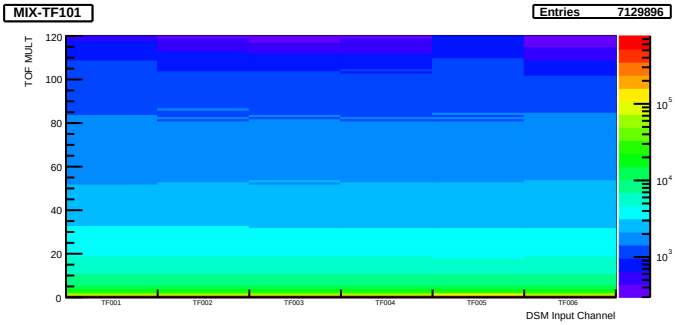

MIX-TF002
Entries 2.495464e+07
TOF MULT
TOF tray

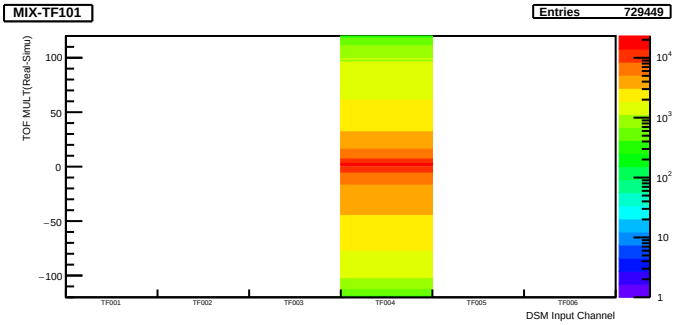

MIX-TF003
Entries 2.495464e+07
TOF MULT
TOF tray

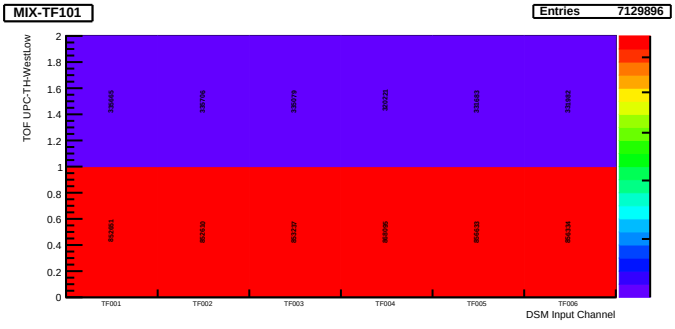

MIX-TF004
Entries 2.495464e+07
TOF MULT
TOF tray

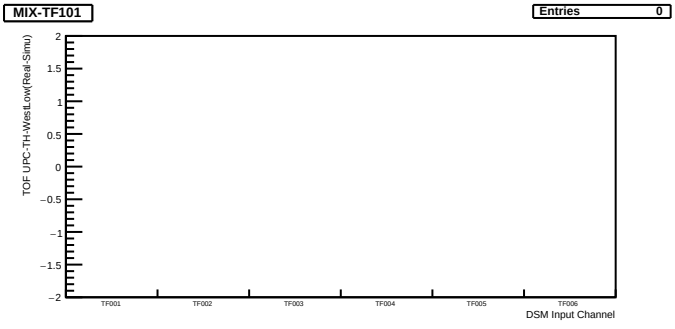

MIX-TF005
Entries 2.495464e+07
TOF MULT
TOF tray

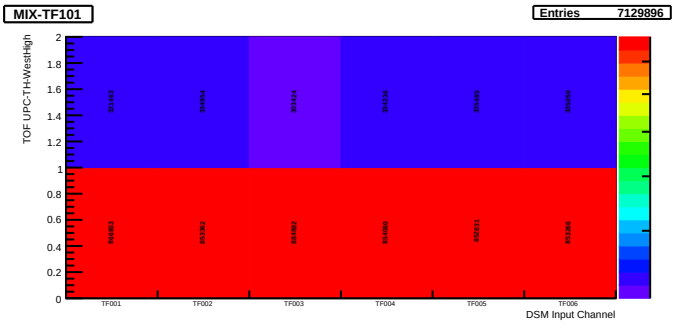

MIX-TF006
Entries 2.495464e+07
TOF MULT
TOF tray

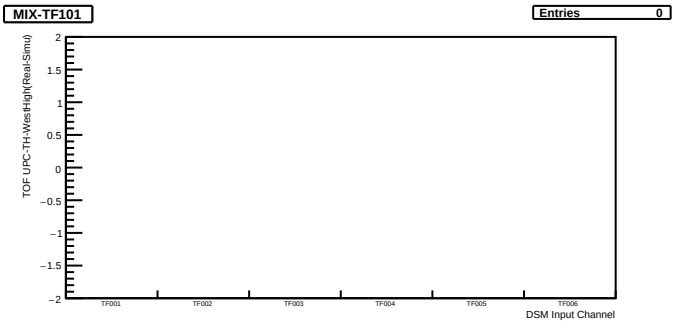

MIX-TF101
Entries 7129896
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

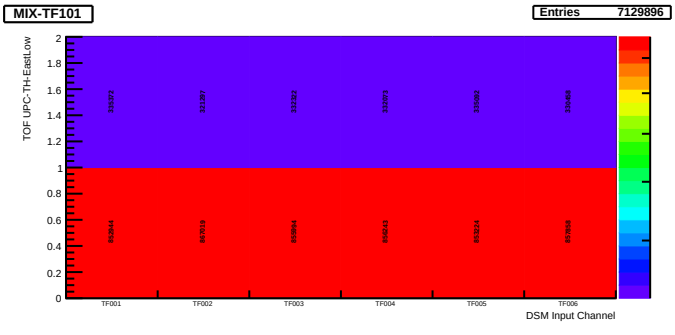

MIX-TF101
Entries 729449
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

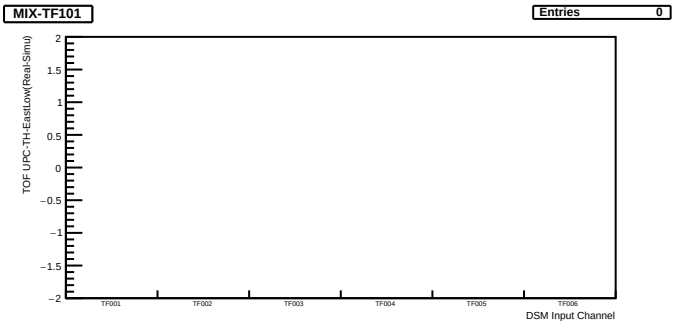

MIX-TF101
Entries 7129896
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

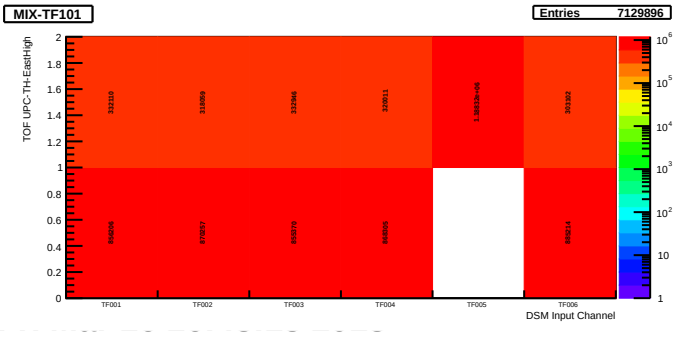

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

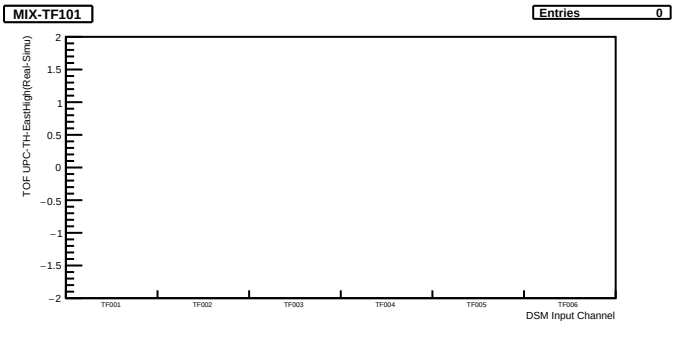

MIX-TF101
Entries 7129896
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

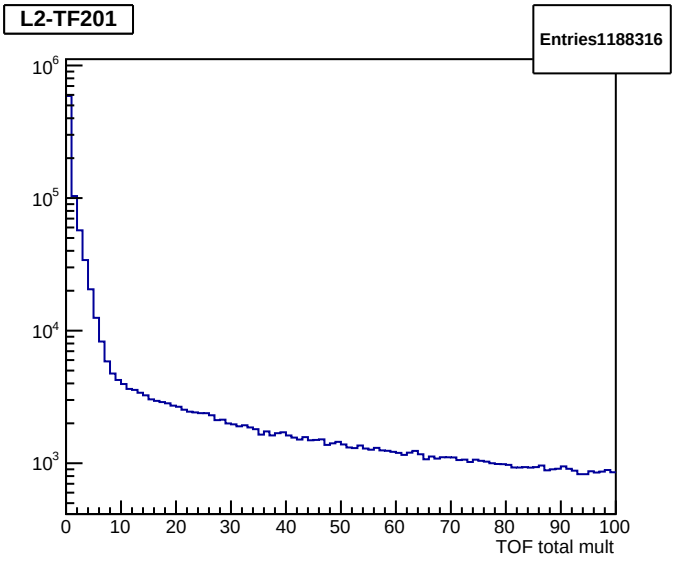

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

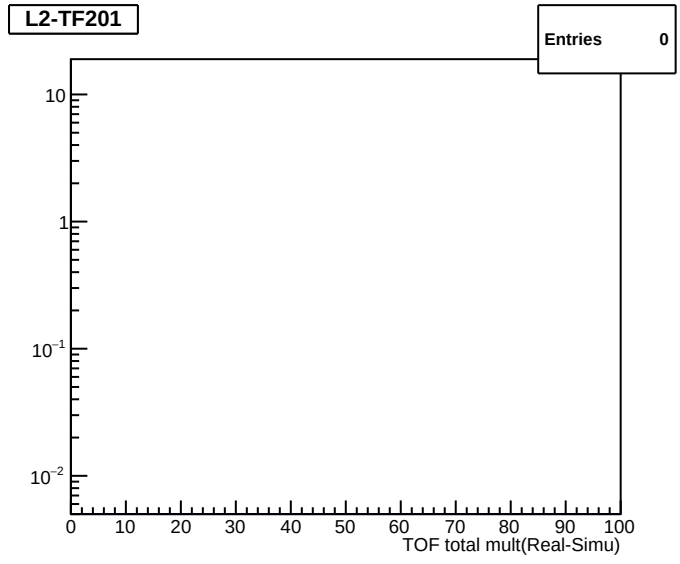

MIX-TF101
Entries 7129896
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

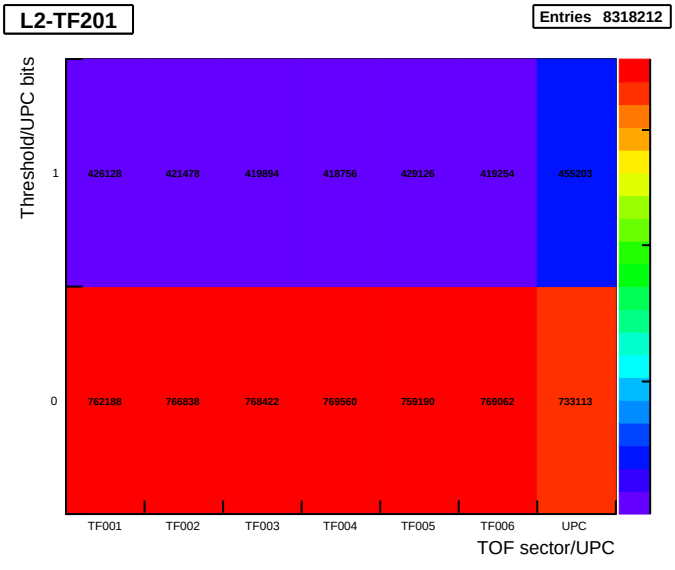

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

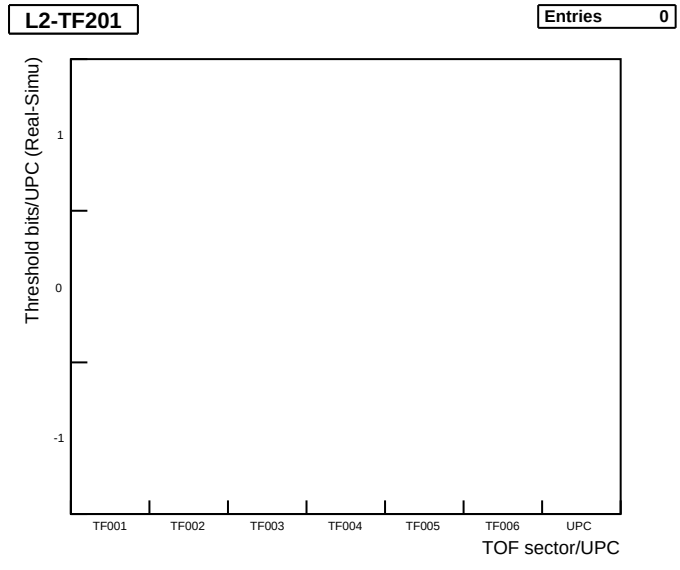

MIX-TF101
Entries 7129896
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

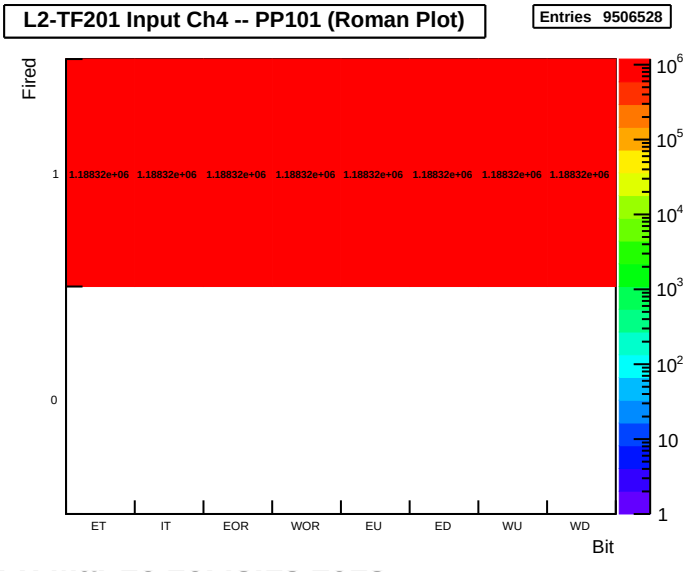

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

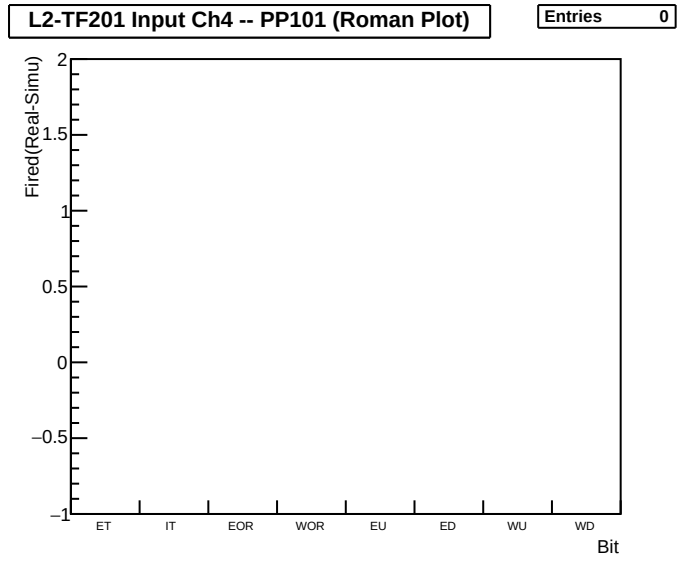

L2-TF201
Entries1188316
TOF total mult

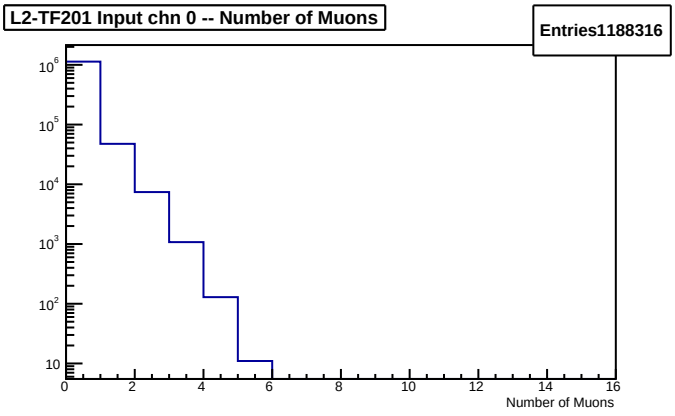

L2-TF201
Entries 0
TOF total mult(Real-Simu)

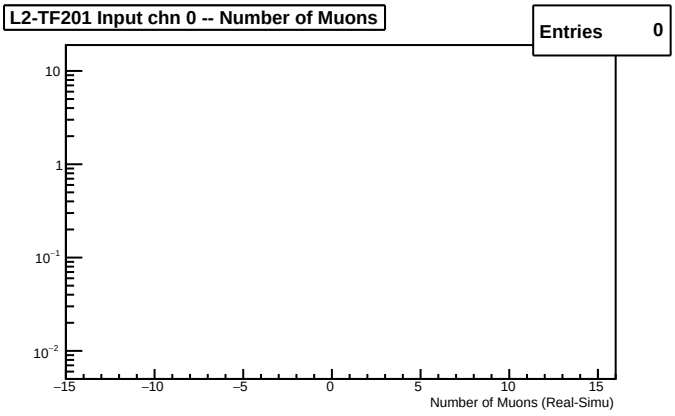

L2-TF201
Entries 8318212
Threshold/UPC bits
426128 421478 419894 418756 429126 419254 455203
762188 766838 768422 769560 759190 769062 733113
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

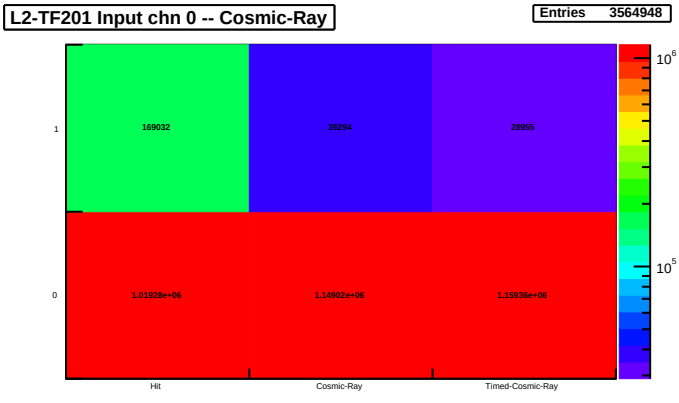

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

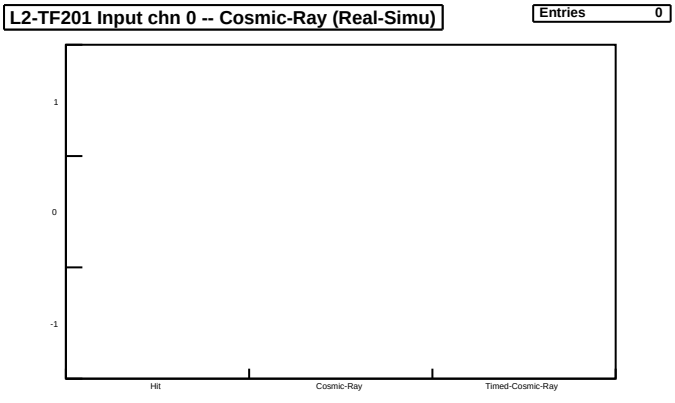

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 9506528
Fired
1.18832e+06 1.18832e+06 1.18832e+06 1.18832e+06 1.18832e+06 1.18832e+06 1.18832e+06 1.18832e+06
ET IT EOR WOR EU ED WU WD
Bit

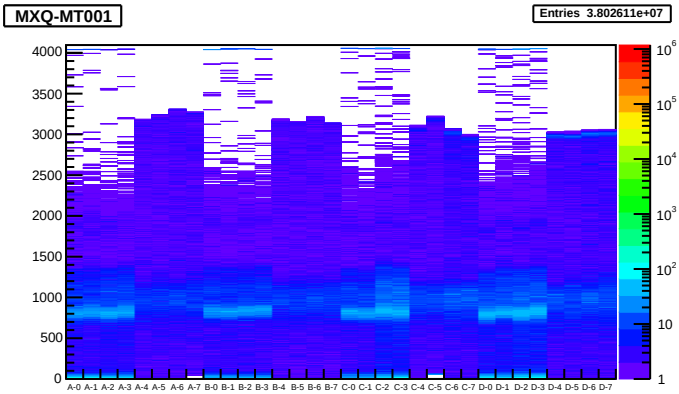

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET IT EOR WOR EU ED WU WD
Bit

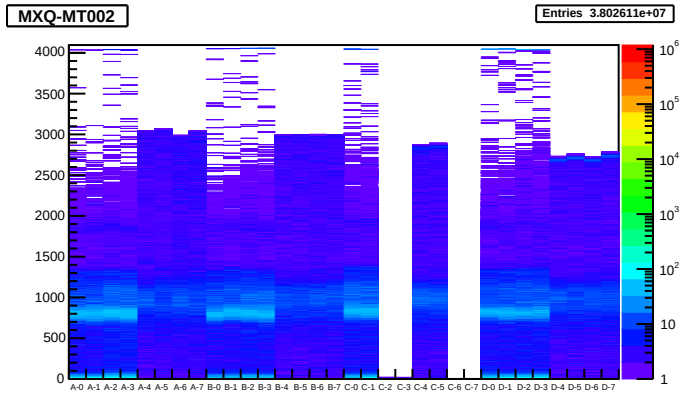

L2-TF201 Input chn 0 -- Number of Muons
Entries1188316
Number of Muons

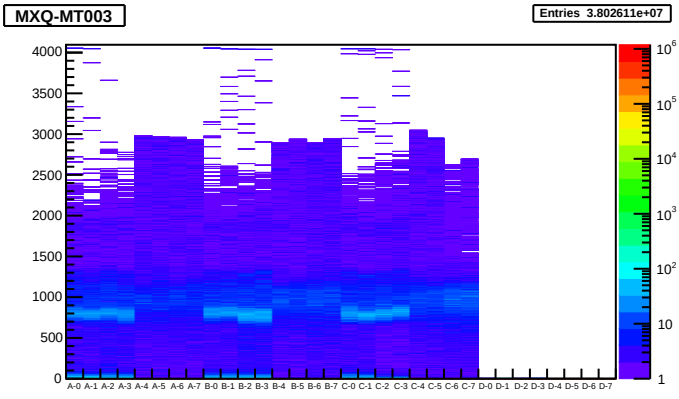

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

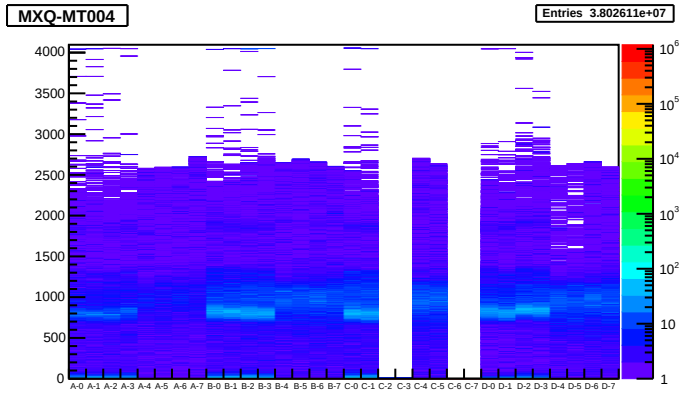

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 3564948
169032
39294
28955
1.01928e+06
1.14902e+06
1.15936e+06
Hit
Cosmic-Ray
Timed-Cosmic-Ray

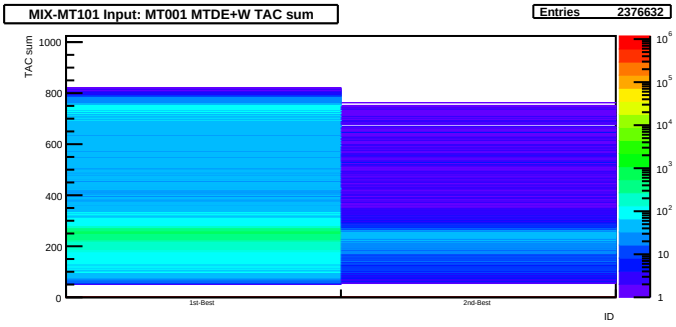

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

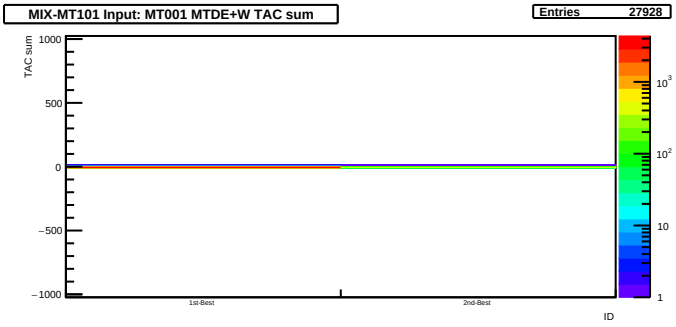

MXQ-MT001
Entries 3.802611e+07

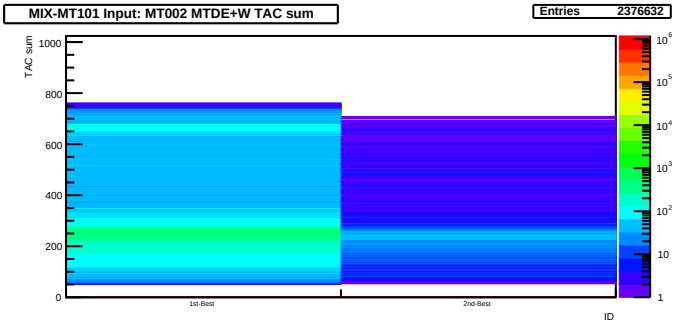

MXQ-MT002
Entries 3.802611e+07

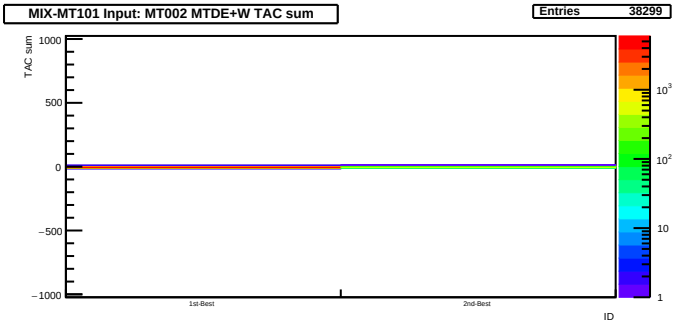

MXQ-MT003
Entries 3.802611e+07

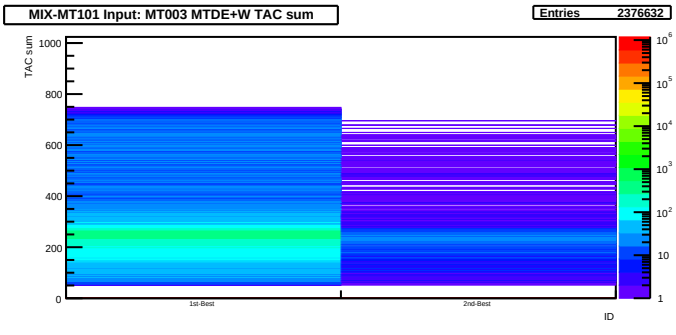

MXQ-MT004
Entries 3.802611e+07

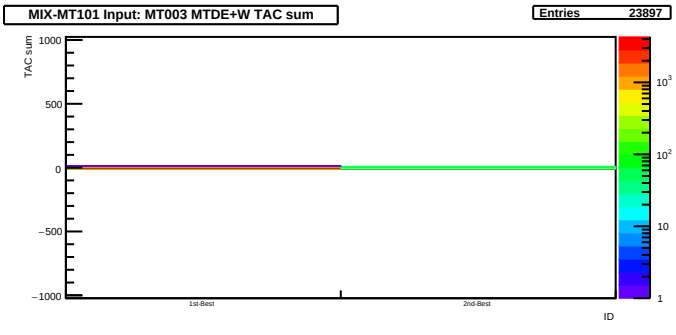

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 2376632
TAC sum
1st-Best
2nd-Best
ID

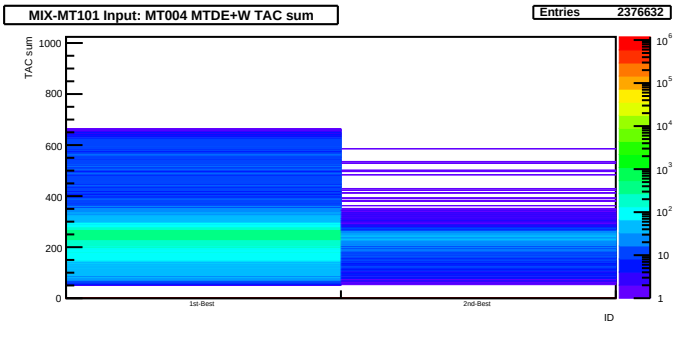

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 27928
TAC sum
1st-Best
2nd-Best
ID

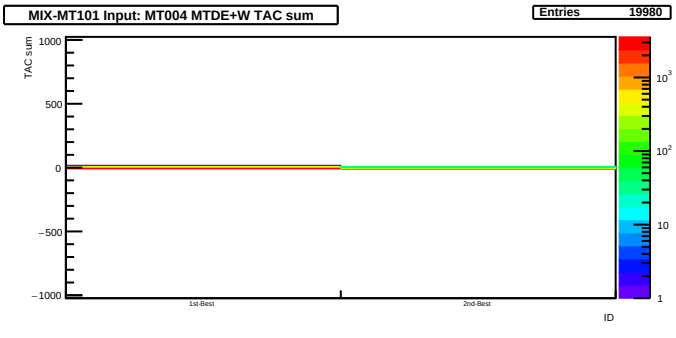

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 2376632
TAC sum
1st-Best
2nd-Best
ID

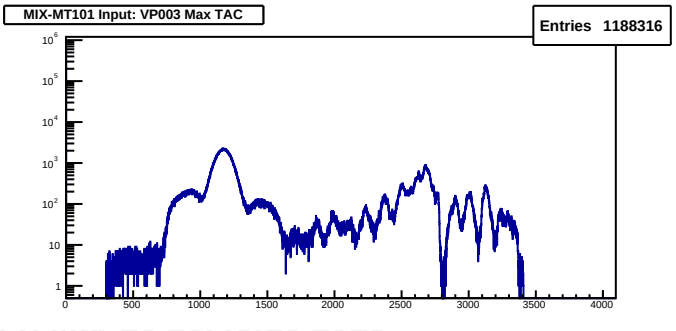

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 38299
TAC sum
1st-Best
2nd-Best
ID

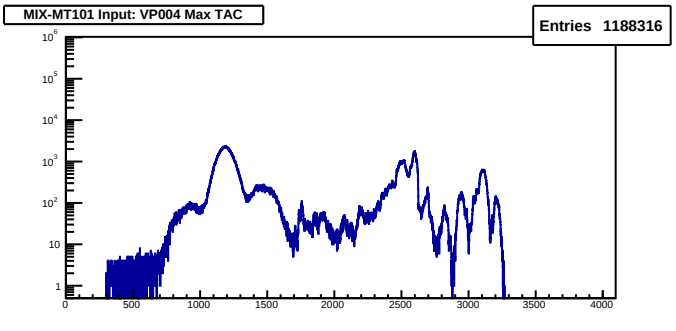

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 2376632
TAC sum
1st-Best
2nd-Best
ID

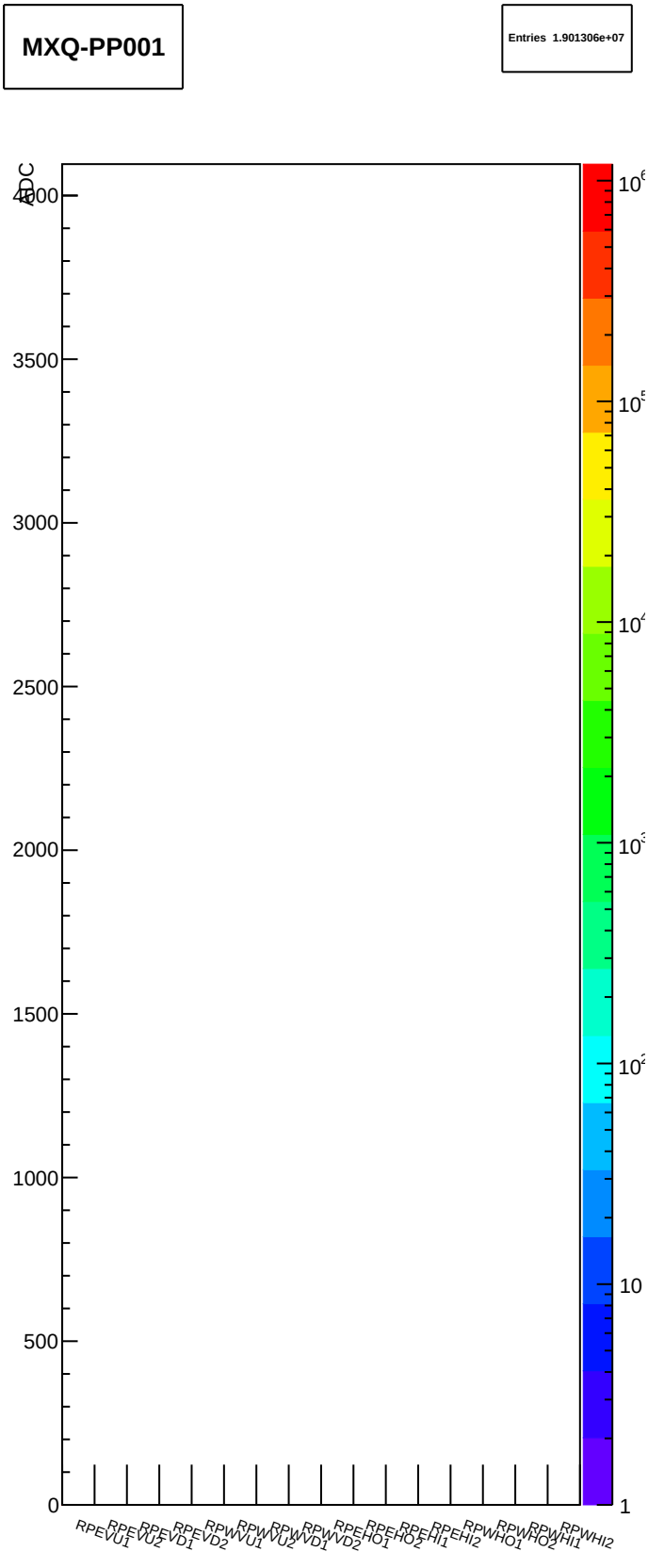

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 23897
TAC sum
1st-Best
2nd-Best
ID

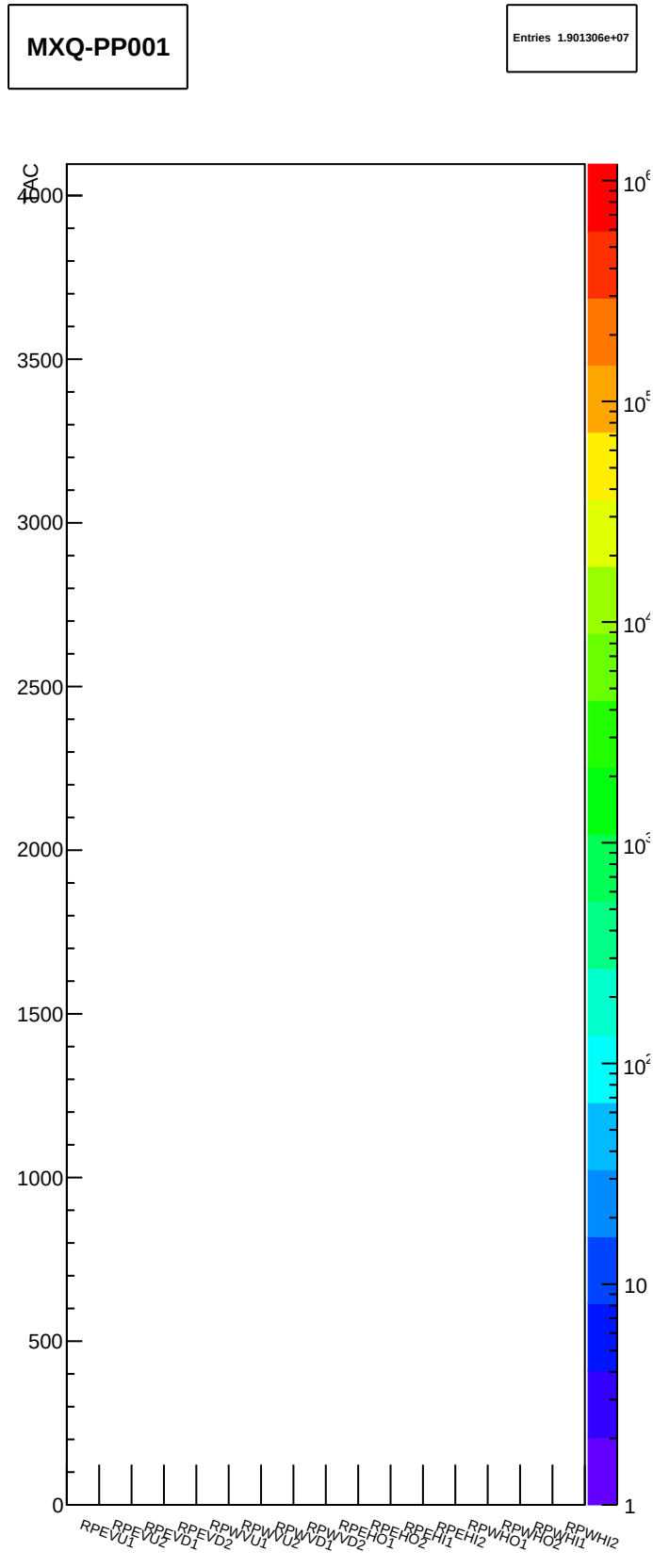

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 2376632
TAC sum
1st-Best
2nd-Best
ID

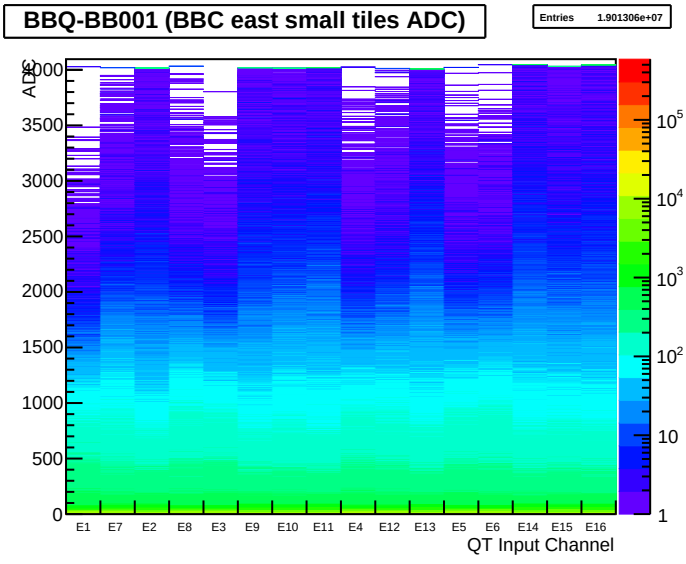

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 19980
TAC sum
1st-Best
2nd-Best
ID

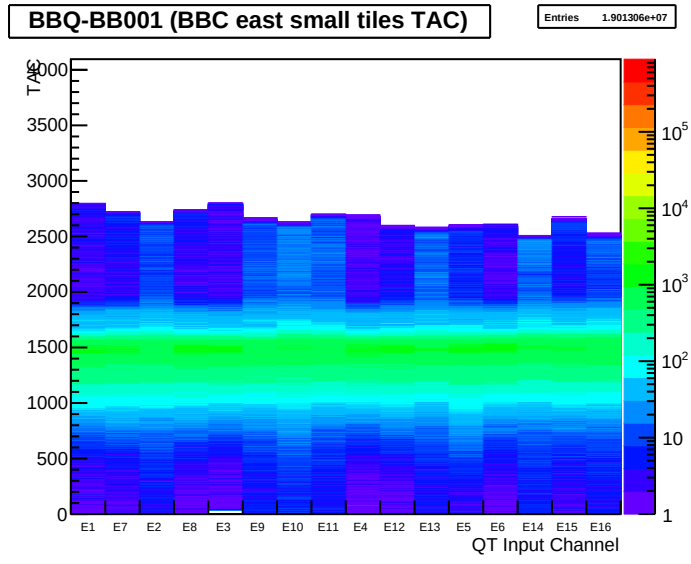

MIX-MT101 Input: VP003 Max TAC
Entries 1188316

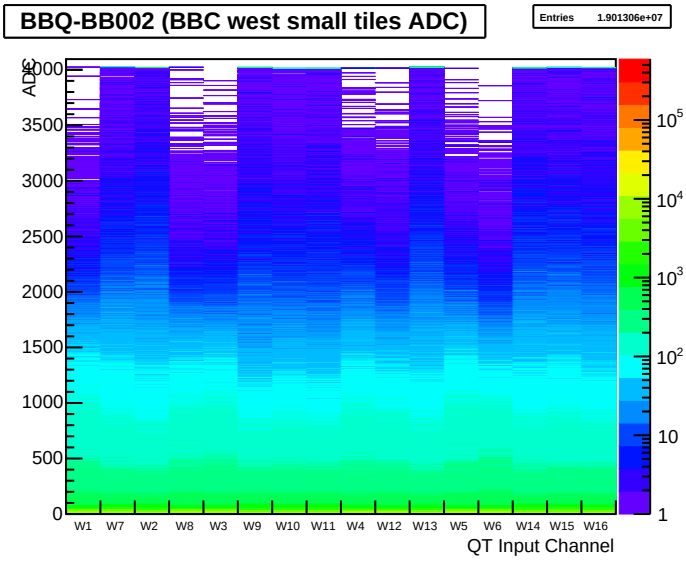

MIX-MT101 Input: VP004 Max TAC
Entries 1188316

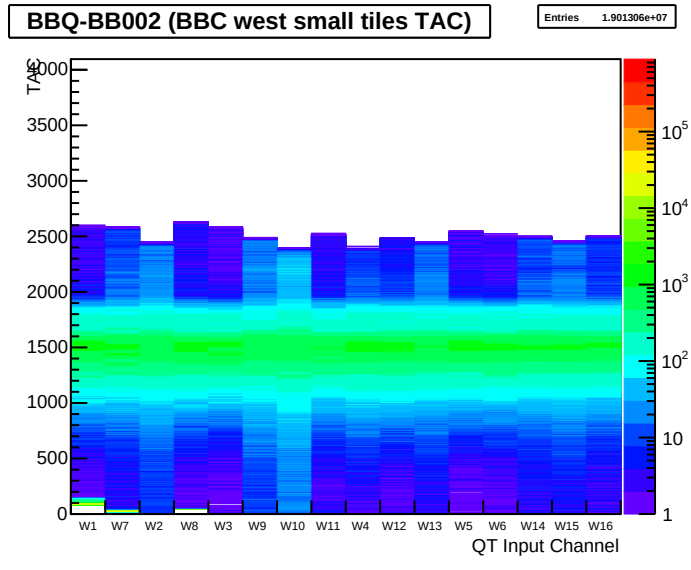

MXQ-PP001
Entries 1.901306e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

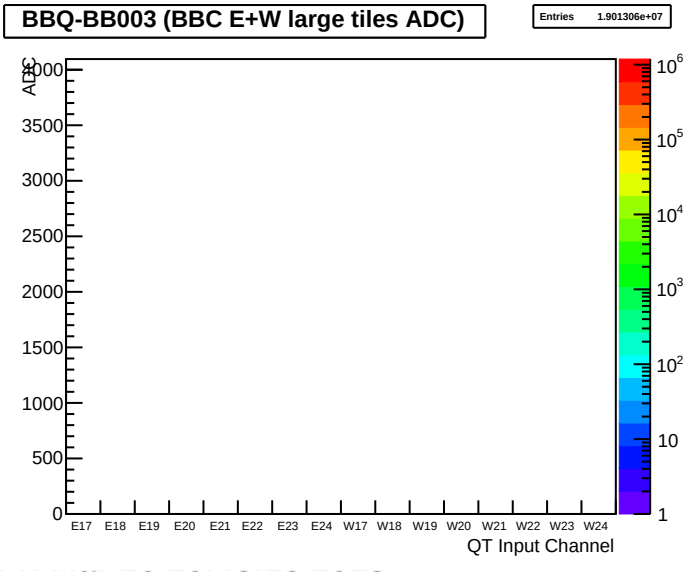

MXQ-PP001
Entries 1.901306e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

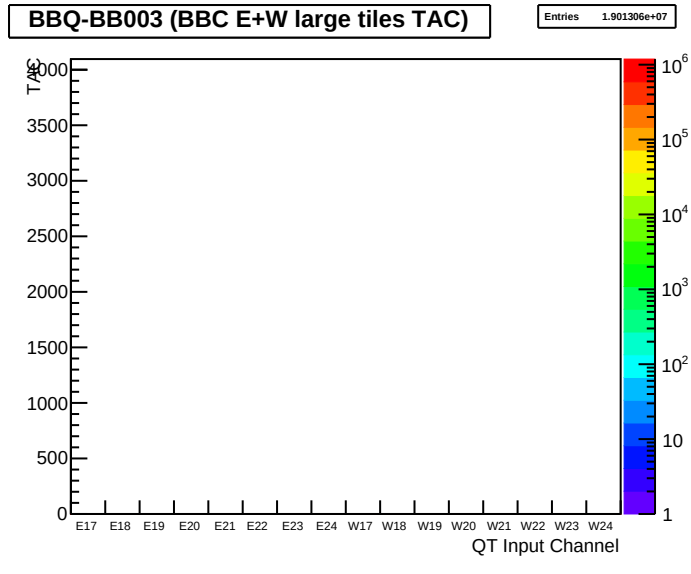

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.901306e+07
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

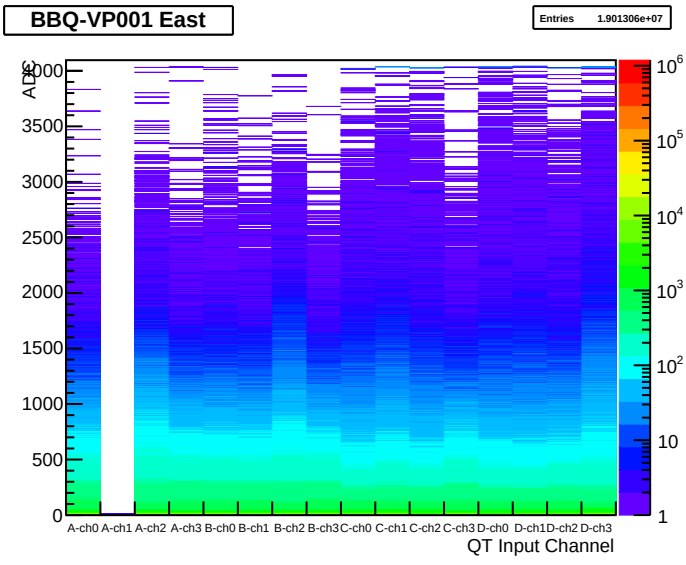

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.901306e+07
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

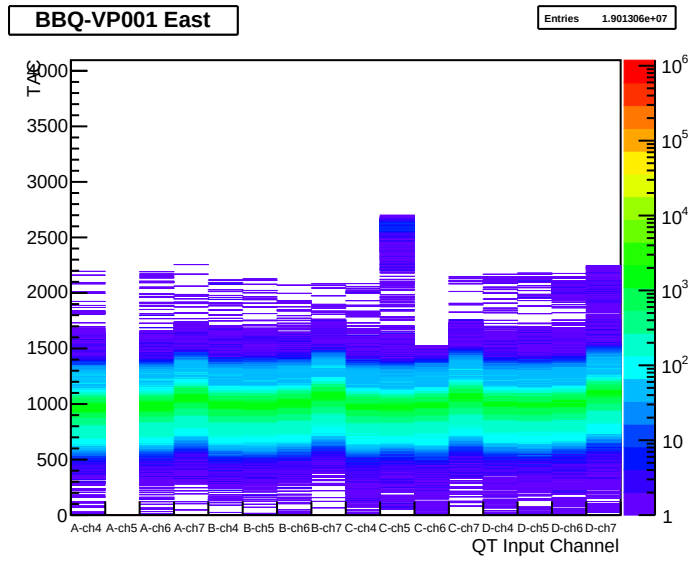

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.901306e+07
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

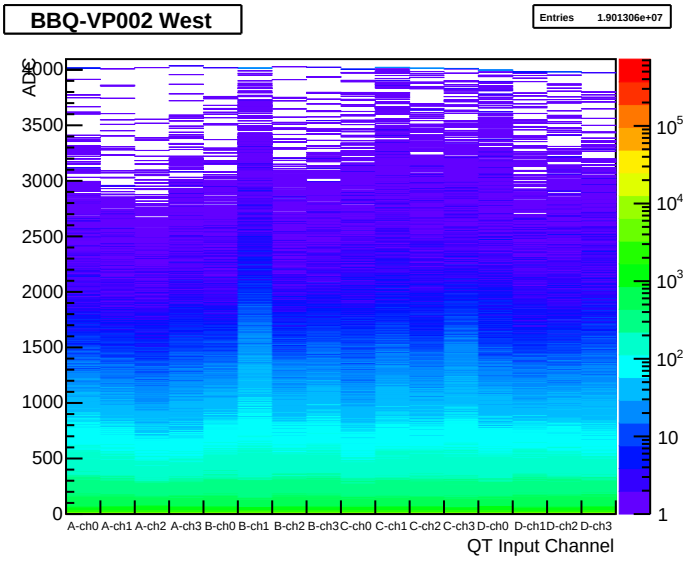

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.901306e+07
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

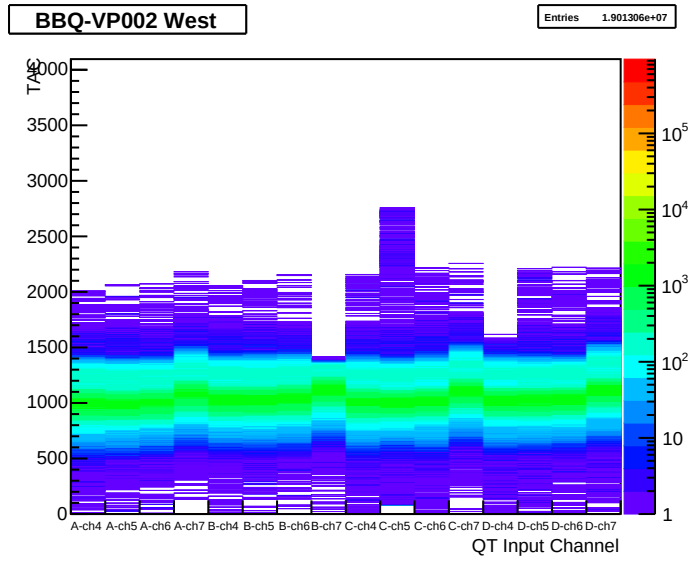

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.901306e+07
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

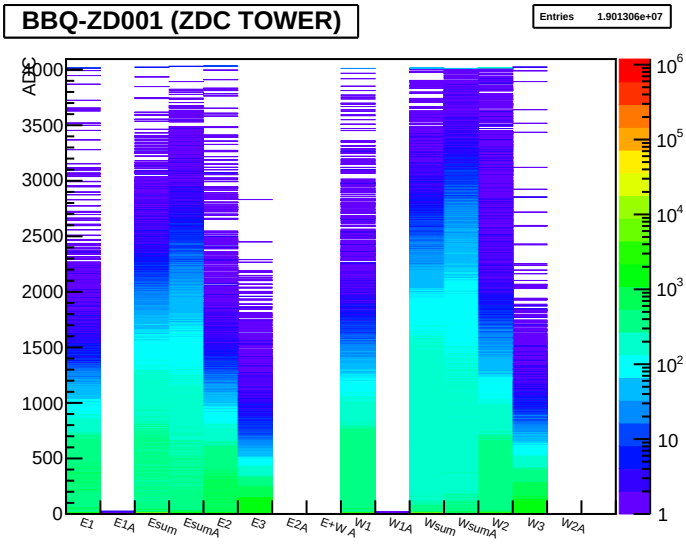

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.901306e+07
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

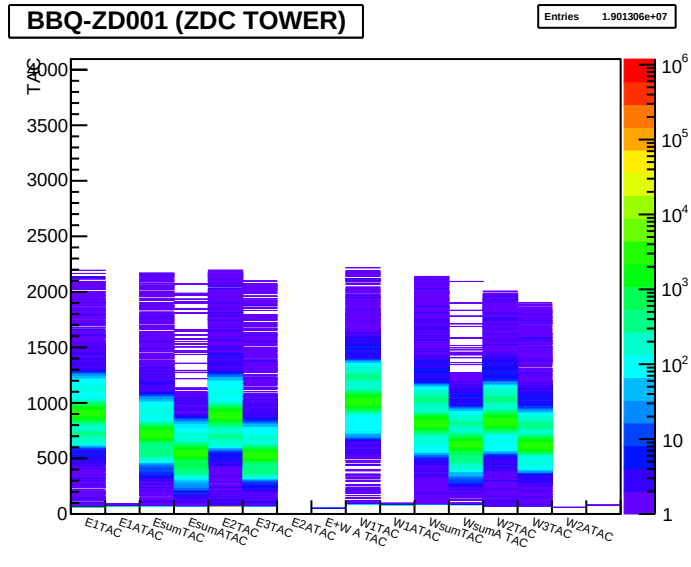

BBQ-VP001 East
Entries 1.901306e+07
ADC
QT Input Channel

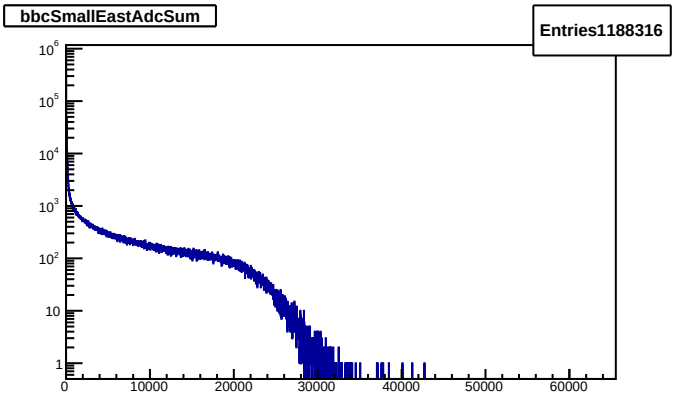

BBQ-VP001 East
Entries 1.901306e+07
TAC
QT Input Channel

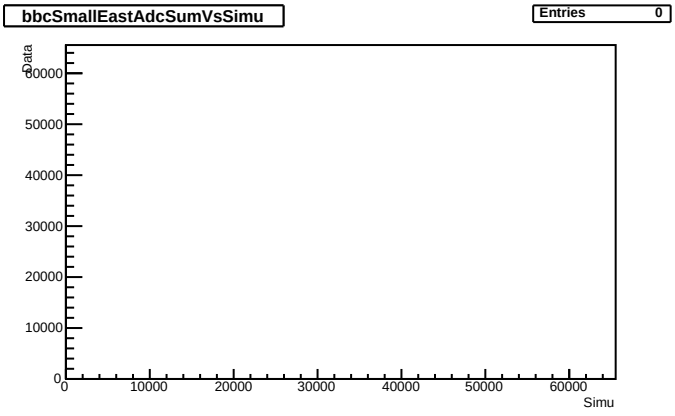

BBQ-VP002 West
Entries 1.901306e+07
ADC
QT Input Channel

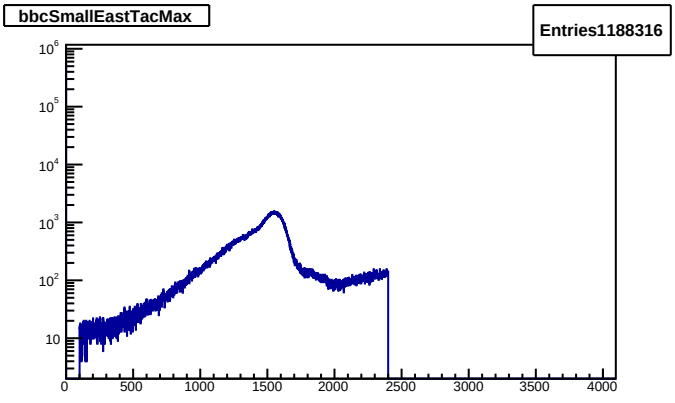

BBQ-VP002 West
Entries 1.901306e+07
TAC
QT Input Channel

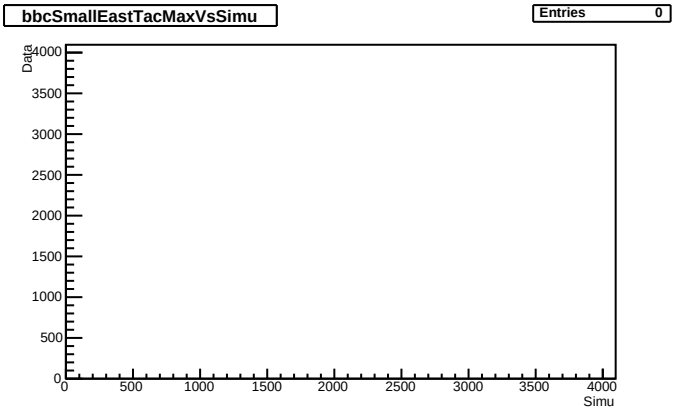

BBQ-ZD001 (ZDC TOWER)
Entries 1.901306e+07
ADC

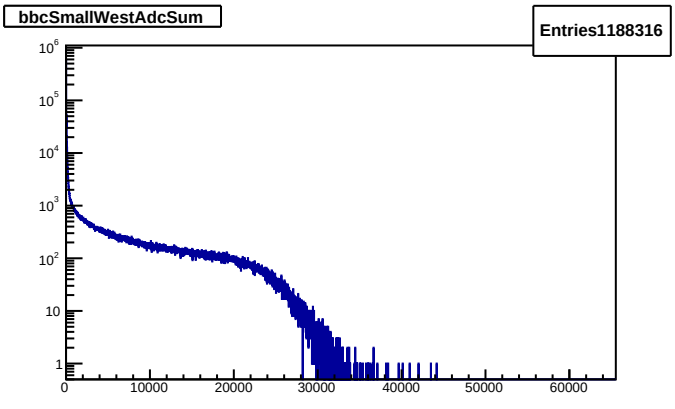

BBQ-ZD001 (ZDC TOWER)
Entries 1.901306e+07
TAC

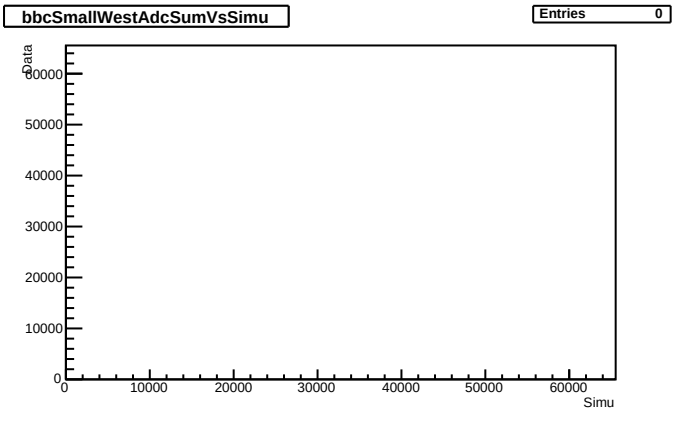

bbcSmallEastAdcSum
Entries1188316

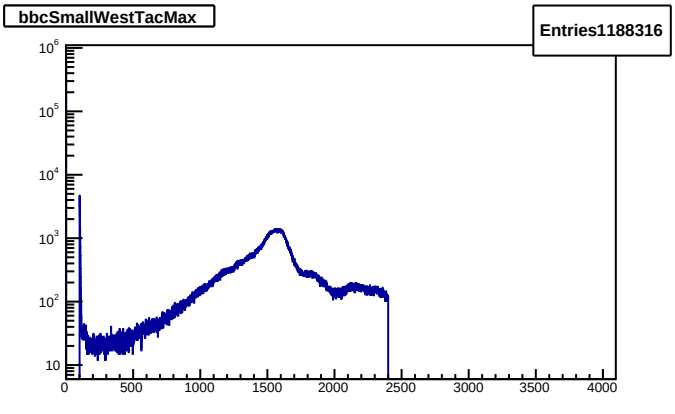

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

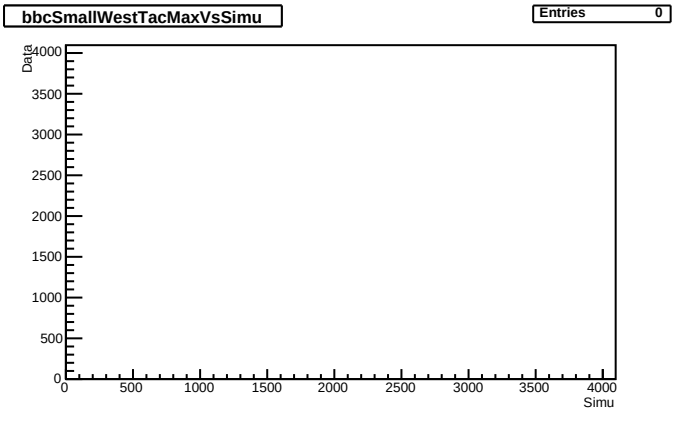

bbcSmallEastTacMax
Entries1188316

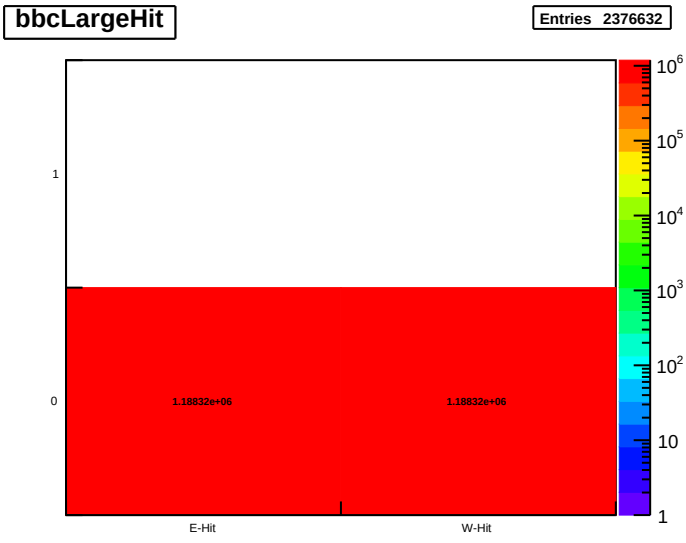

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

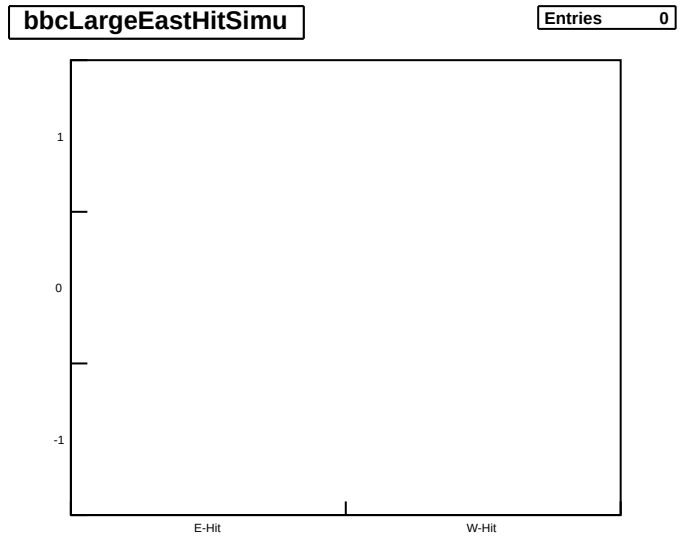

bbcSmallWestAdcSum
Entries1188316

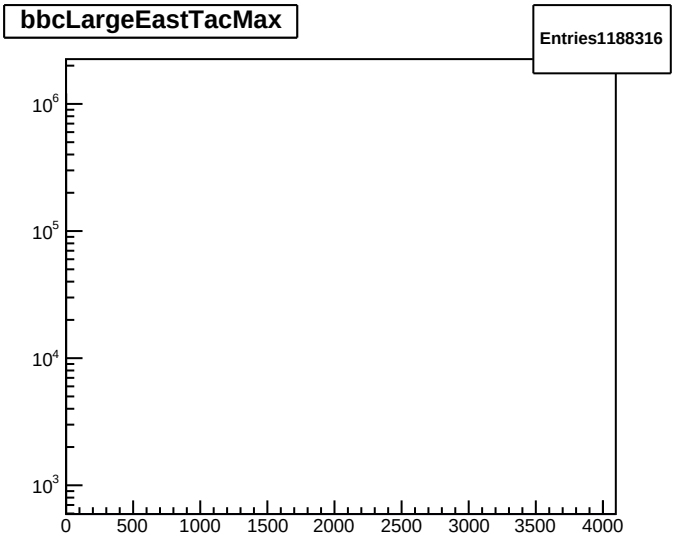

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

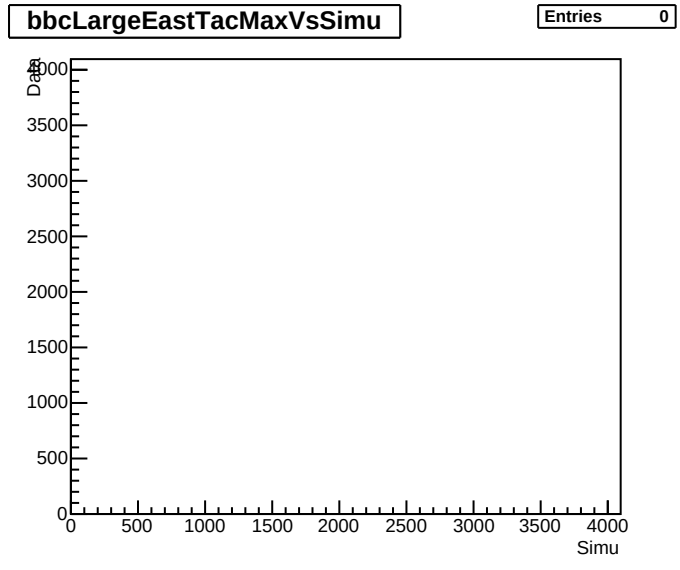

bbcSmallWestTacMax
Entries1188316

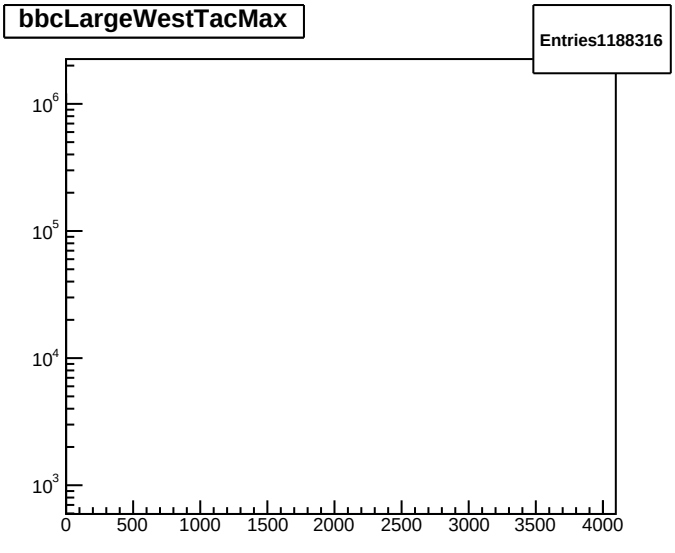

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

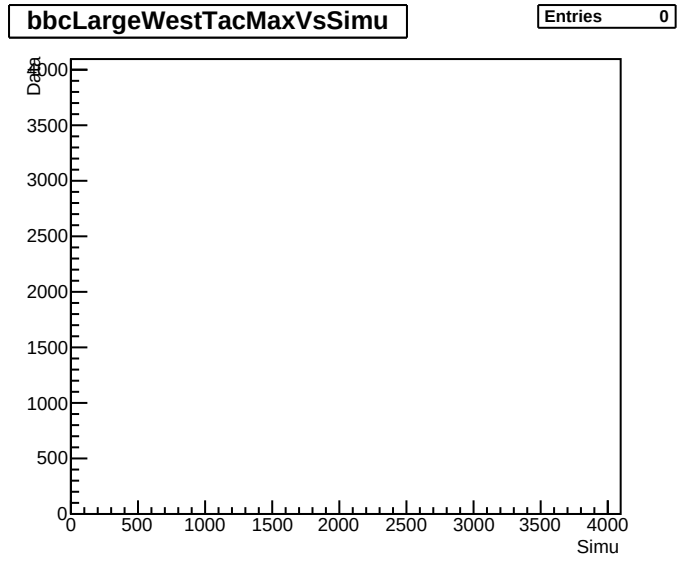

bbcLargeHit
Entries 2376632
1.18832e+06
1.18832e+06
E-Hit
W-Hit

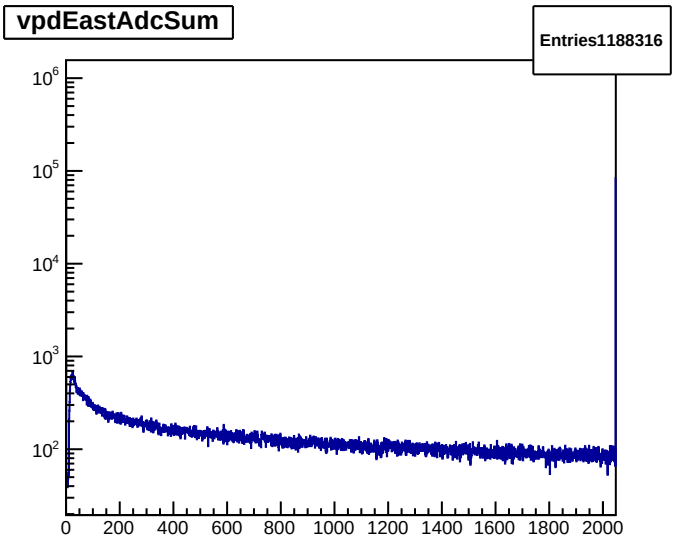

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

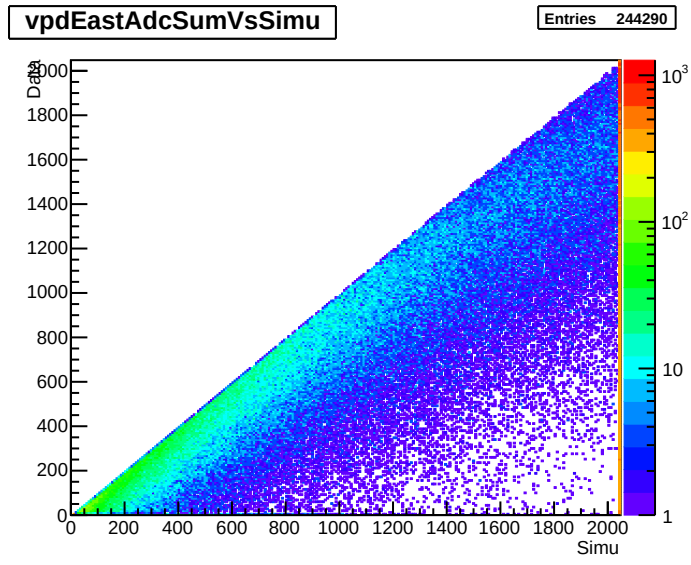

bbcLargeEastTacMax
Entries1188316

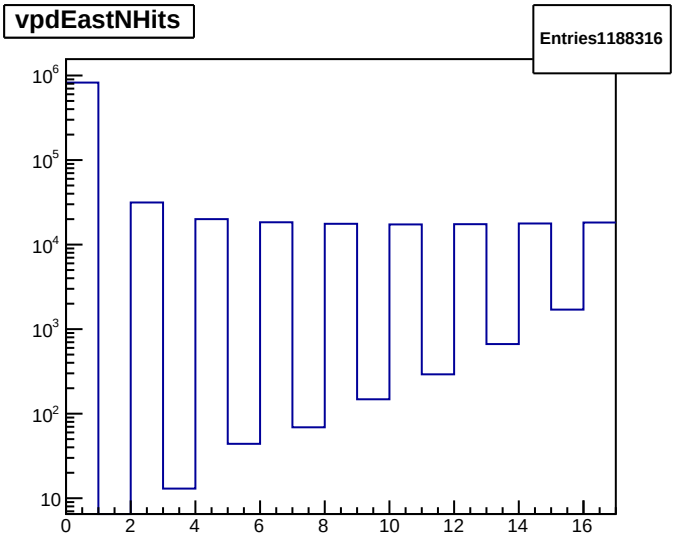

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

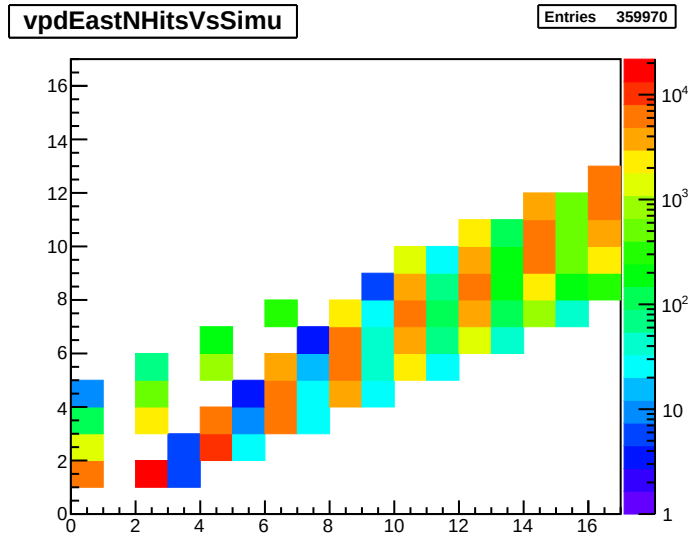

bbcLargeWestTacMax
Entries1188316

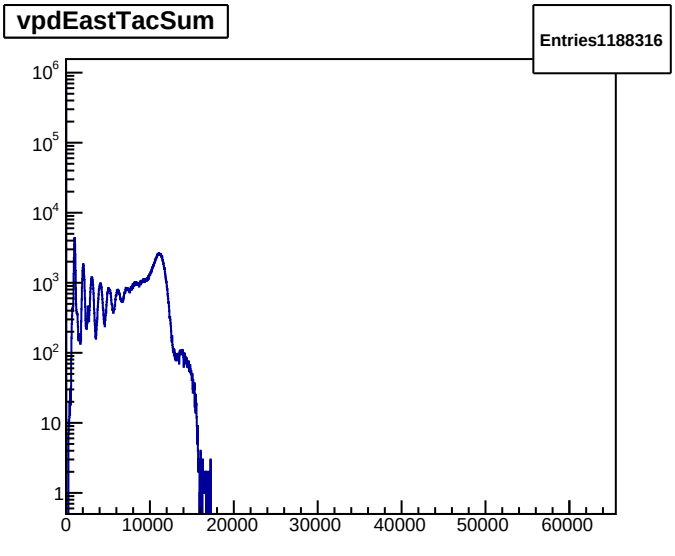

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

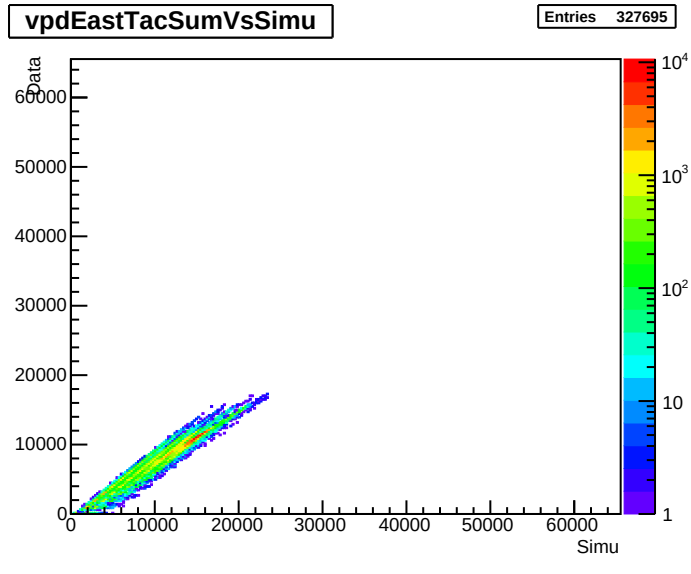

vpdEastAdcSum
Entries1188316

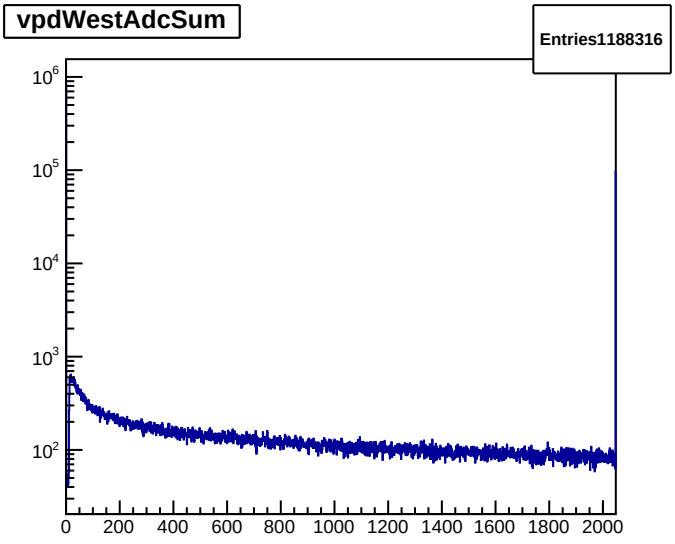

vpdEastAdcSumVsSimu
Entries 244290
Data
Simu

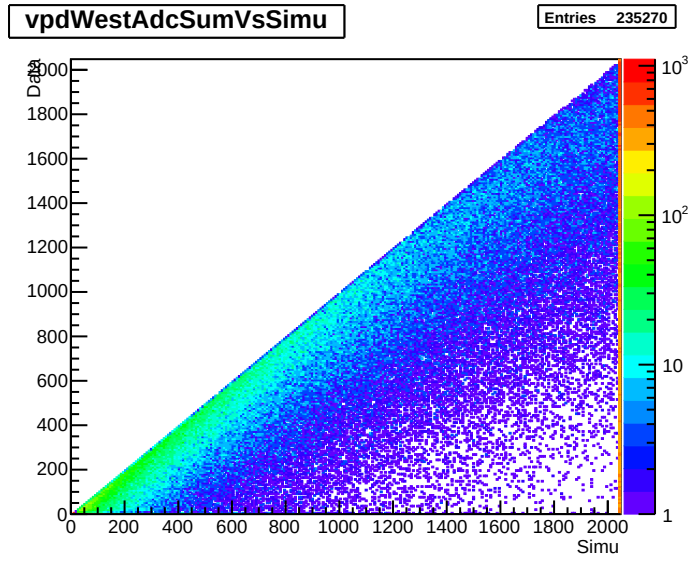

vpdEastNHits
Entries1188316

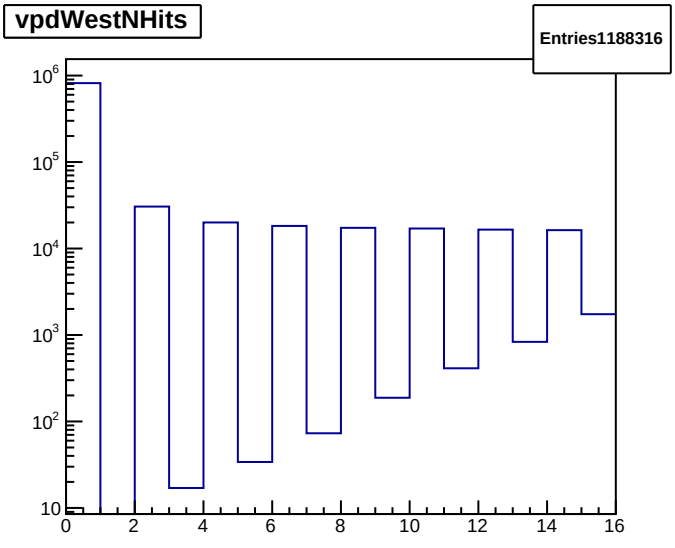

vpdEastNHitsVsSimu
Entries 359970

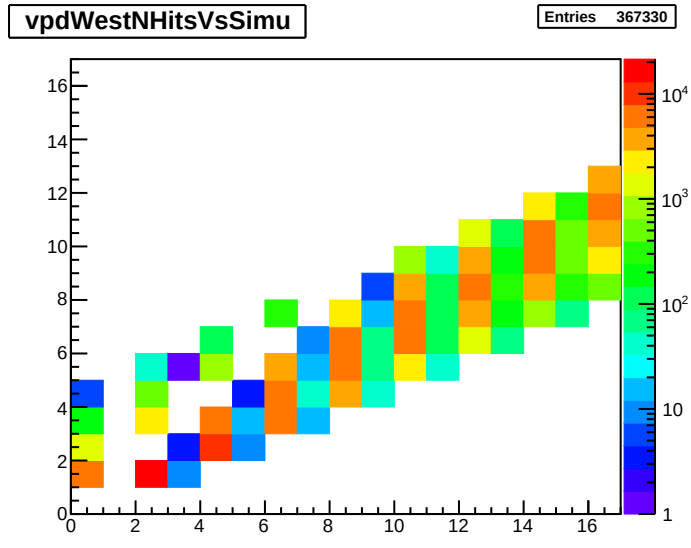

vpdEastTacSum
Entries1188316

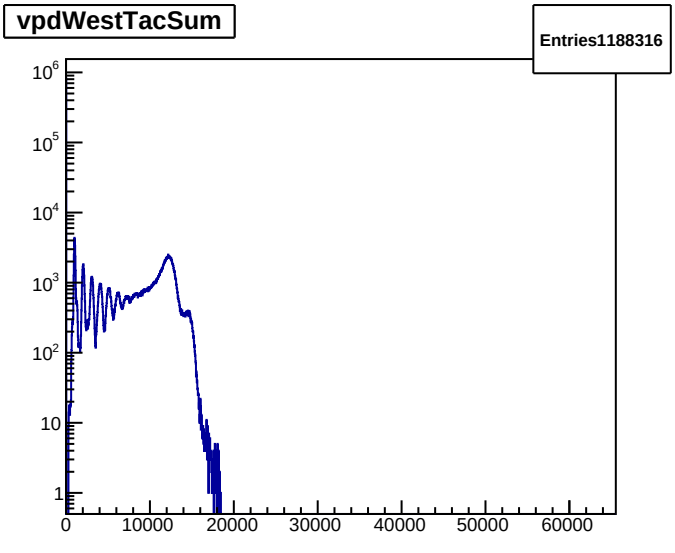

vpdEastTacSumVsSimu
Entries 327695
Data
Simu

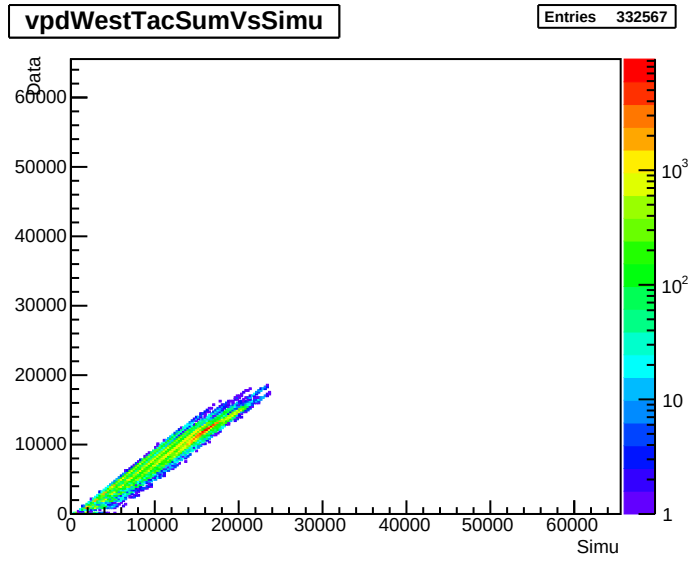

vpdWestAdcSum
Entries1188316

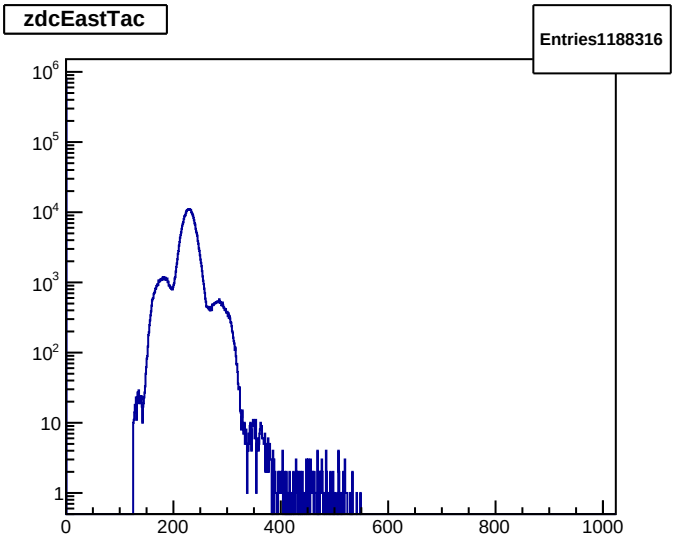

vpdWestAdcSumVsSimu
Entries 235270
Data
Simu

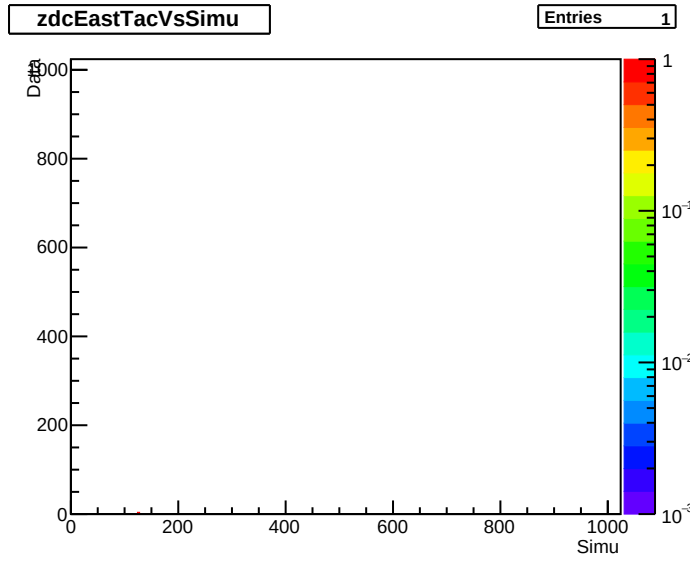

vpdWestNHits
Entries1188316

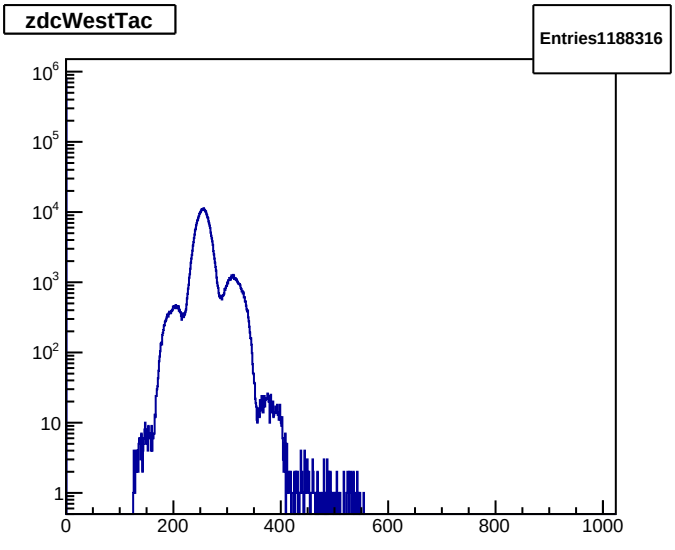

vpdWestNHitsVsSimu
Entries 367330

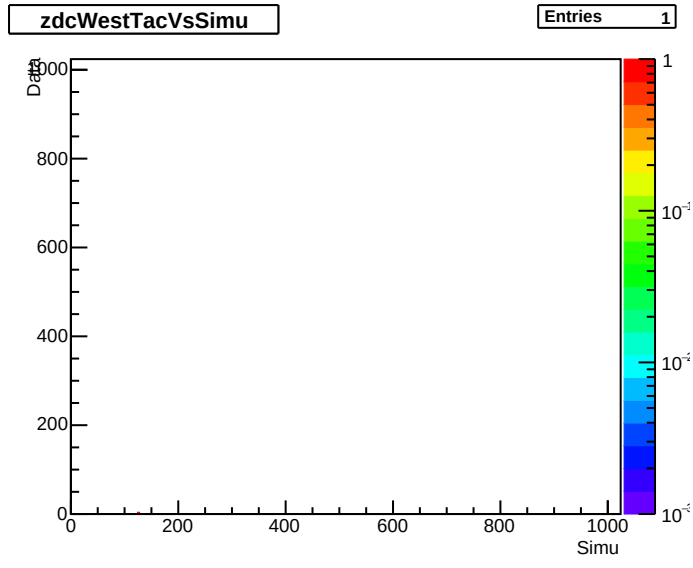

vpdWestTacSum
Entries1188316

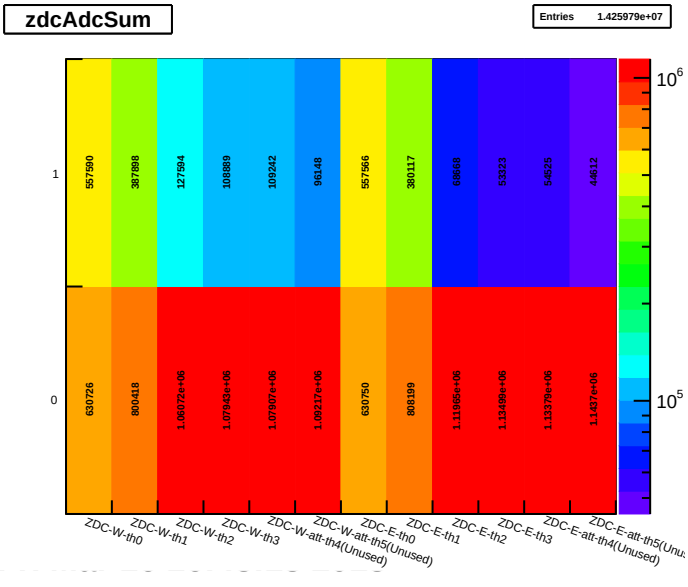

vpdWestTacSumVsSimu
Entries 332567
Data
Simu

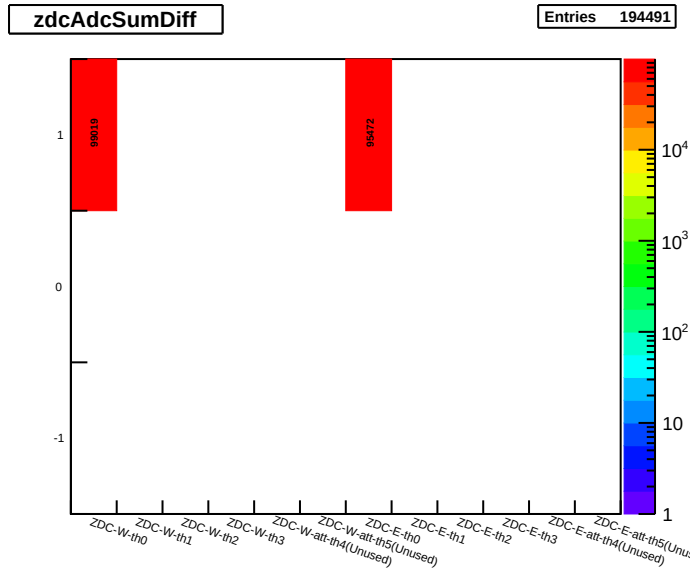

zdcEastTac
Entries1188316

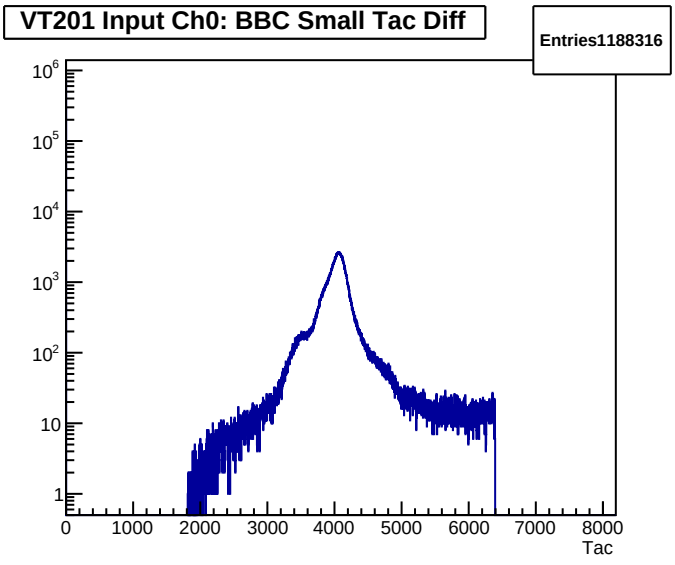

zdcEastTacVsSimu
Entries 1
Data
Simu

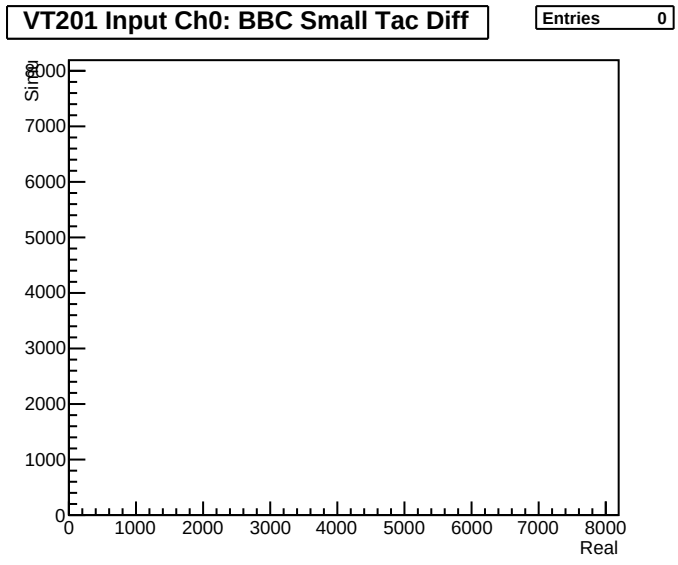

zdcWestTac
Entries1188316

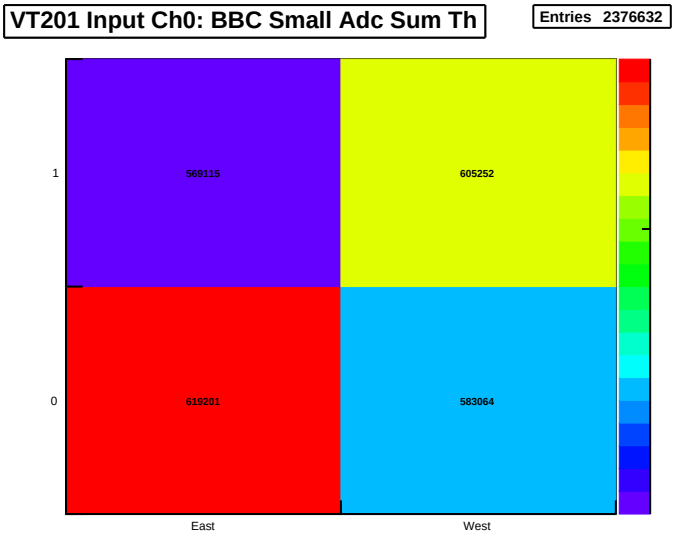

zdcWestTacVsSimu
Entries 1
Data
Simu

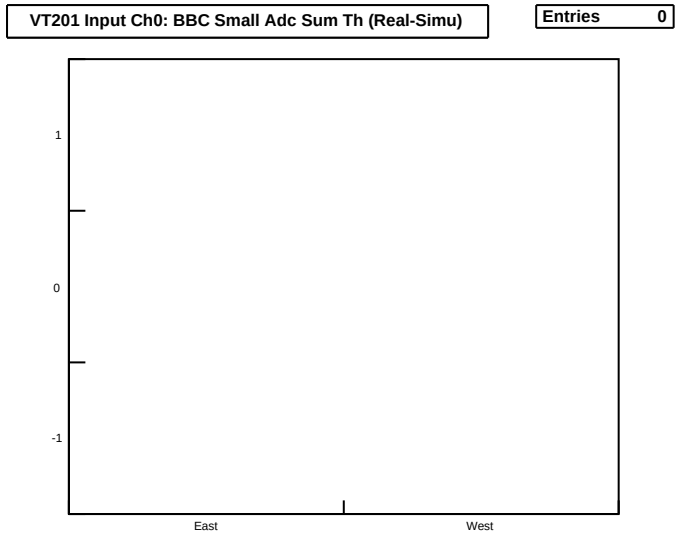

zdcAdcSum
Entries 1.425979e+07
557590
387898
127594
108889
109242
96148
557566
380117
68668
53323
54525
44612
630726
800418
1.06072e+06
1.07943e+06
1.07907e+06
1.08217e+06
630750
808199
1.11965e+06
1.13499e+06
1.13379e+06
1.1437e+06
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

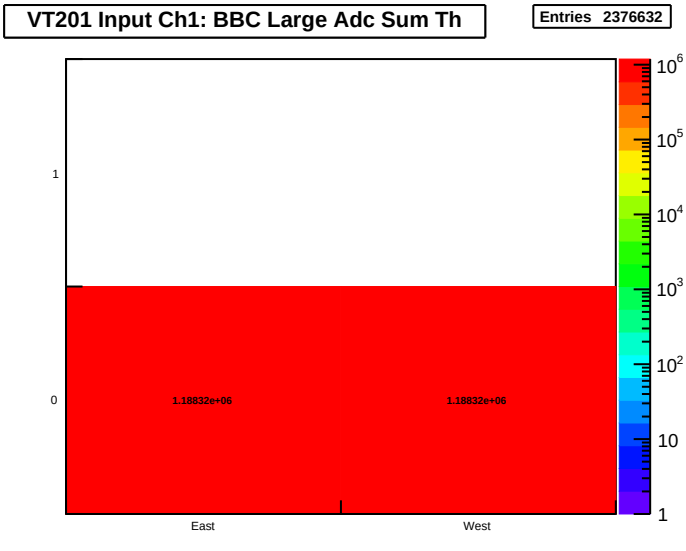

zdcAdcSumDiff
Entries 194491
99019
95472
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

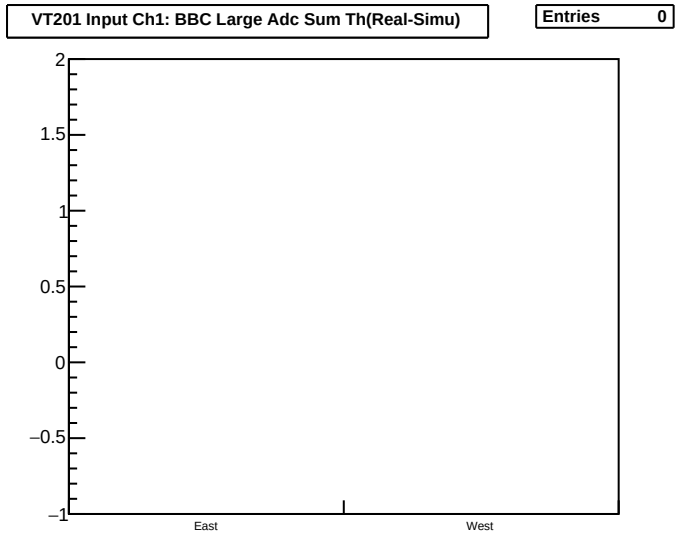

VT201 Input Ch0: BBC Small Tac Diff
Entries1188316
Tac

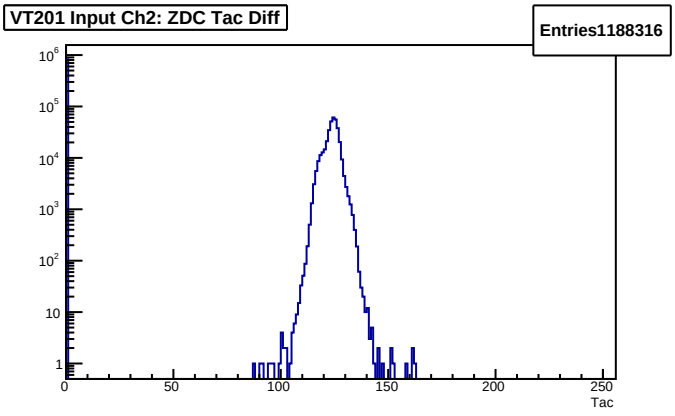

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

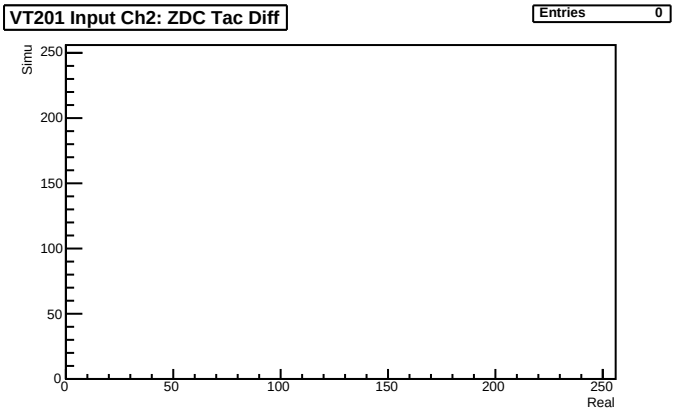

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 2376632
569115
605252
619201
583064
East
West

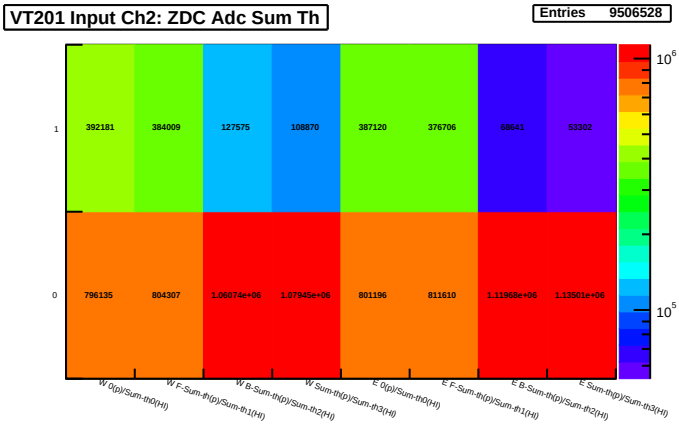

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

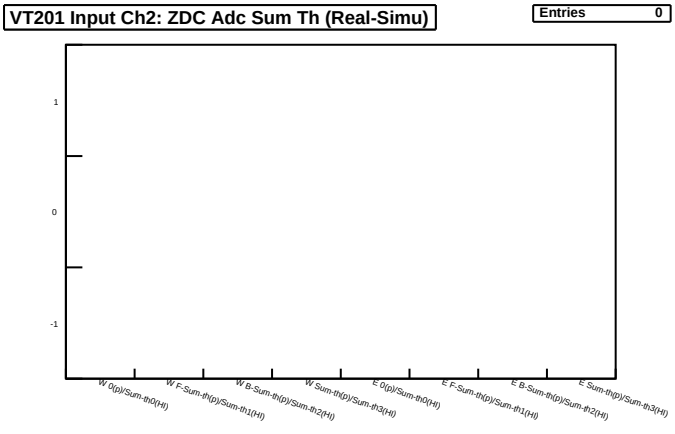

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 2376632
1.18832e+06
1.18832e+06
East
West

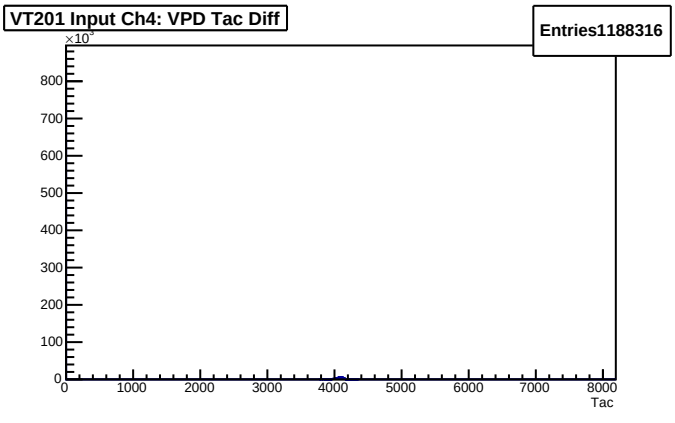

VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

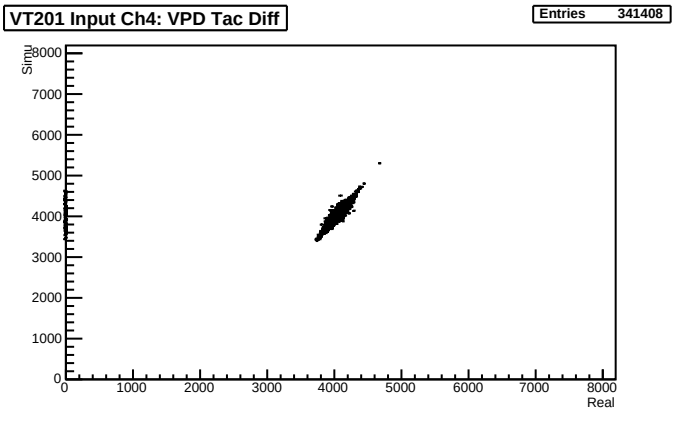

VT201 Input Ch2: ZDC Tac Diff
Entries1188316
Tac

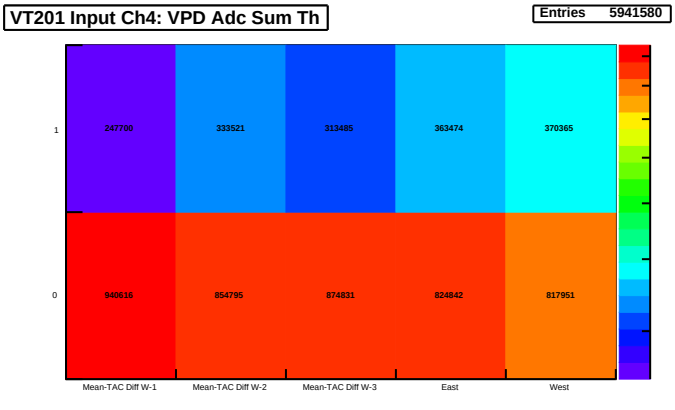

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

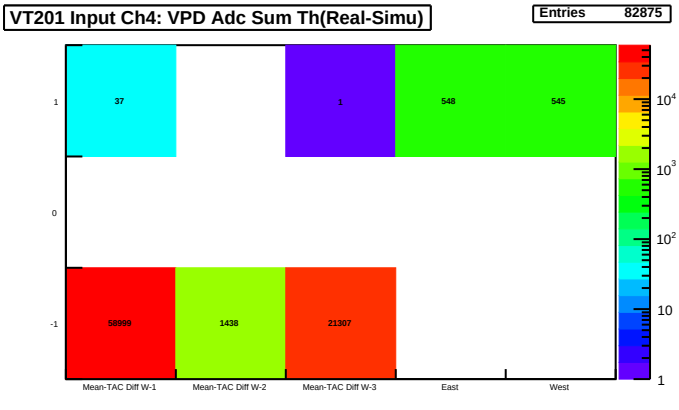

VT201 Input Ch2: ZDC Adc Sum Th
Entries 9506528
392181
384009
127575
108870
387120
376706
68641
53302
796135
804307
1.06074e+06
1.07945e+06
801196
811610
1.11968e+06
1.13501e+06
W 0(p)/Sum-th0(Hi)
W F-Sum-th(p)/Sum-th1(Hi)
W B-Sum-th(p)/Sum-th2(Hi)
W Sum-th(p)/Sum-th3(Hi)
E 0(p)/Sum-th0(Hi)
E F-Sum-th(p)/Sum-th1(Hi)
E B-Sum-th(p)/Sum-th2(Hi)
E Sum-th(p)/Sum-th3(Hi)

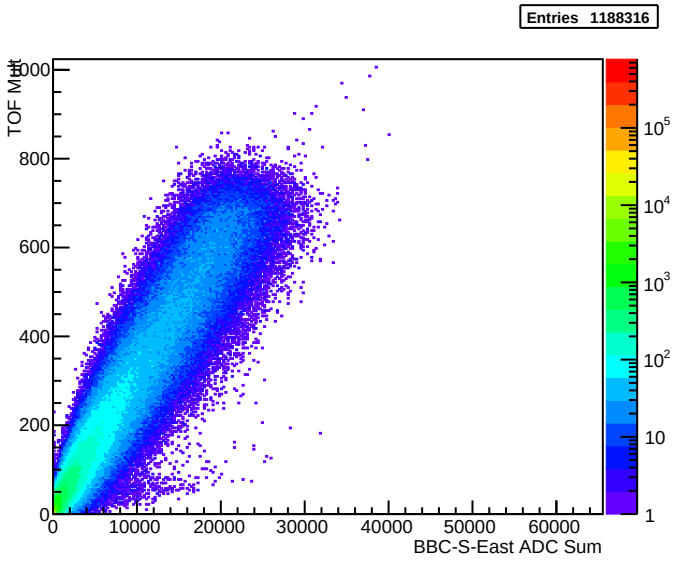

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hi)
W F-Sum-th(p)/Sum-th1(Hi)
W B-Sum-th(p)/Sum-th2(Hi)
W Sum-th(p)/Sum-th3(Hi)
E 0(p)/Sum-th0(Hi)
E F-Sum-th(p)/Sum-th1(Hi)
E B-Sum-th(p)/Sum-th2(Hi)
E Sum-th(p)/Sum-th3(Hi)

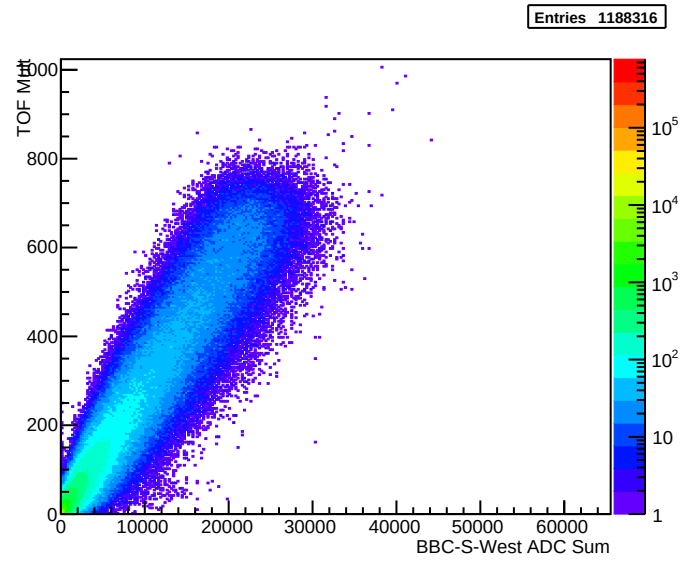

VT201 Input Ch4: VPD Tac Diff
Entries1188316
Tac

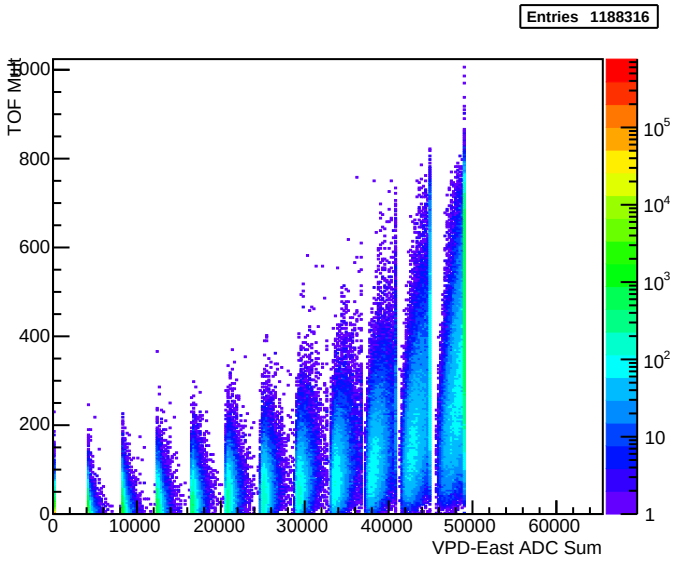

VT201 Input Ch4: VPD Tac Diff
Entries 341408
Simu
Real

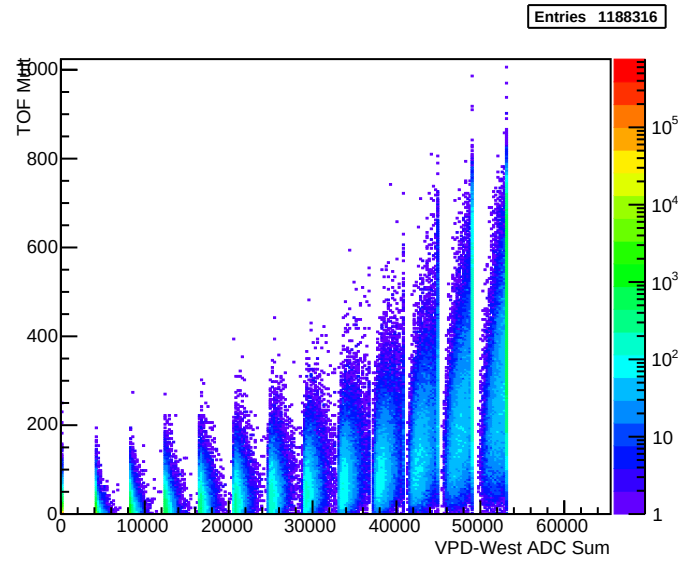

VT201 Input Ch4: VPD Adc Sum Th
Entries 5941580
247700
333521
313485
363474
370365
940616
854795
874831
824842
817951
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

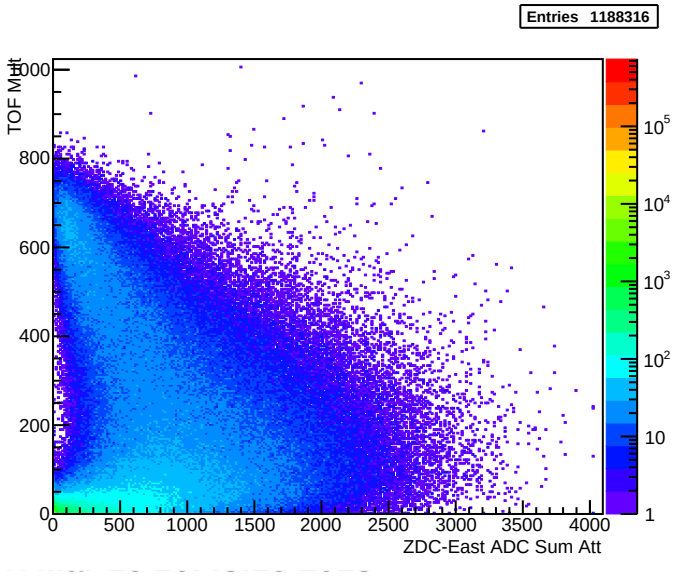

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 82875
37
1
548
545
58999
1438
21307
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

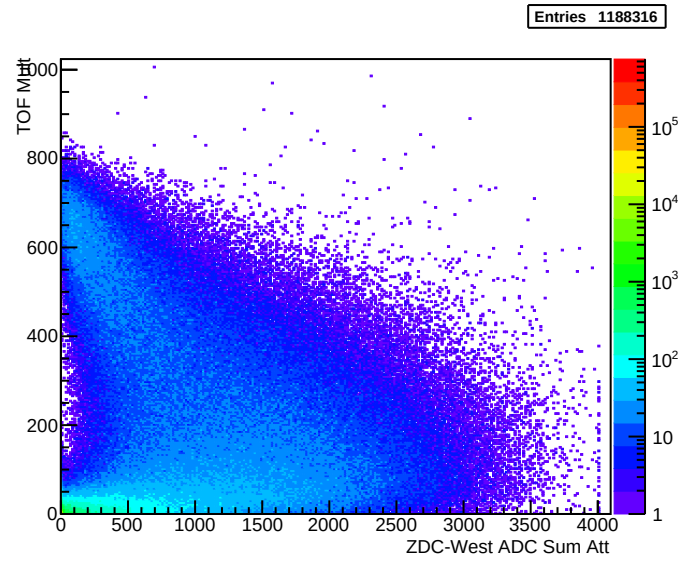

Entries 1188316
TOF Mult
BBC-S-East ADC Sum

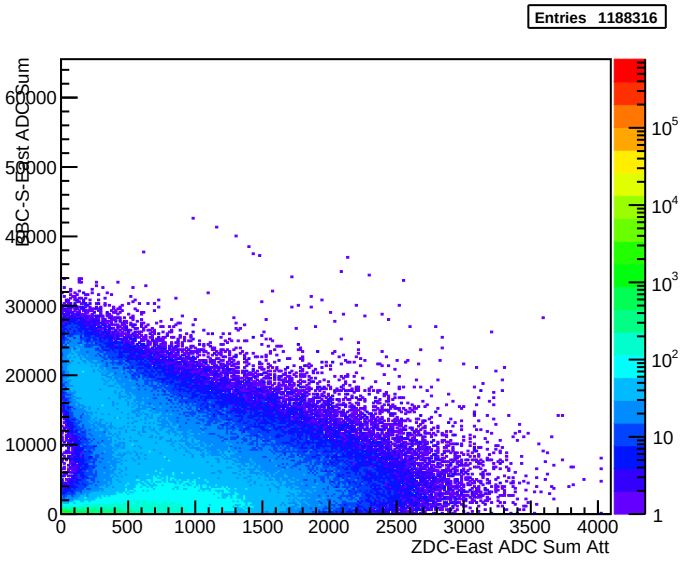

Entries 1188316
TOF Mult
BBC-S-West ADC Sum

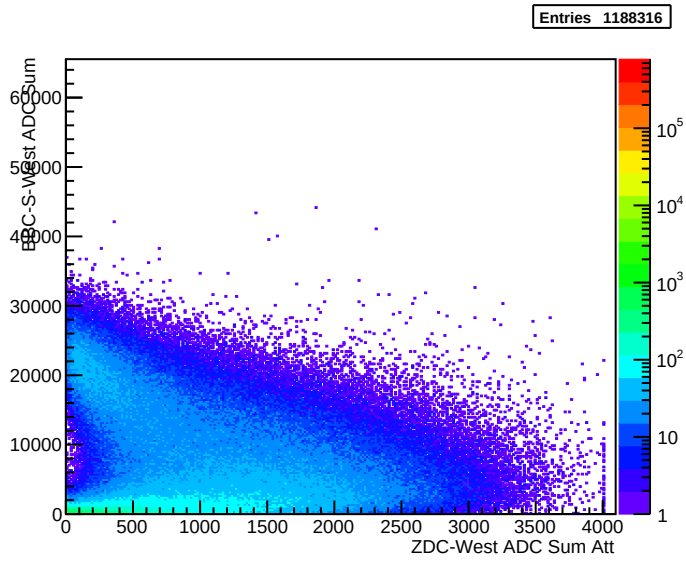

Entries 1188316
TOF Mult
VPD-East ADC Sum

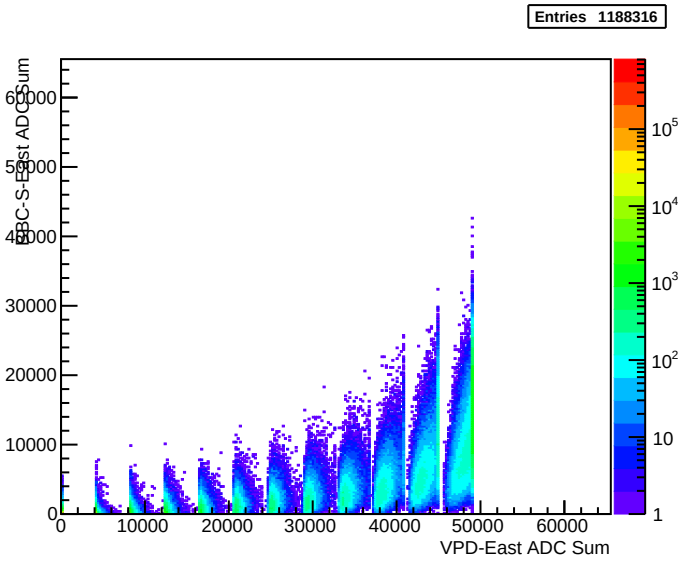

Entries 1188316
TOF Mult
VPD-West ADC Sum

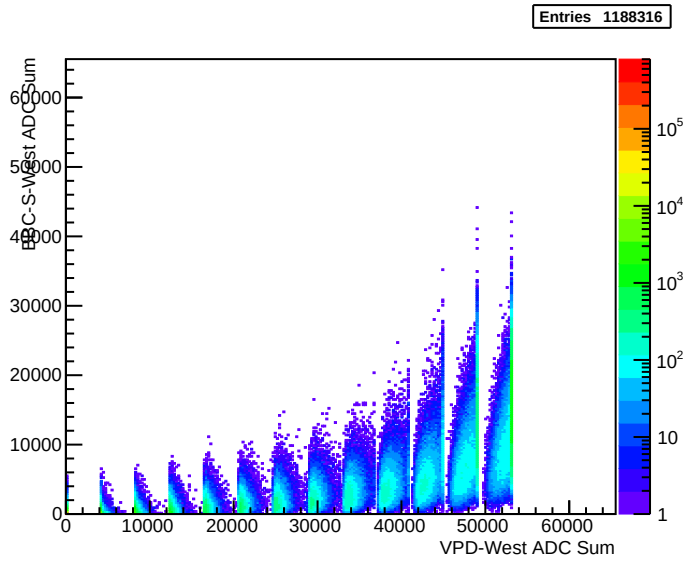

Entries 1188316
TOF Mult
ZDC-East ADC Sum Att

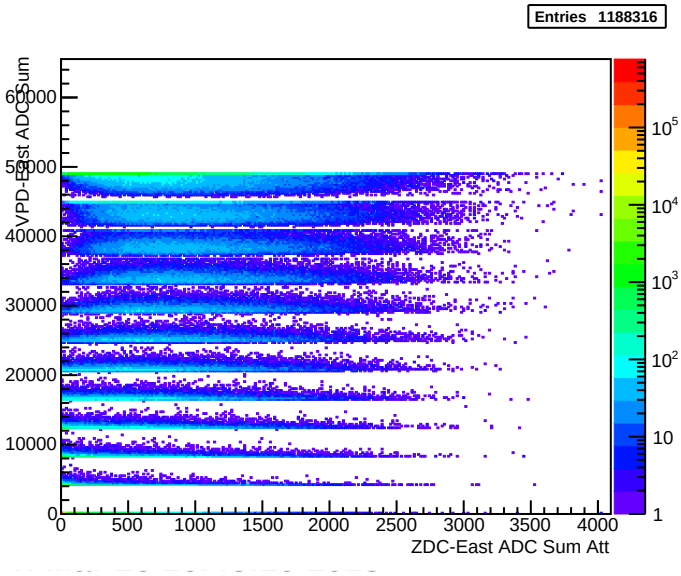

Entries 1188316
TOF Mult
ZDC-West ADC Sum Att

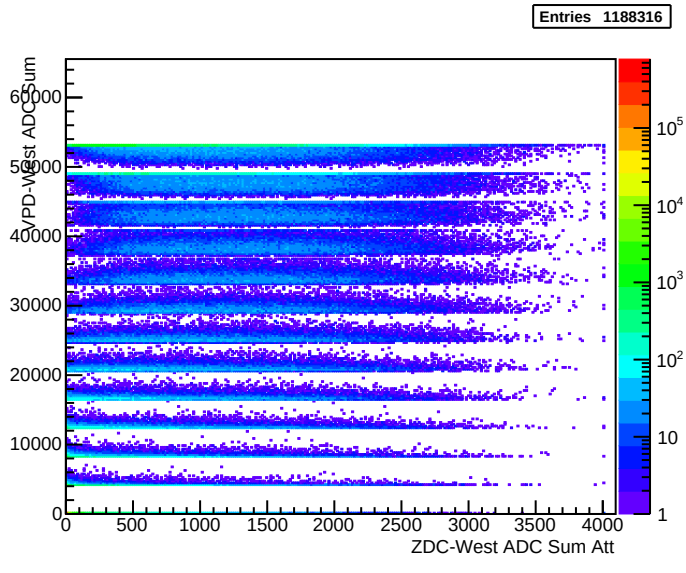

Entries 1188316
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

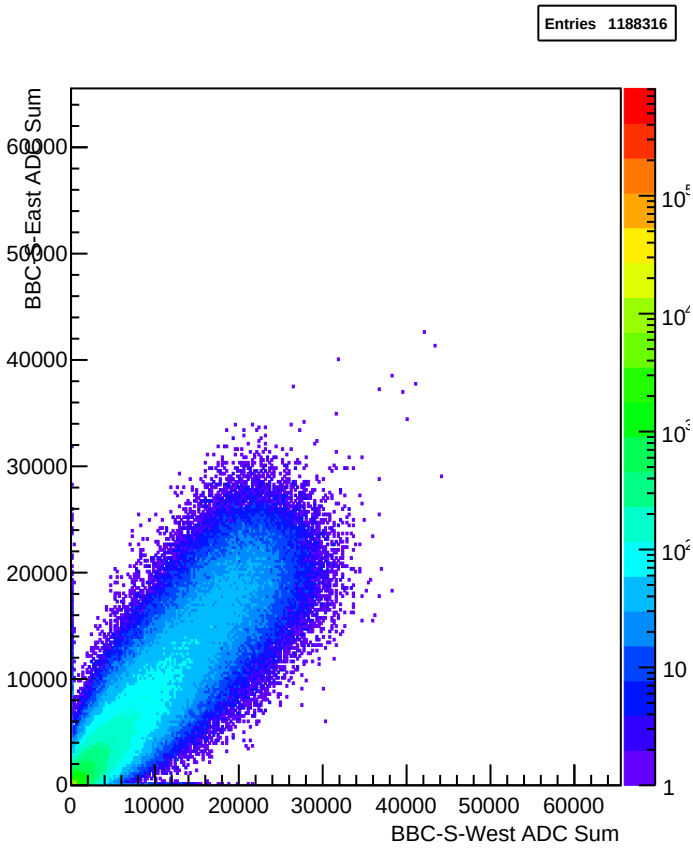

Entries 1188316
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

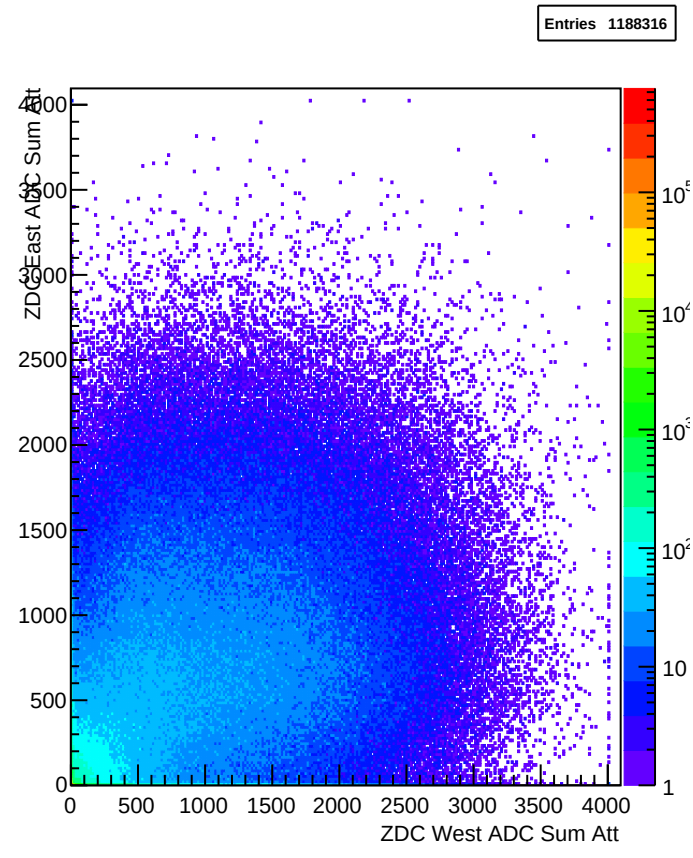

Entries 1188316
BBC-S-East ADC Sum
VPD-East ADC Sum

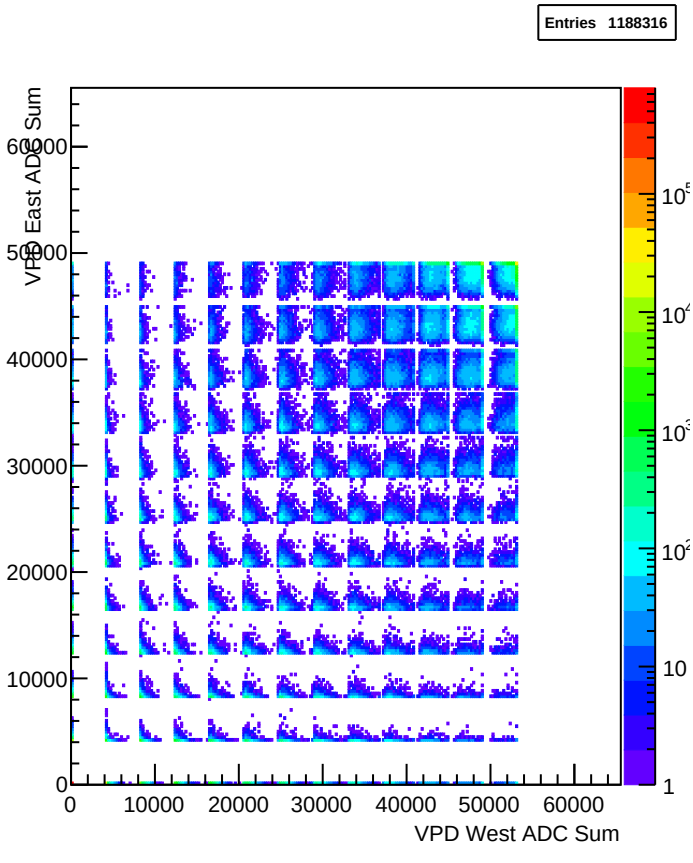

Entries 1188316
BBC-S-West ADC Sum
VPD-West ADC Sum

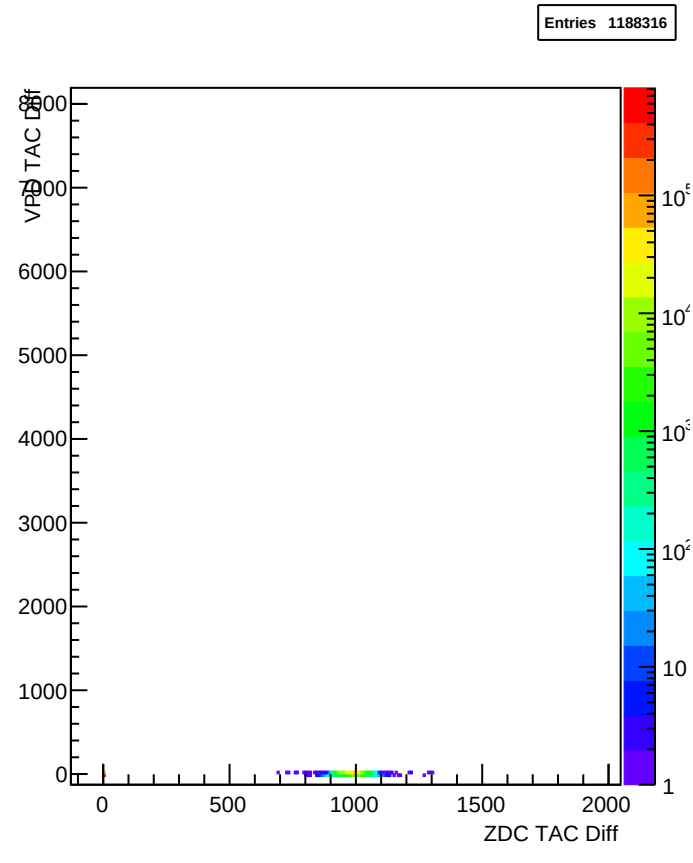

Entries 1188316
VPD-East ADC Sum
ZDC-East ADC Sum Att

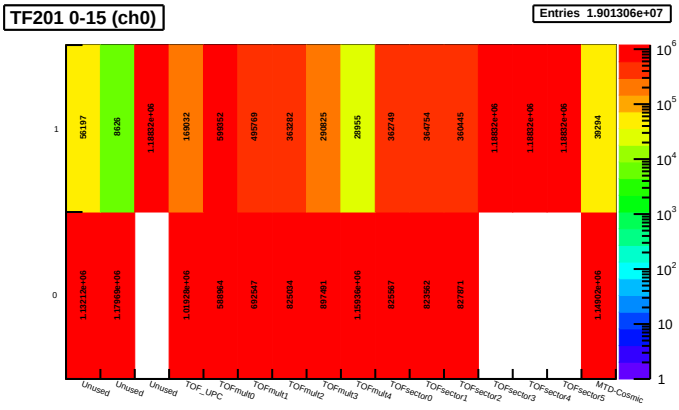

Entries 1188316
VPD-West ADC Sum
ZDC-West ADC Sum Att

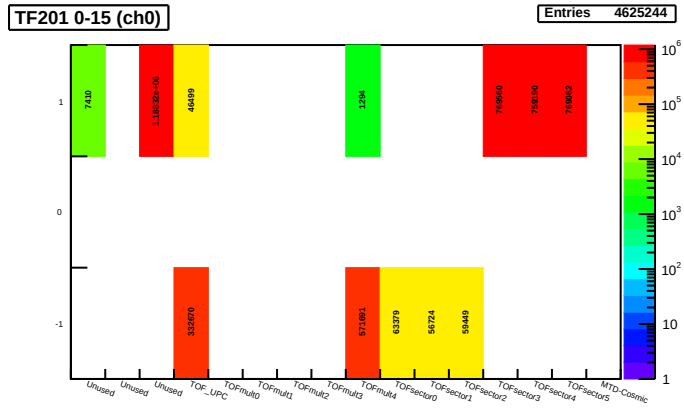

Entries 1188316
BBC-East ADC Sum
BBC-S-West ADC Sum

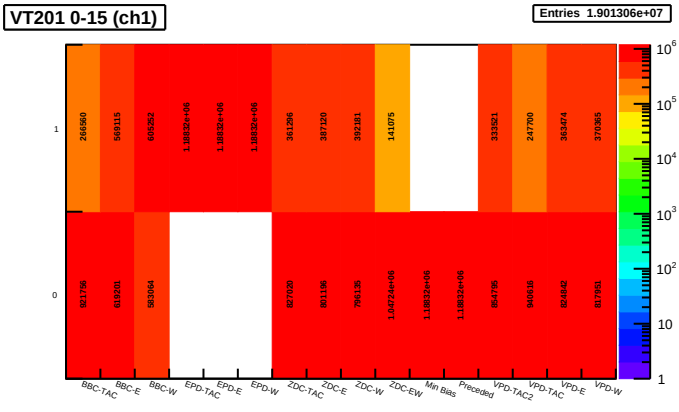

Entries 1188316
ZDC East ADC Sum Att
ZDC West ADC Sum Att

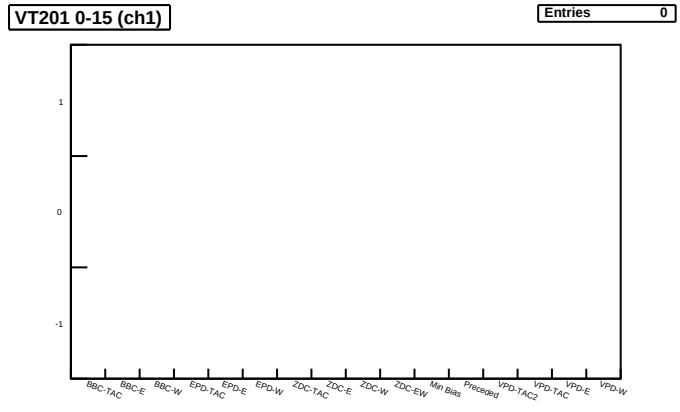

Entries 1188316
VPD East ADC Sum
VPD West ADC Sum

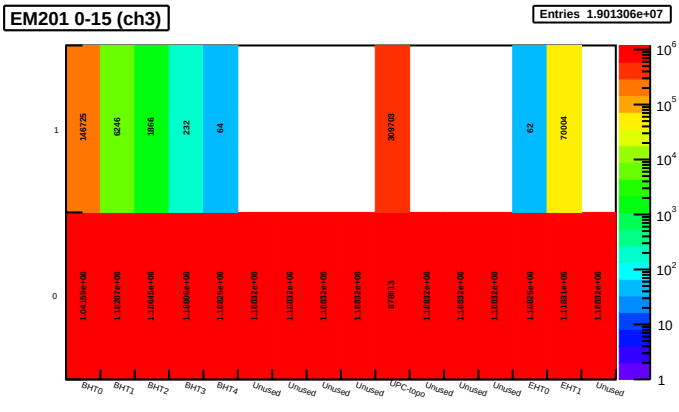

Entries 1188316
VPD TAC Diff
ZDC TAC Diff

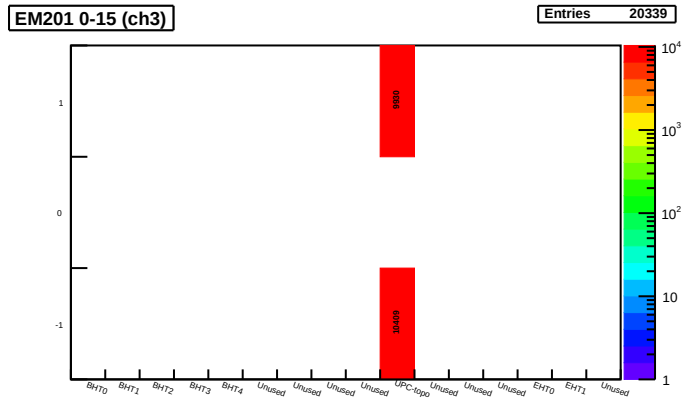

TF201 0-15 (ch0)
Entries 1.901306e+07
Unused
Unused
Unused
TOF_UPC
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5
MTD-Cosmic

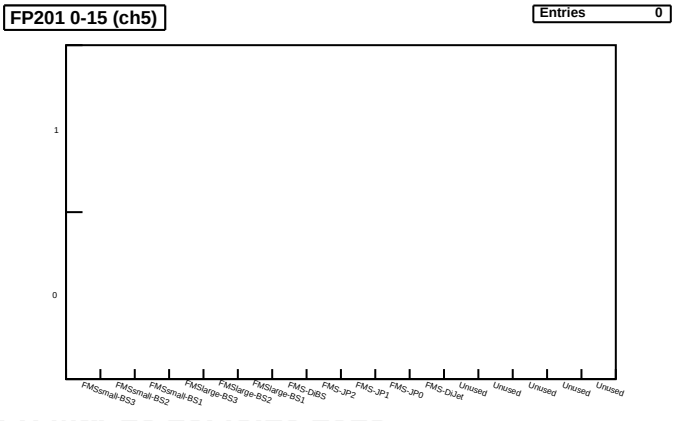

TF201 0-15 (ch0)
Entries 4625244
Unused
Unused
Unused
TOF_UPC
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5
MTD-Cosmic

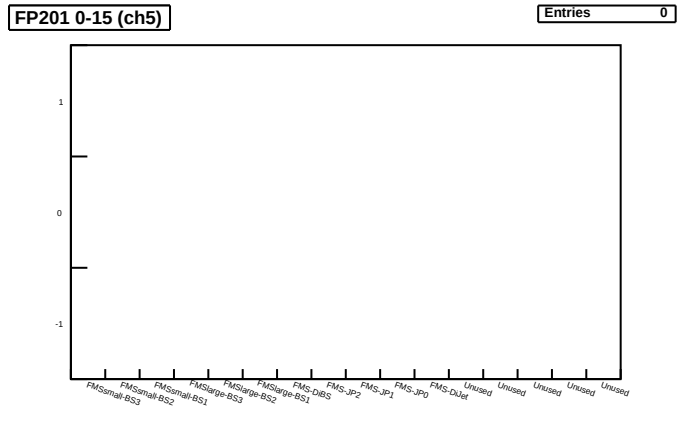

VT201 0-15 (ch1)
Entries 1.901306e+07
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-EW
Min Bias
Preceded
VPD-TAC2
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-EW
Min Bias
Preceded
VPD-TAC2
VPD-TAC
VPD-E
VPD-W


EM201 0-15 (ch3)
Entries 1.901306e+07
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
Unused
Unused
Unused
UPC-topo
Unused
Unused
Unused
EHT0
EHT1
Unused


EM201 0-15 (ch3)
Entries 20339
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
Unused
Unused
Unused
UPC-topo
Unused
Unused
Unused
EHT0
EHT1
Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused
Unused
Unused
Unused
Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused
Unused
Unused
Unused
Unused