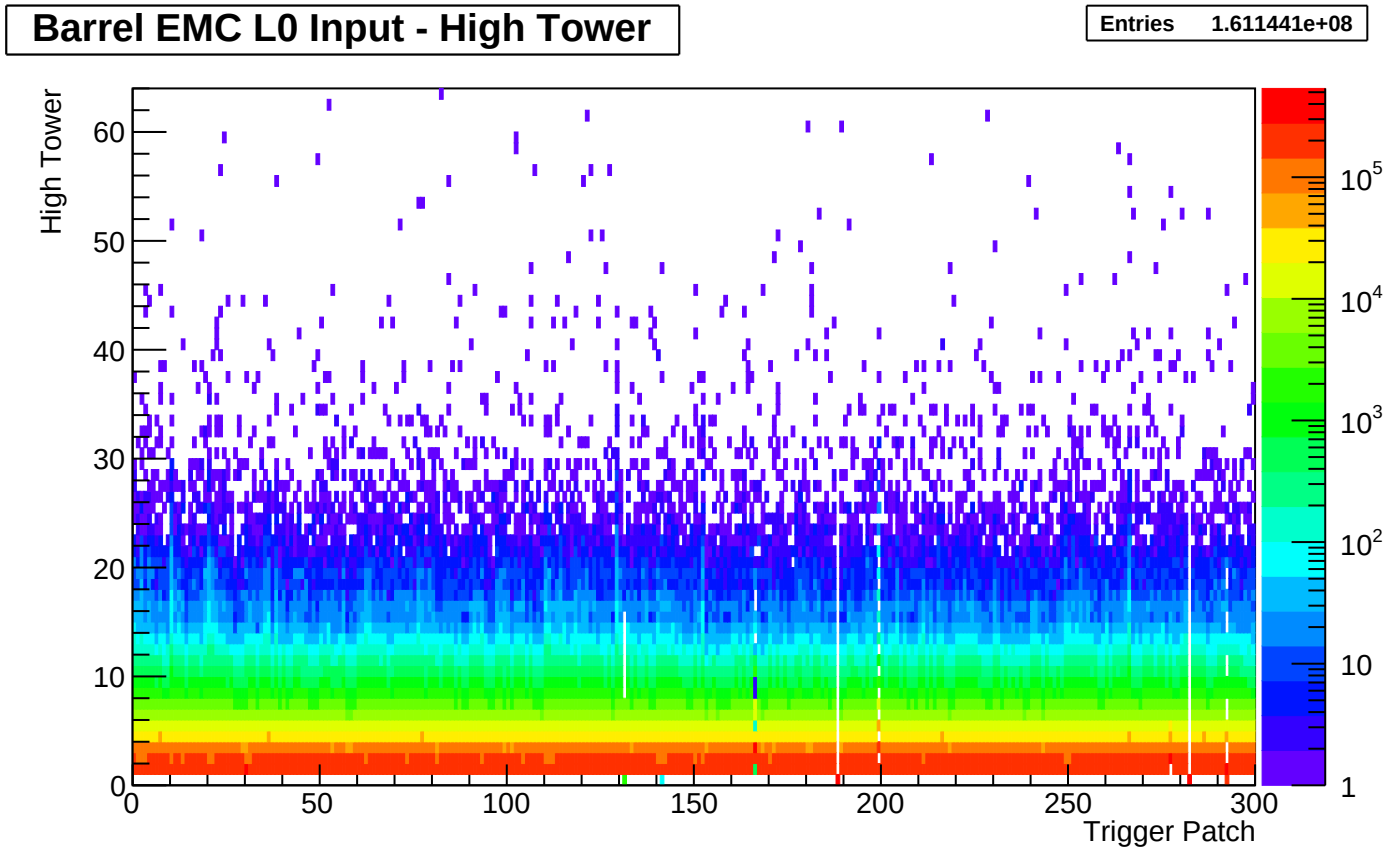
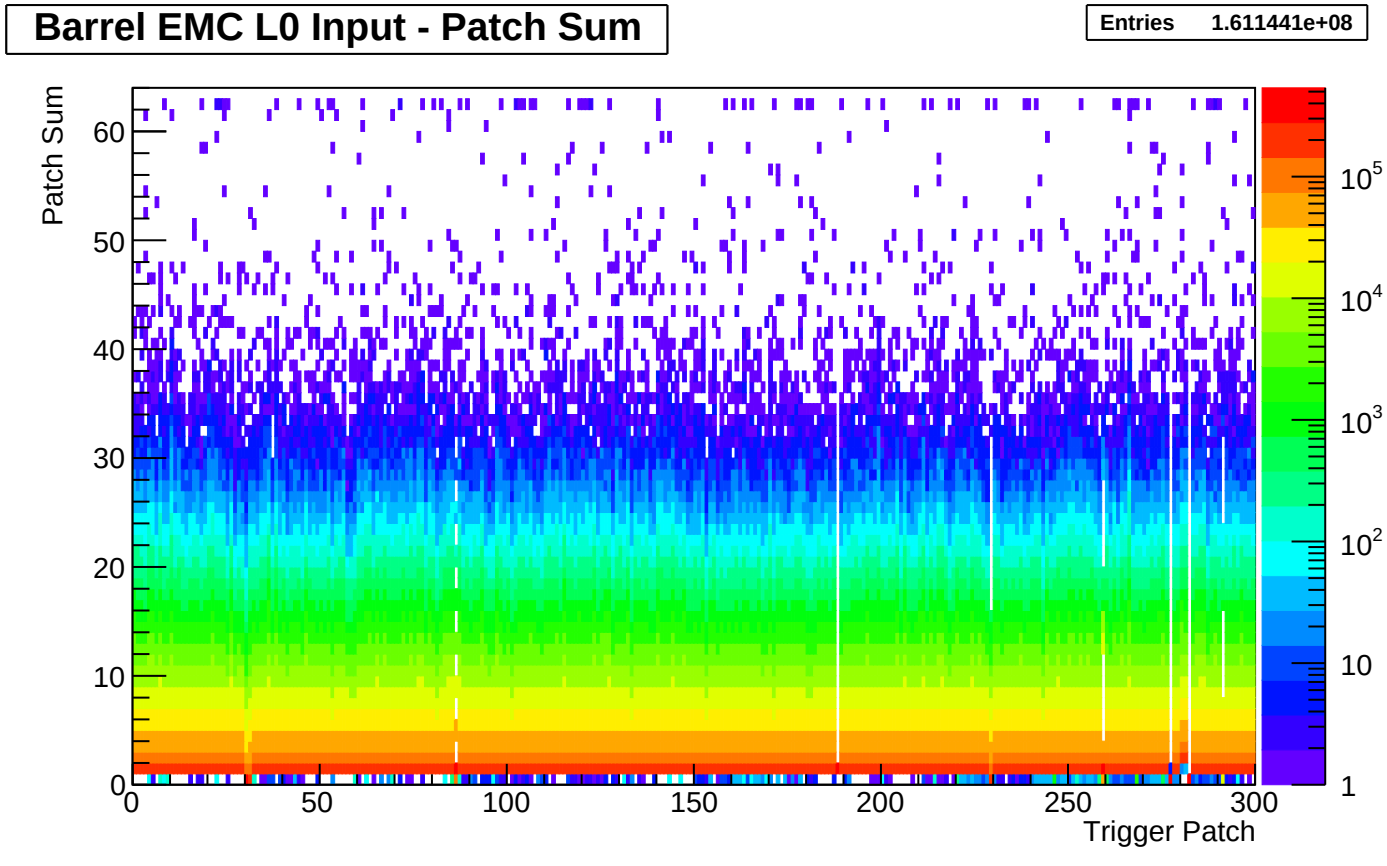
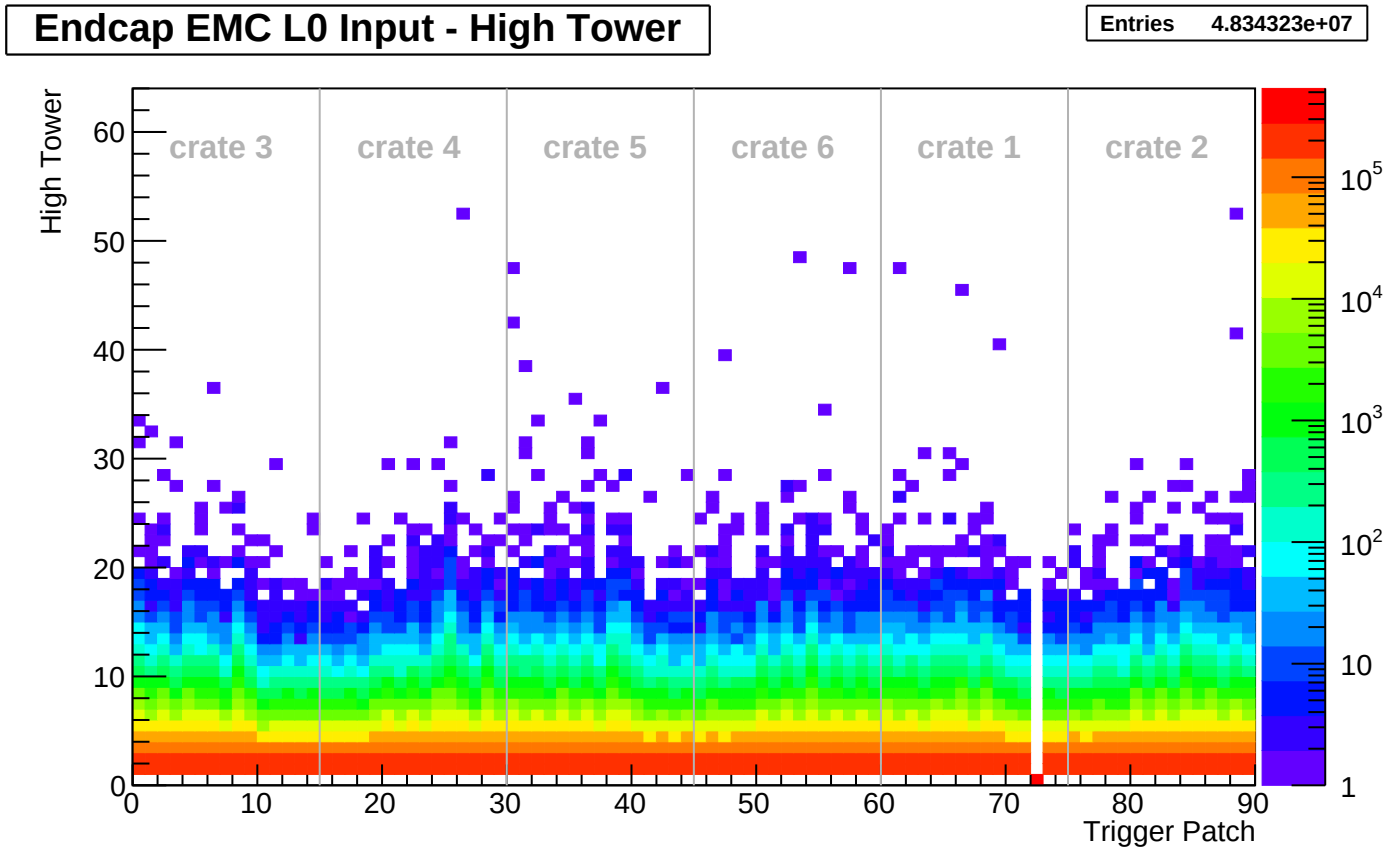
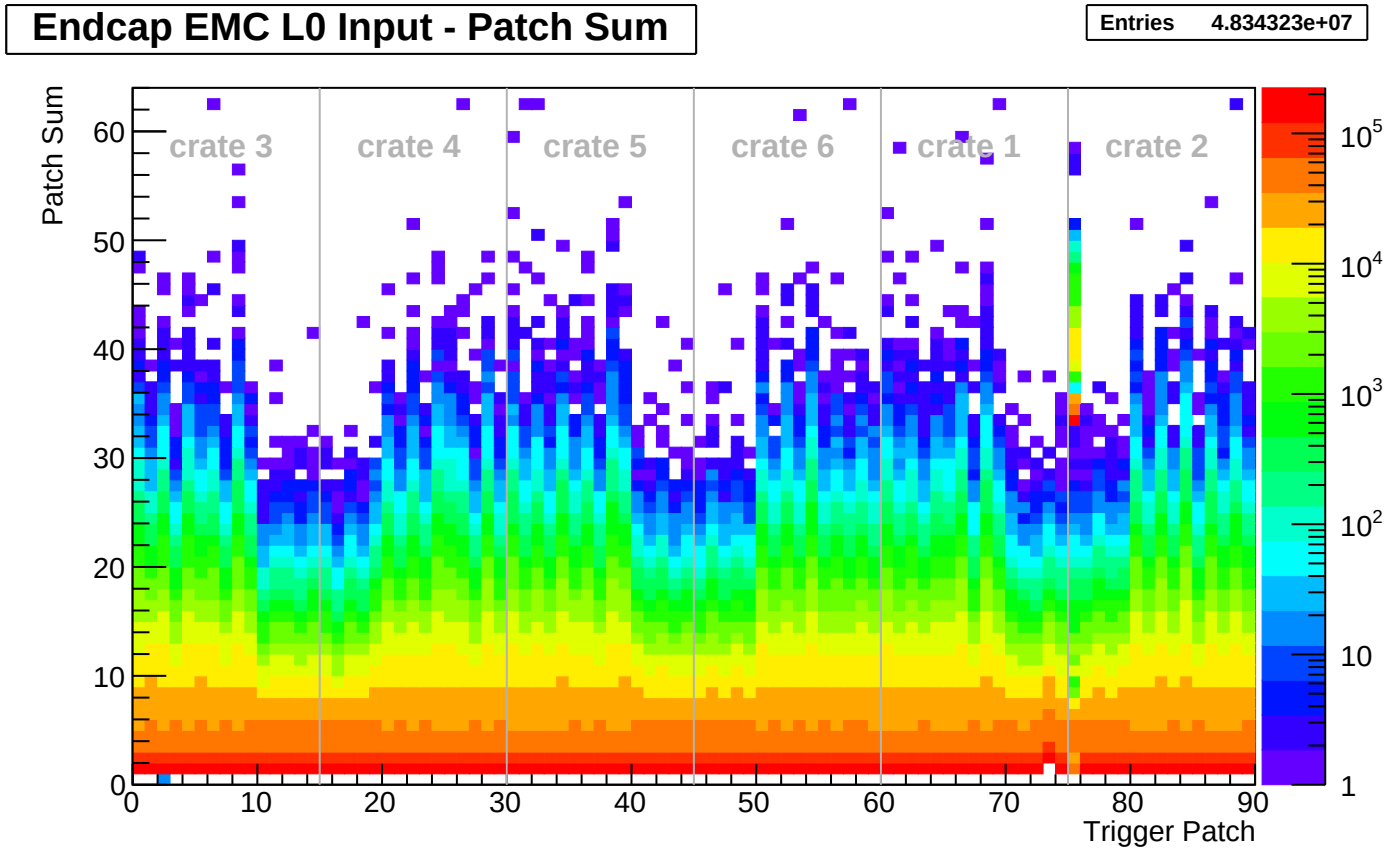
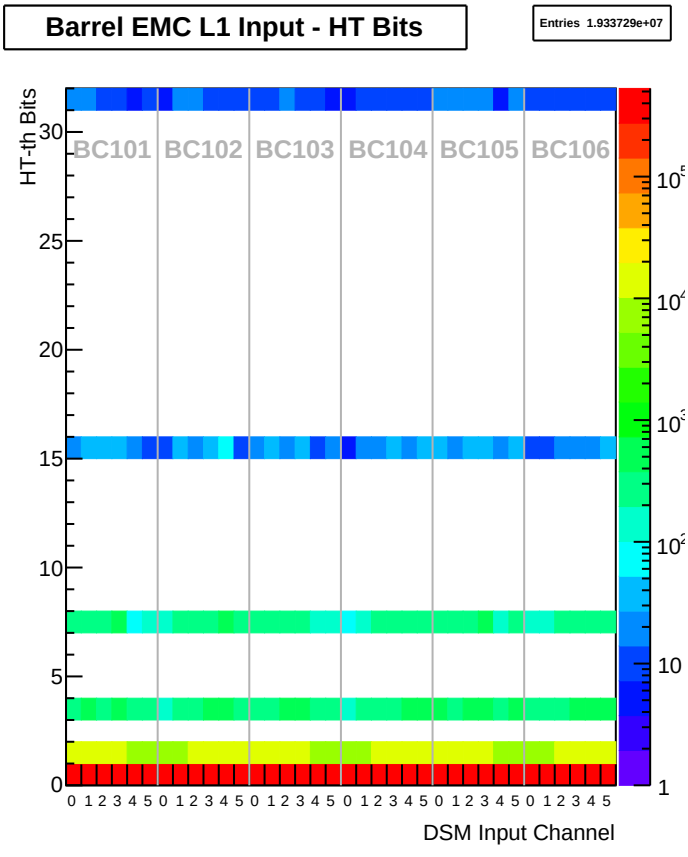
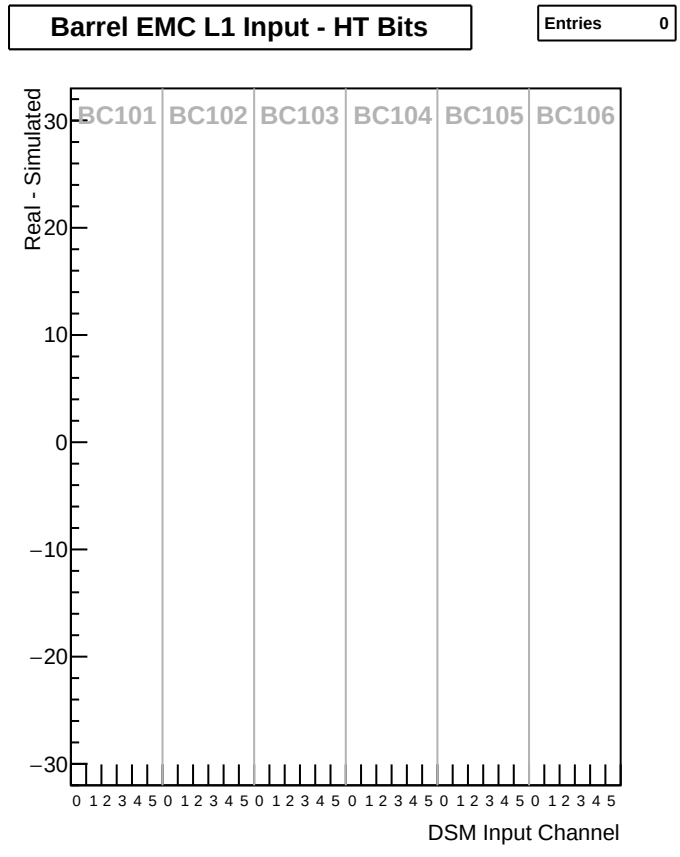

Barrel EMC L0 Input - High Tower
Entries 1.611441e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

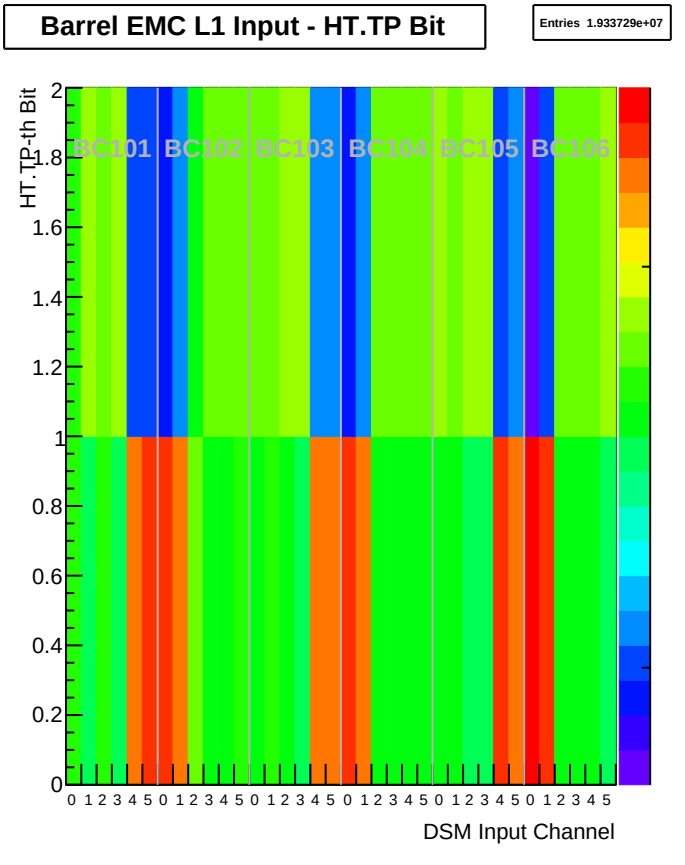

Barrel EMC L0 Input - Patch Sum
Entries 1.611441e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

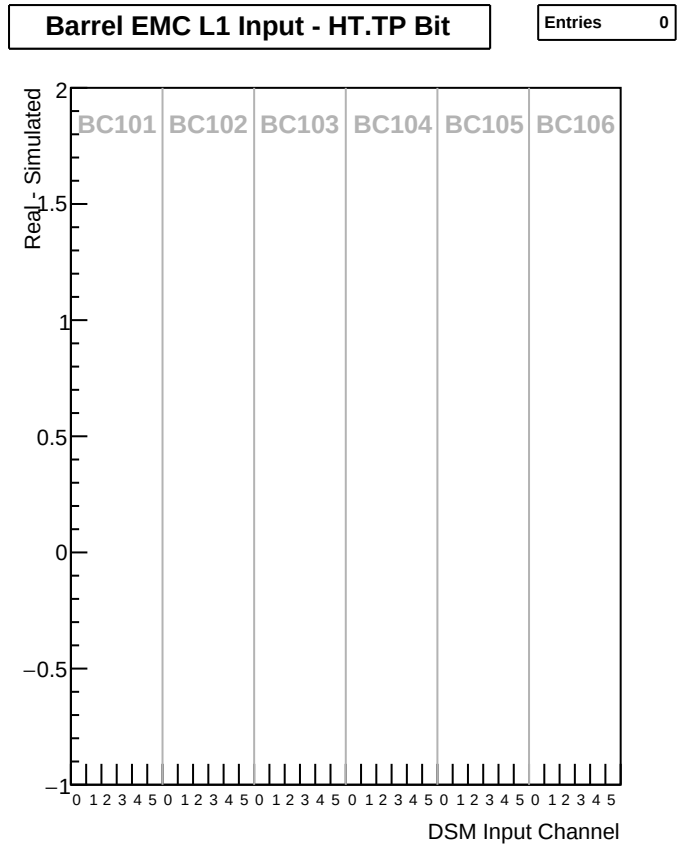

Endcap EMC L0 Input - High Tower
Entries 4.834323e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

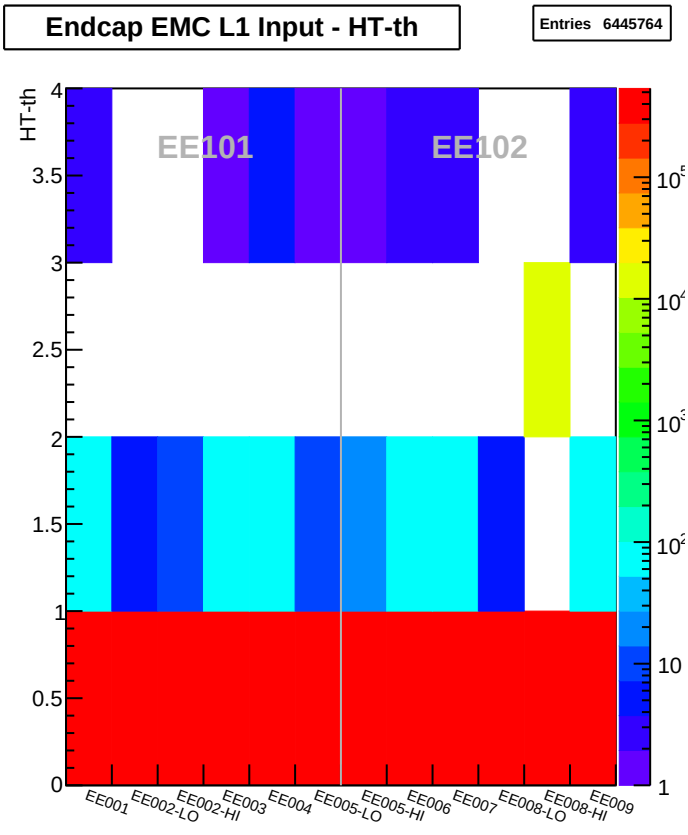

Endcap EMC L0 Input - Patch Sum
Entries 4.834323e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

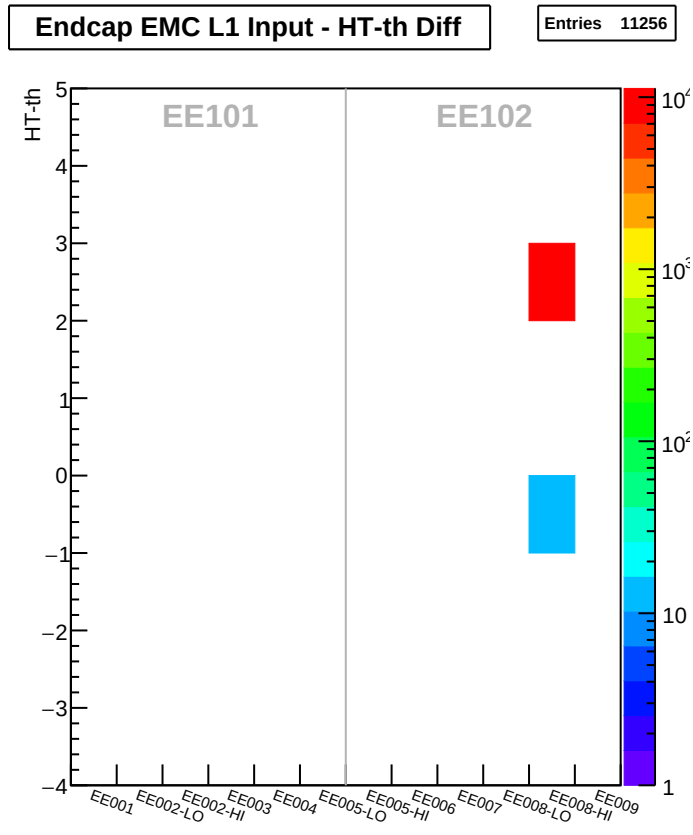

Barrel EMC L1 Input - HT Bits
Entries 1.933729e+07
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

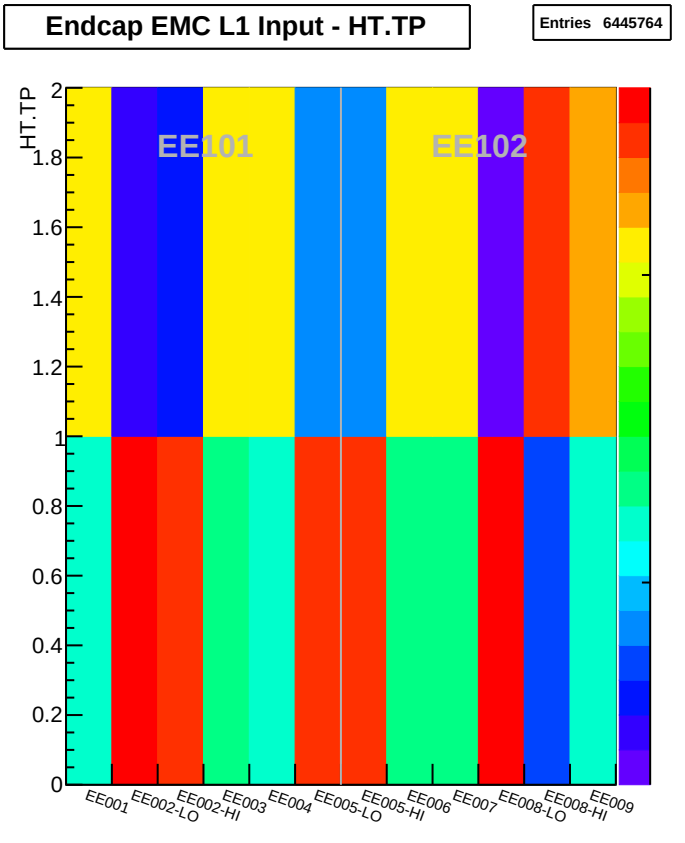

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

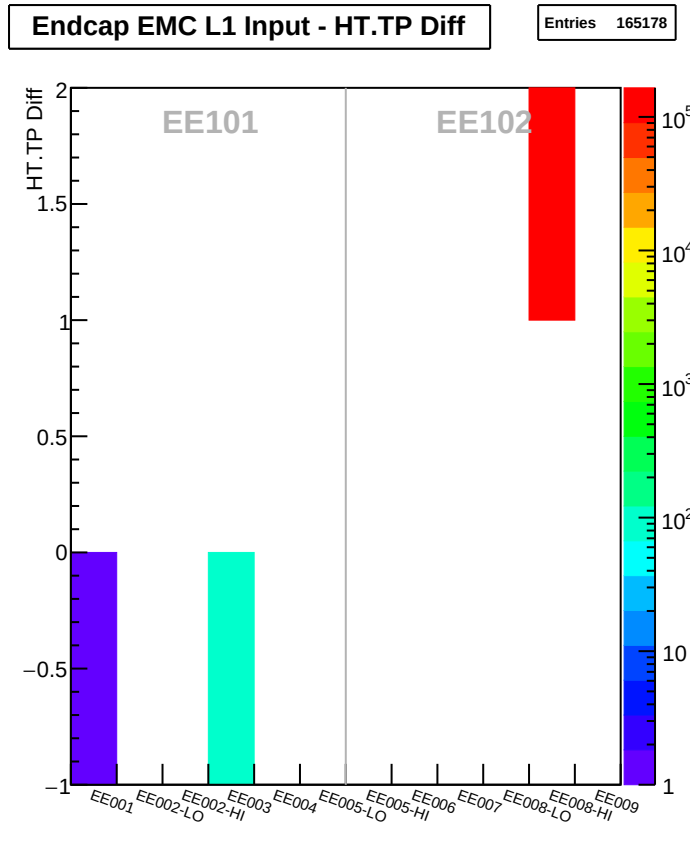

Barrel EMC L1 Input - HT.TP Bit
Entries 1.933729e+07
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

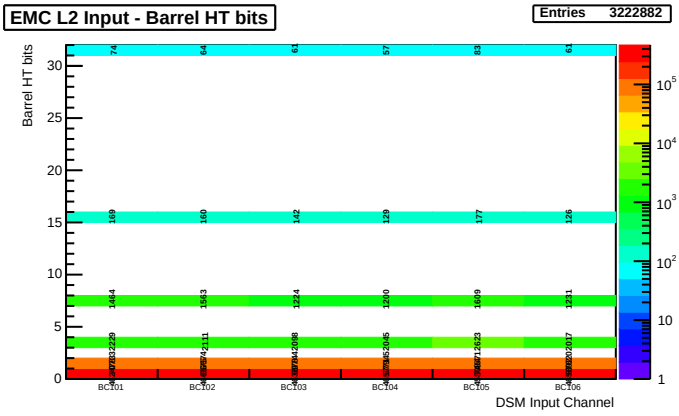

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

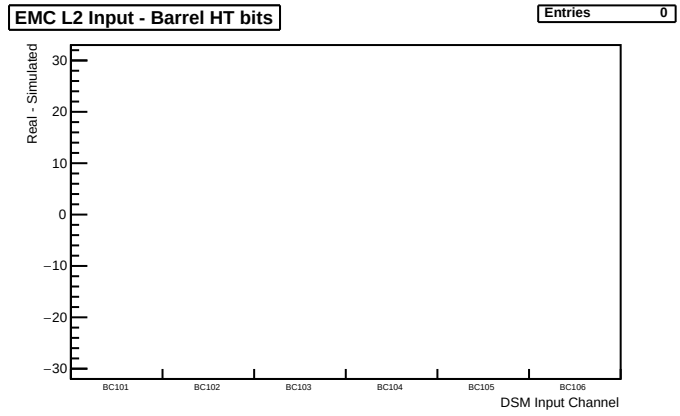

Endcap EMC L1 Input - HT-th
Entries 6445764
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

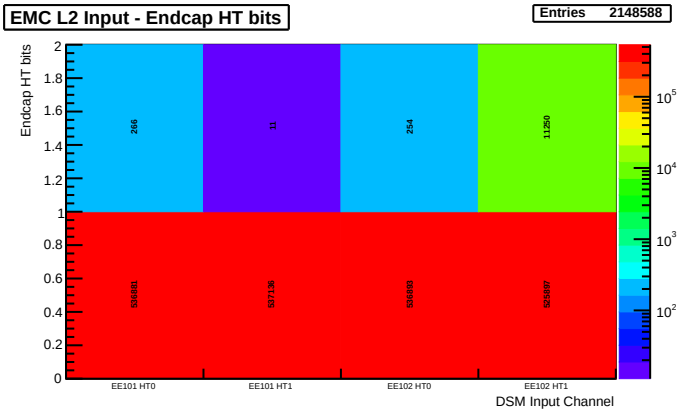

Endcap EMC L1 Input - HT-th Diff
Entries 11256
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

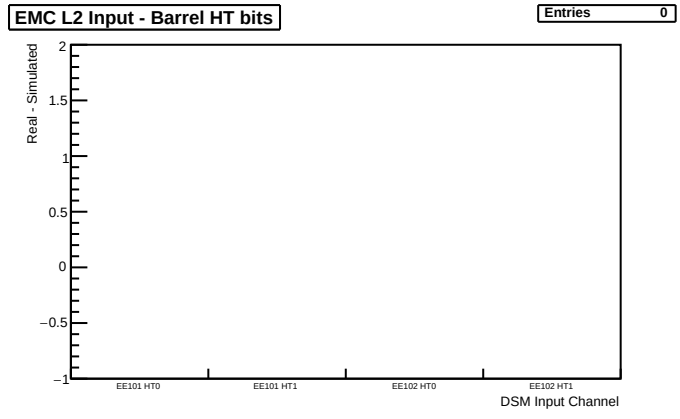

Endcap EMC L1 Input - HT.TP
Entries 6445764
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

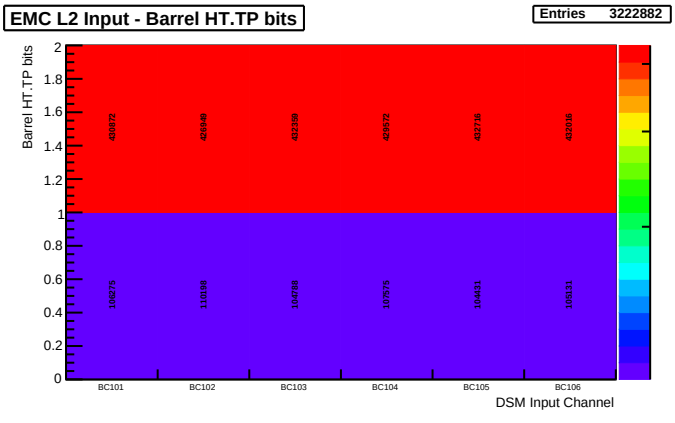

Endcap EMC L1 Input - HT.TP Diff
Entries 165178
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

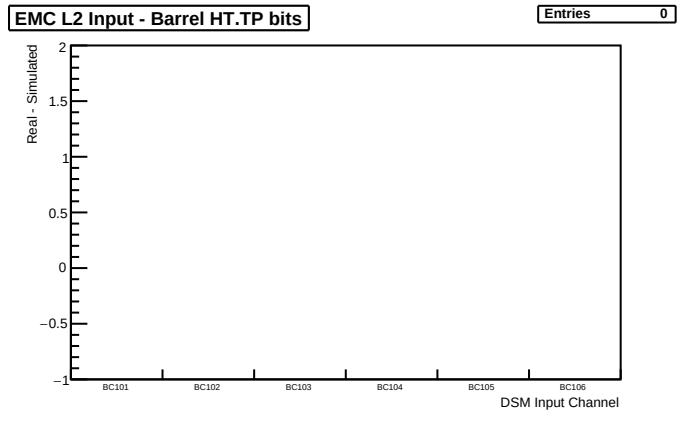

EMC L2 Input - Barrel HT bits
Entries 3222882
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

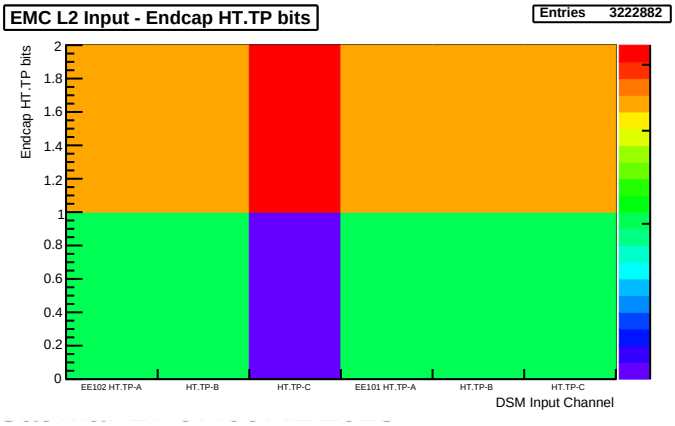

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

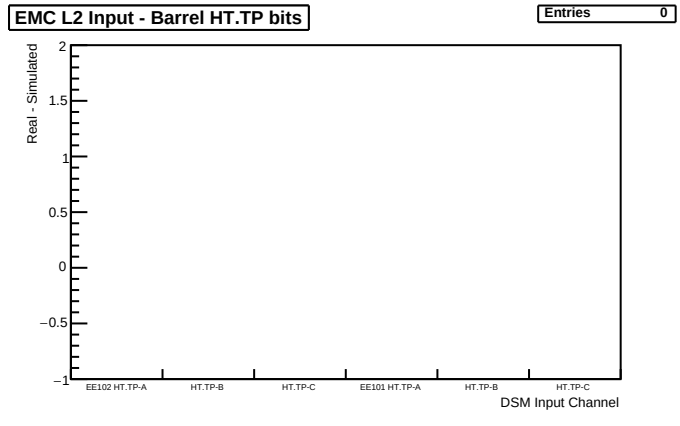

EMC L2 Input - Endcap HT bits
Entries 2148588
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

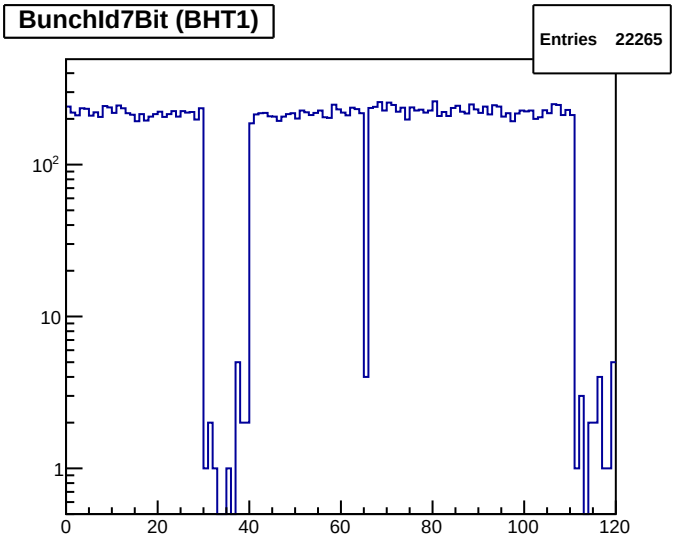

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

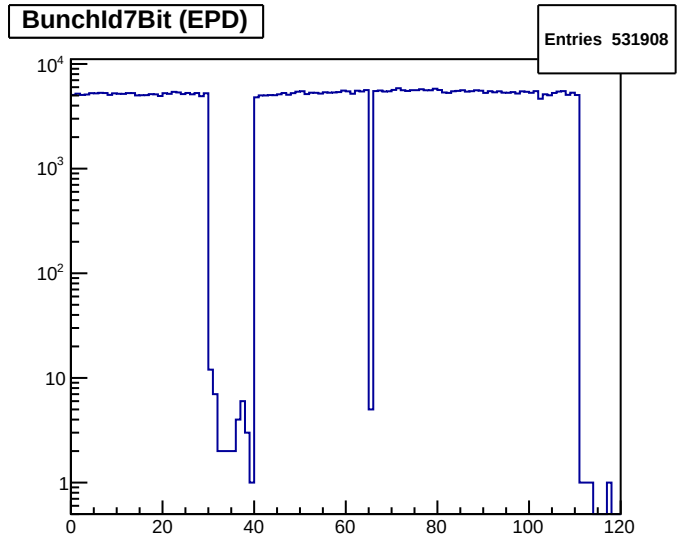

EMC L2 Input - Barrel HT.TP bits
Entries 3222882
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

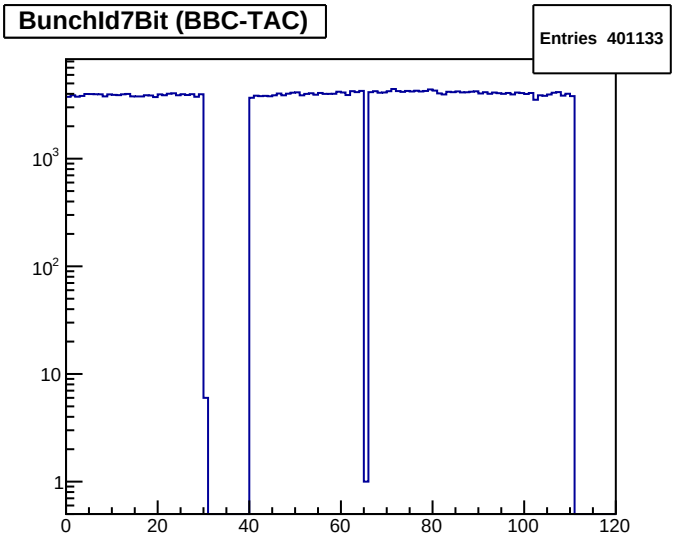

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

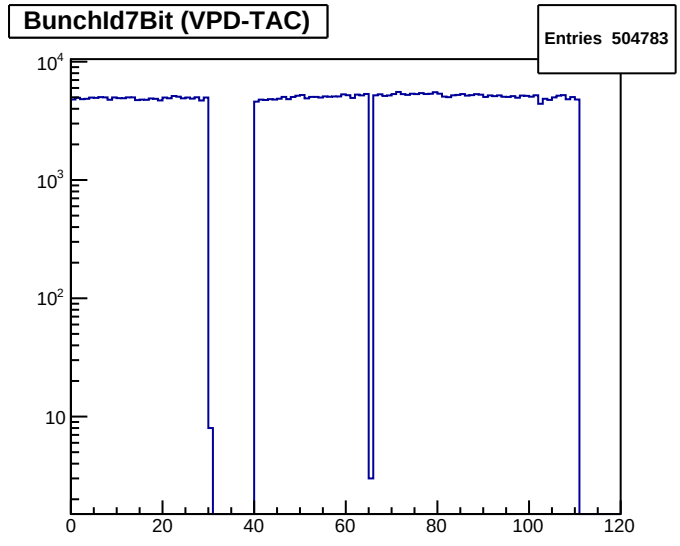

EMC L2 Input - Endcap HT.TP bits
Entries 3222882
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

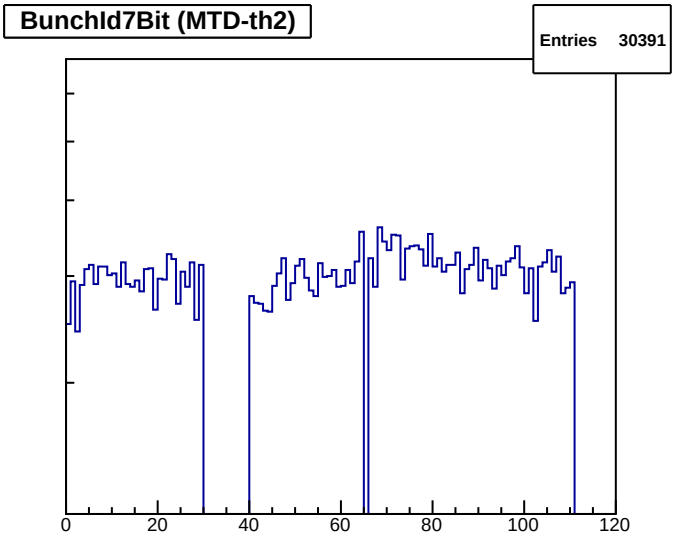

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

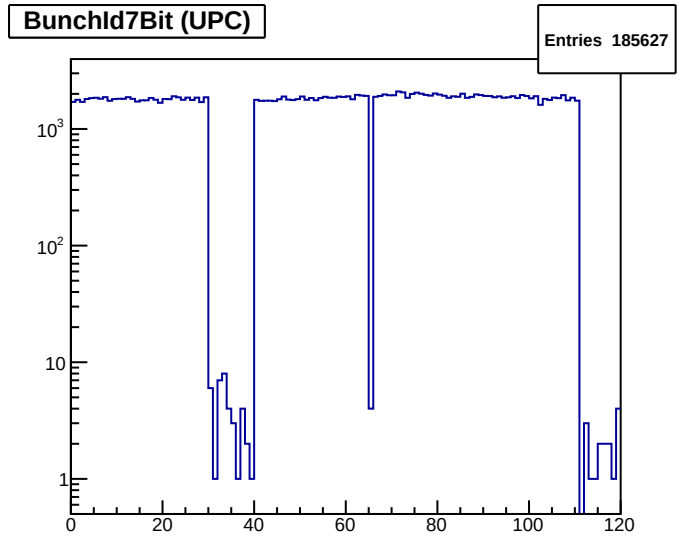

BunchId7Bit (BHT1)
Entries 22265

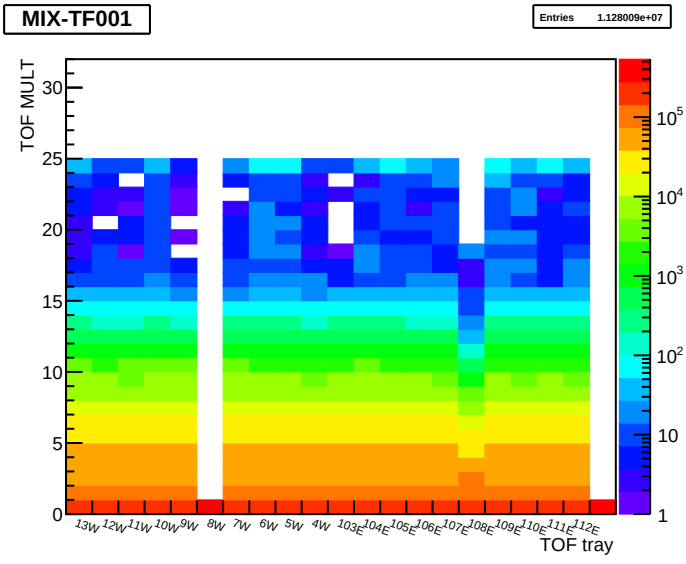

BunchId7Bit (EPD)
Entries 531908

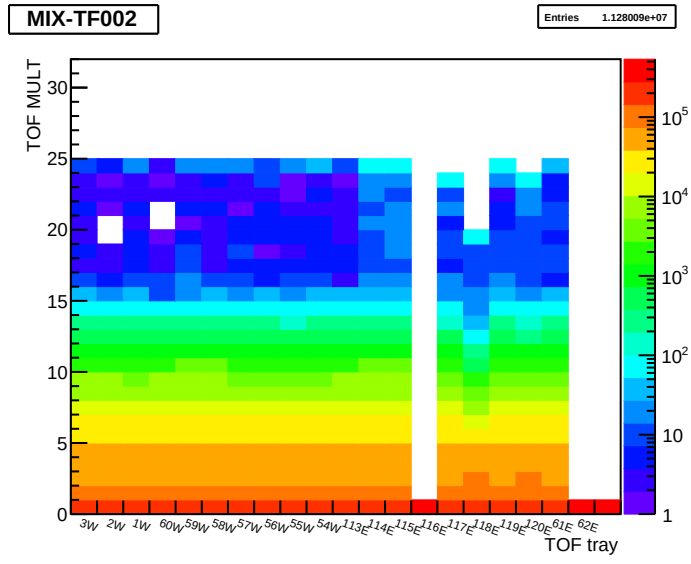

BunchId7Bit (BBC-TAC)
Entries 401133

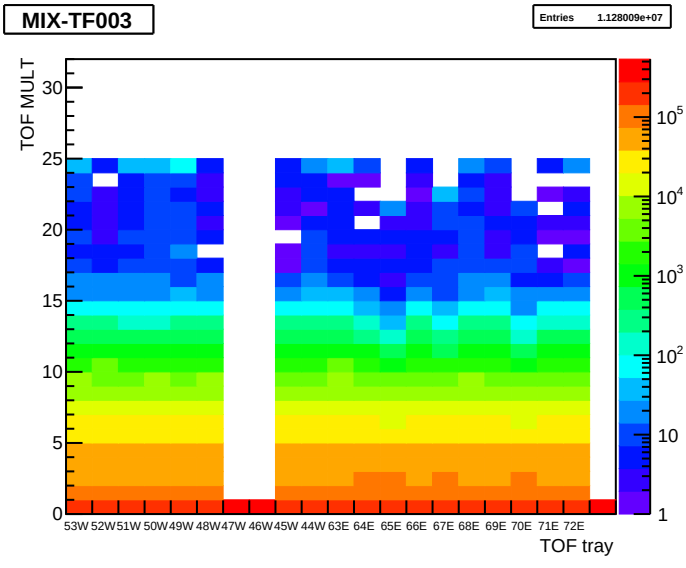

BunchId7Bit (VPD-TAC)
Entries 504783

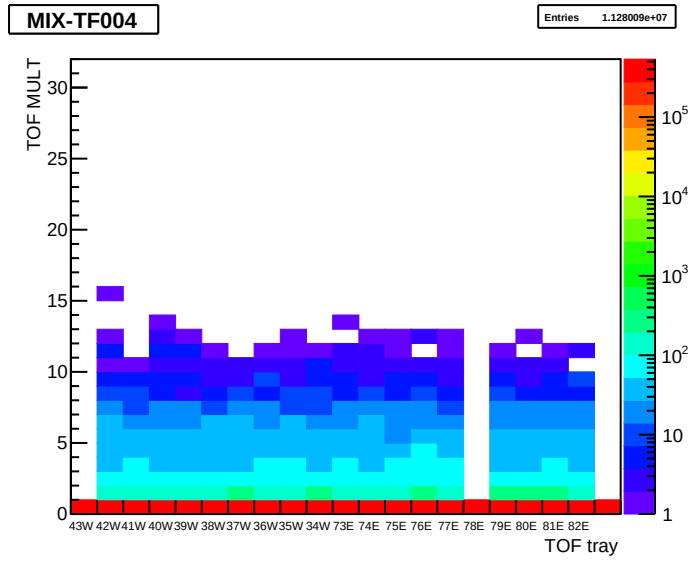

BunchId7Bit (MTD-th2)
Entries 30391

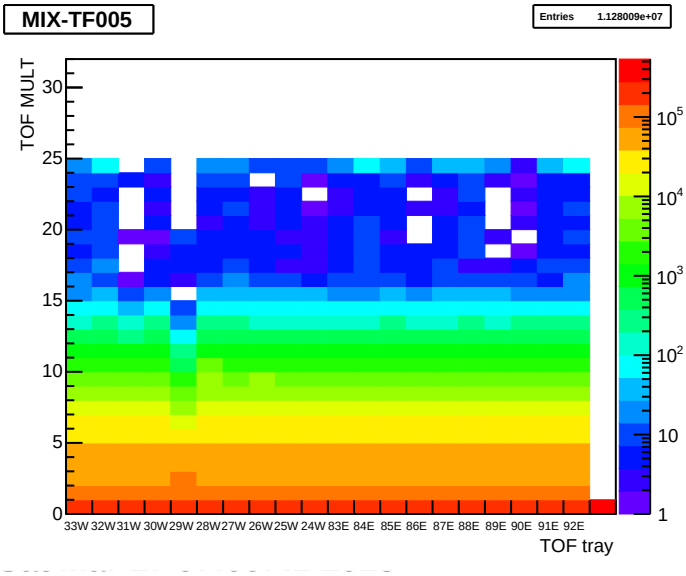

BunchId7Bit (UPC)
Entries 185627

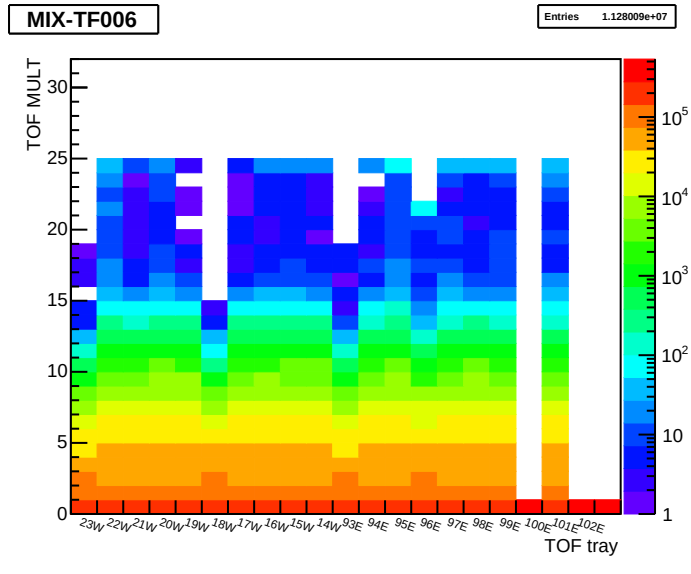

MIX-TF001
Entries 1.128009e+07
TOF MULT
TOF tray

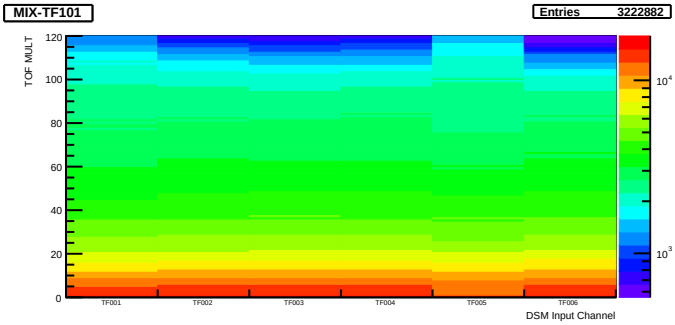

MIX-TF002
Entries 1.128009e+07
TOF MULT
TOF tray

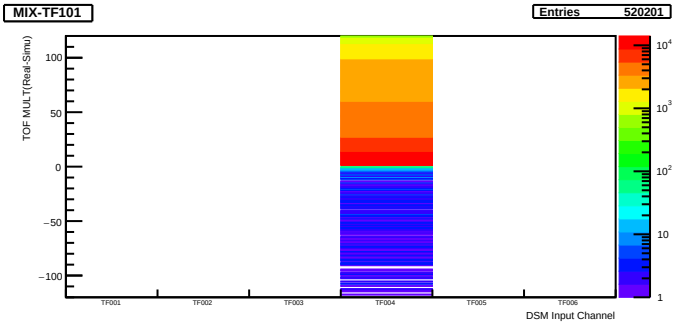

MIX-TF003
Entries 1.128009e+07
TOF MULT
TOF tray

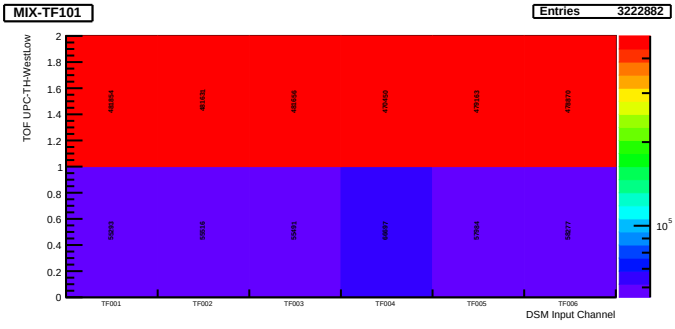

MIX-TF004
Entries 1.128009e+07
TOF MULT
TOF tray

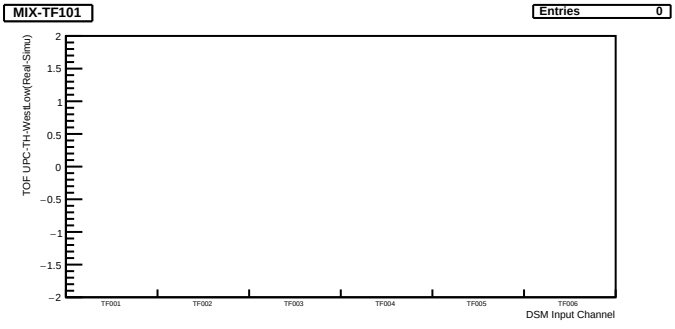

MIX-TF005
Entries 1.128009e+07
TOF MULT
TOF tray

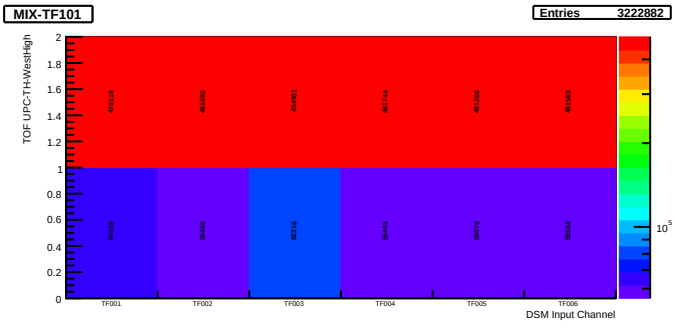

MIX-TF006
Entries 1.128009e+07
TOF MULT
TOF tray

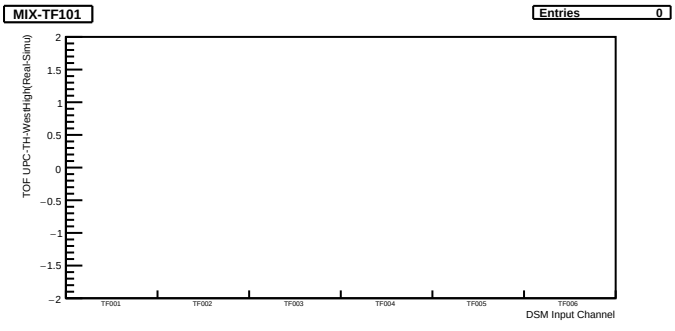

MIX-TF101
Entries 3222882
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

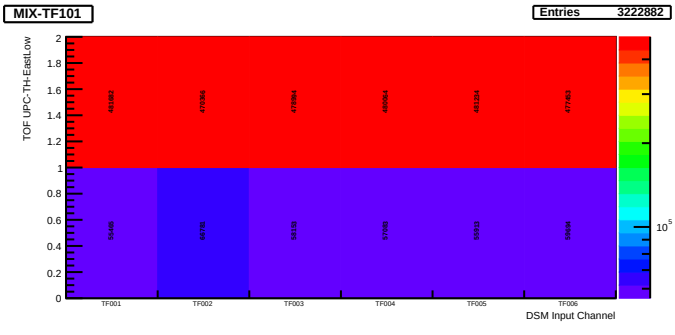

MIX-TF101
Entries 520201
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

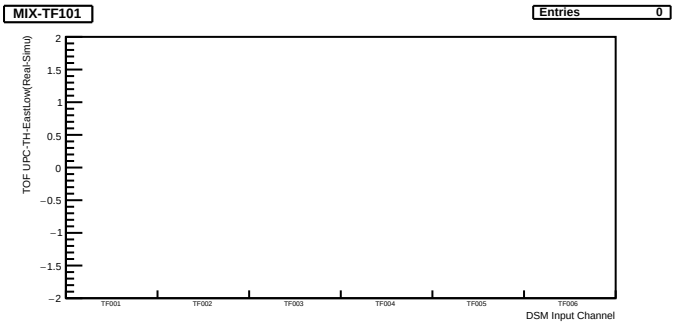

MIX-TF101
Entries 3222882
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

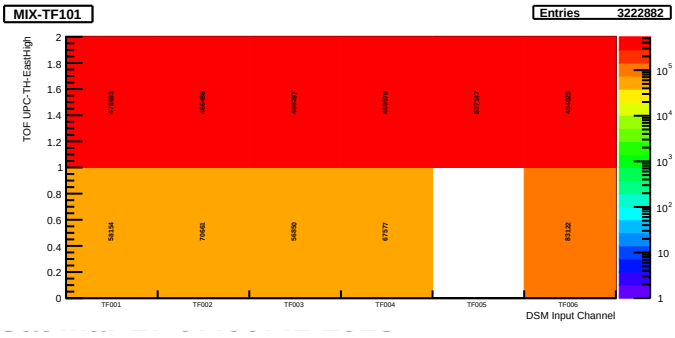

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

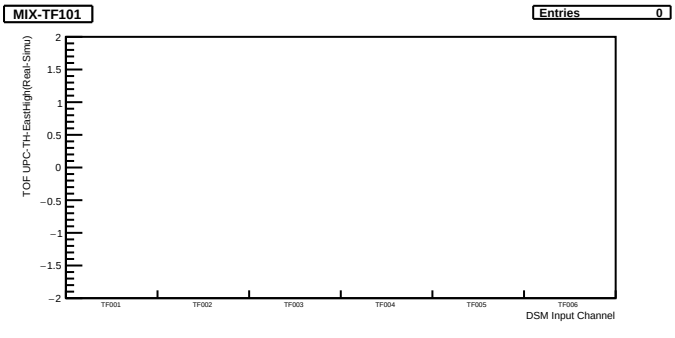

MIX-TF101
Entries 3222882
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

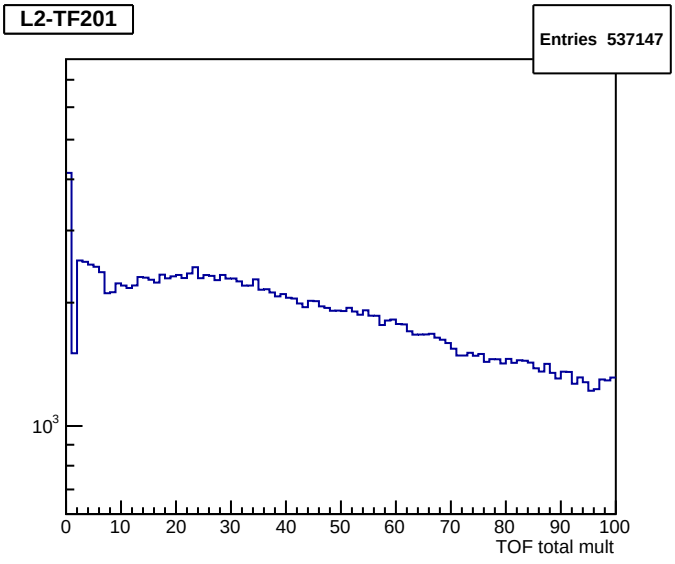

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

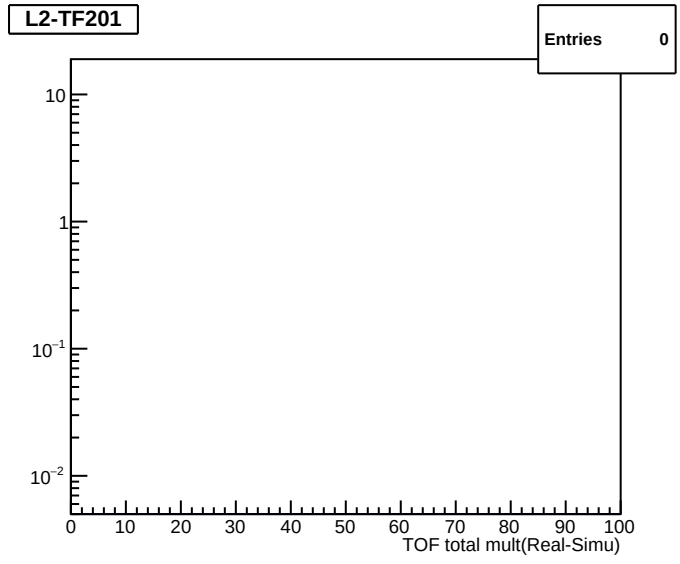

MIX-TF101
Entries 3222882
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

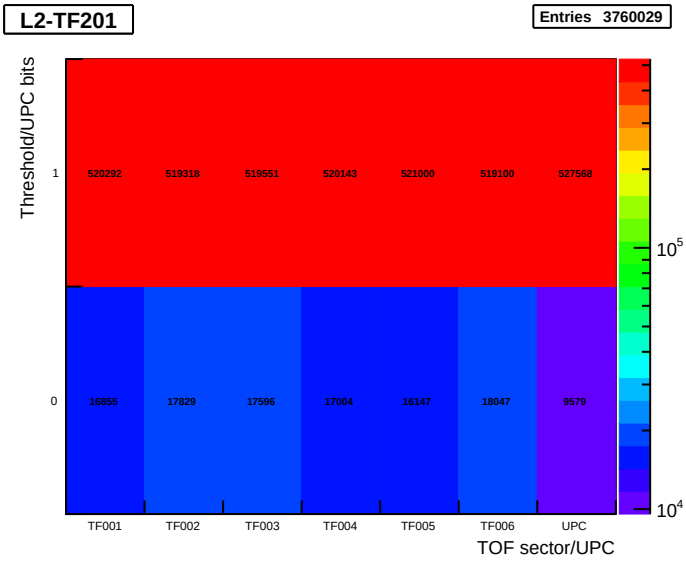

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

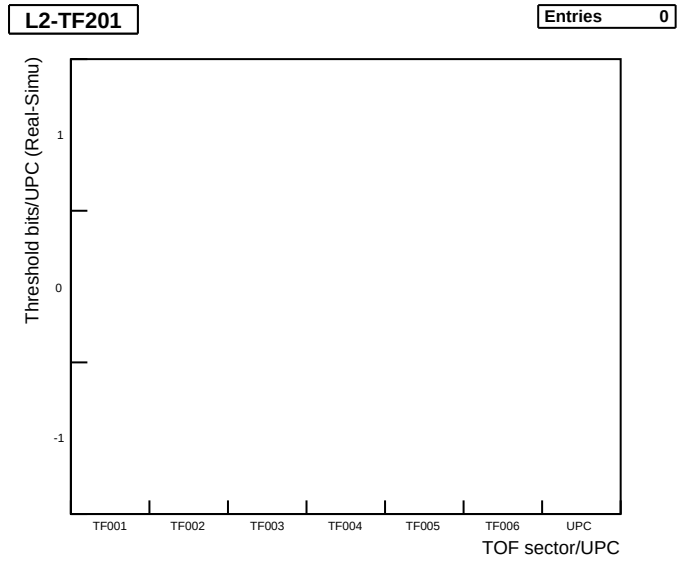

MIX-TF101
Entries 3222882
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

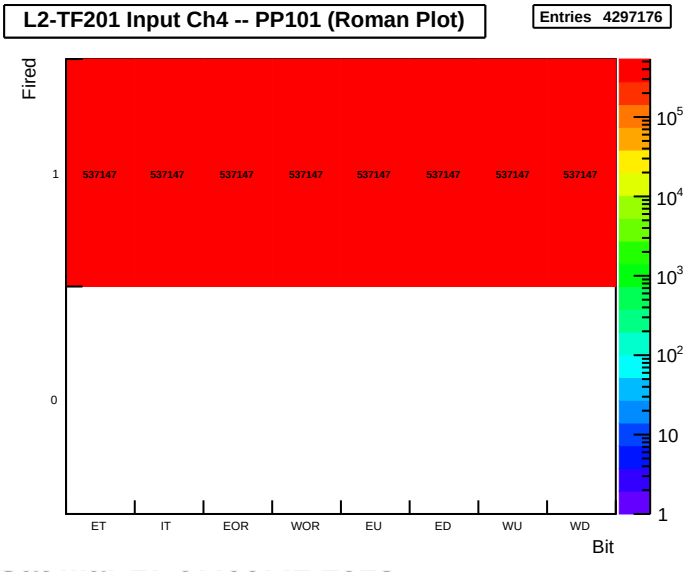

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

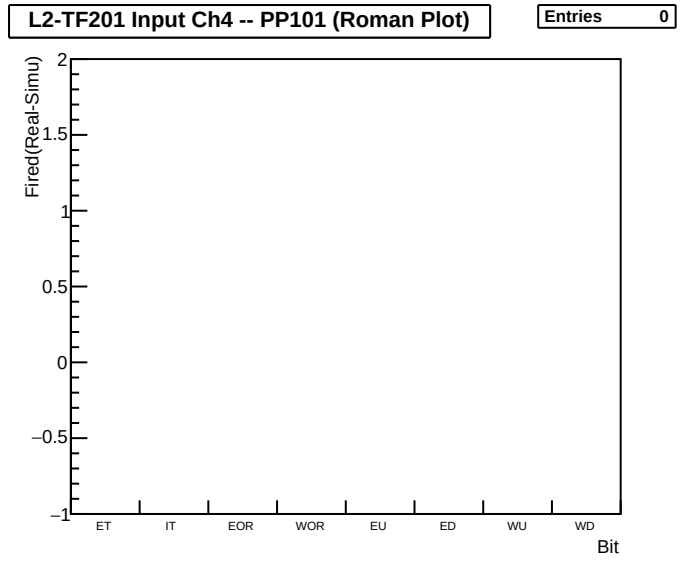

L2-TF201
Entries 537147
10^3
0 10 20 30 40 50 60 70 80 90 100
TOF total mult

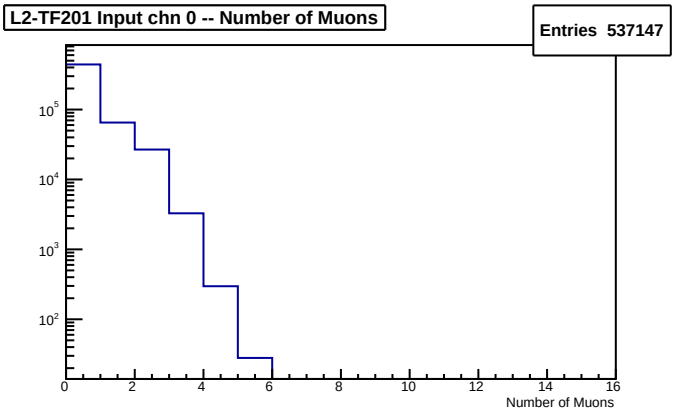

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
0 10 20 30 40 50 60 70 80 90 100
TOF total mult(Real-Simu)

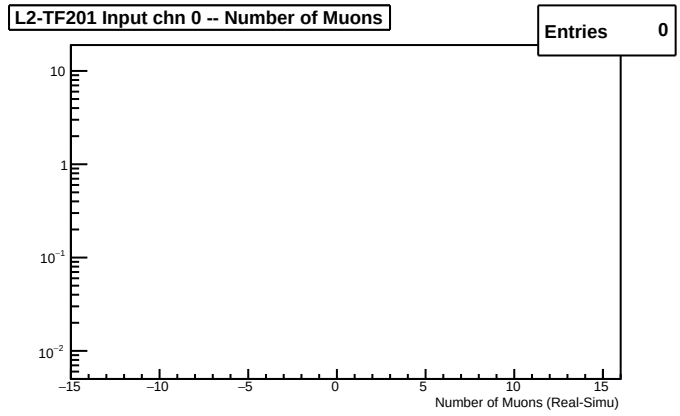

L2-TF201
Entries 3760029
Threshold/UPC bits
1
0
520292 519318 519551 520143 521000 519100 527568
16855 17829 17596 17004 16147 18047 9579
10^5
10^4
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

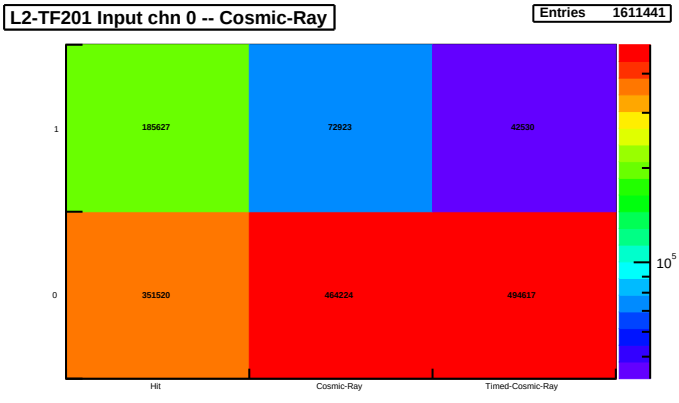

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

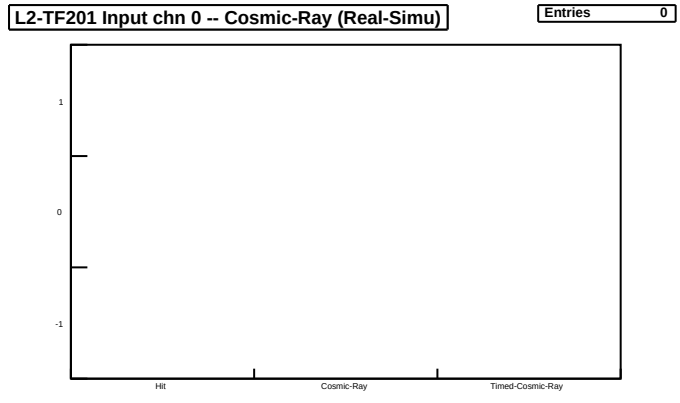

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 4297176
Fired
1
0
537147 537147 537147 537147 537147 537147 537147 537147
10^5
10^4
10^3
10^2
10
1
ET IT EOR WOR EU ED WU WD
Bit

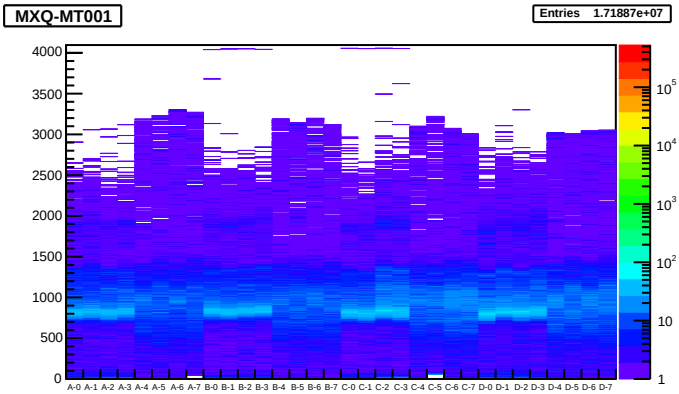

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
2
1.5
1
0.5
0
−0.5
−1
ET IT EOR WOR EU ED WU WD
Bit

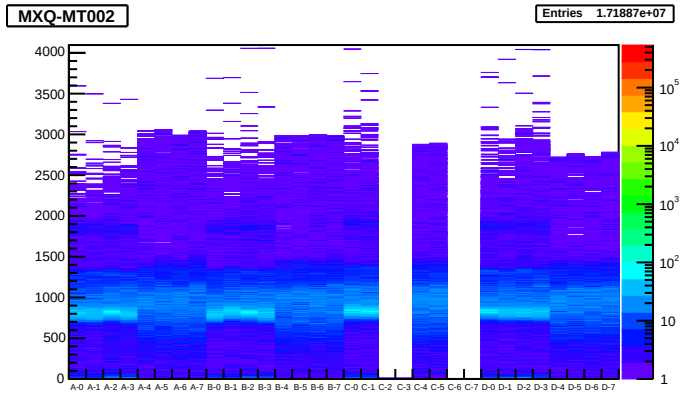

L2-TF201 Input chn 0 -- Number of Muons
Entries 537147
Number of Muons

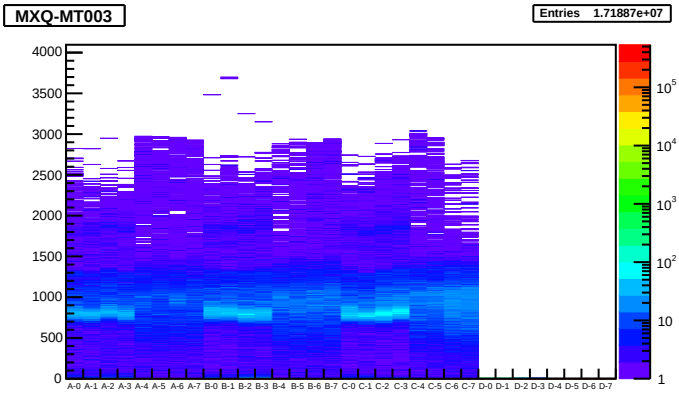

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

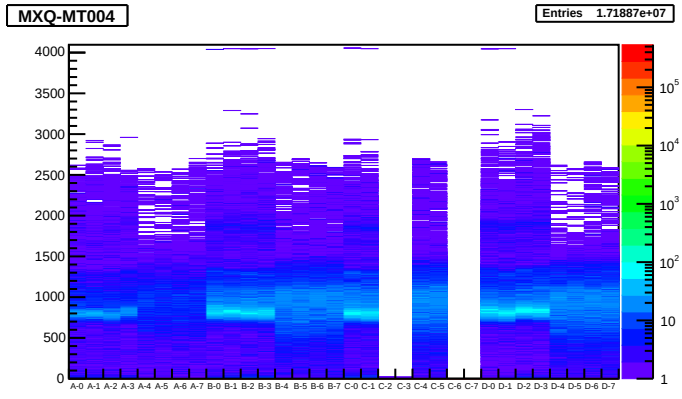

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 1611441
185627
72923
42530
351520
464224
494617
Hit
Cosmic-Ray
Timed-Cosmic-Ray

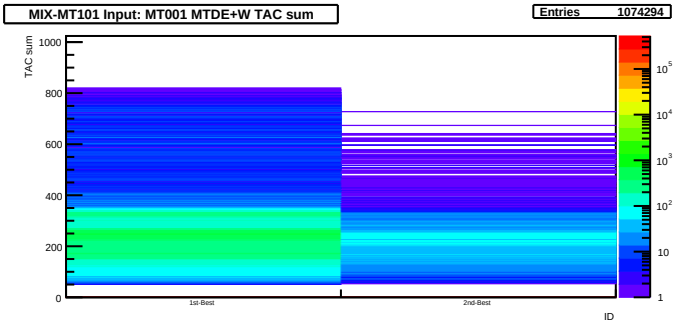

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

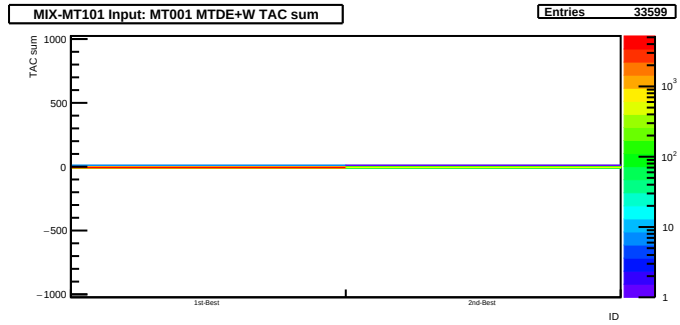

MXQ-MT001
Entries 1.71887e+07

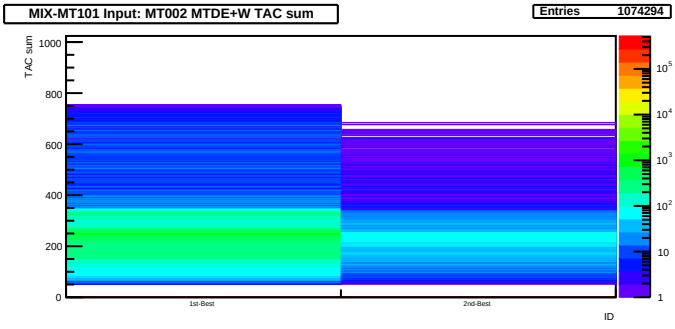

MXQ-MT002
Entries 1.71887e+07

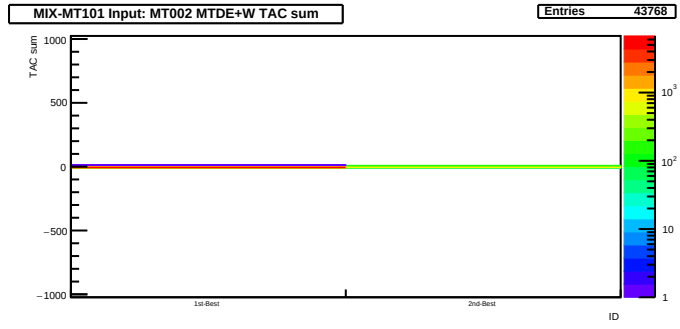

MXQ-MT003
Entries 1.71887e+07

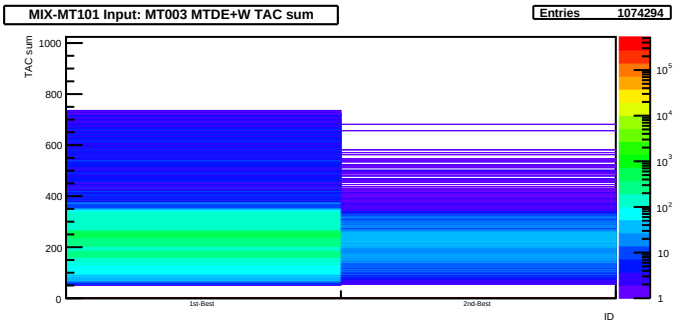

MXQ-MT004
Entries 1.71887e+07

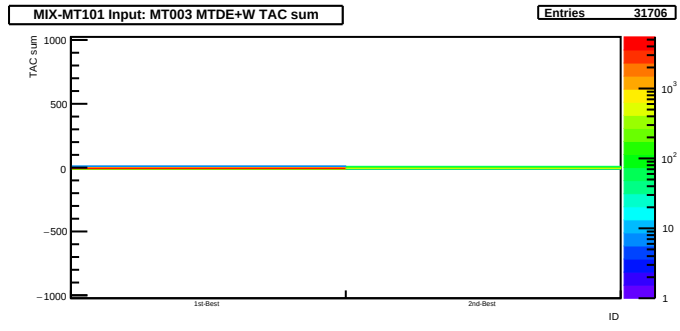

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 1074294
TAC sum
1st-Best
2nd-Best
ID

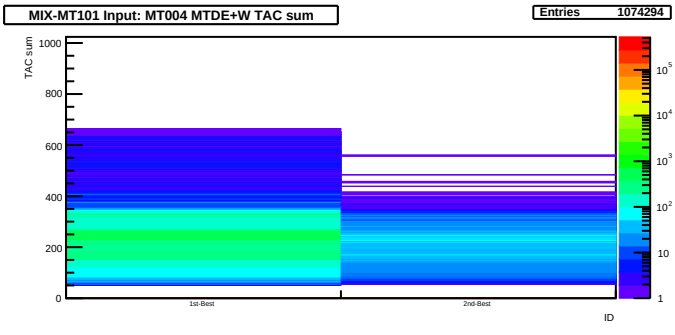

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 33599
TAC sum
1st-Best
2nd-Best
ID

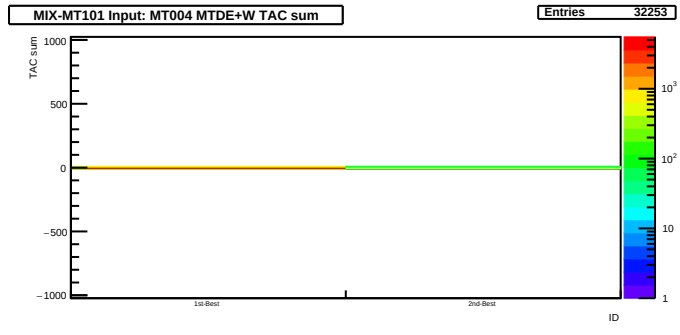

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 1074294
TAC sum
1st-Best
2nd-Best
ID

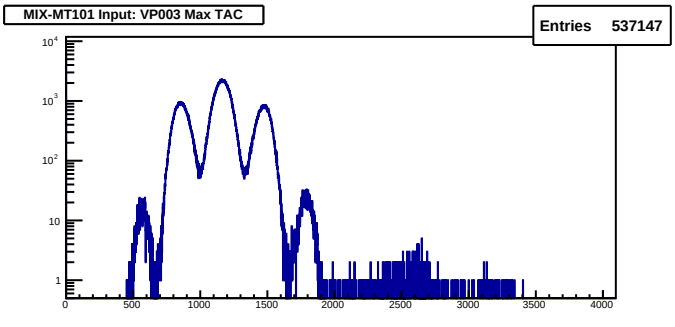

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 43768
TAC sum
1st-Best
2nd-Best
ID

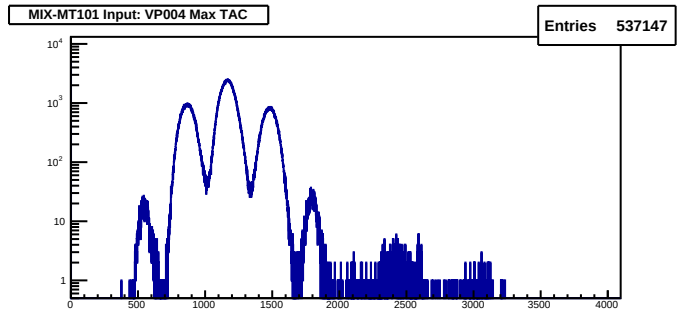

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 1074294
TAC sum
1st-Best
2nd-Best
ID

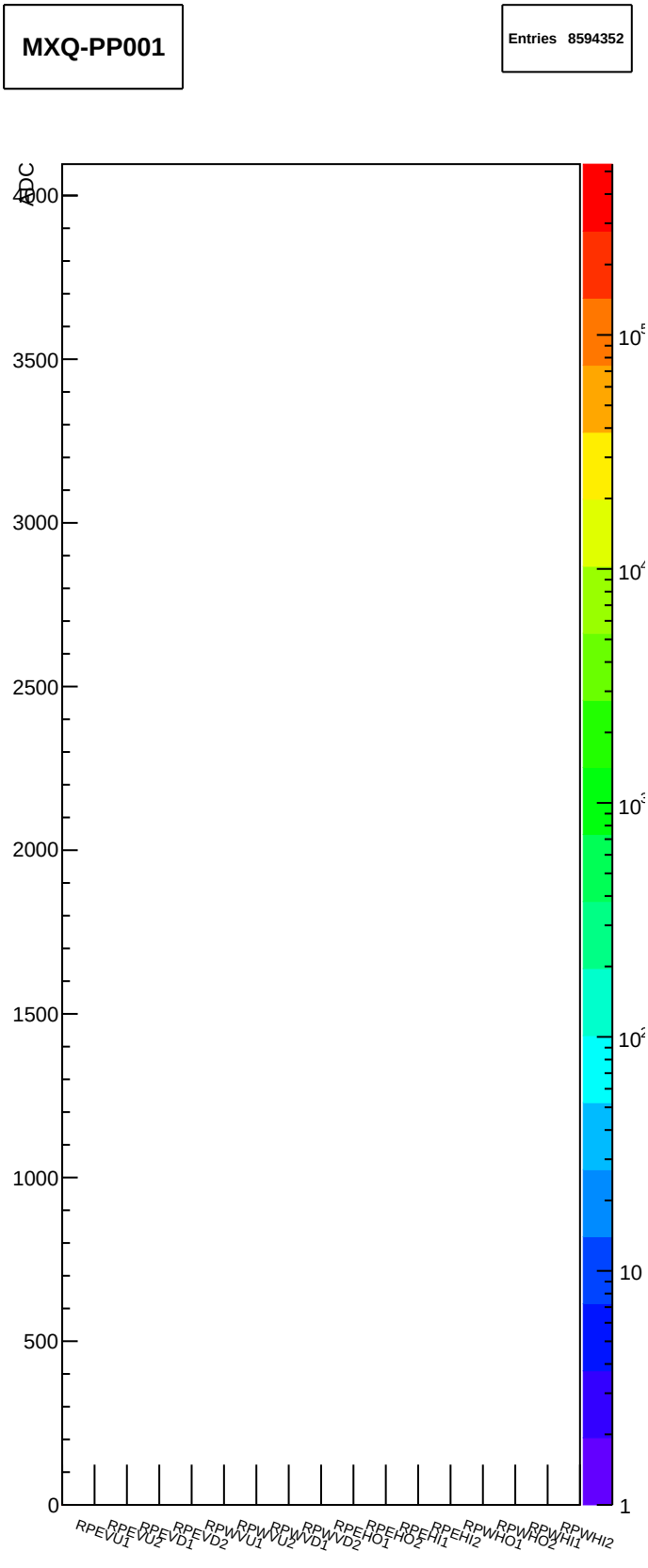

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 31706
TAC sum
1st-Best
2nd-Best
ID

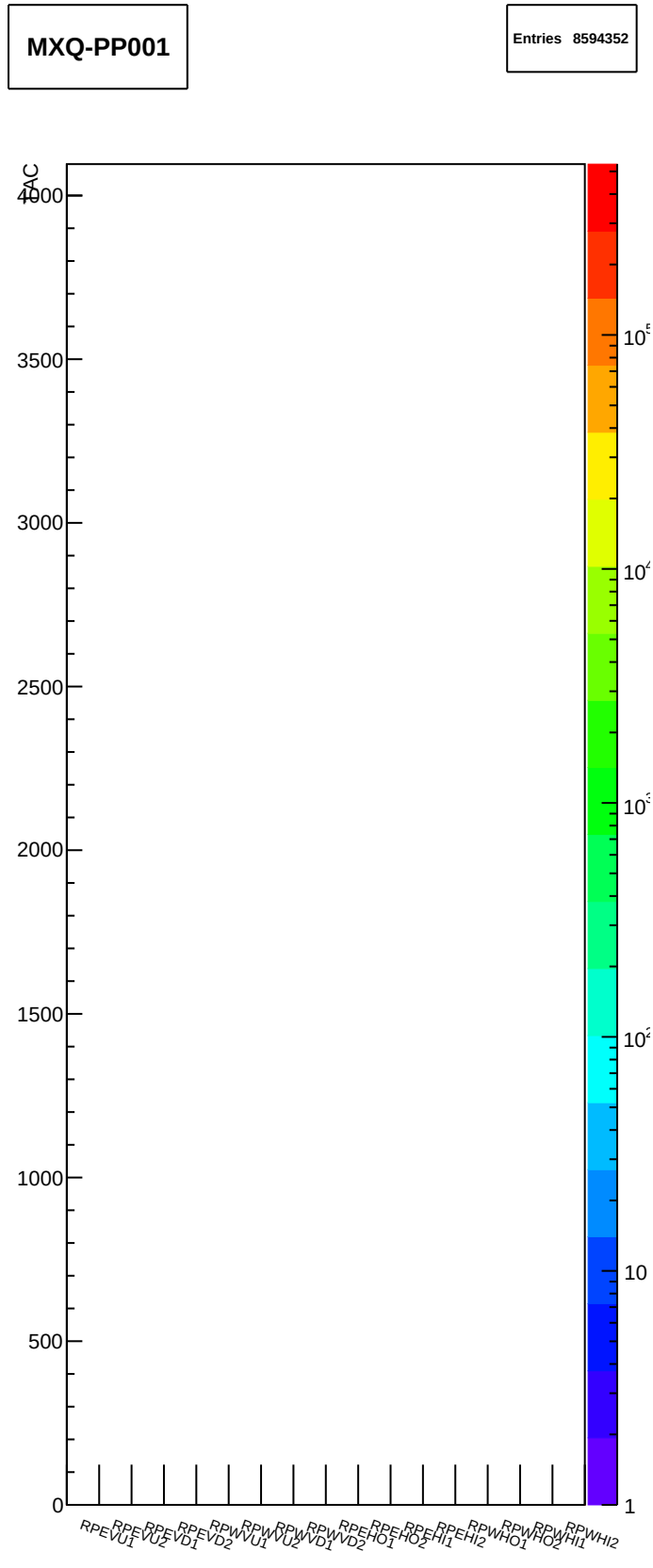

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 1074294
TAC sum
1st-Best
2nd-Best
ID

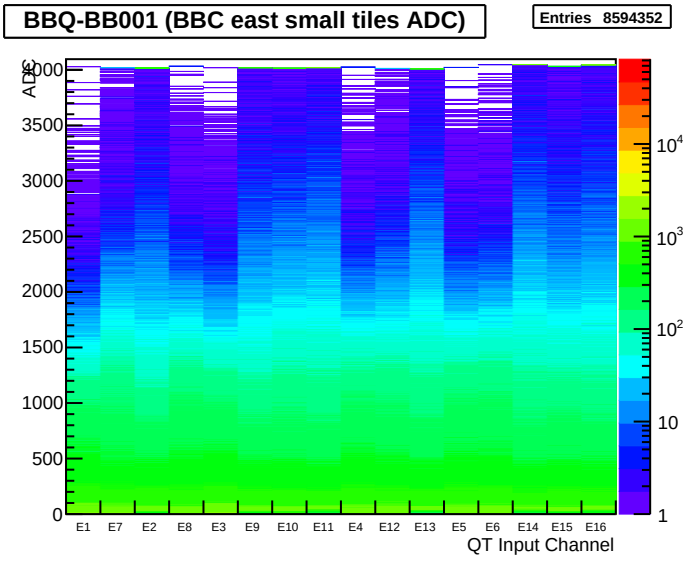

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 32253
TAC sum
1st-Best
2nd-Best
ID

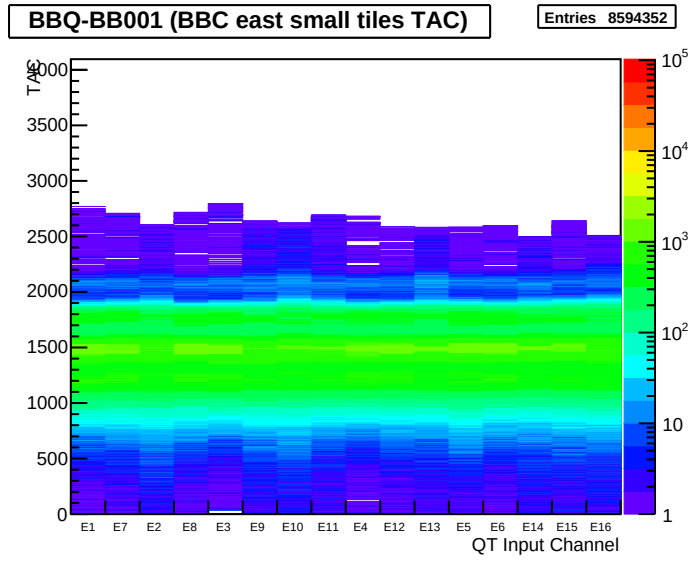

MIX-MT101 Input: VP003 Max TAC
Entries 537147

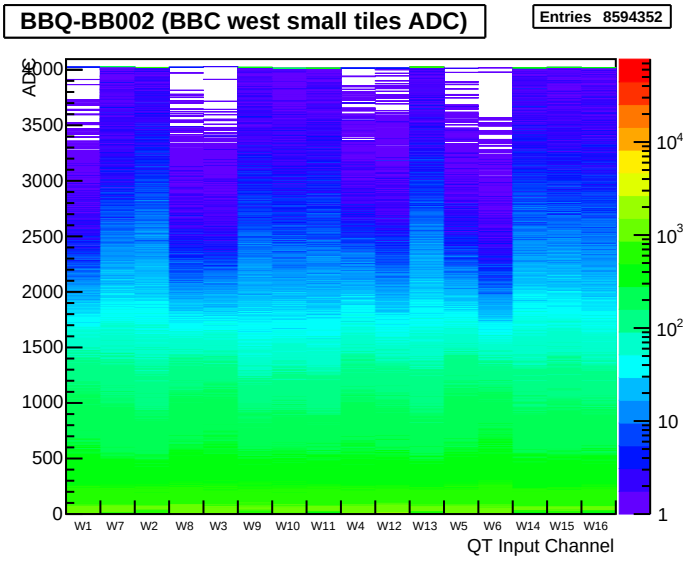

MIX-MT101 Input: VP004 Max TAC
Entries 537147

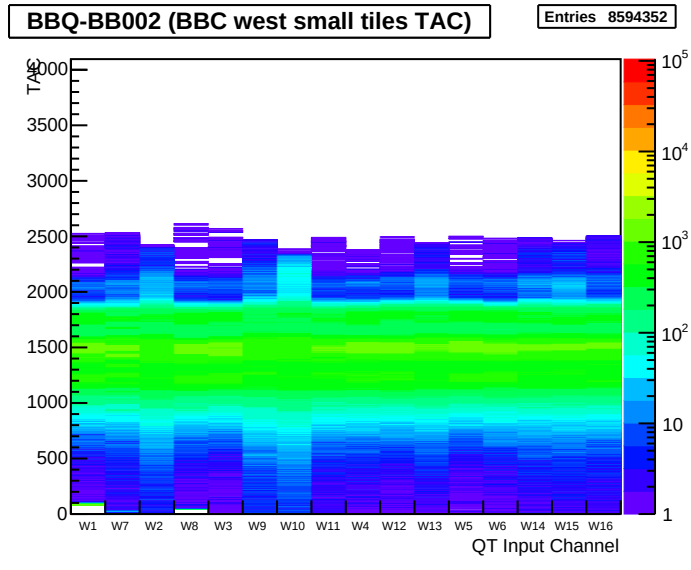

MXQ-PP001
Entries 8594352
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

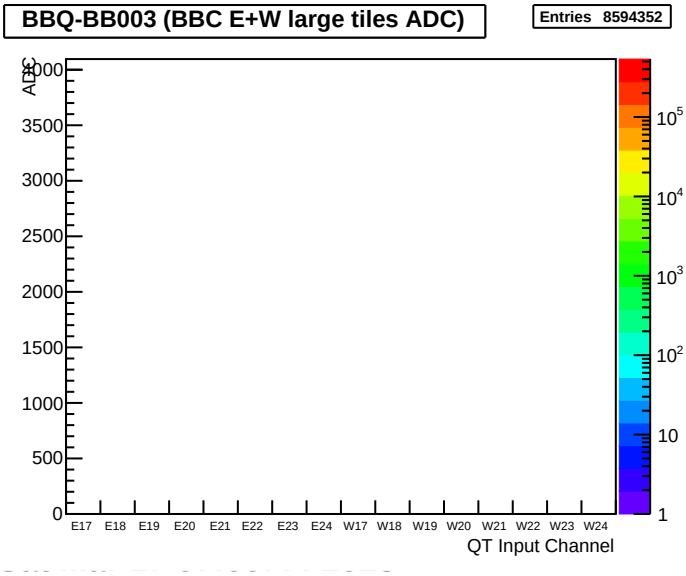

MXQ-PP001
Entries 8594352
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

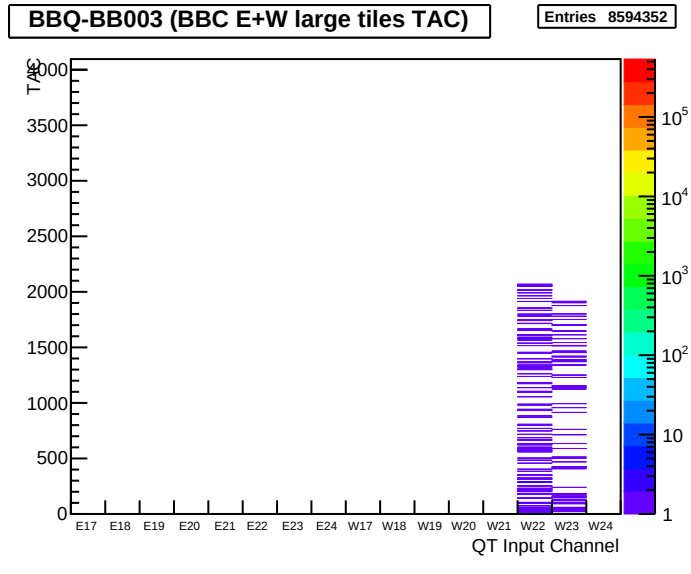

BBQ-BB001 (BBC east small tiles ADC)
Entries 8594352
ADC
QT Input Channel

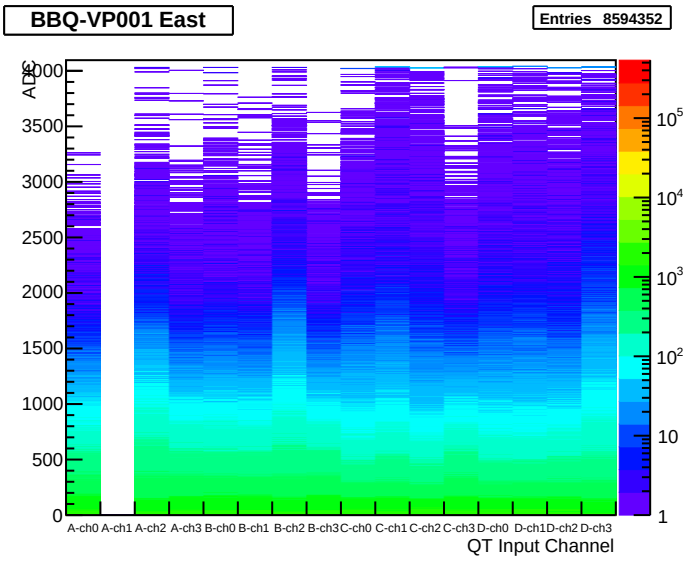

BBQ-BB001 (BBC east small tiles TAC)
Entries 8594352
TAC
QT Input Channel

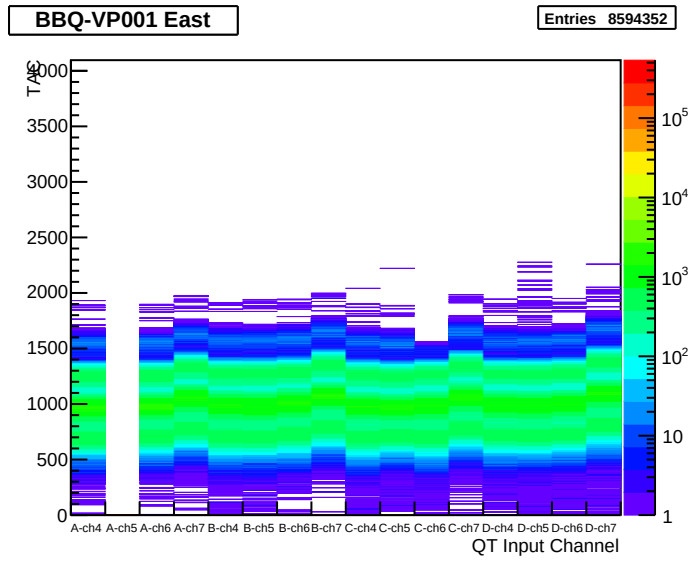

BBQ-BB002 (BBC west small tiles ADC)
Entries 8594352
ADC
QT Input Channel

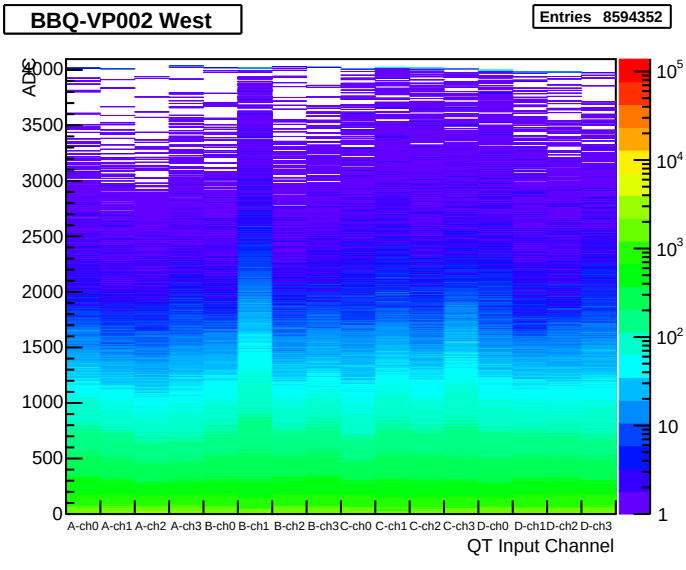

BBQ-BB002 (BBC west small tiles TAC)
Entries 8594352
TAC
QT Input Channel

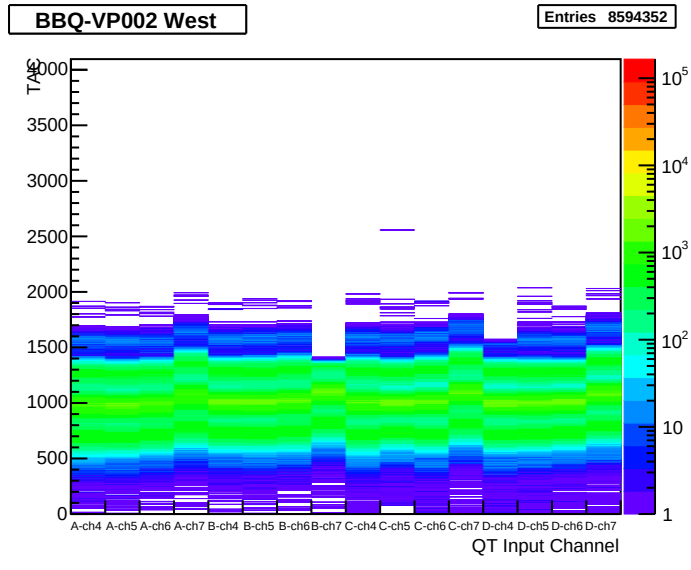

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8594352
ADC
QT Input Channel

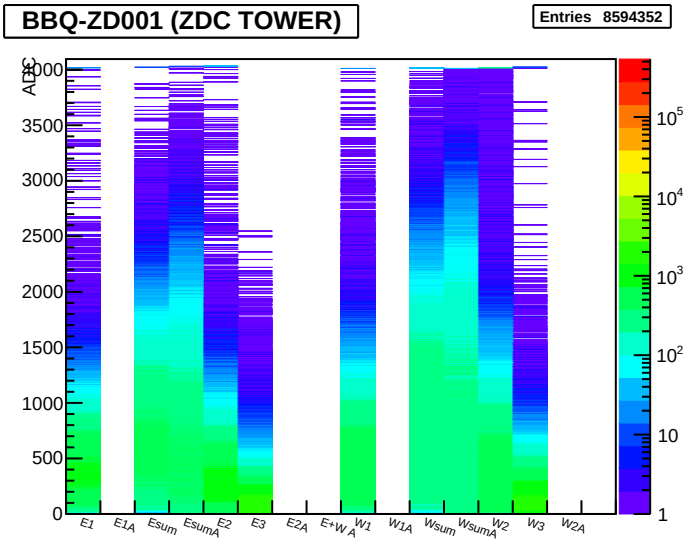

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8594352
TAC
QT Input Channel

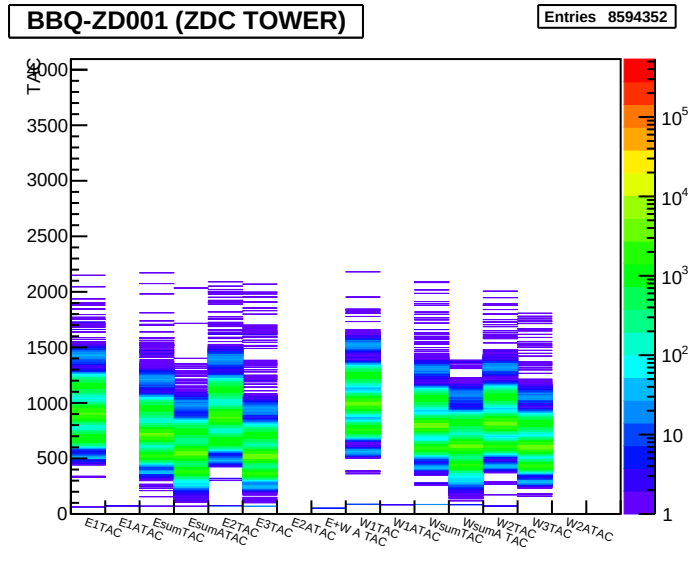

BBQ-VP001 East
Entries 8594352
ADC
QT Input Channel

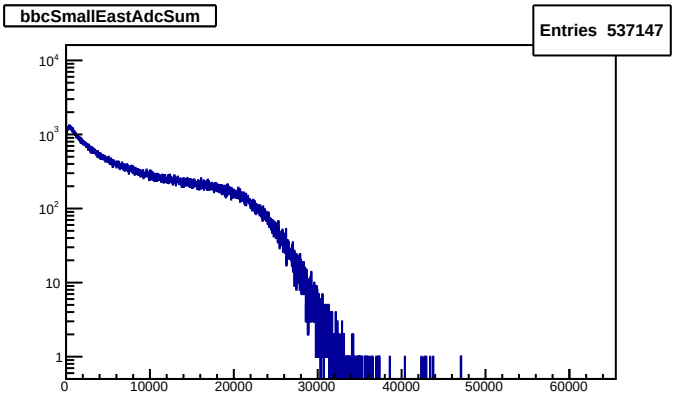

BBQ-VP001 East
Entries 8594352
TAC
QT Input Channel

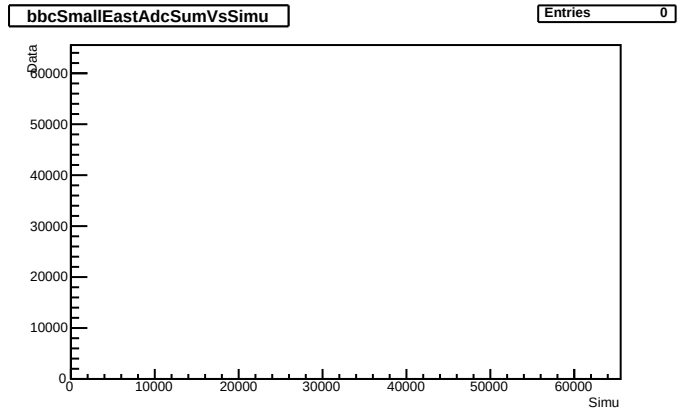

BBQ-VP002 West
Entries 8594352
ADC
QT Input Channel

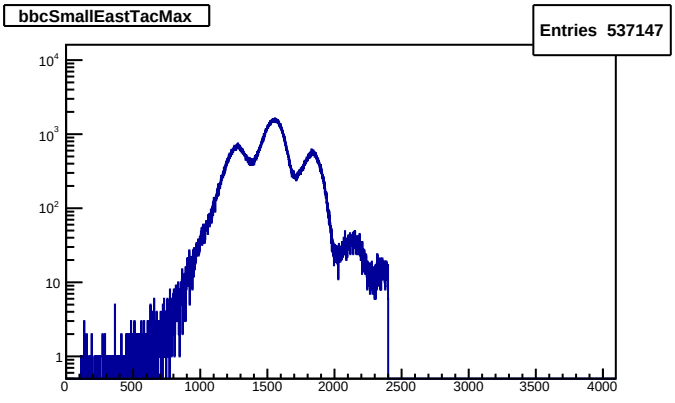

BBQ-VP002 West
Entries 8594352
TAC
QT Input Channel

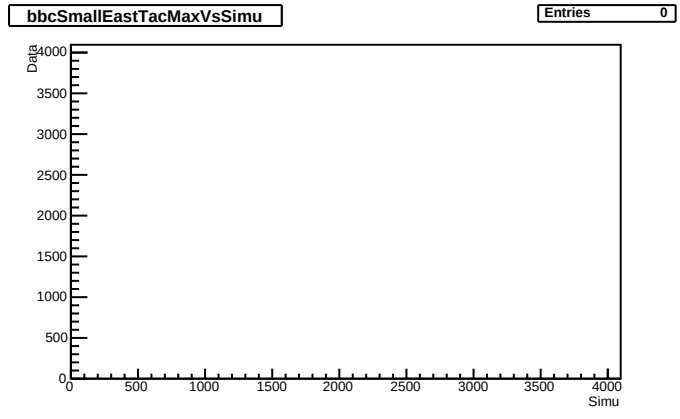

BBQ-ZD001 (ZDC TOWER)
Entries 8594352
ADC

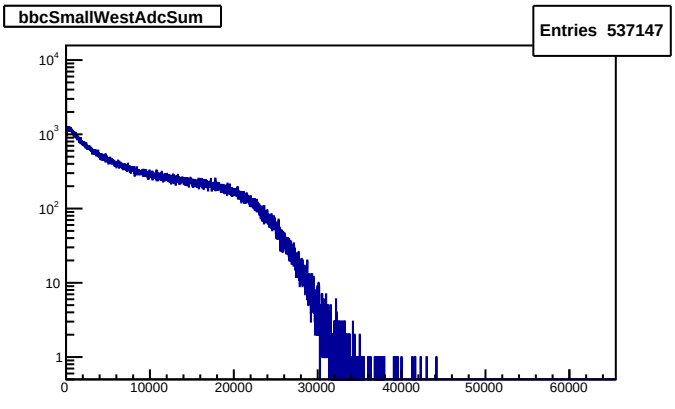

BBQ-ZD001 (ZDC TOWER)
Entries 8594352
TAC

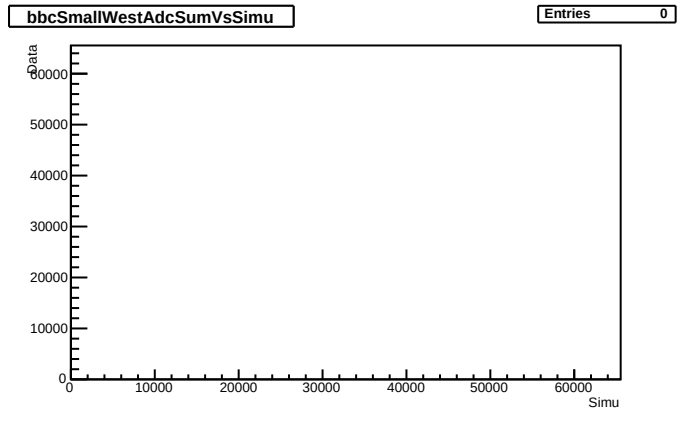

bbcSmallEastAdcSum
Entries 537147

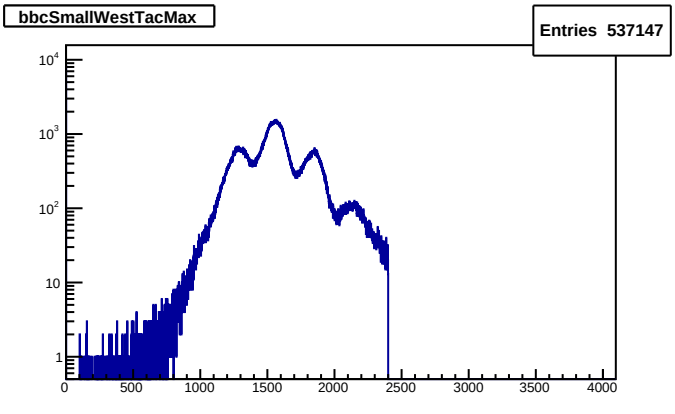

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

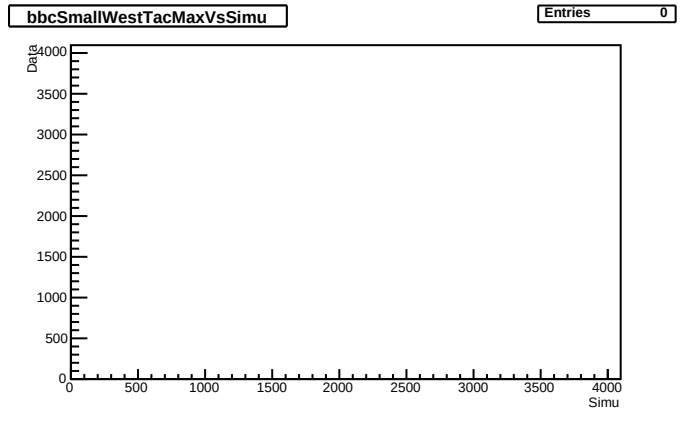

bbcSmallEastTacMax
Entries 537147

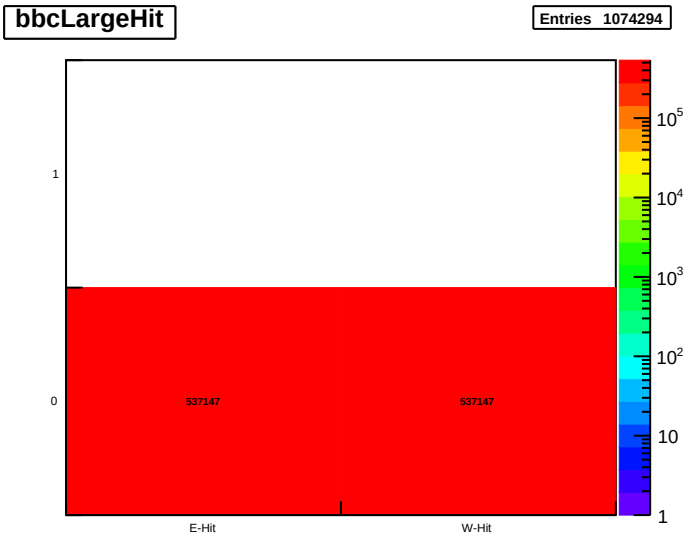

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

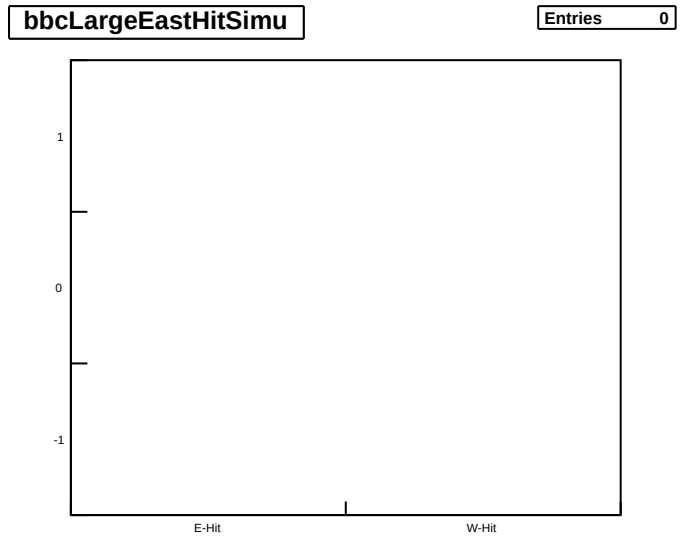

bbcSmallWestAdcSum
Entries 537147

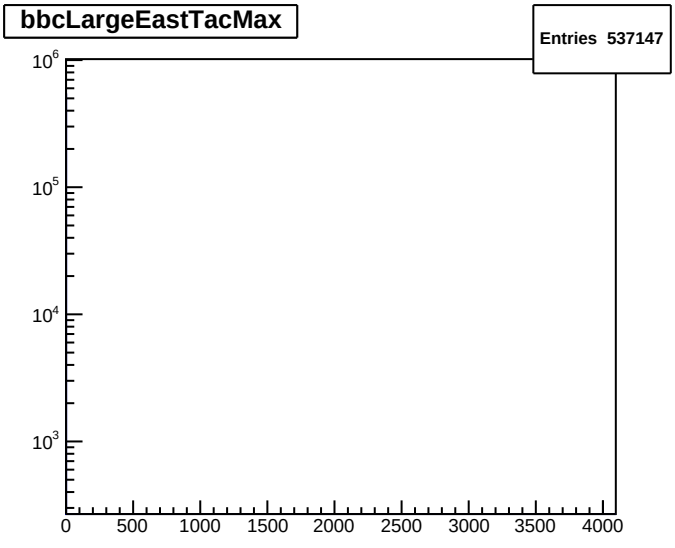

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

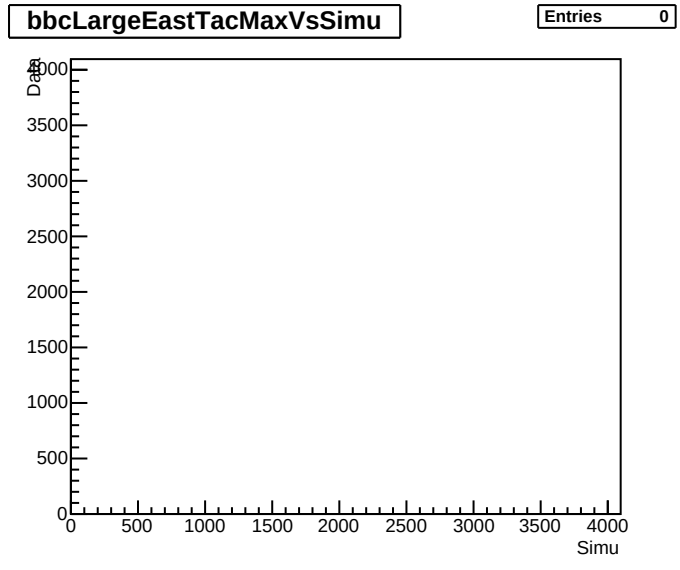

bbcSmallWestTacMax
Entries 537147

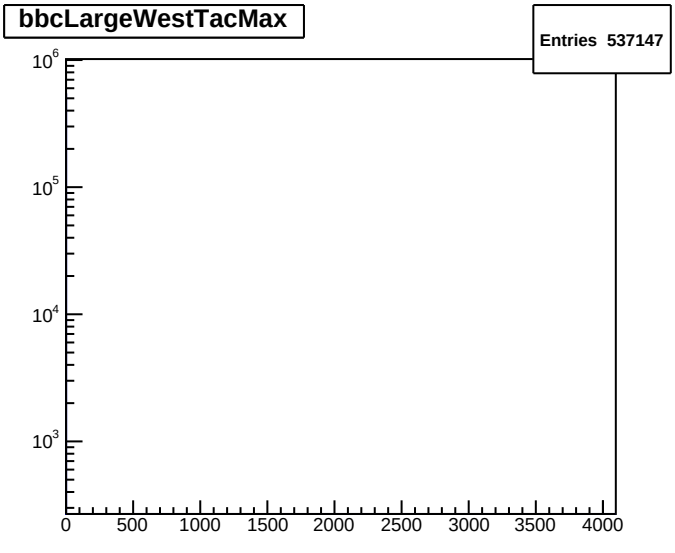

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

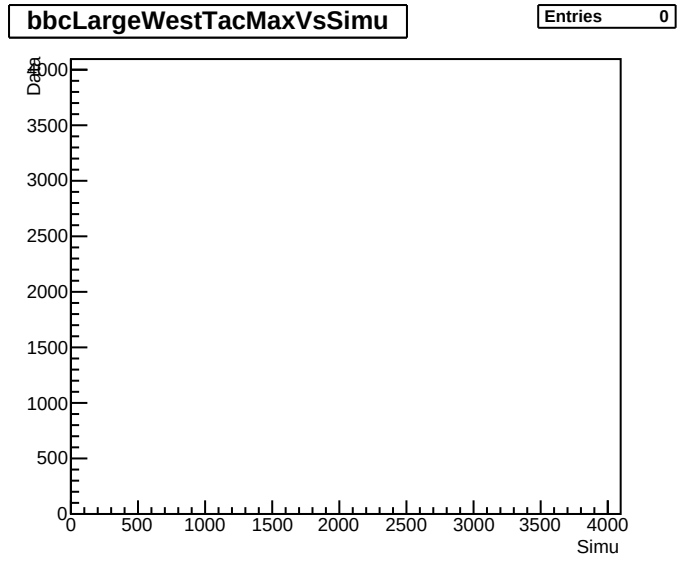

bbcLargeHit
Entries 1074294
537147
537147
E-Hit
W-Hit

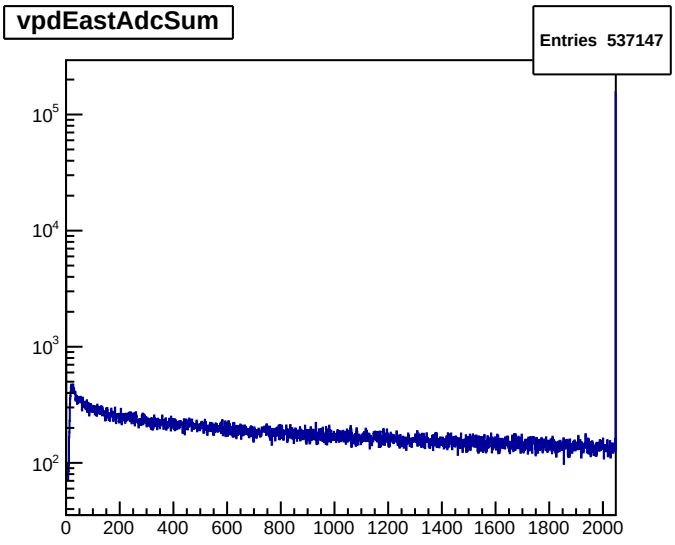

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

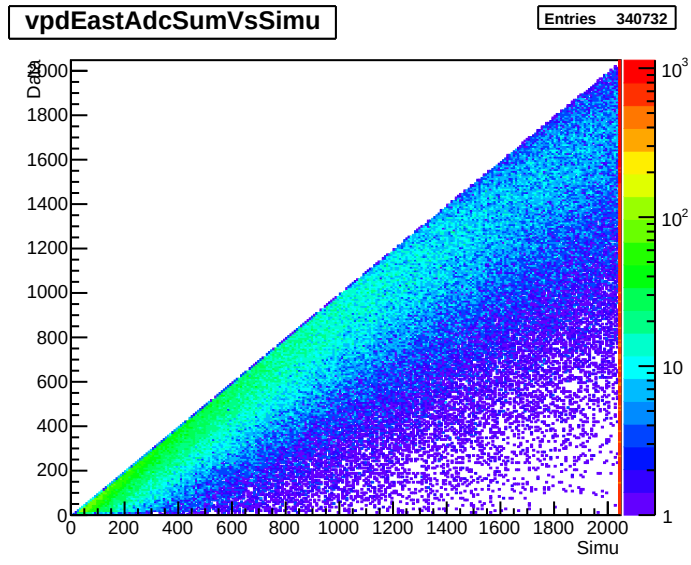

bbcLargeEastTacMax
Entries 537147

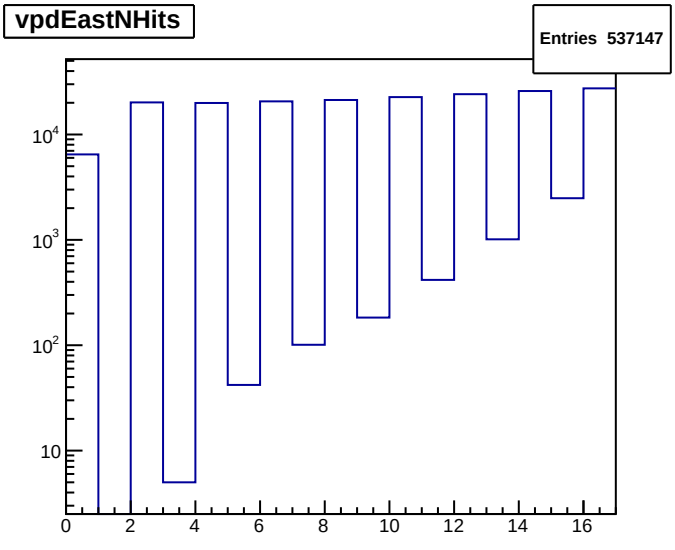

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

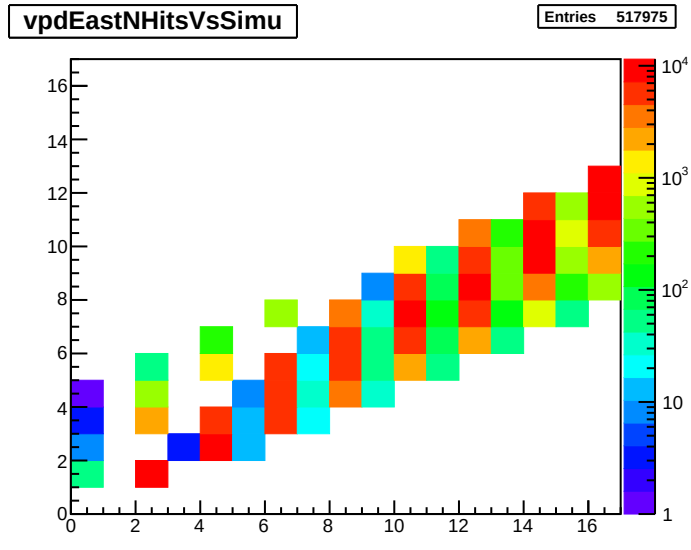

bbcLargeWestTacMax
Entries 537147

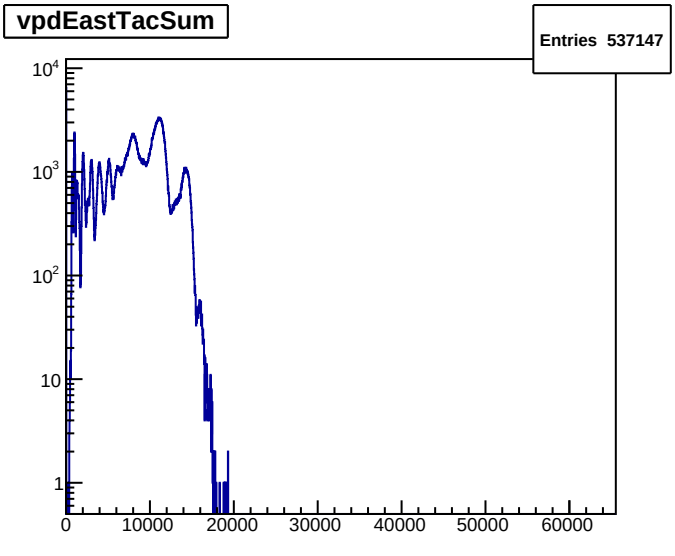

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

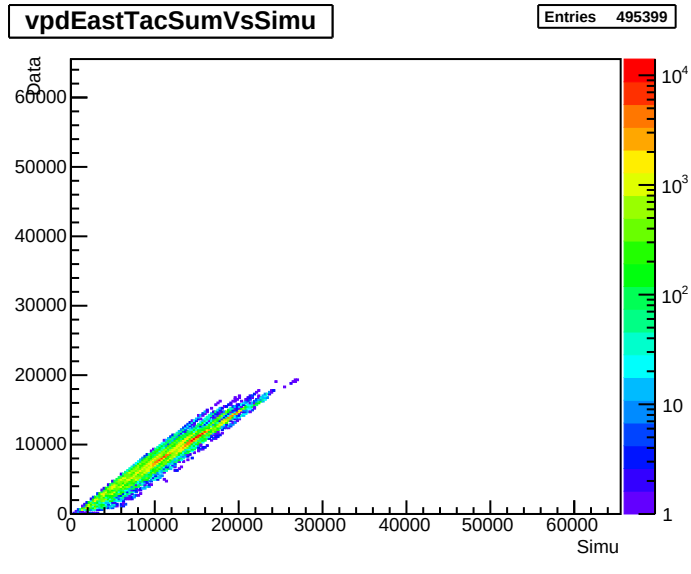

vpdEastAdcSum
Entries 537147

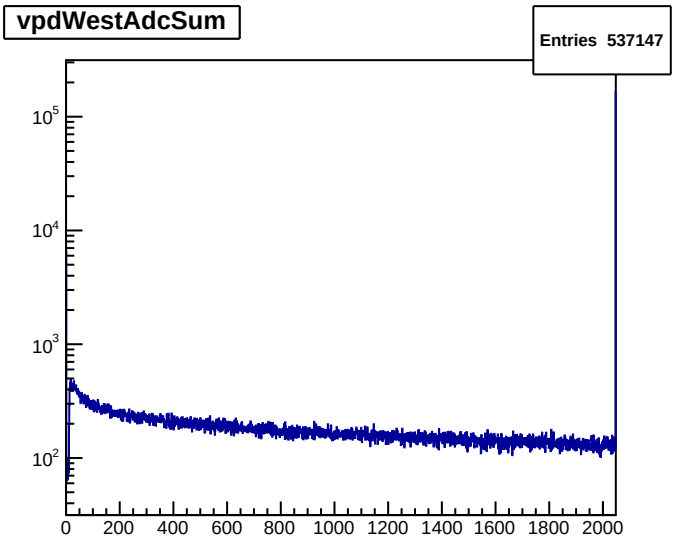

vpdEastAdcSumVsSimu
Entries 340732
Data
Simu

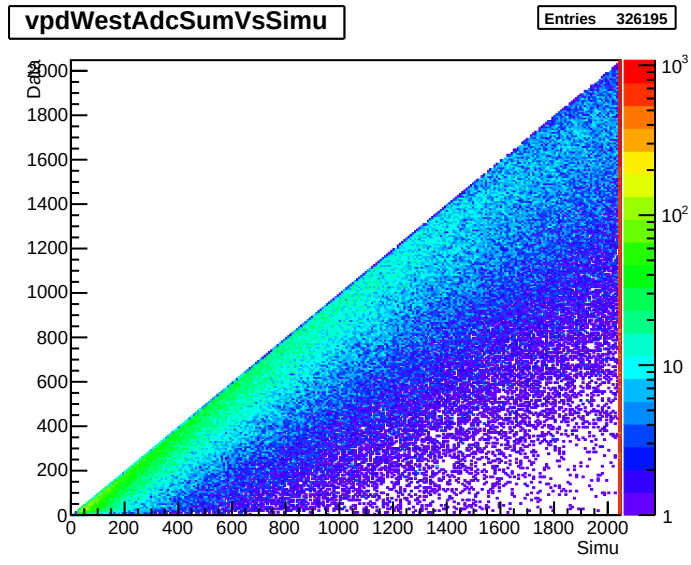

vpdEastNHits
Entries 537147

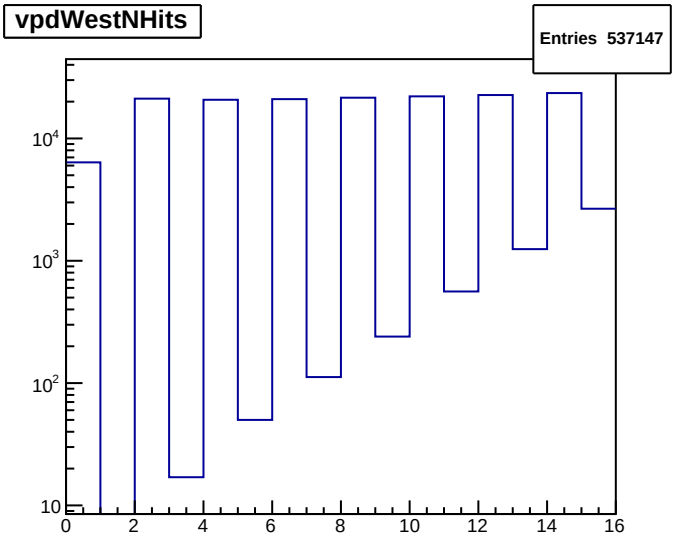

vpdEastNHitsVsSimu
Entries 517975

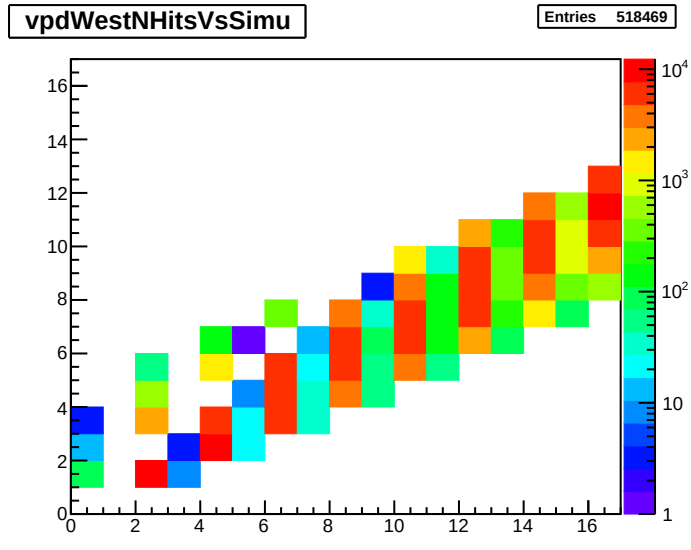

vpdEastTacSum
Entries 537147

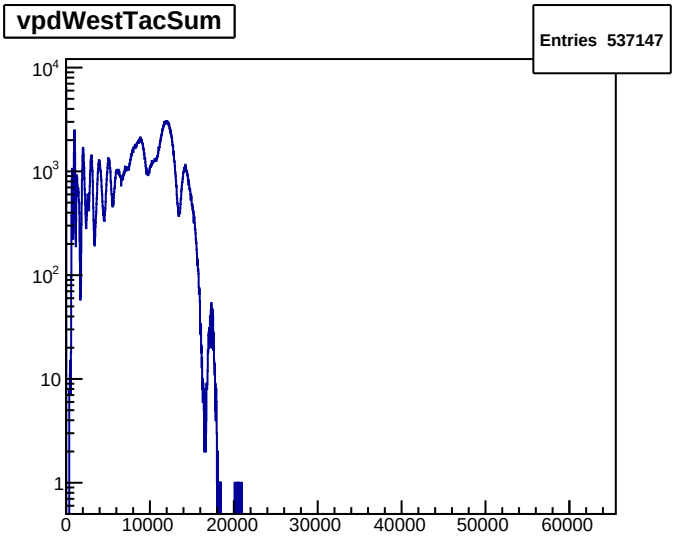

vpdEastTacSumVsSimu
Entries 495399
Data
Simu

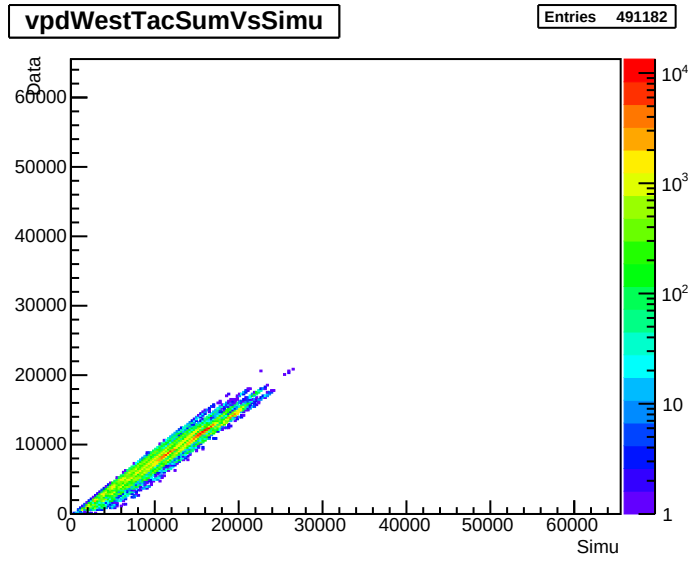

vpdWestAdcSum
Entries 537147

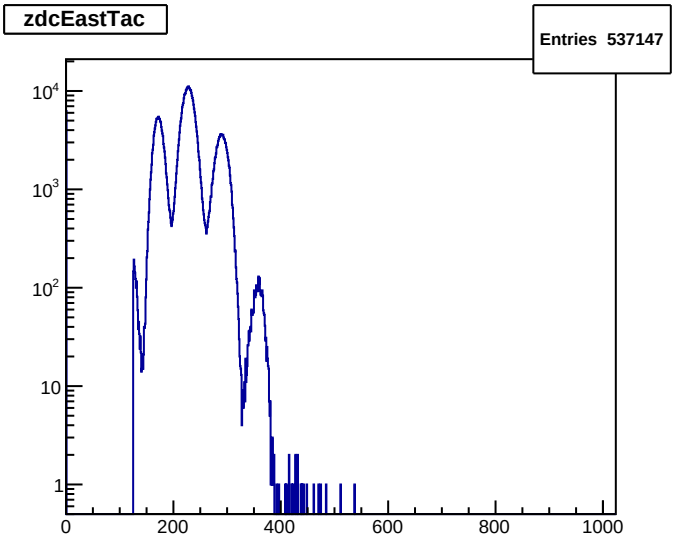

vpdWestAdcSumVsSimu
Entries 326195
Data
Simu

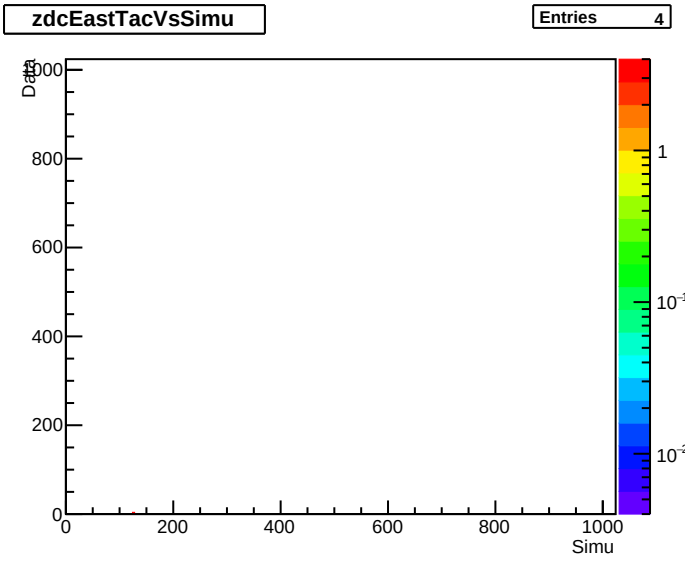

vpdWestNHits
Entries 537147

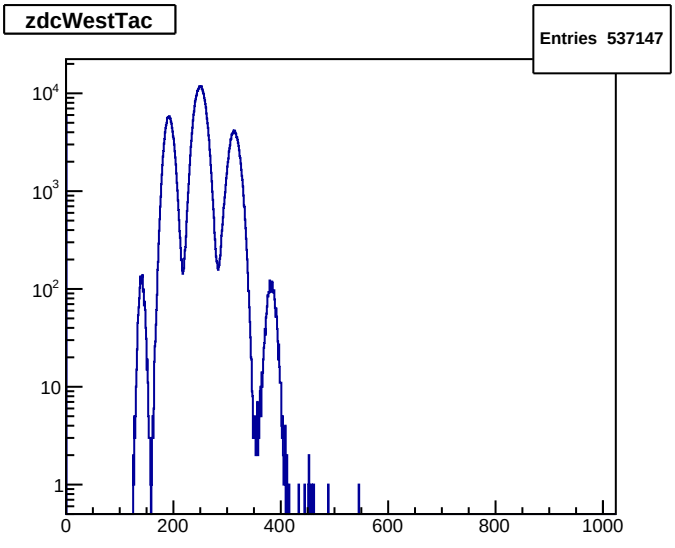

vpdWestNHitsVsSimu
Entries 518469

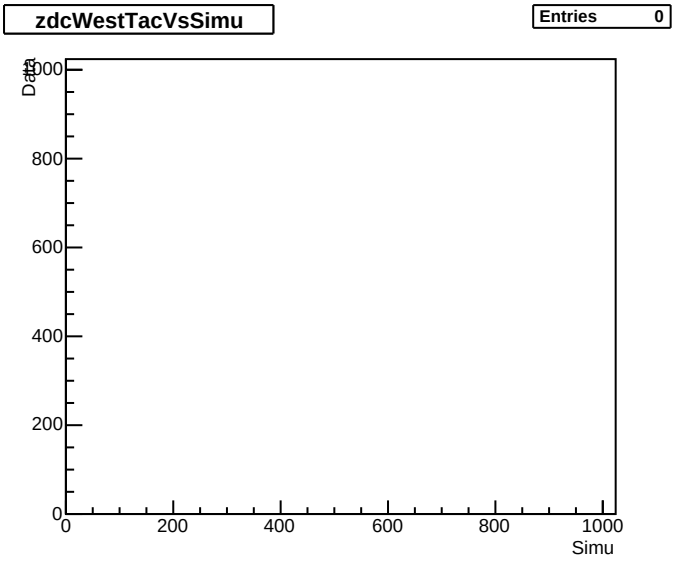

vpdWestTacSum
Entries 537147

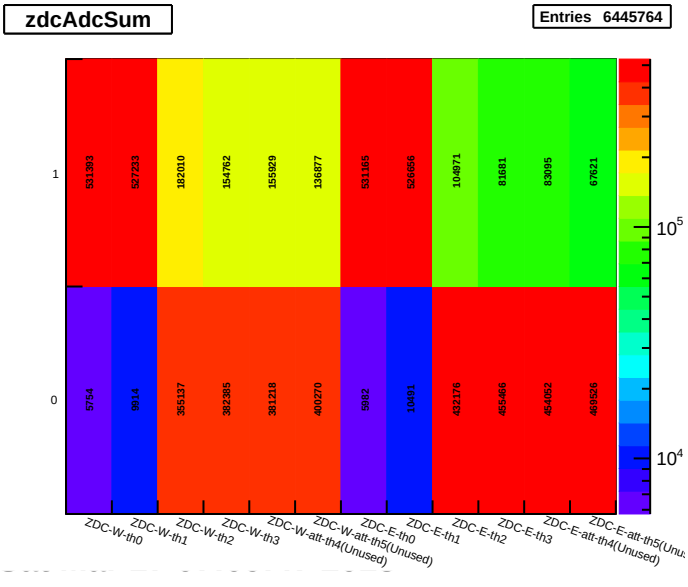

vpdWestTacSumVsSimu
Entries 491182
Data
Simu

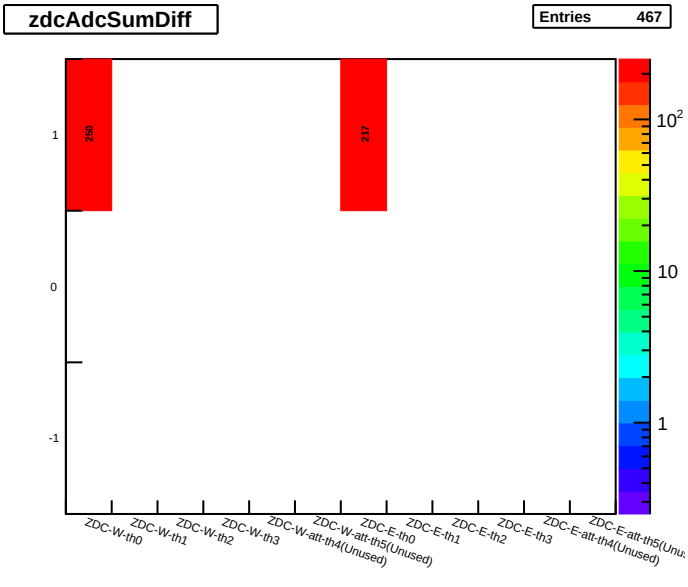

zdcEastTac
Entries 537147

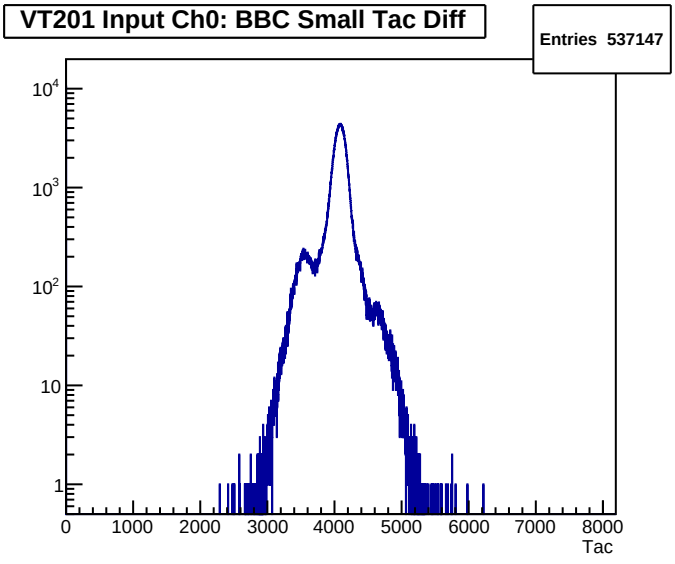

zdcEastTacVsSimu
Entries 4
Data
Simu

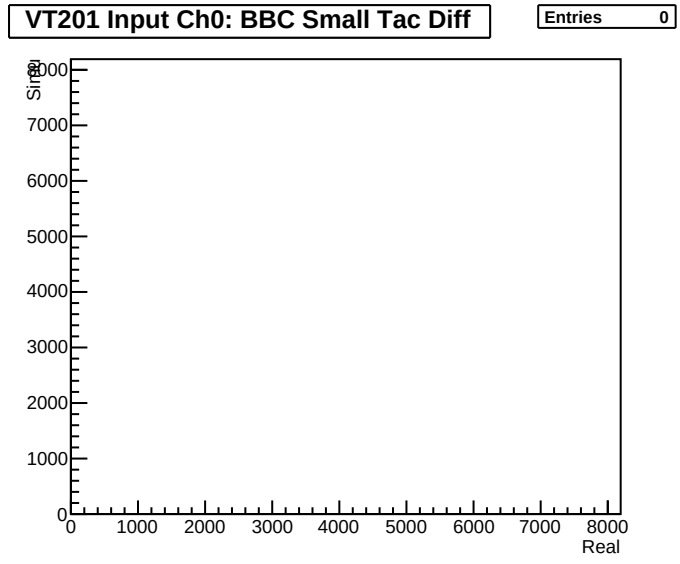

zdcWestTac
Entries 537147

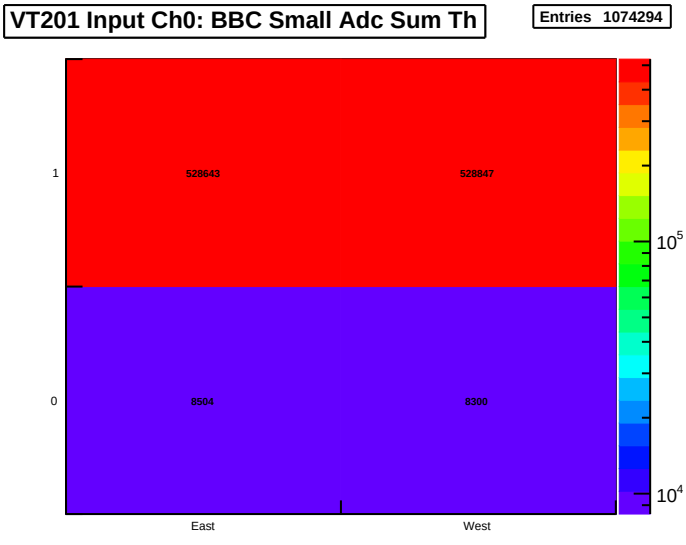

zdcWestTacVsSimu
Entries 0
Data
Simu

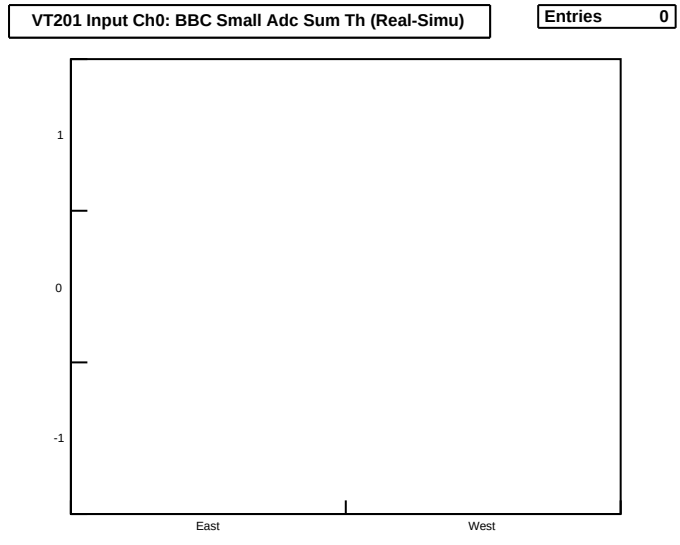

zdcAdcSum
Entries 6445764
531393
527233
182010
154762
155929
136877
531165
526656
104971
81681
83095
67621
5754
9914
355137
382385
381218
400270
5932
10491
432176
455466
454052
469526
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

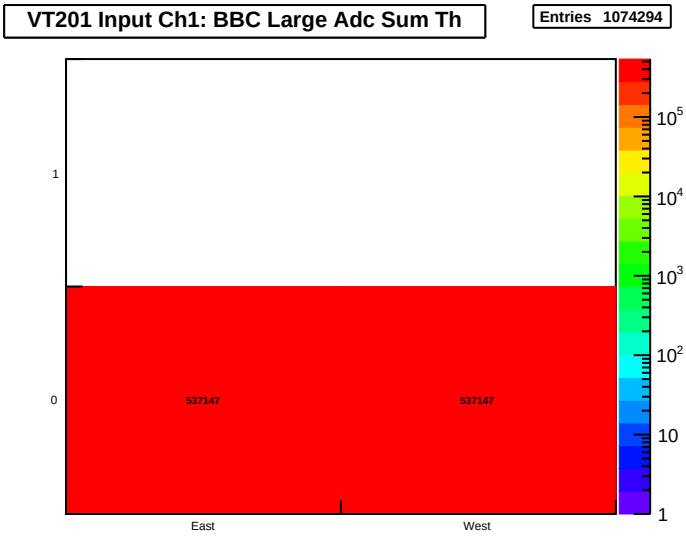

zdcAdcSumDiff
Entries 467
250
217
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

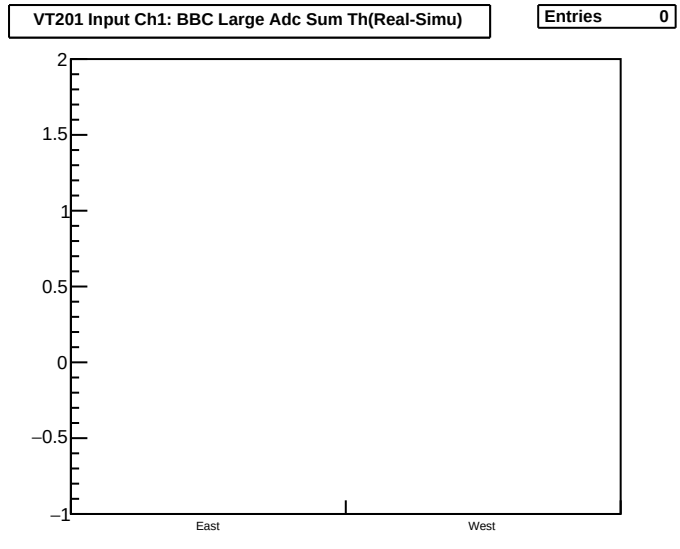

VT201 Input Ch0: BBC Small Tac Diff
Entries 537147
Tac

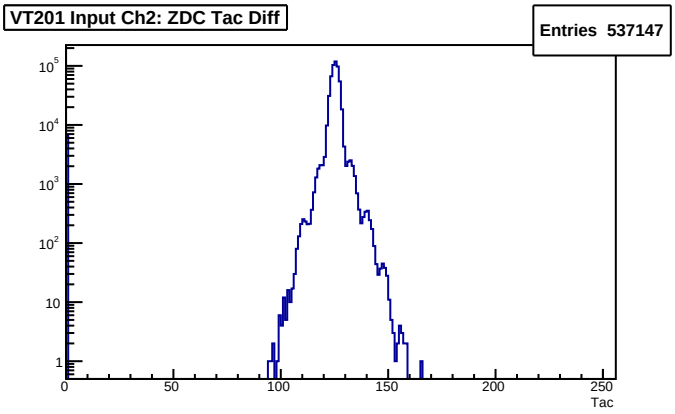

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

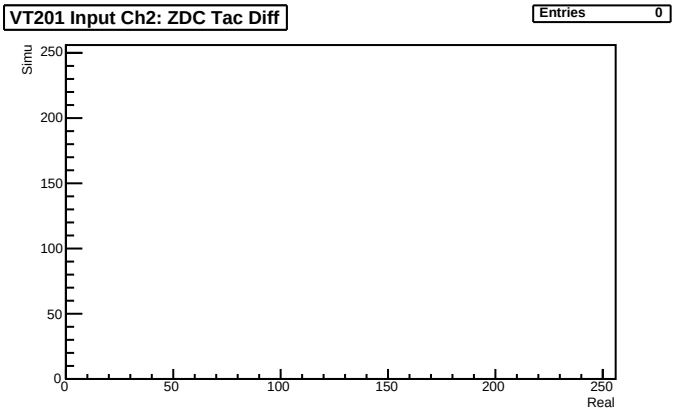

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 1074294
528643
528847
8504
8300
East
West

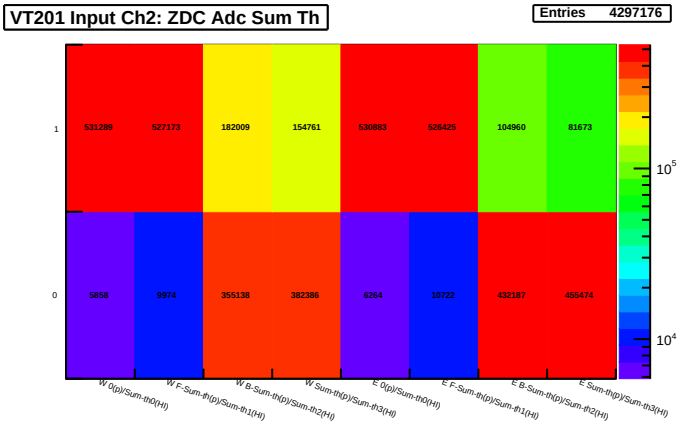

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

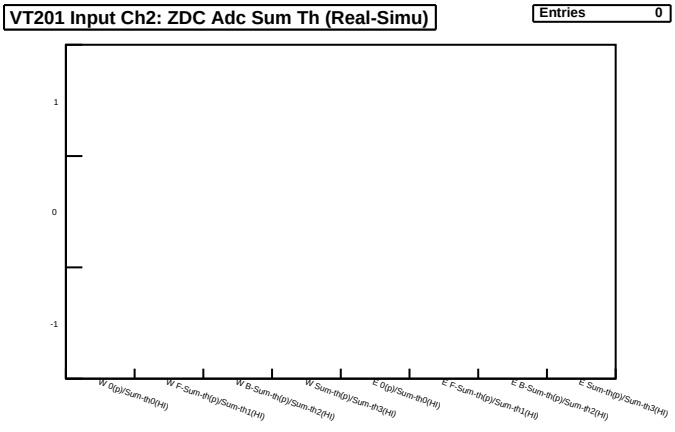

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 1074294
537147
537147
East
West

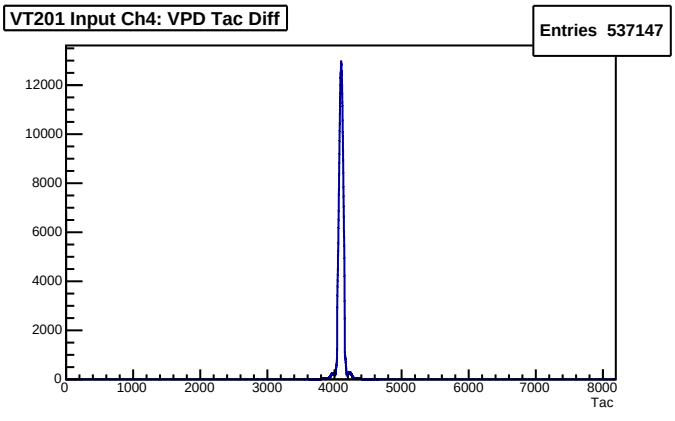

VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

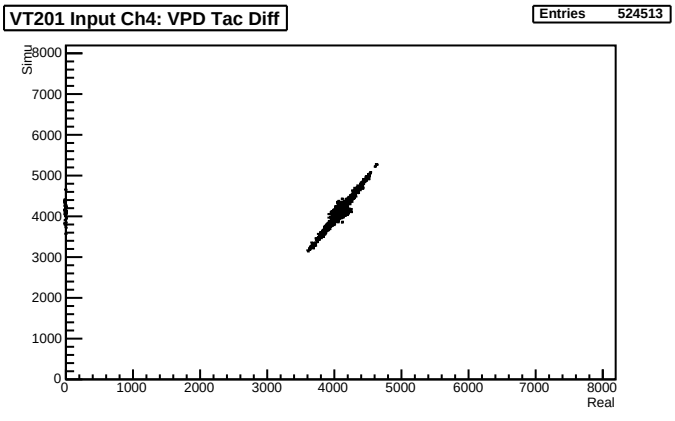

VT201 Input Ch2: ZDC Tac Diff
Entries 537147
Tac

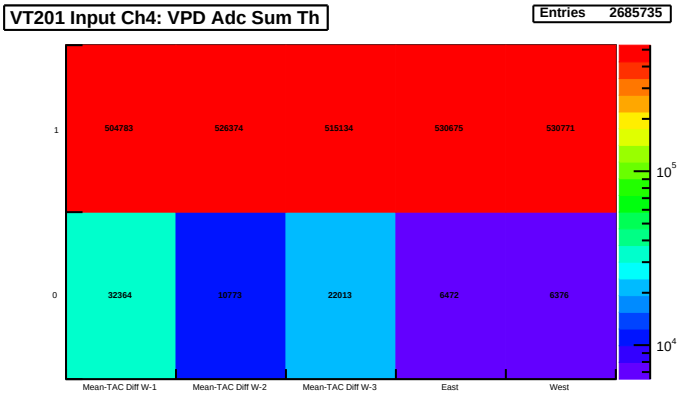

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

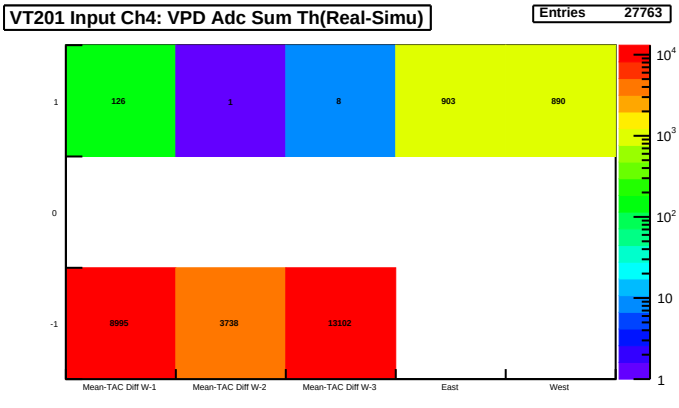

VT201 Input Ch2: ZDC Adc Sum Th
Entries 4297176
531289
527173
182009
154761
530883
526425
104960
81673
5858
9974
355138
382386
6264
10722
432187
455474
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

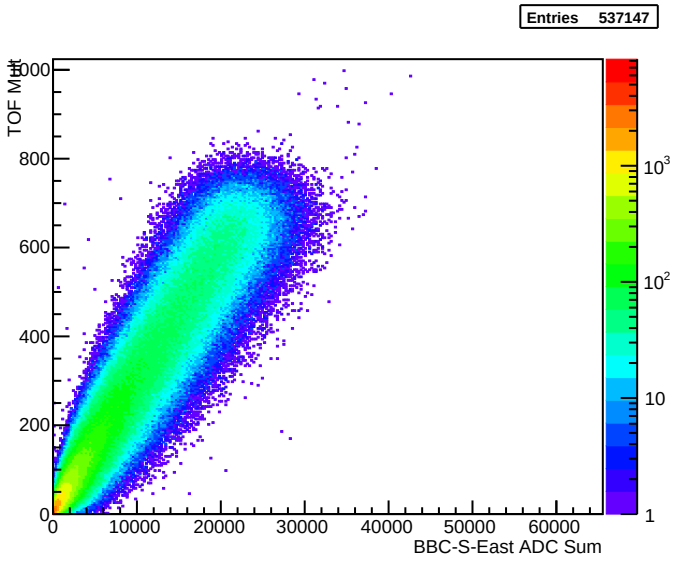

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

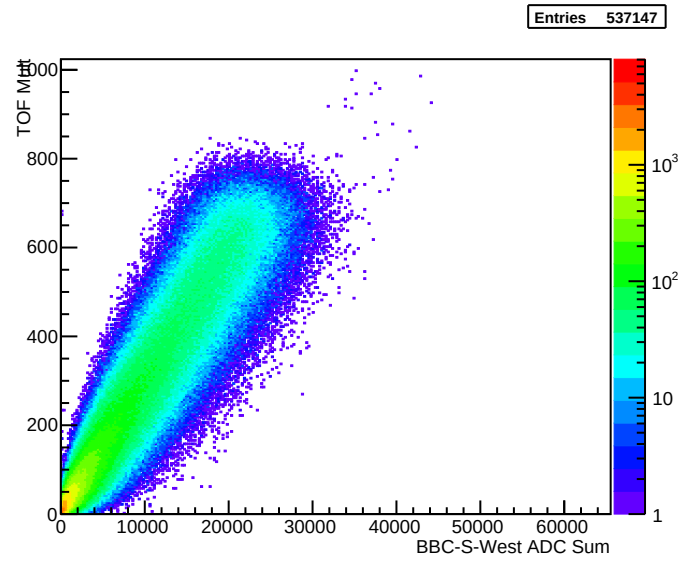

VT201 Input Ch4: VPD Tac Diff
Entries 537147
Tac

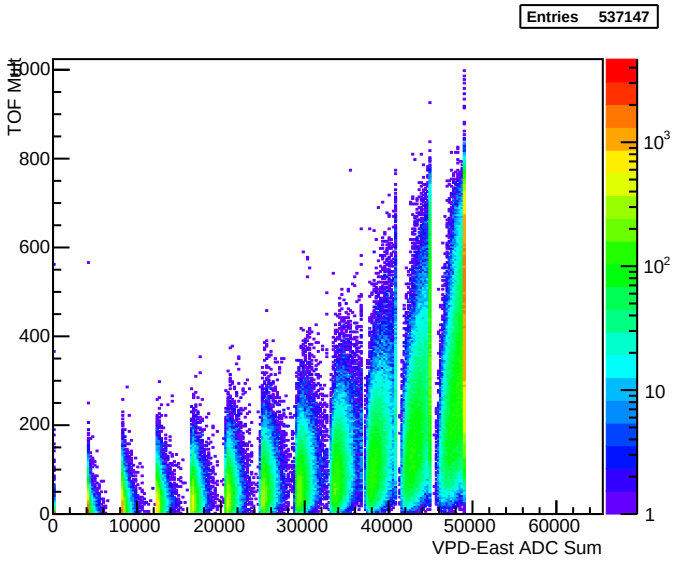

VT201 Input Ch4: VPD Tac Diff
Entries 524513
Simu
Real

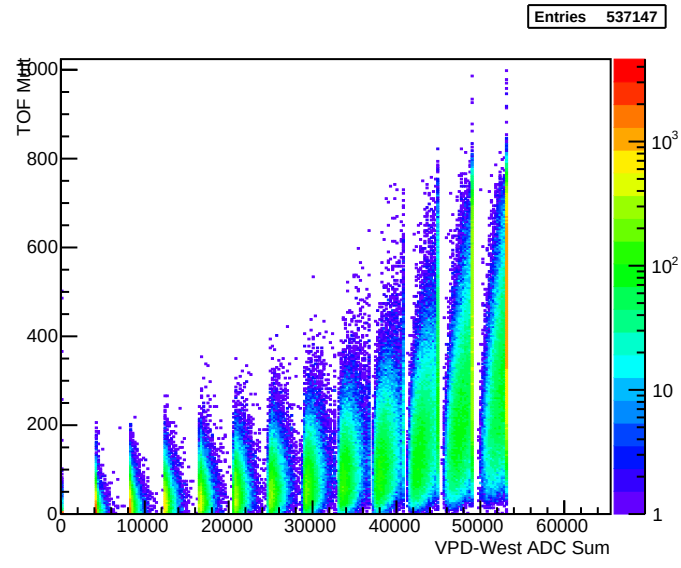

VT201 Input Ch4: VPD Adc Sum Th
Entries 2685735
504783
526374
515134
530675
530771
32364
10773
22013
6472
6376
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

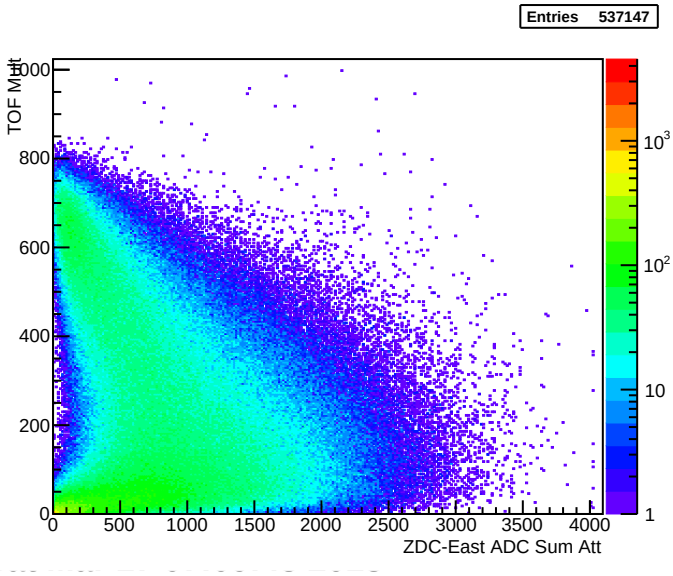

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 27763
126
1
8
903
890
8995
3738
13102
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

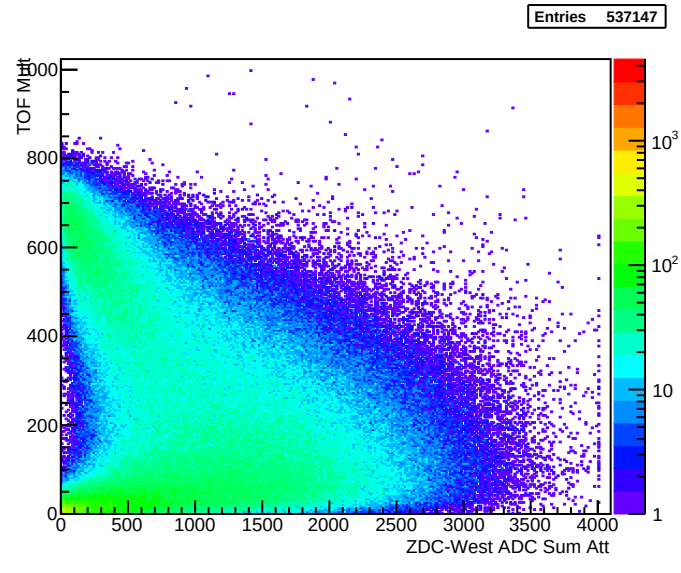

Entries 537147
TOF Mult
BBC-S-East ADC Sum

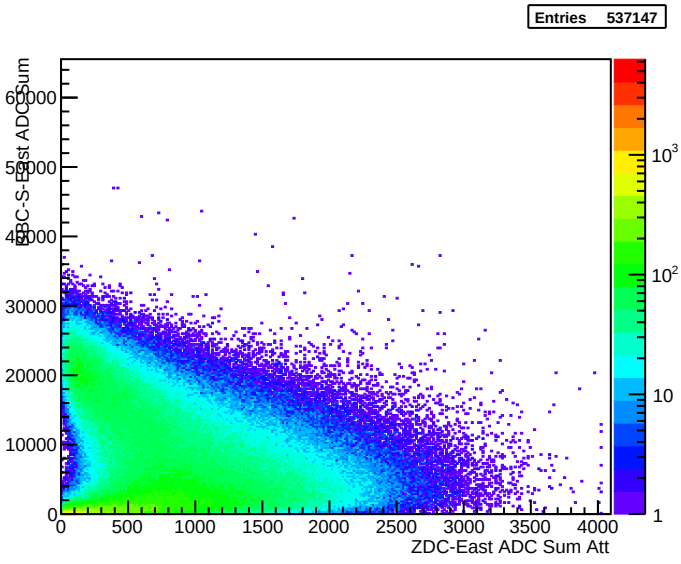

Entries 537147
TOF Mult
BBC-S-West ADC Sum

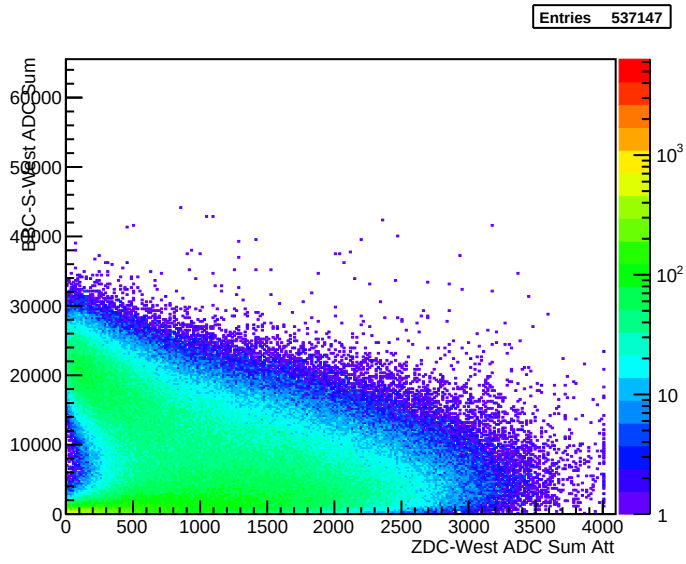

Entries 537147
TOF Mult
VPD-East ADC Sum

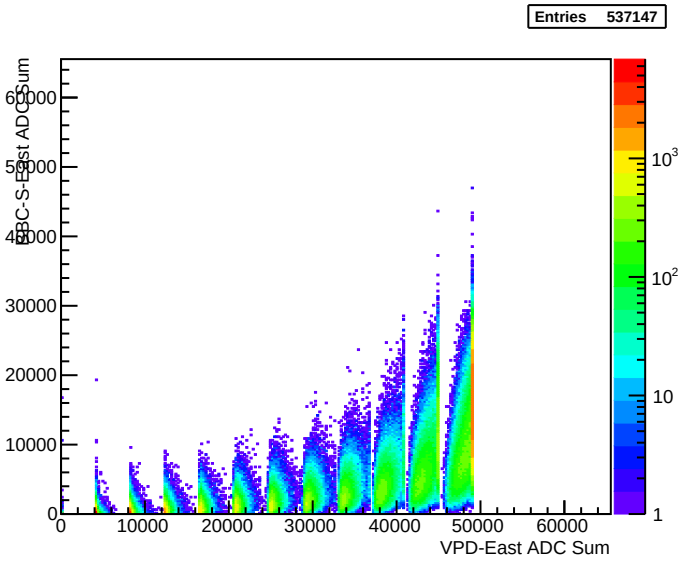

Entries 537147
TOF Mult
VPD-West ADC Sum

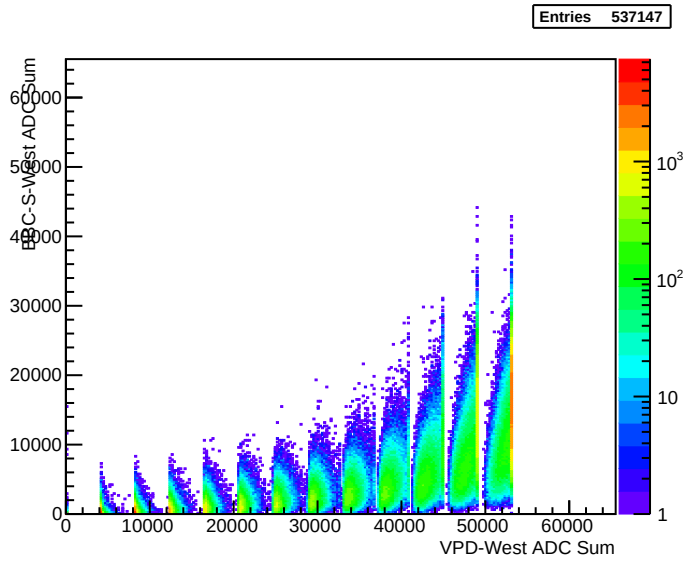

Entries 537147
TOF Mult
ZDC-East ADC Sum Att

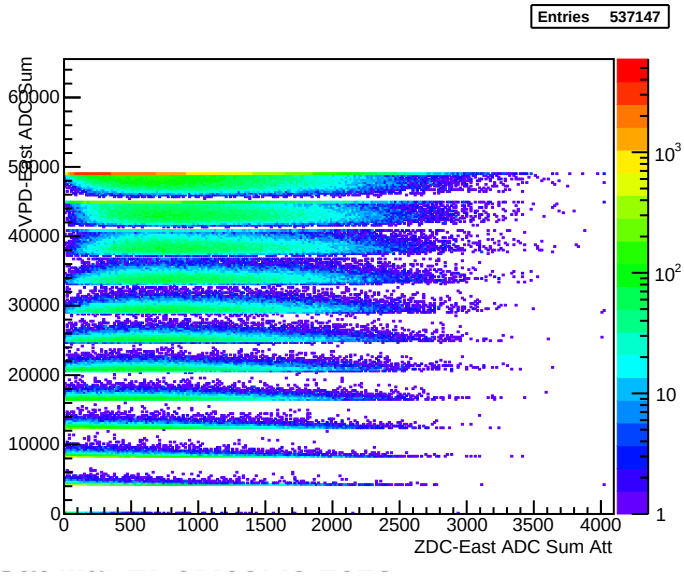

Entries 537147
TOF Mult
ZDC-West ADC Sum Att

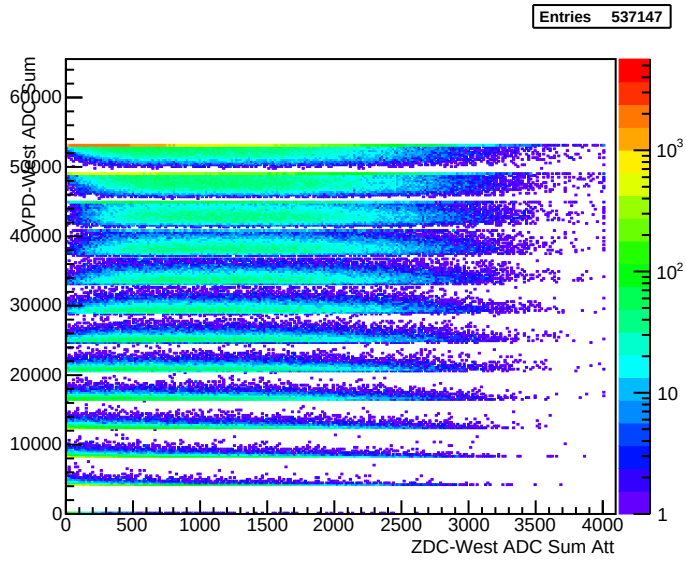

Entries 537147
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

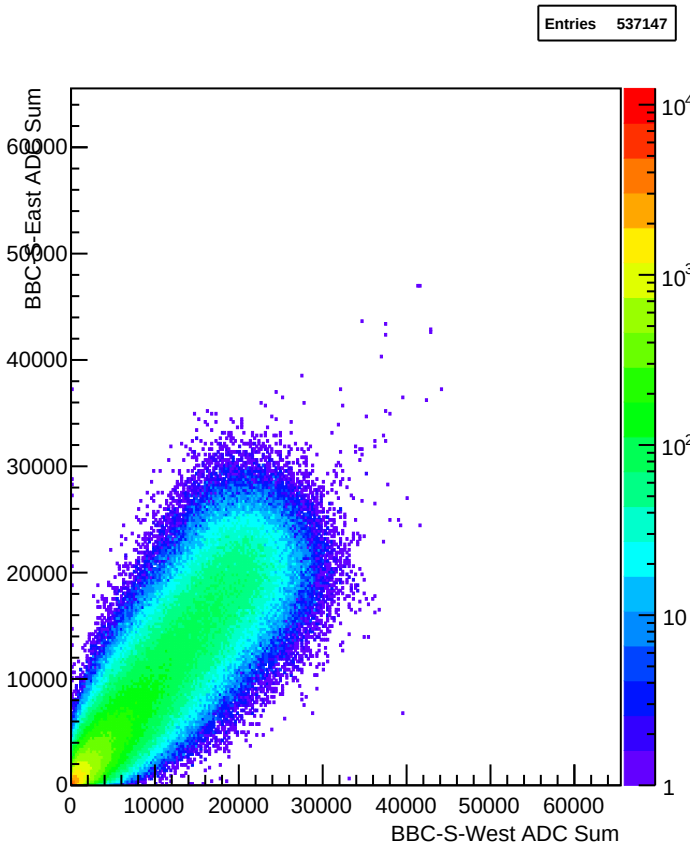

Entries 537147
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

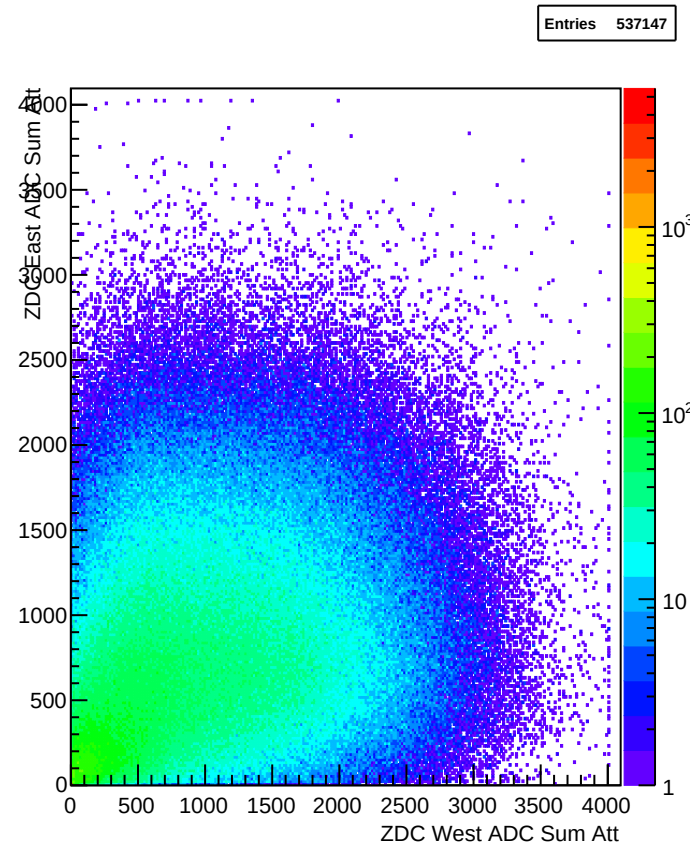

Entries 537147
BBC-S-East ADC Sum
VPD-East ADC Sum

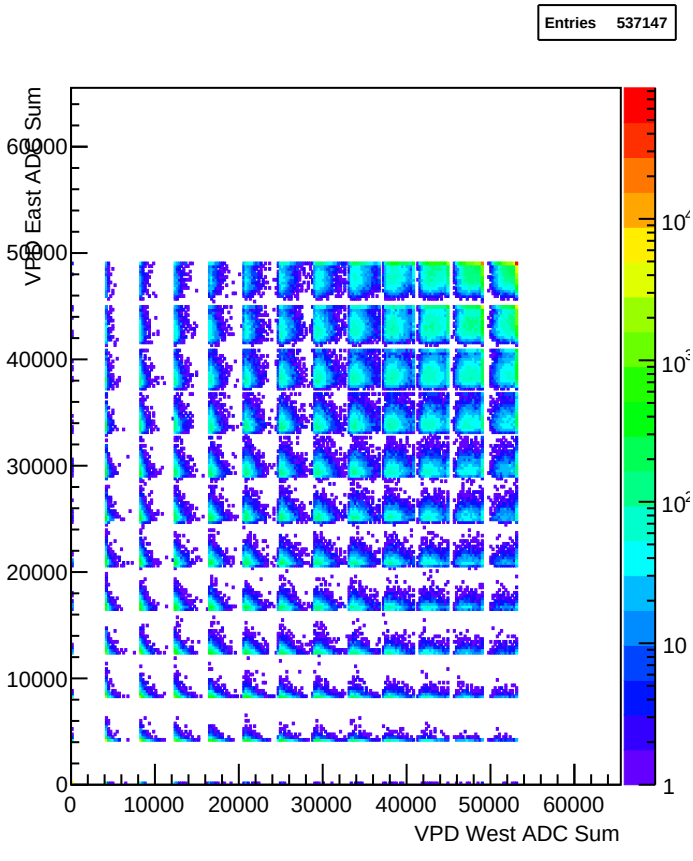

Entries 537147
BBC-S-West ADC Sum
VPD-West ADC Sum

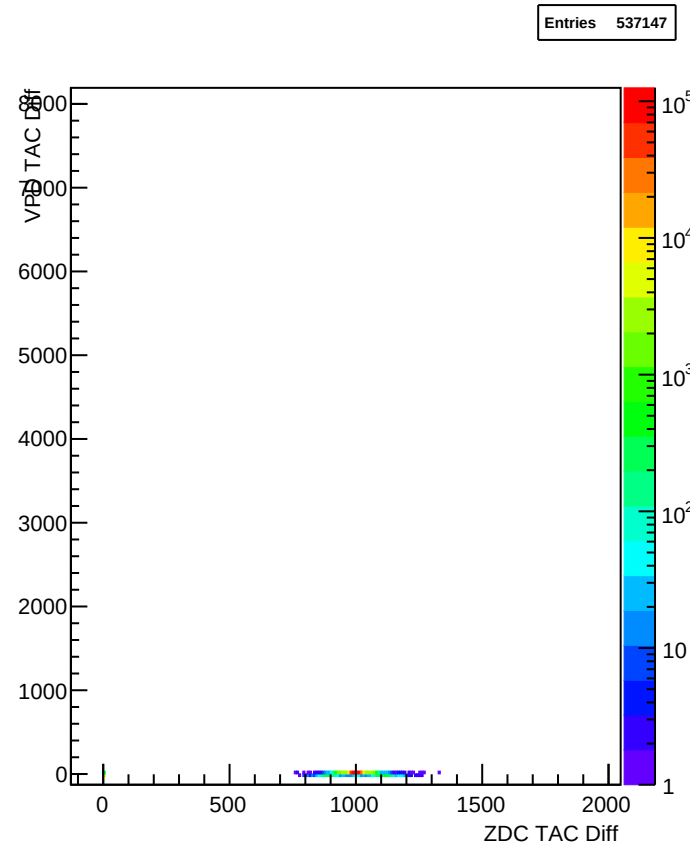

Entries 537147
VPD-East ADC Sum
ZDC-East ADC Sum Att

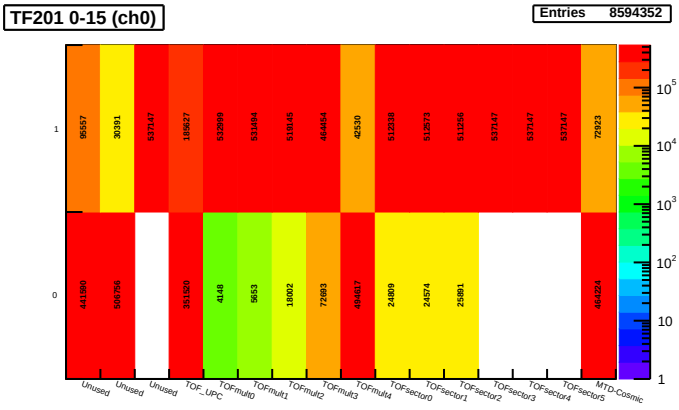

Entries 537147
VPD-West ADC Sum
ZDC-West ADC Sum Att

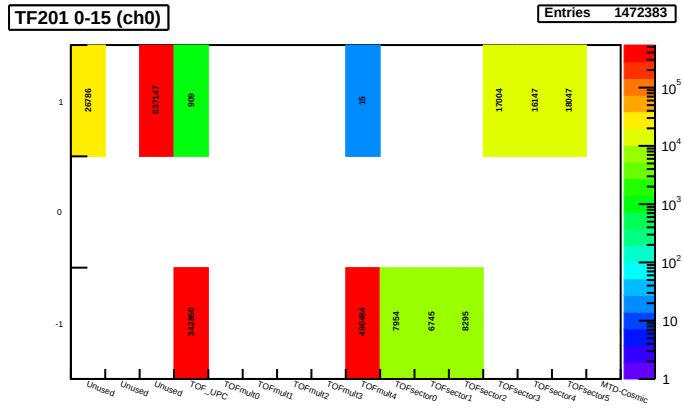

Entries 537147
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 537147
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 537147
VPD East ADC Sum
VPD West ADC Sum


Entries 537147
VPD TAC Diff
ZDC TAC Diff


TF201 0-15 (ch0)
Entries 8594352
95557 30391 537147 189627 532899 531494 519845 464454 42530 512338 512573 511256 537147 537147 537147 72923
441590 506756 351520 4148 5653 18002 72693 494617 24809 24574 25891 464224
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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Entries 1472383
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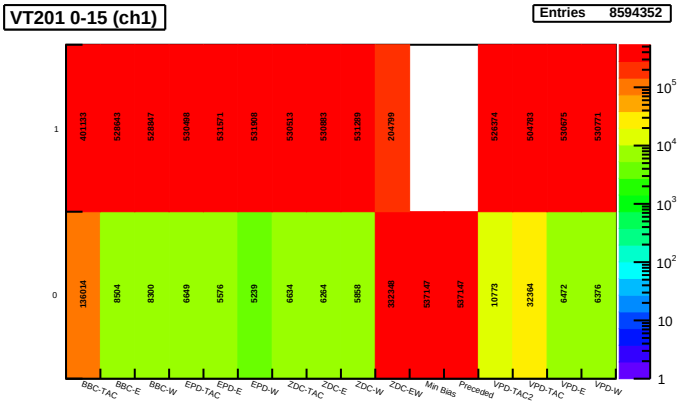
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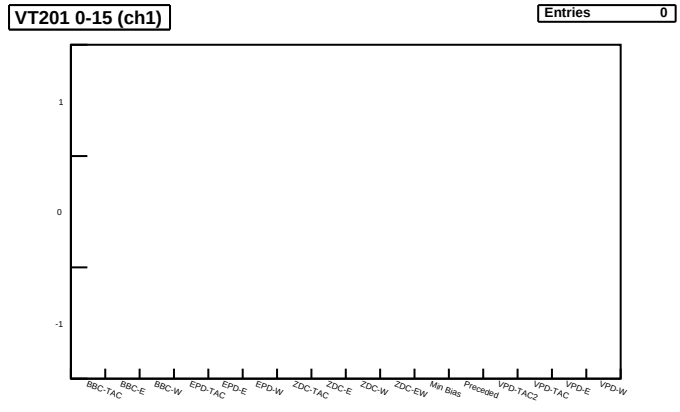
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W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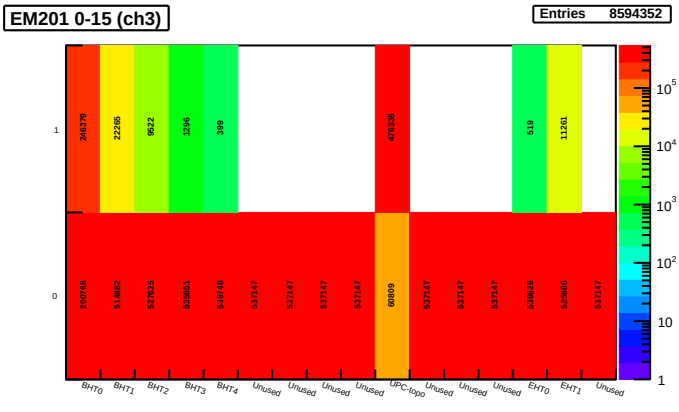
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290768 514882 527635 538651 538748 537147 537147 537147 537147 60809 537147 537147 537147 538628 528886 537147
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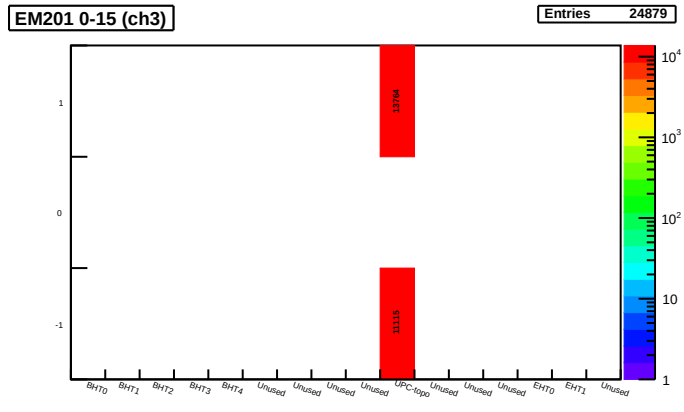
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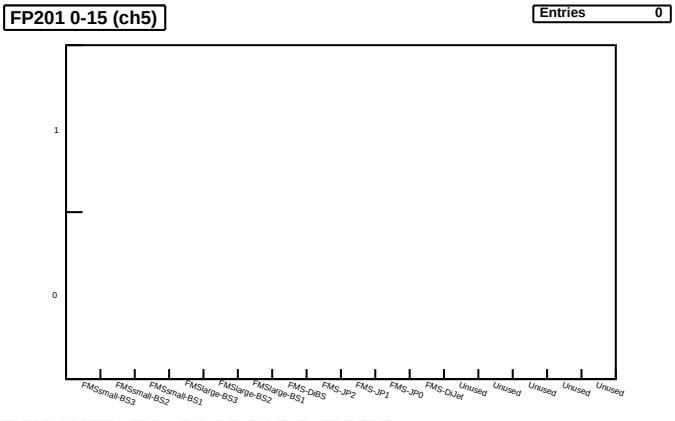
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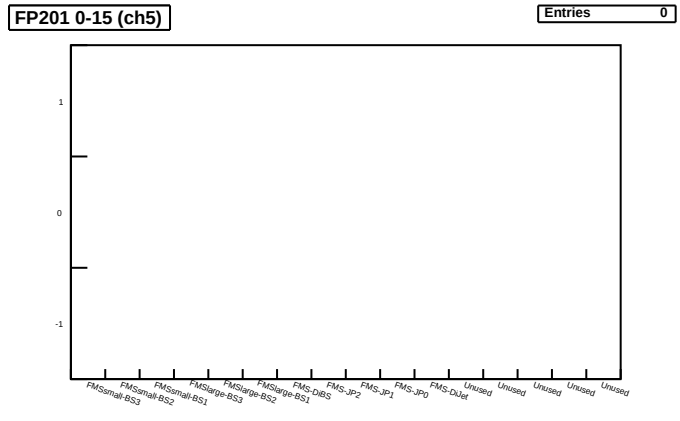
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