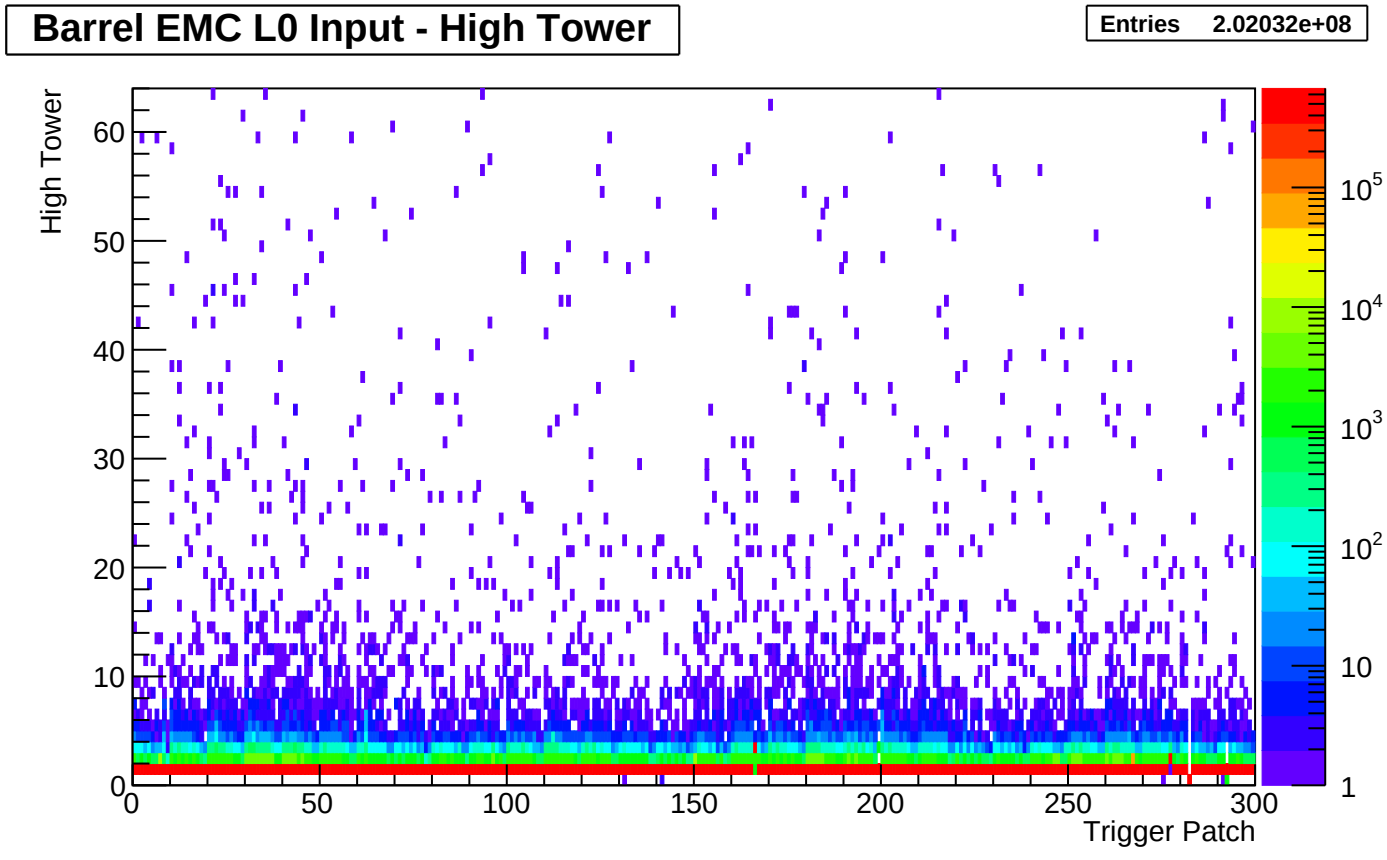
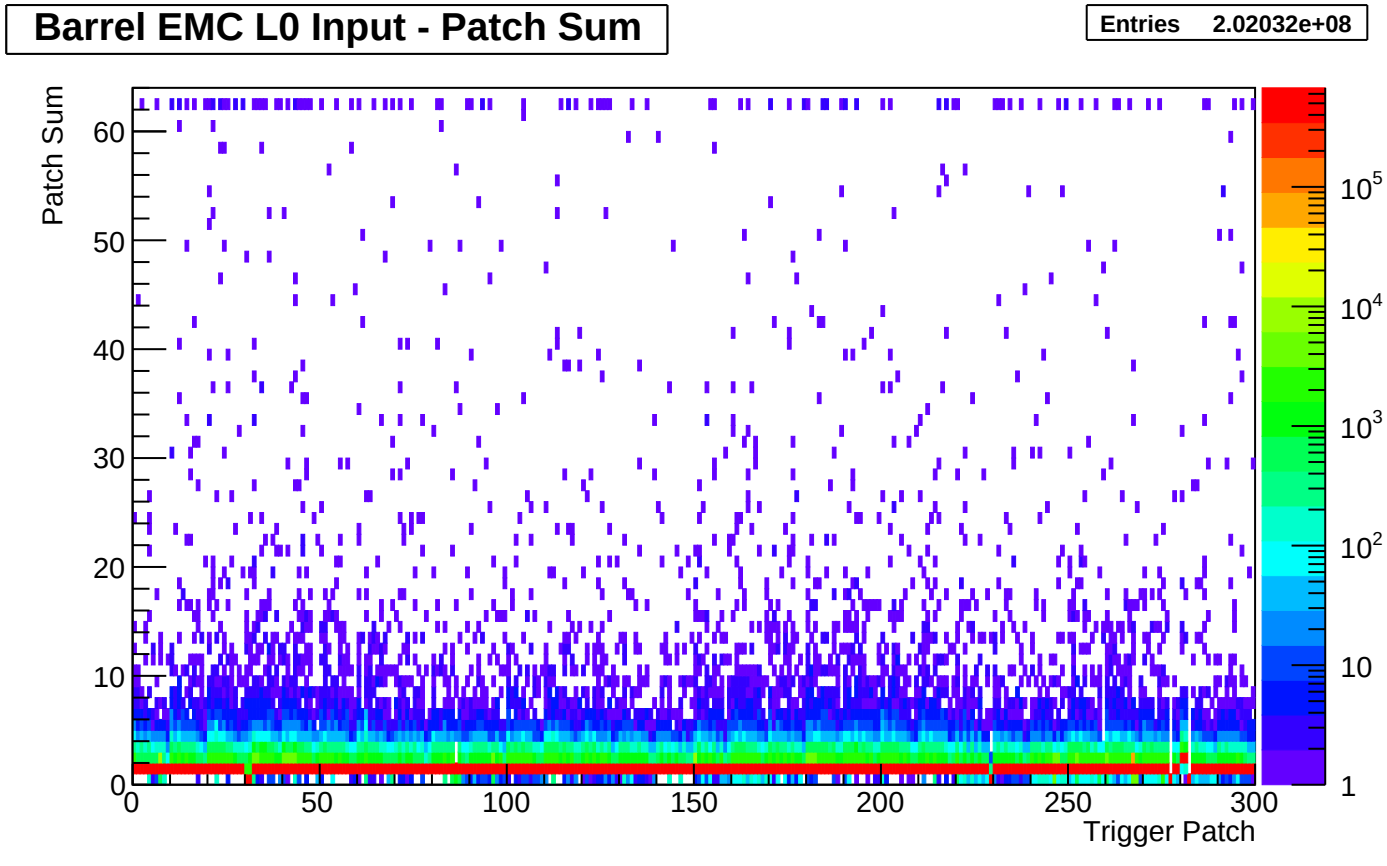
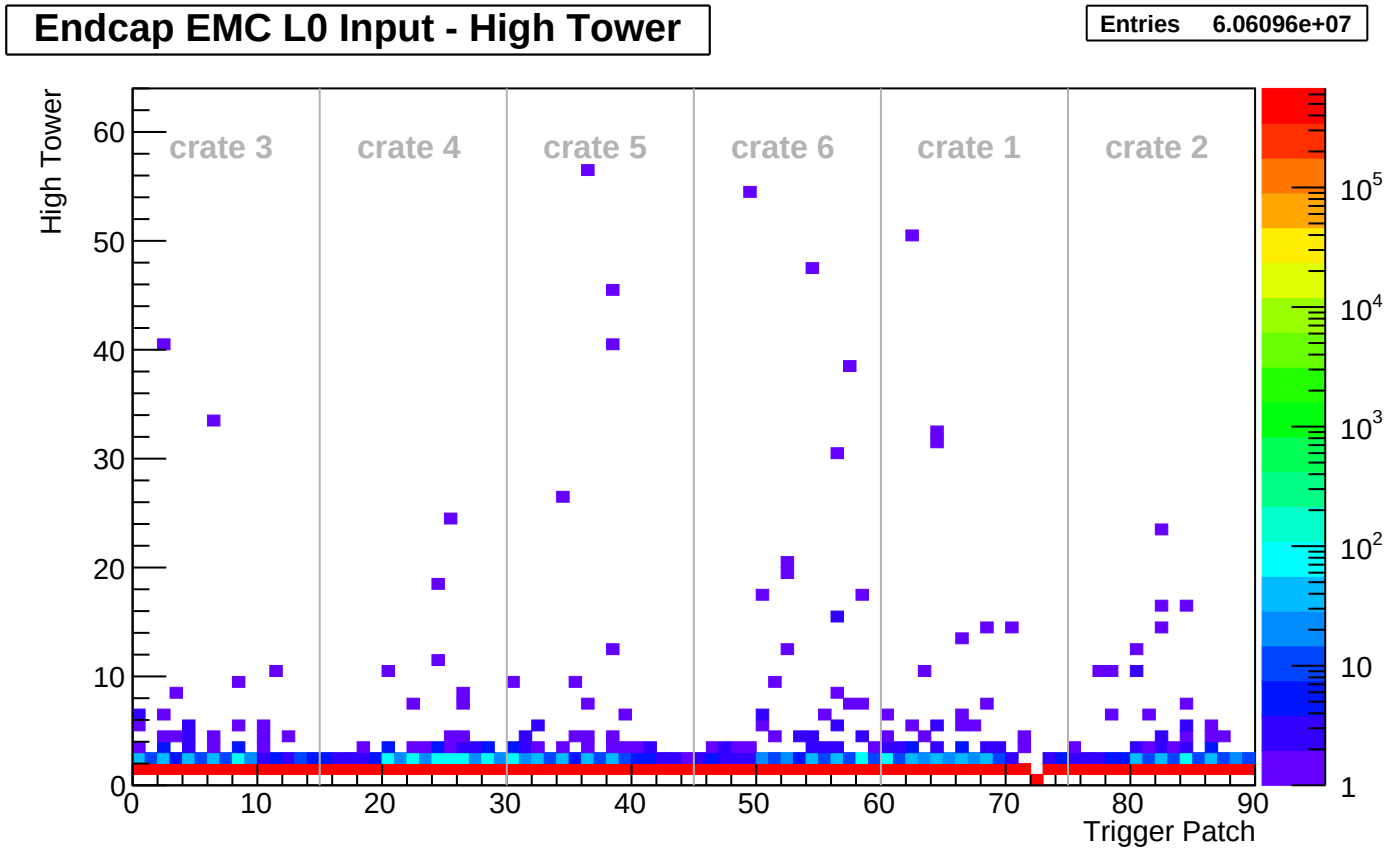
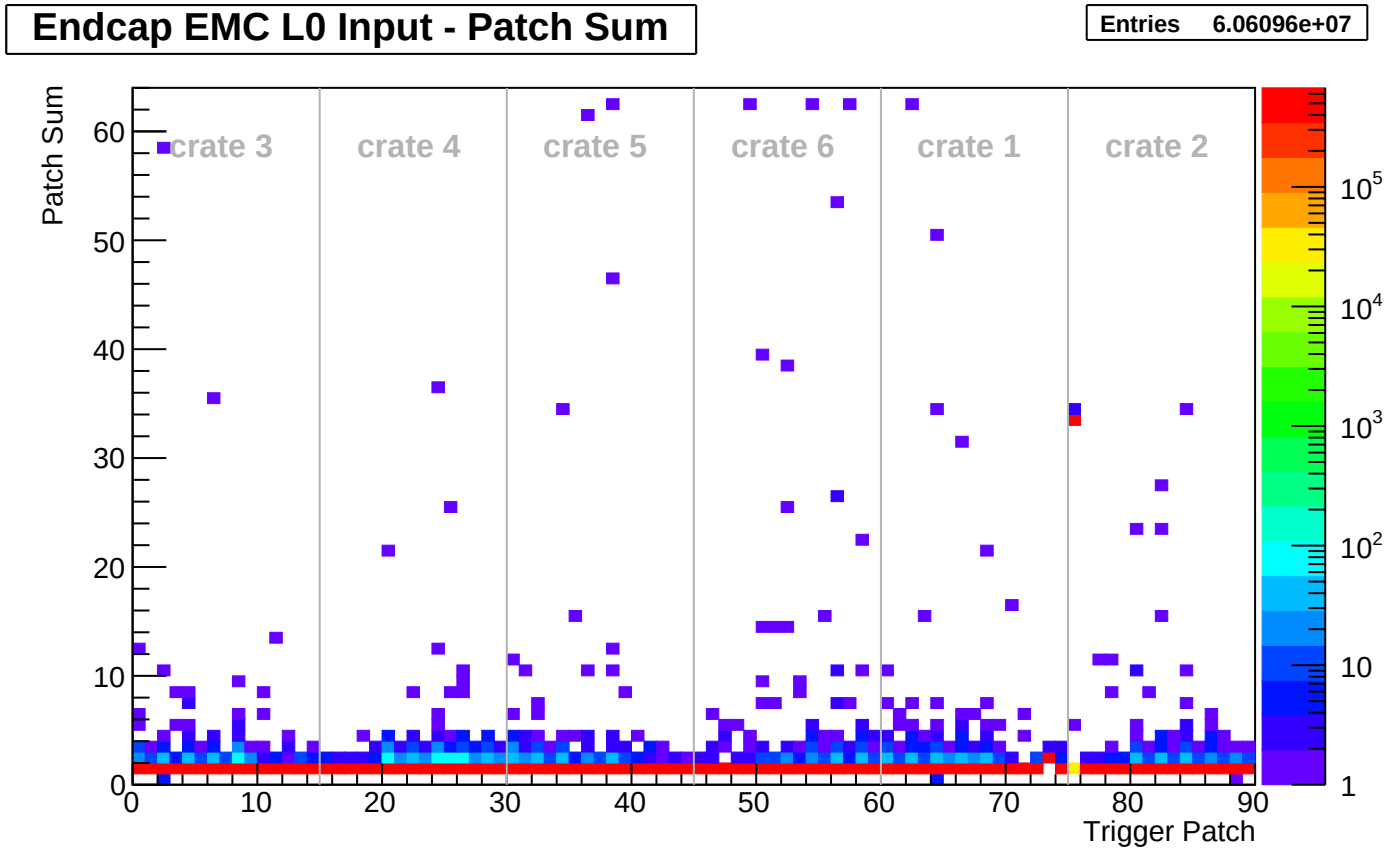
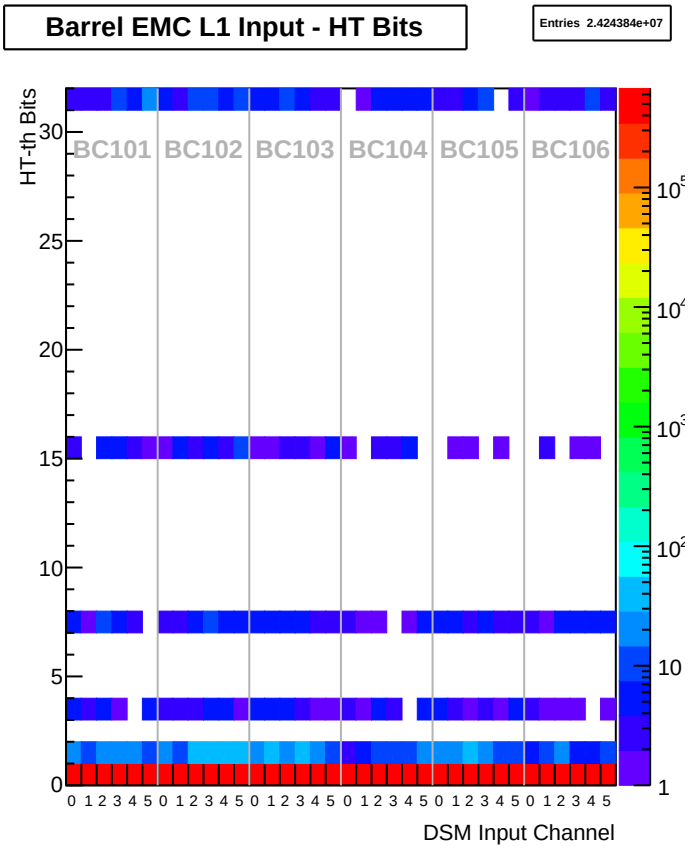
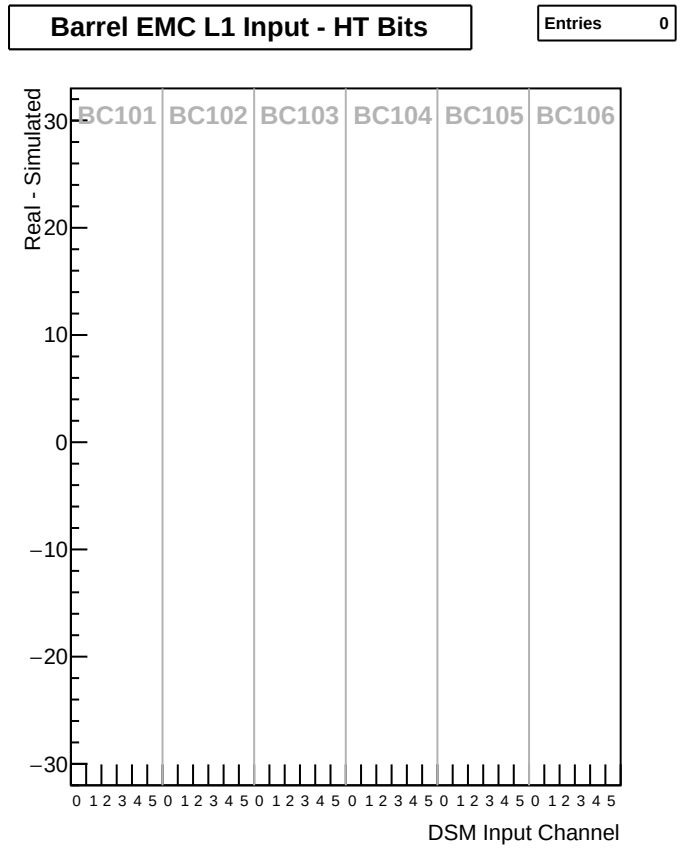

Barrel EMC L0 Input - High Tower
Entries 2.02032e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

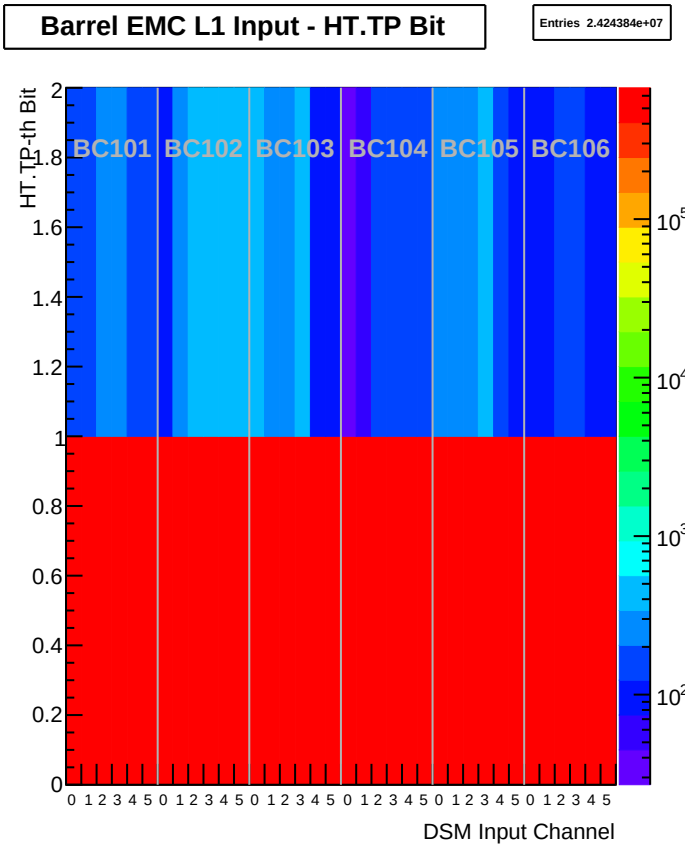

Barrel EMC L0 Input - Patch Sum
Entries 2.02032e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

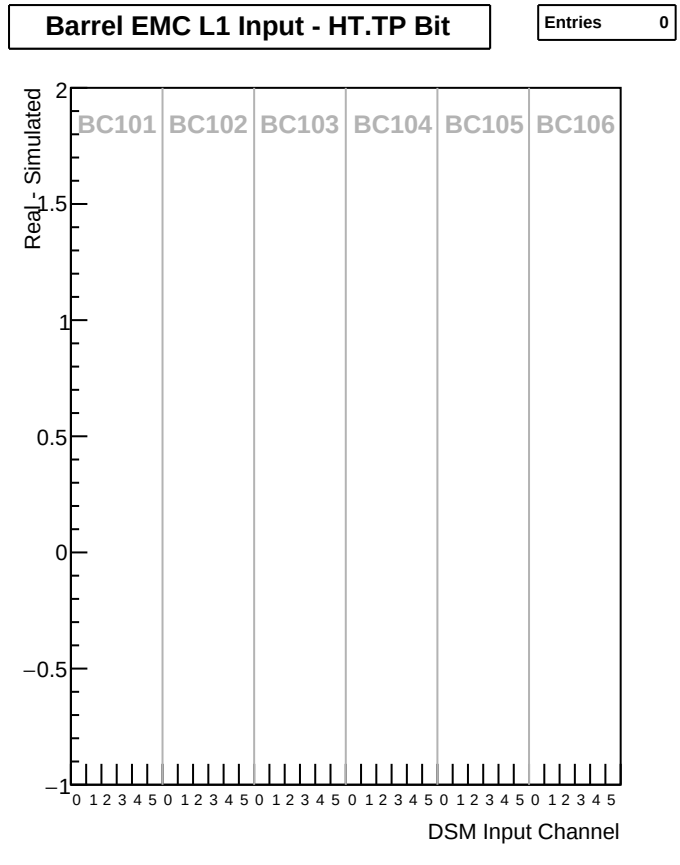

Endcap EMC L0 Input - High Tower
Entries 6.06096e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

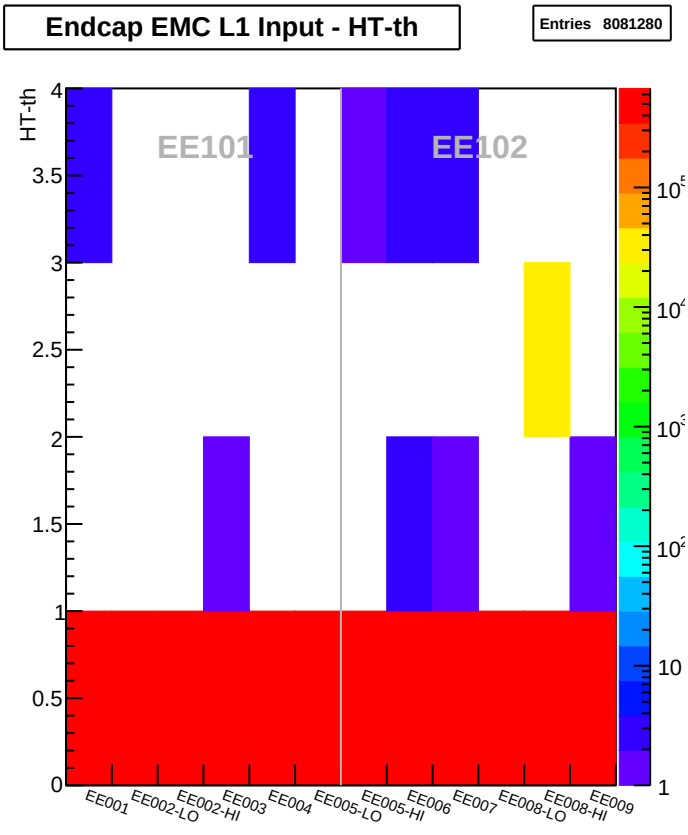

Endcap EMC L0 Input - Patch Sum
Entries 6.06096e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

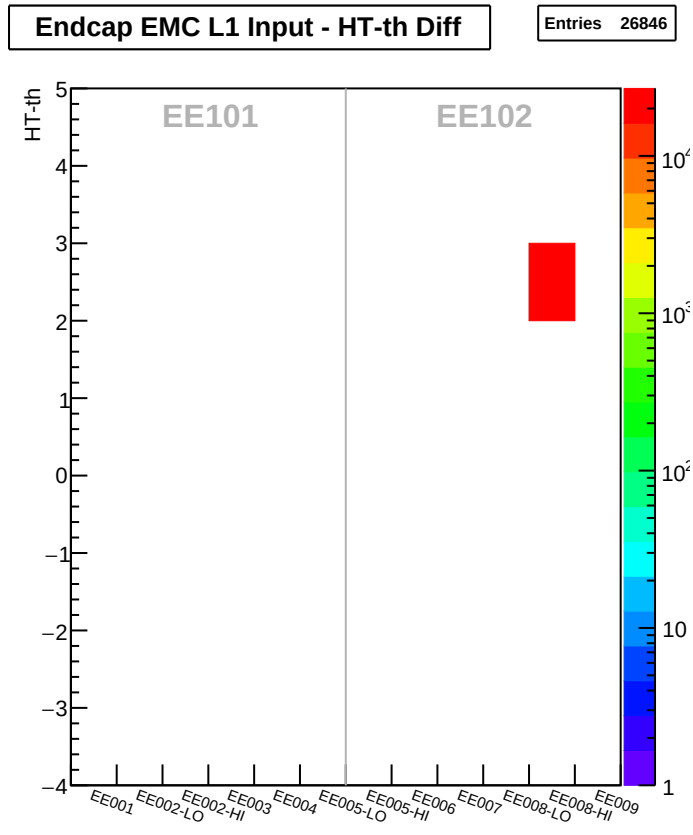

Barrel EMC L1 Input - HT Bits
Entries 2.424384e+07
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

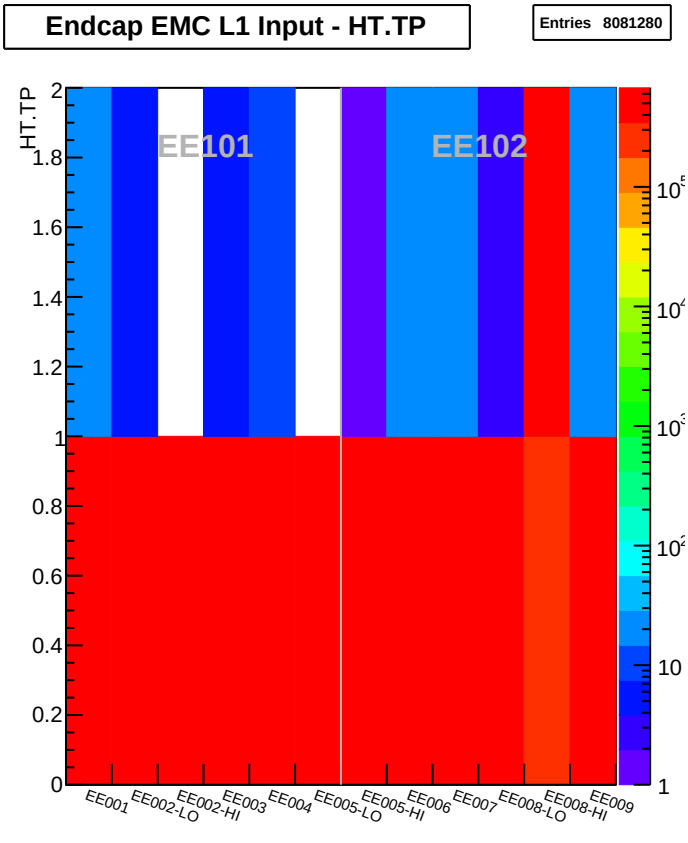

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

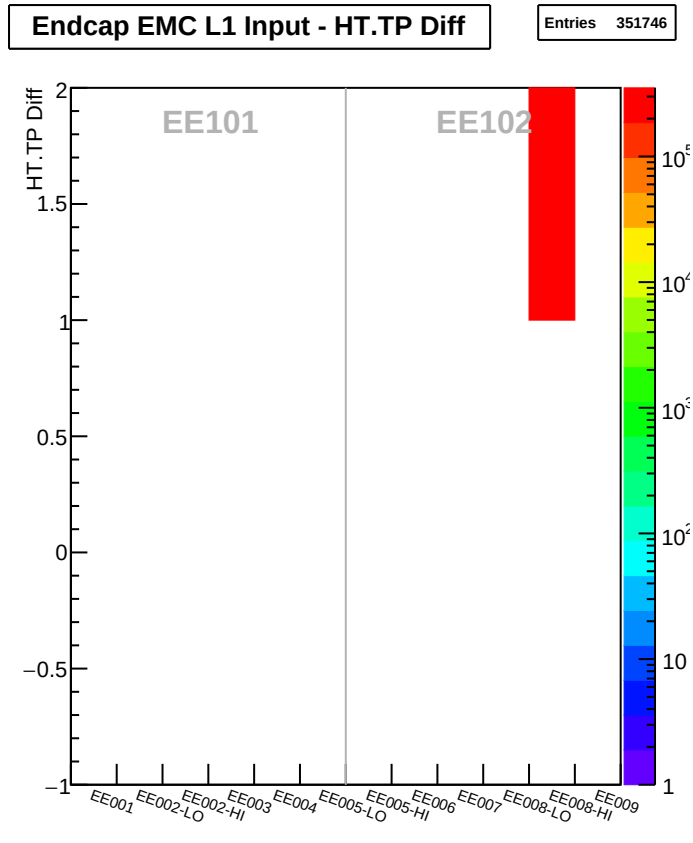

Barrel EMC L1 Input - HT.TP Bit
Entries 2.424384e+07
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

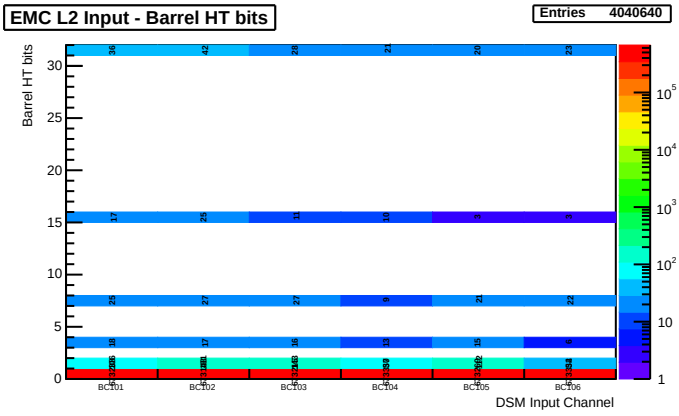

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

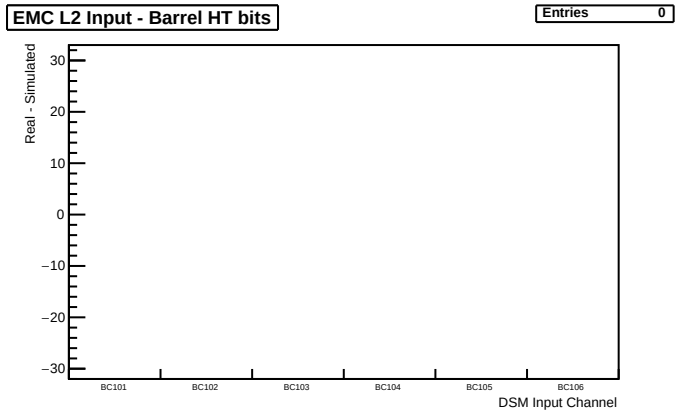

Endcap EMC L1 Input - HT-th
Entries 8081280
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

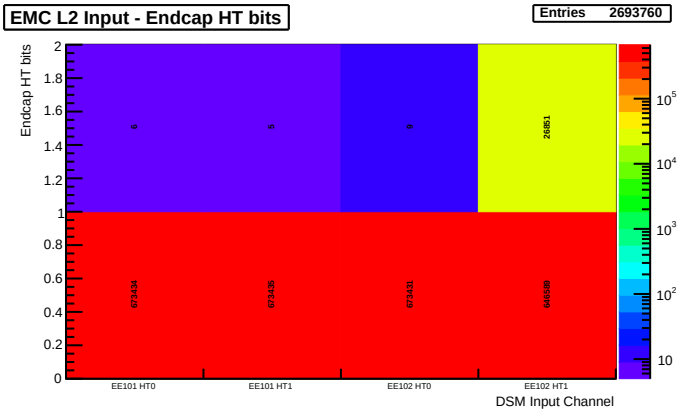

Endcap EMC L1 Input - HT-th Diff
Entries 26846
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

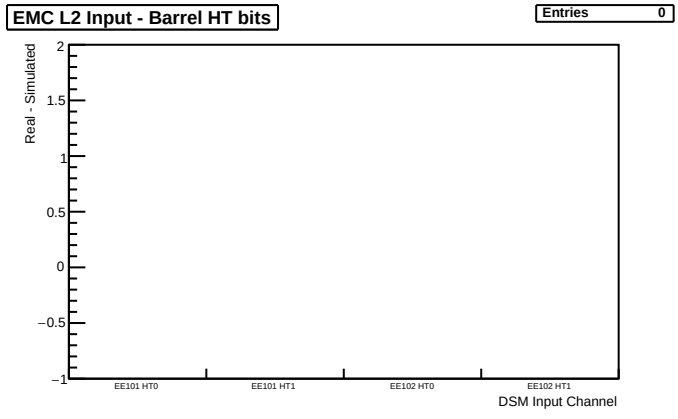

Endcap EMC L1 Input - HT.TP
Entries 8081280
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

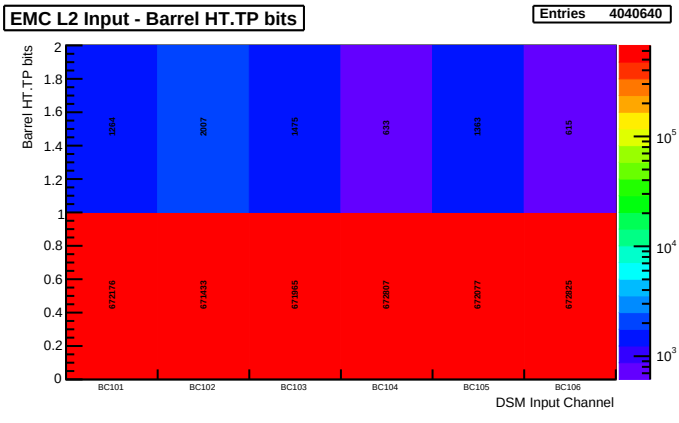

Endcap EMC L1 Input - HT.TP Diff
Entries 351746
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

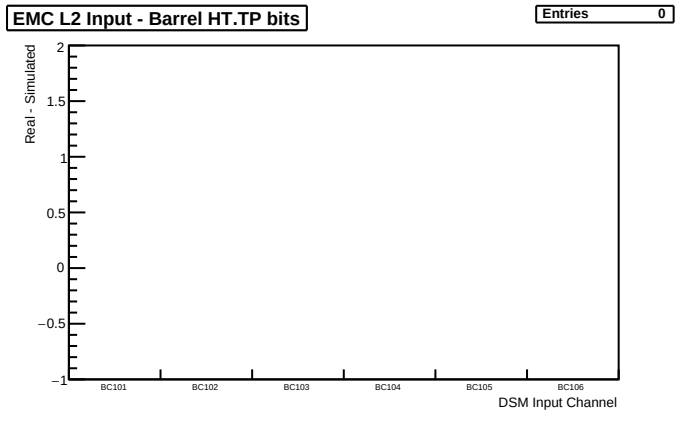

EMC L2 Input - Barrel HT bits
Entries 4040640
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

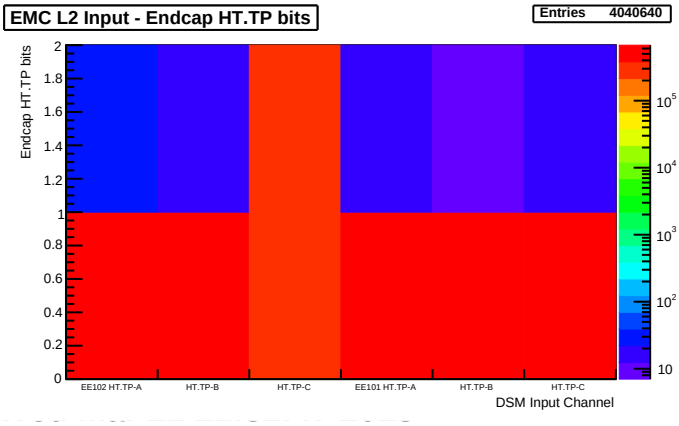

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

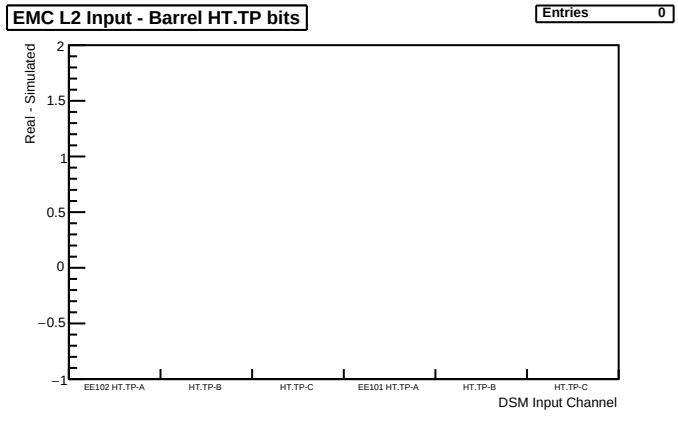

EMC L2 Input - Endcap HT bits
Entries 2693760
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

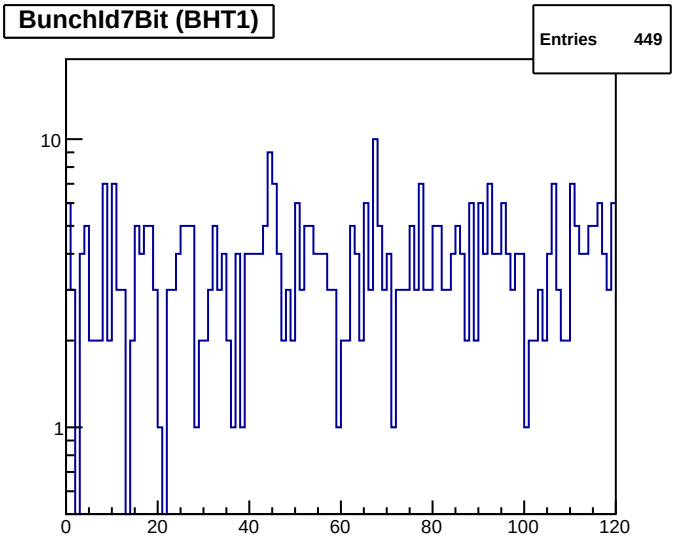

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

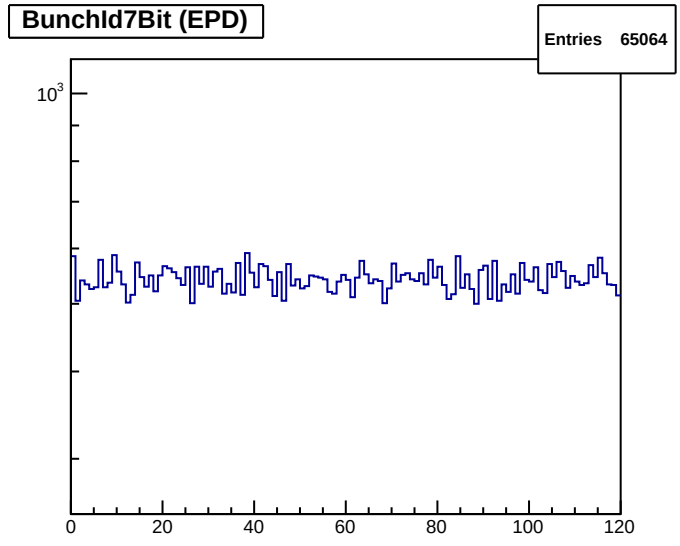

EMC L2 Input - Barrel HT.TP bits
Entries 4040640
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

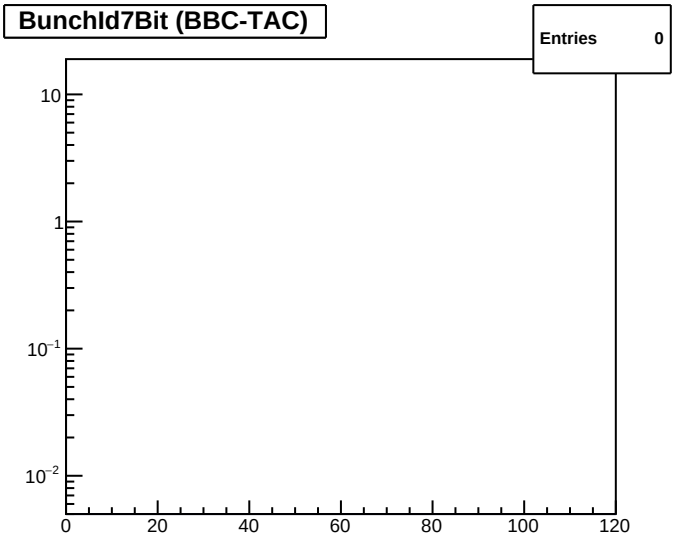

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

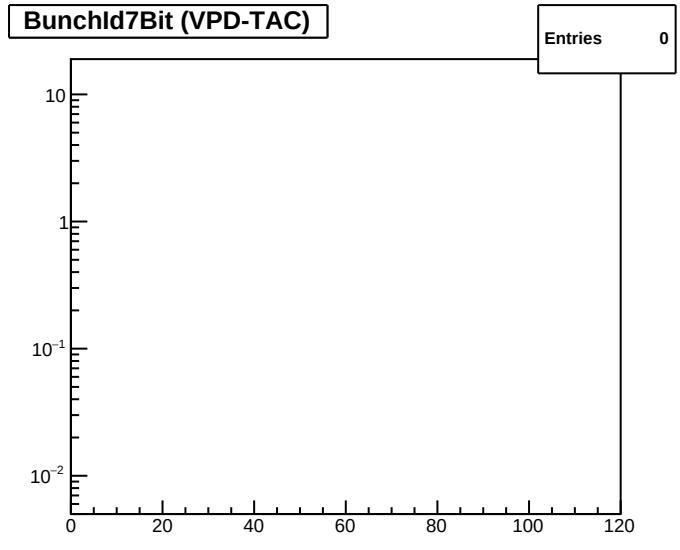

EMC L2 Input - Endcap HT.TP bits
Entries 4040640
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

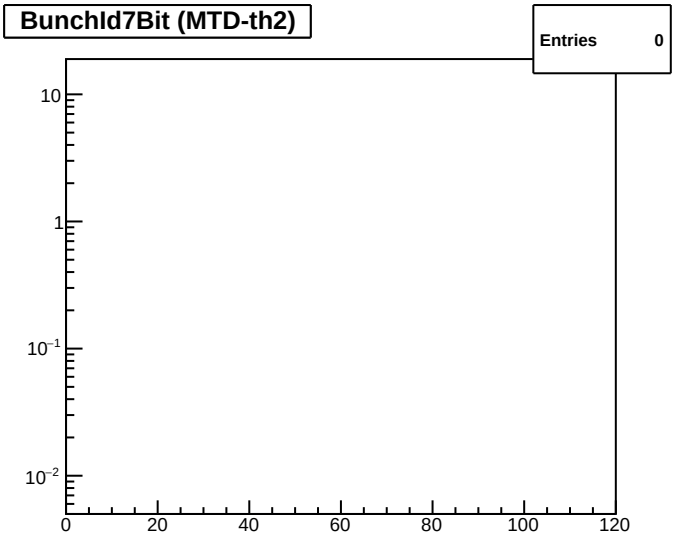

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

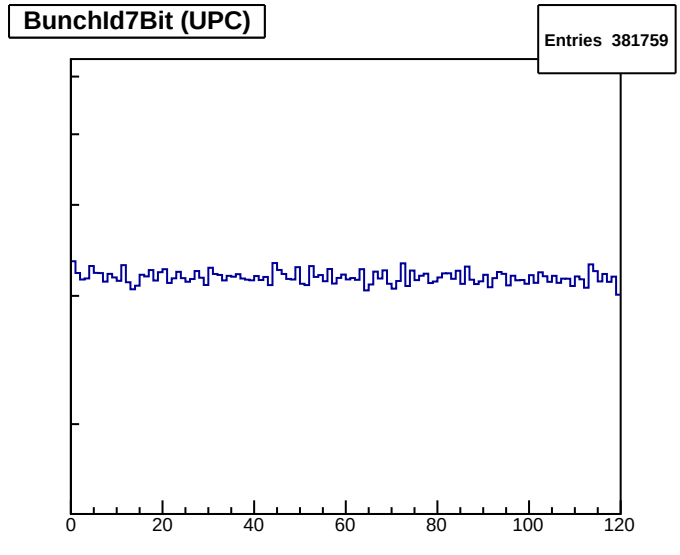

BunchId7Bit (BHT1)
Entries 449
10
1
0
20
40
60
80
100
120

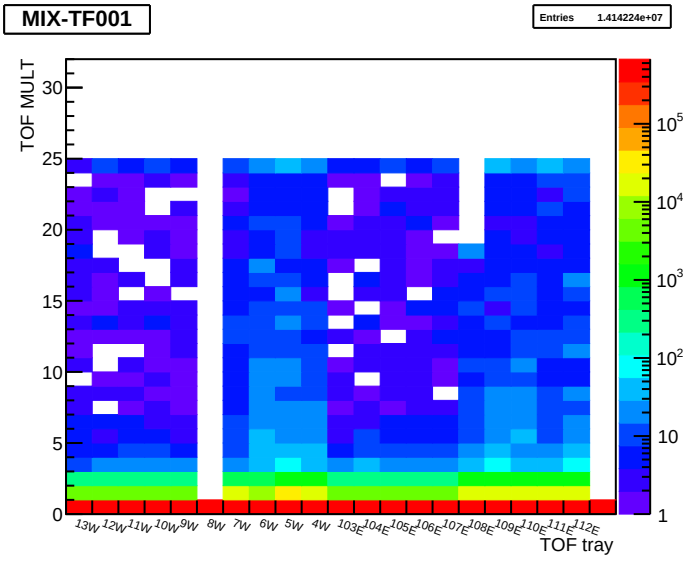

BunchId7Bit (EPD)
Entries 65064
10^3
0
20
40
60
80
100
120

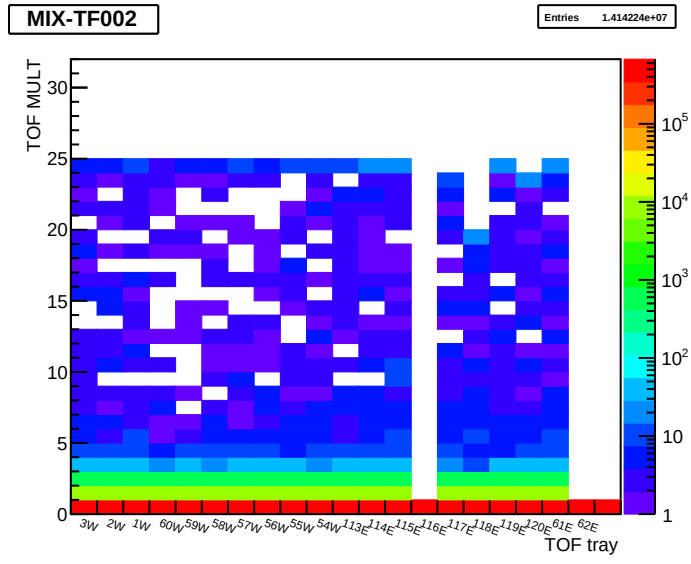

BunchId7Bit (BBC-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

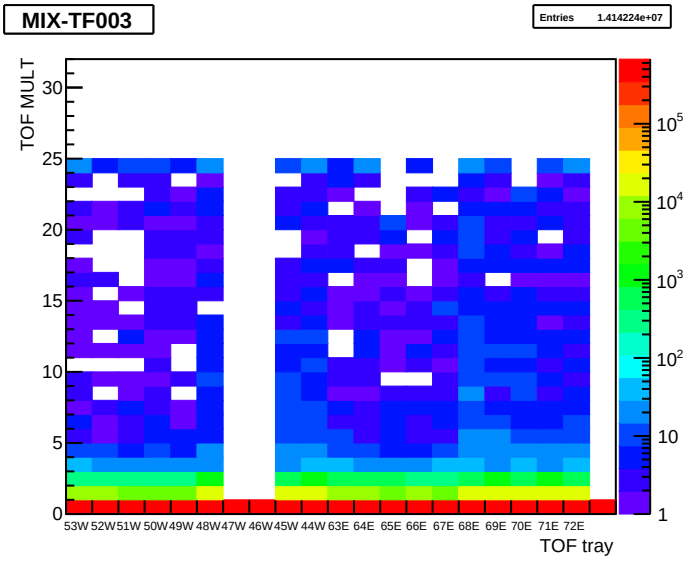

BunchId7Bit (VPD-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

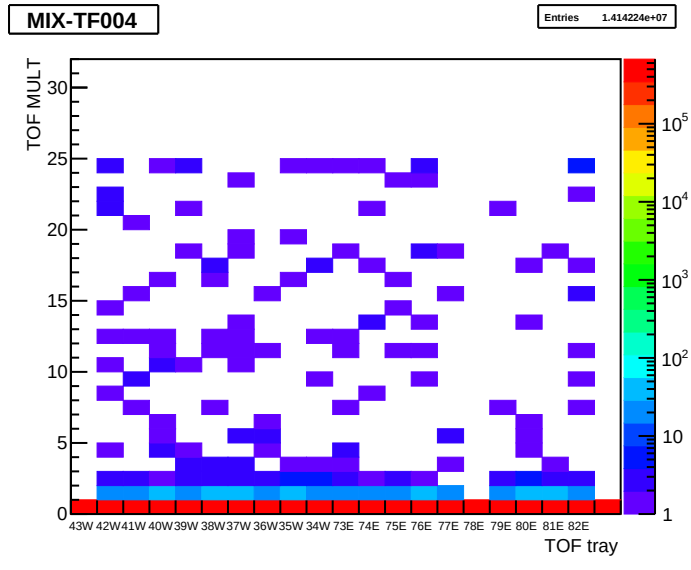

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

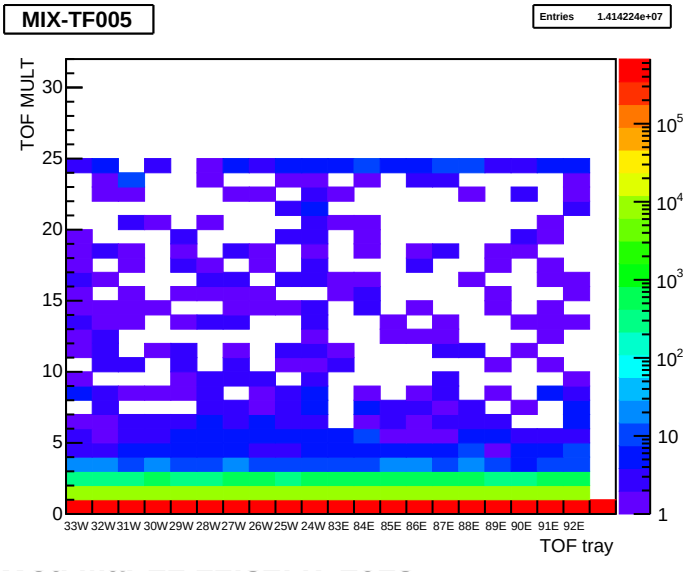

BunchId7Bit (UPC)
Entries 381759
0
20
40
60
80
100
120

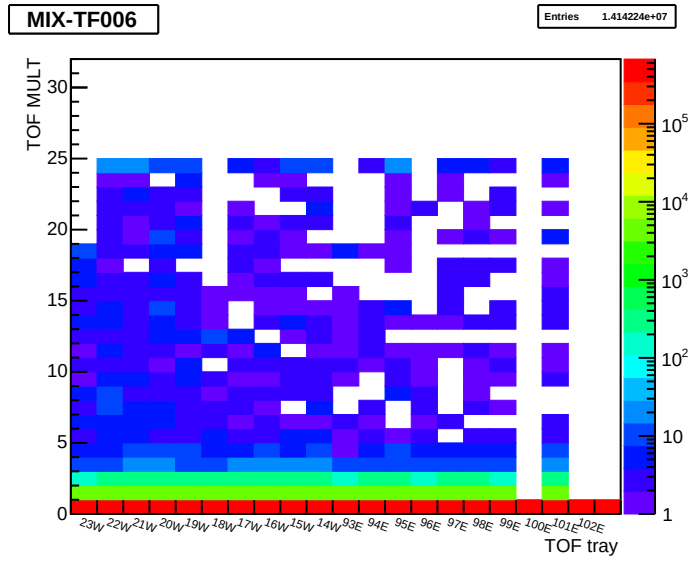

MIX-TF001
Entries 1.414224e+07
TOF MULT
TOF tray

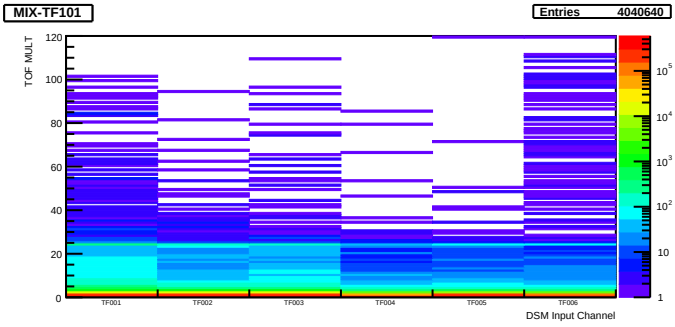

MIX-TF002
Entries 1.414224e+07
TOF MULT
TOF tray

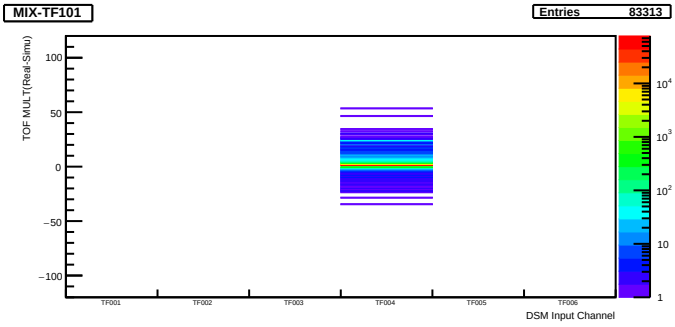

MIX-TF003
Entries 1.414224e+07
TOF MULT
TOF tray

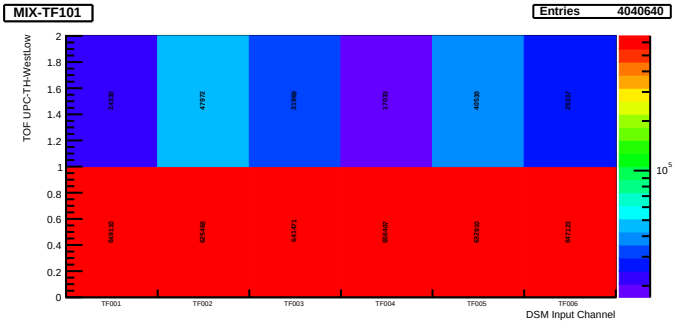

MIX-TF004
Entries 1.414224e+07
TOF MULT
TOF tray

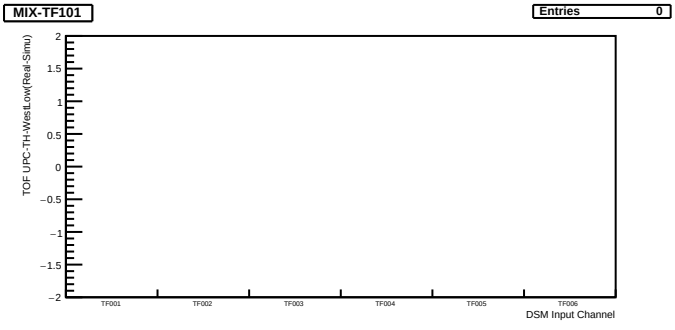

MIX-TF005
Entries 1.414224e+07
TOF MULT
TOF tray

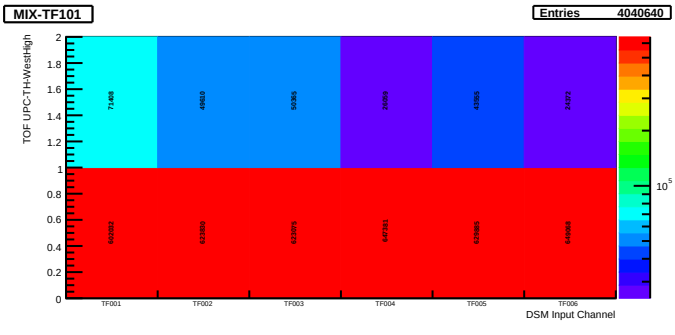

MIX-TF006
Entries 1.414224e+07
TOF MULT
TOF tray

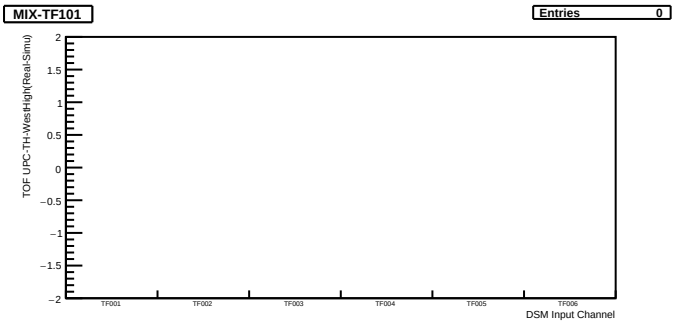

MIX-TF101
Entries 4040640
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

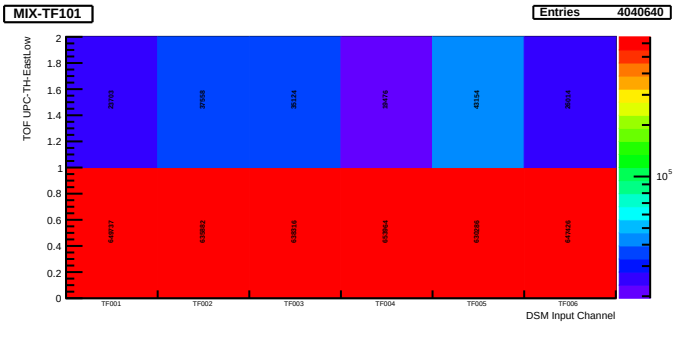

MIX-TF101
Entries 83313
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

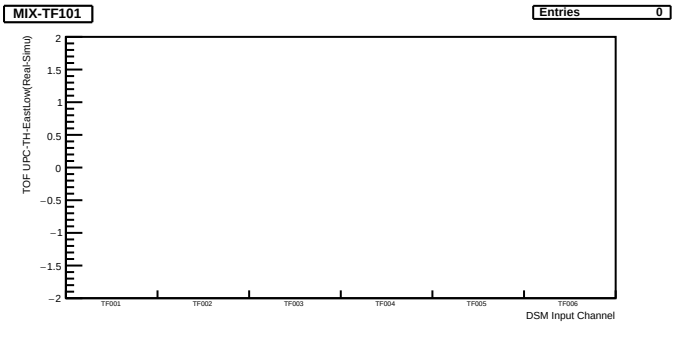

MIX-TF101
Entries 4040640
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

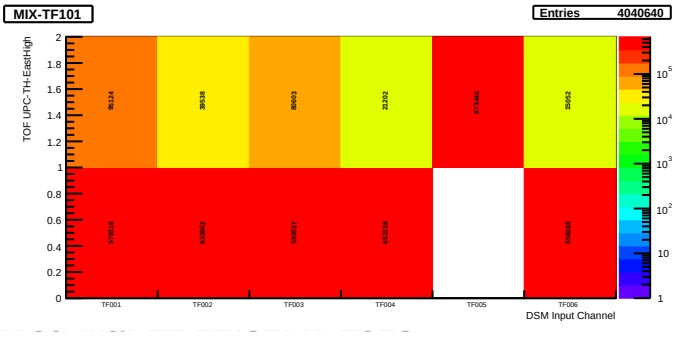

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

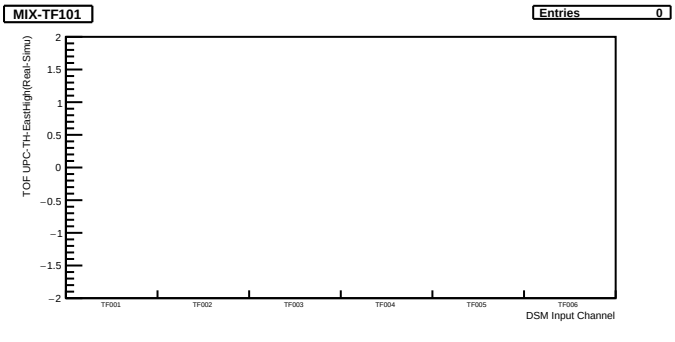

MIX-TF101
Entries 4040640
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

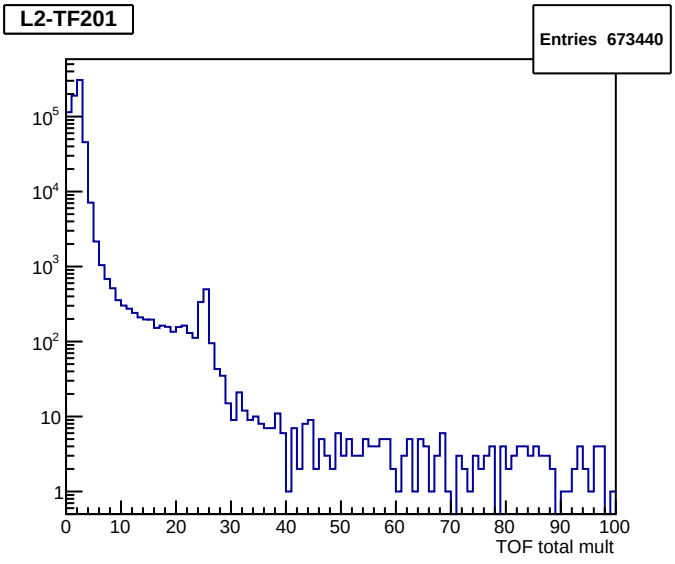

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

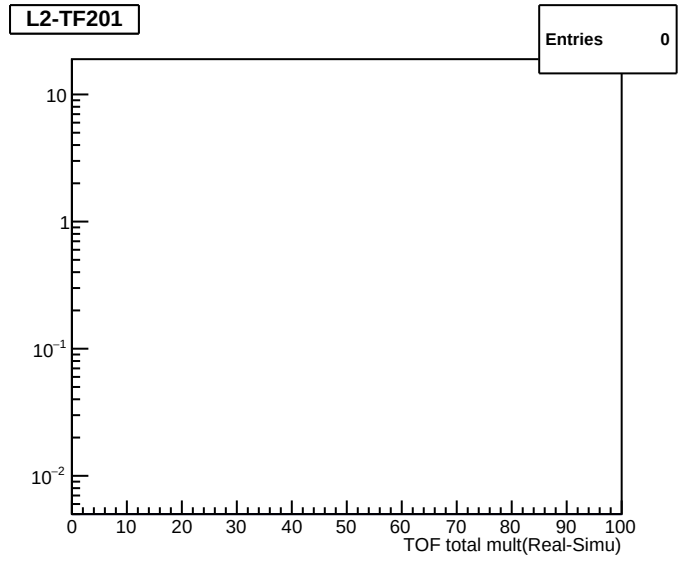

MIX-TF101
Entries 4040640
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

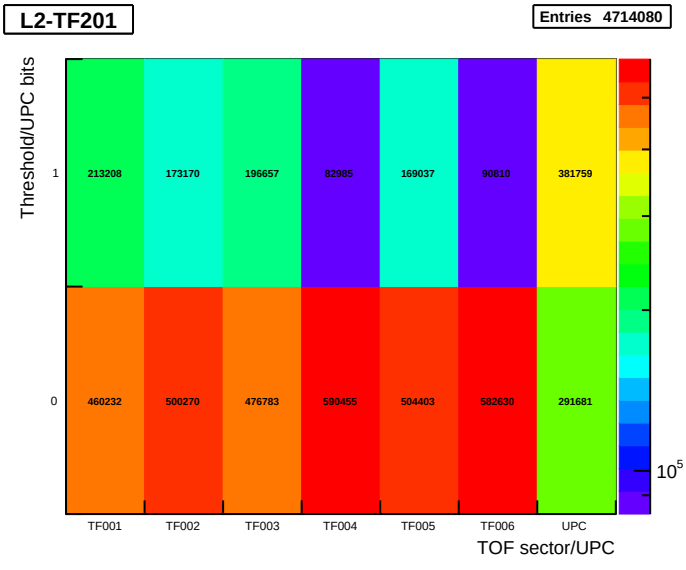

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

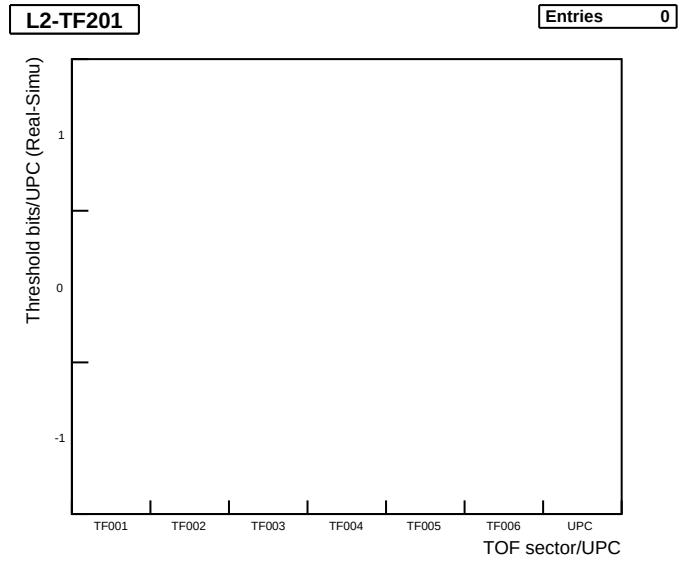

MIX-TF101
Entries 4040640
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

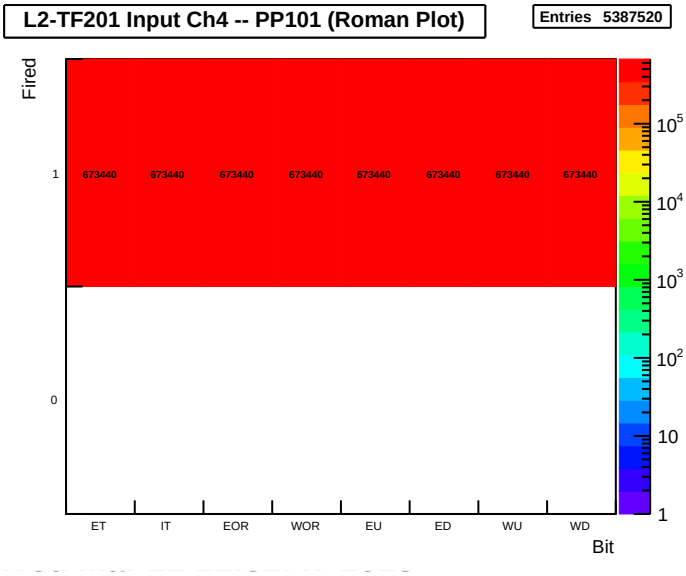

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

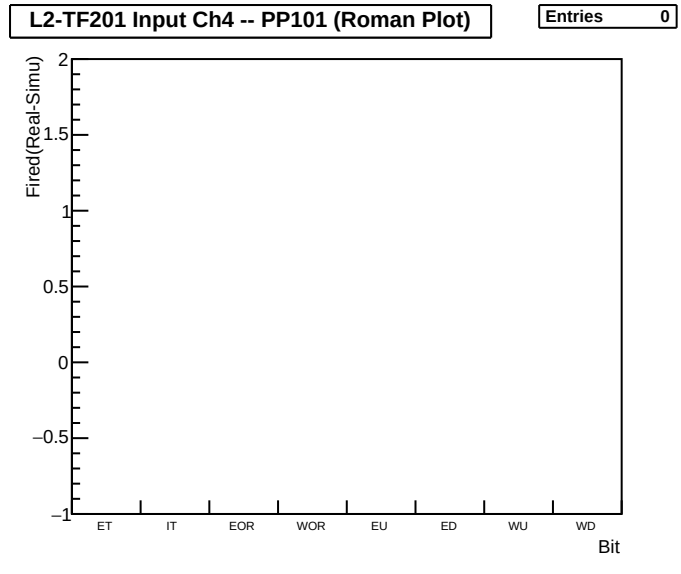

L2-TF201
Entries 673440
TOF total mult

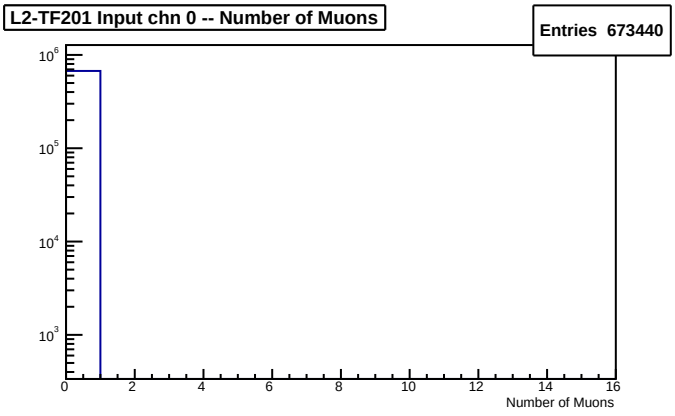

L2-TF201
Entries 0
TOF total mult(Real-Simu)

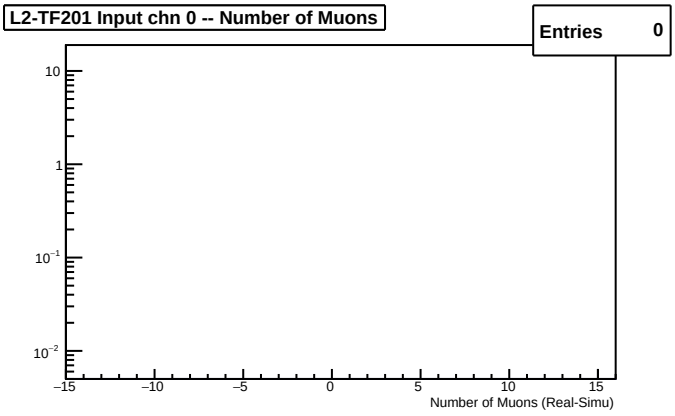

L2-TF201
Entries 4714080
Threshold/UPC bits
213208
173170
196657
82985
169037
90810
381759
460232
500270
476783
590455
504403
582630
291681
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

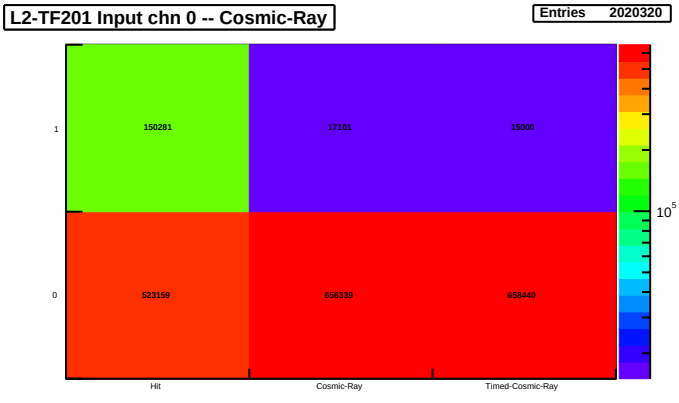

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

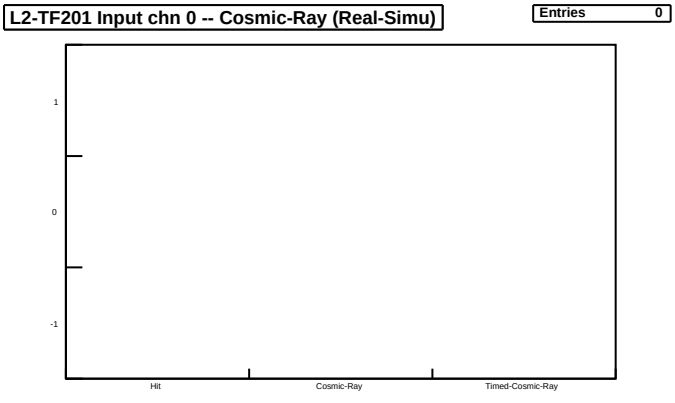

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 5387520
Fired
673440
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

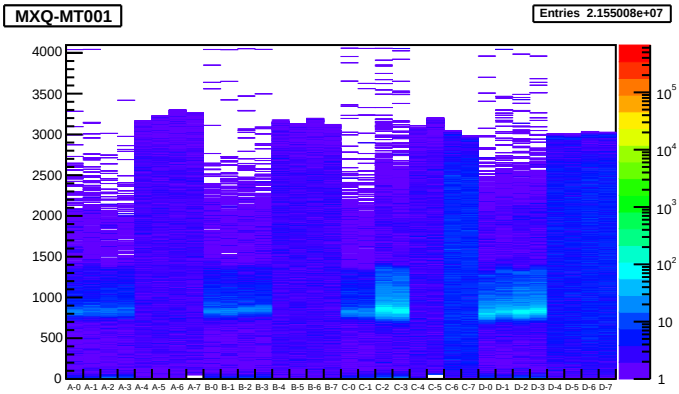

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

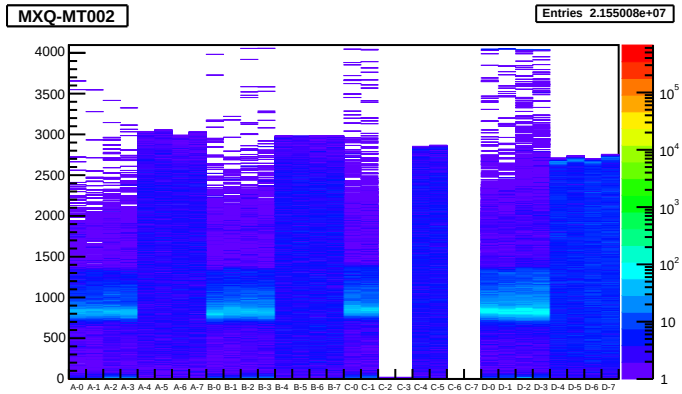

L2-TF201 Input chn 0 -- Number of Muons
Entries 673440
Number of Muons

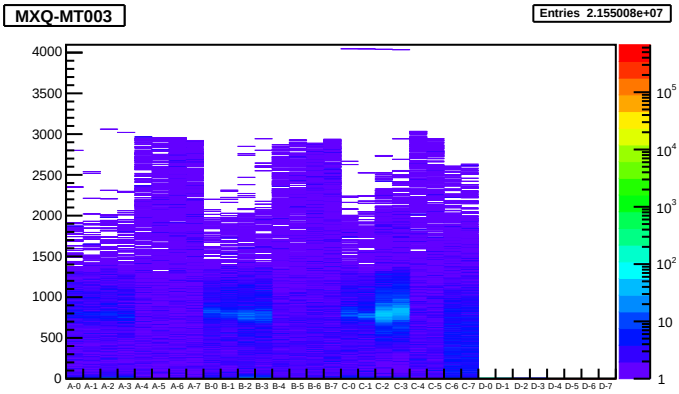

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

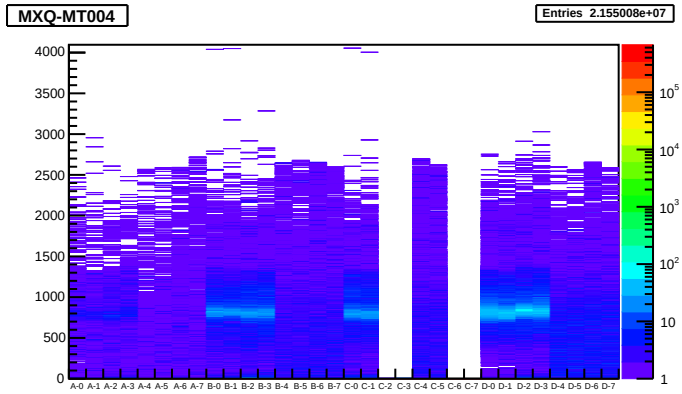

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 2020320
150281
17101
15000
523159
656339
658440
Hit
Cosmic-Ray
Timed-Cosmic-Ray

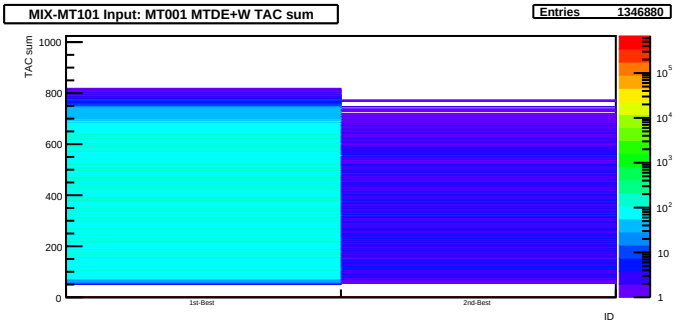

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

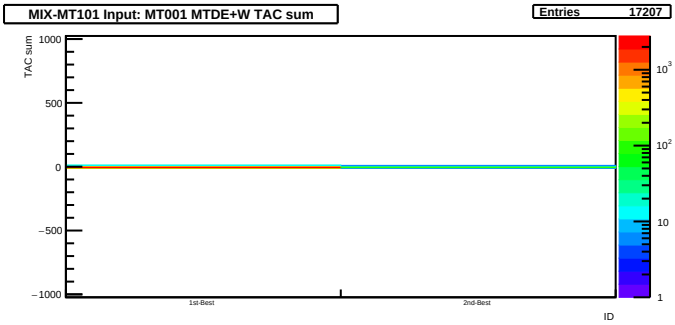

MXQ-MT001
Entries 2.155008e+07

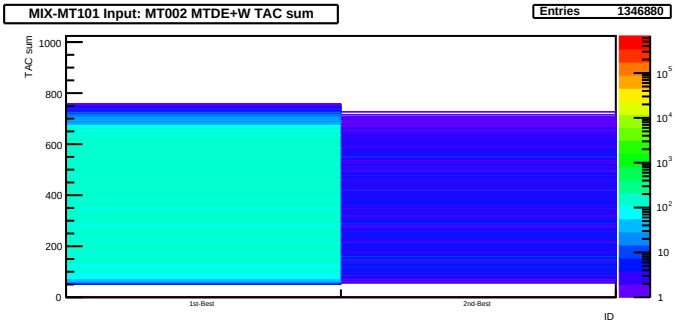

MXQ-MT002
Entries 2.155008e+07

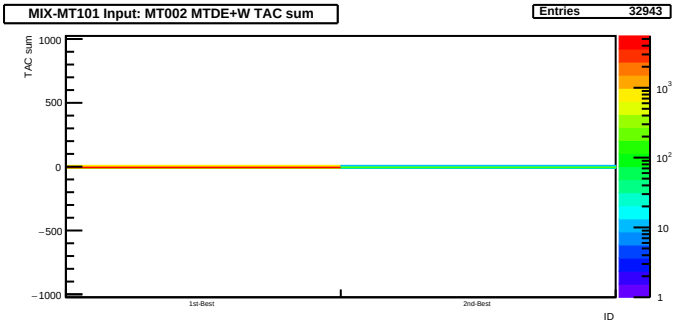

MXQ-MT003
Entries 2.155008e+07

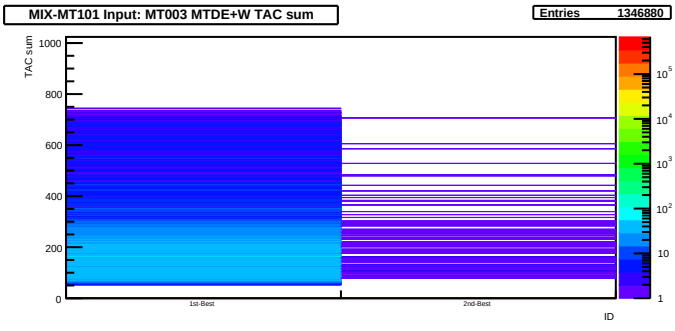

MXQ-MT004
Entries 2.155008e+07

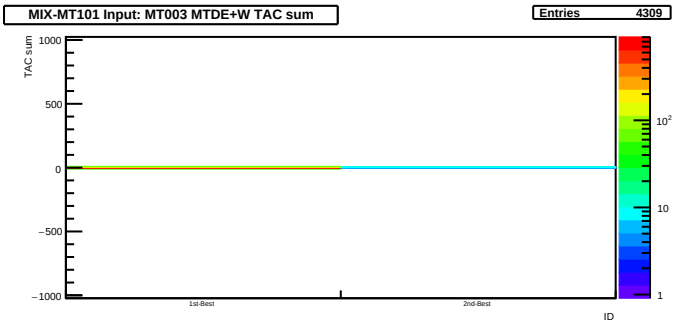

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 1346880
TAC sum
1st-Best
2nd-Best
ID

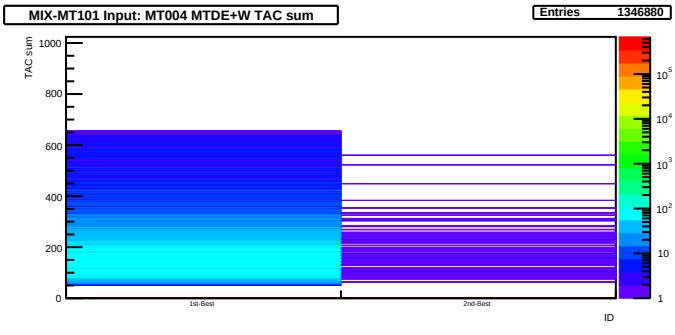

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 17207
TAC sum
1st-Best
2nd-Best
ID

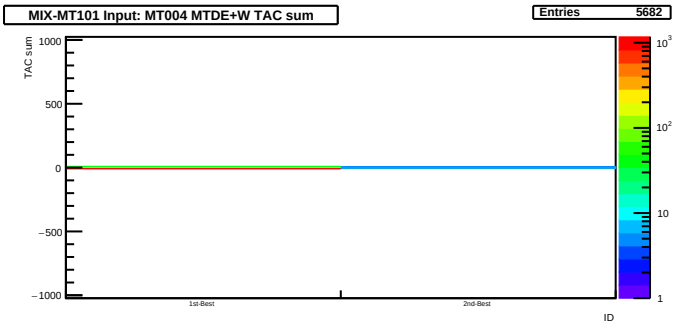

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 1346880
TAC sum
1st-Best
2nd-Best
ID

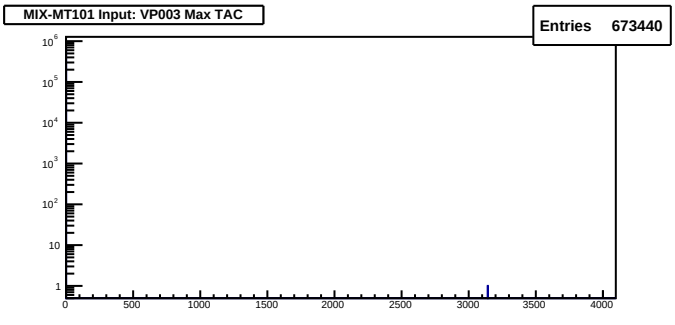

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 32943
TAC sum
1st-Best
2nd-Best
ID

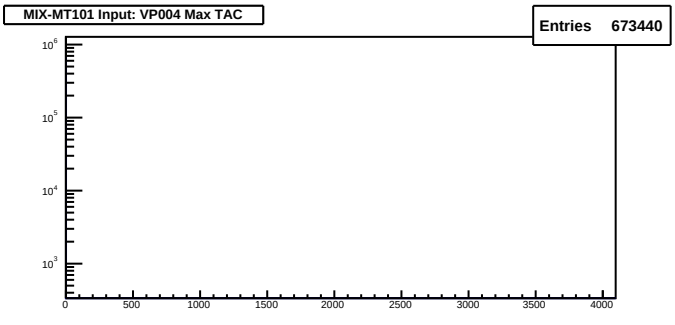

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 1346880
TAC sum
1st-Best
2nd-Best
ID

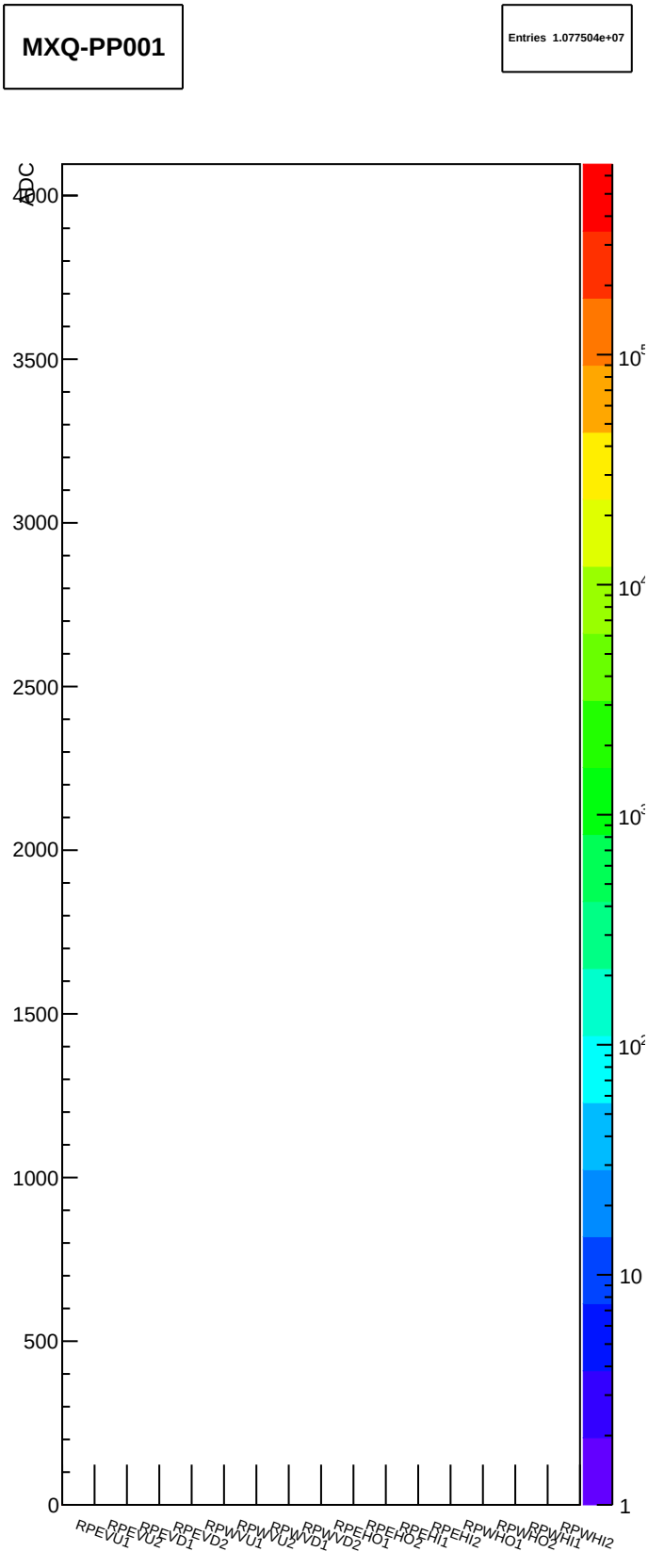

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 4309
TAC sum
1st-Best
2nd-Best
ID

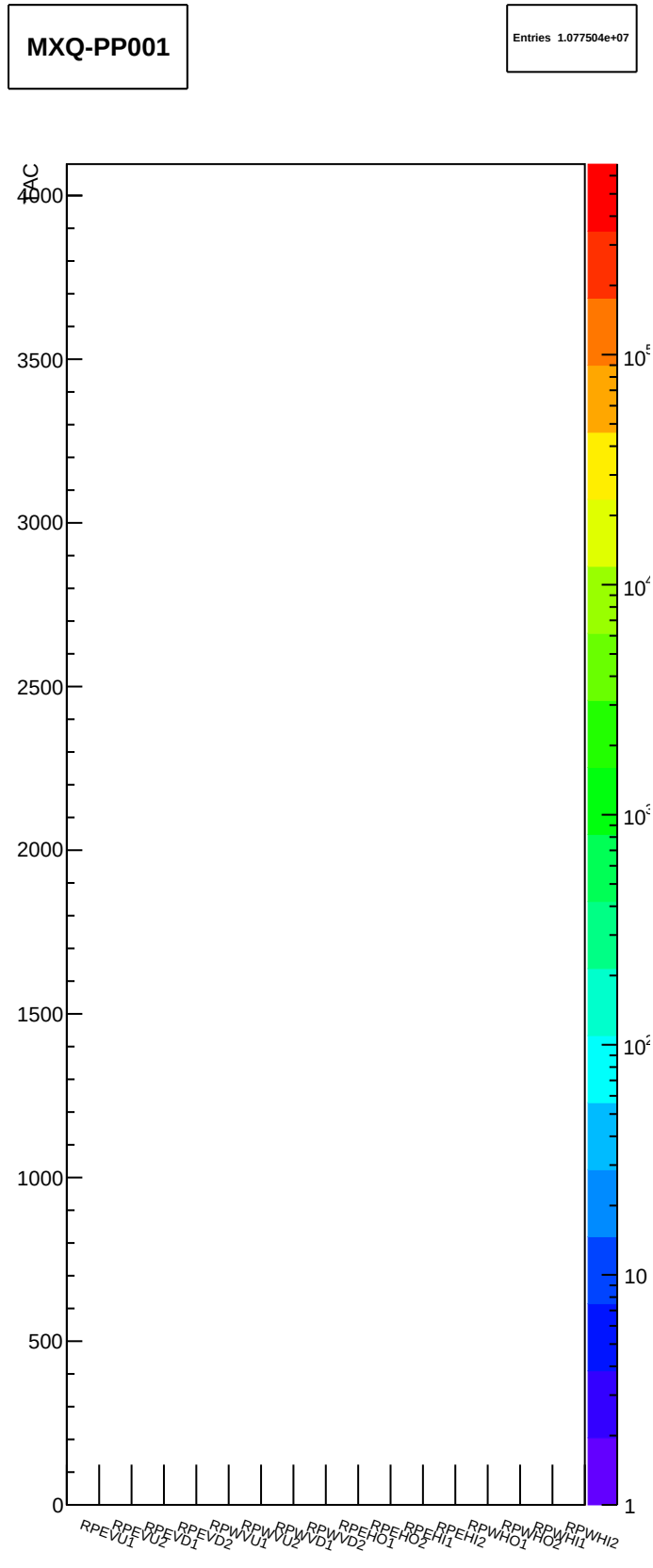

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 1346880
TAC sum
1st-Best
2nd-Best
ID

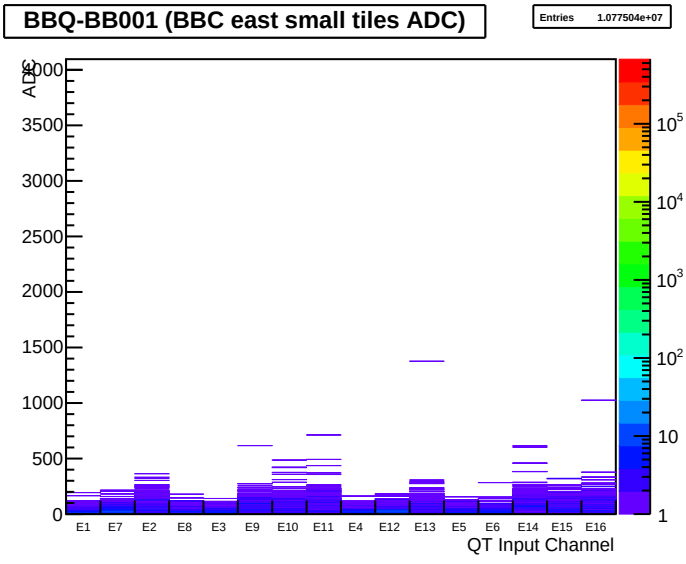

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 5682
TAC sum
1st-Best
2nd-Best
ID

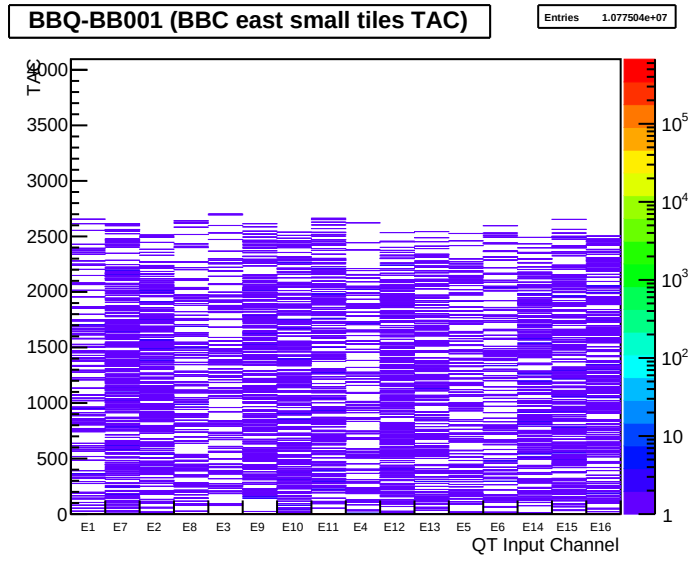

MIX-MT101 Input: VP003 Max TAC
Entries 673440

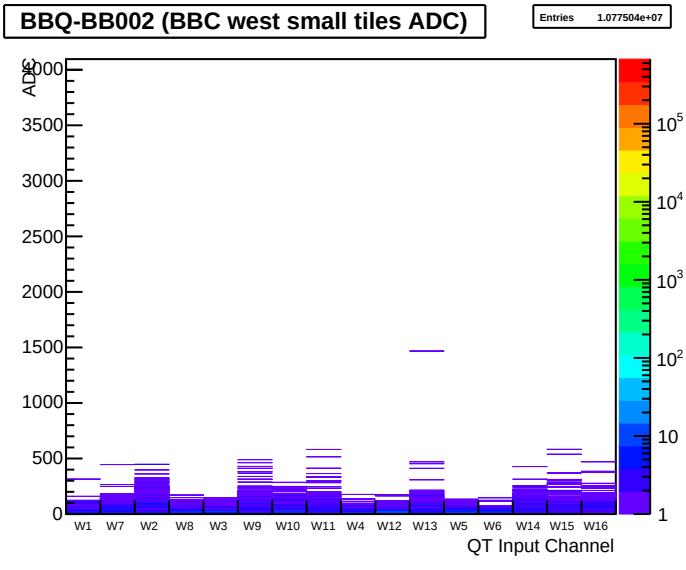

MIX-MT101 Input: VP004 Max TAC
Entries 673440

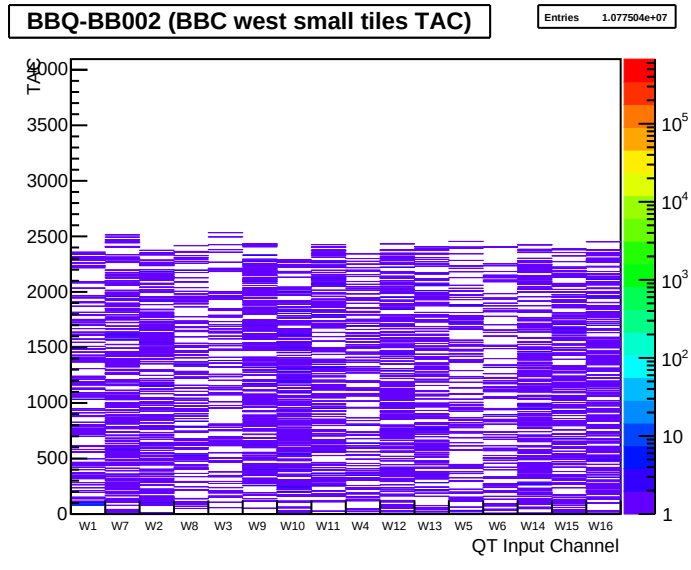

MXQ-PP001
Entries 1.077504e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
1
10

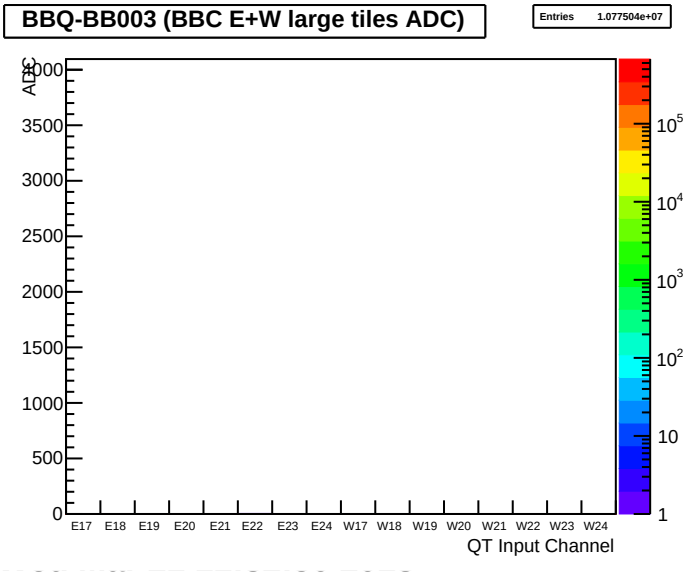

MXQ-PP001
Entries 1.077504e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
1
10

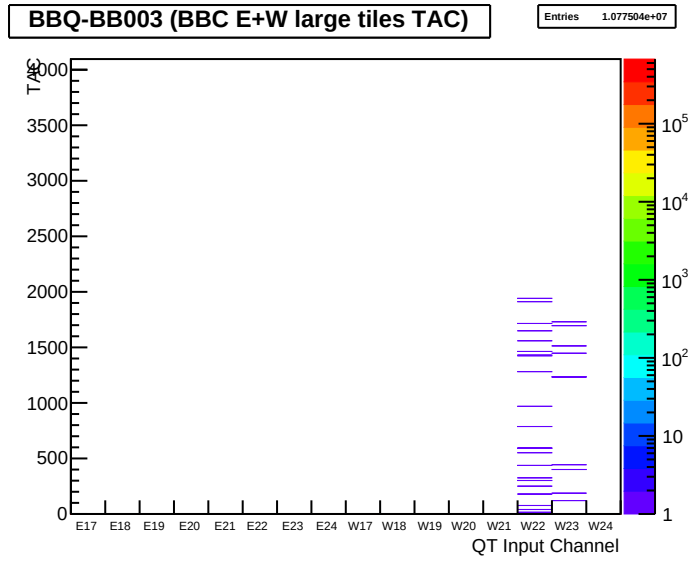

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.077504e+07
ADC
QT Input Channel

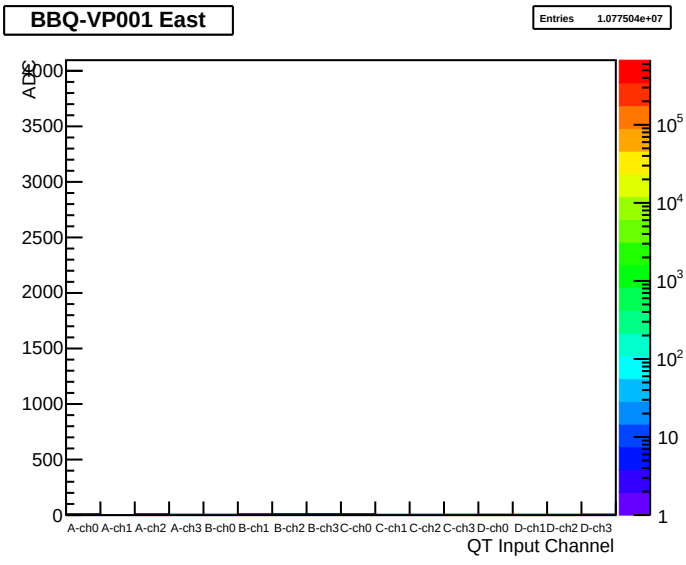

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.077504e+07
TAC
QT Input Channel

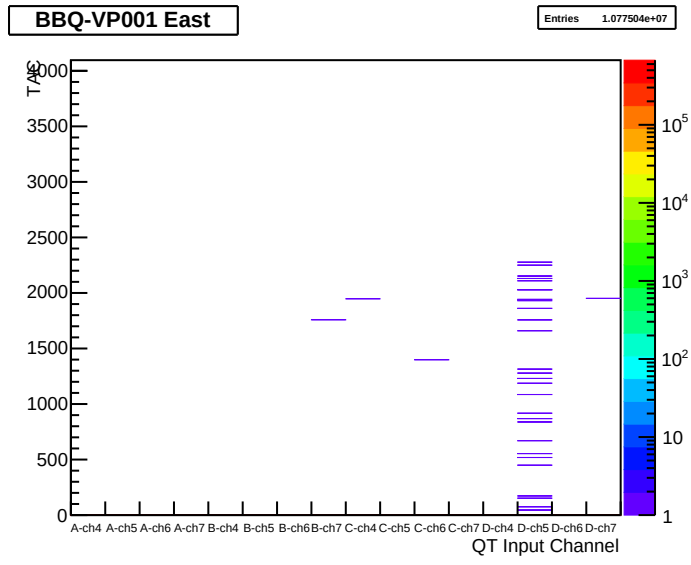

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.077504e+07
ADC
QT Input Channel

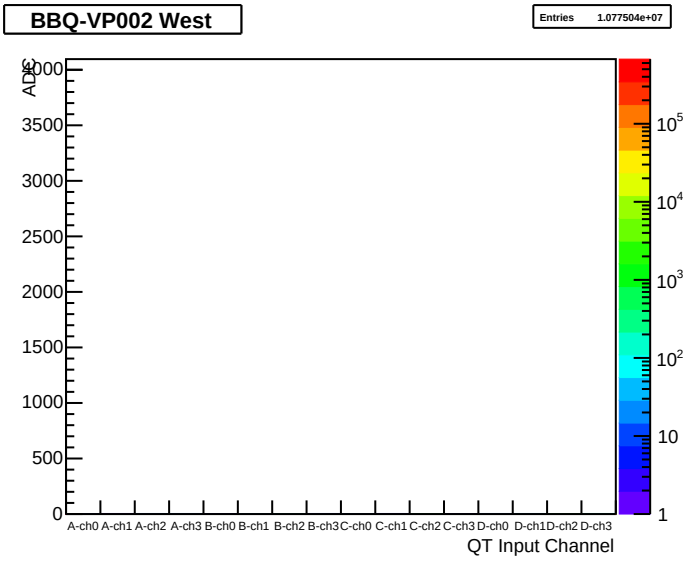

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.077504e+07
TAC
QT Input Channel

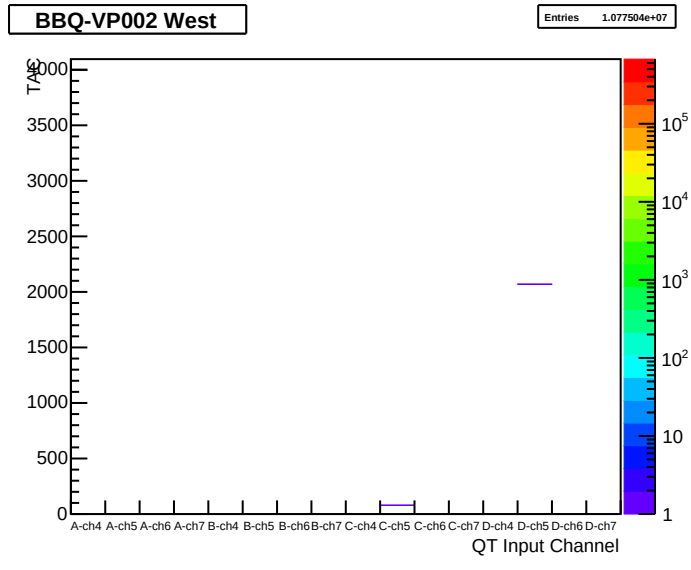

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.077504e+07
ADC
QT Input Channel

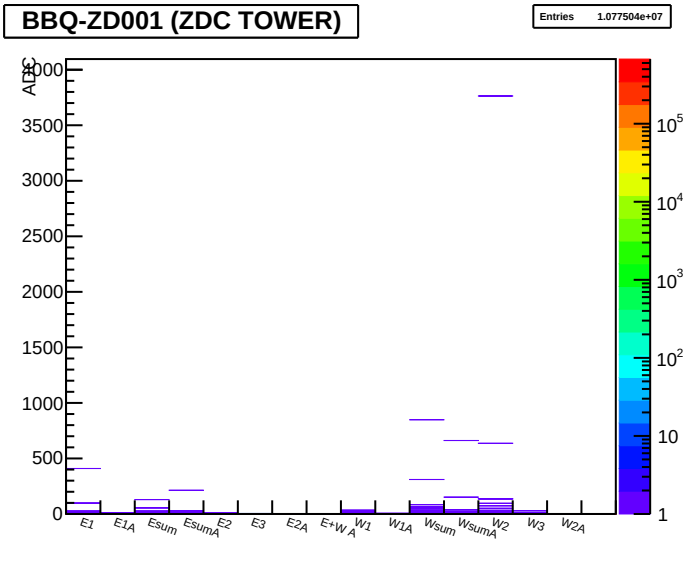

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.077504e+07
TAC
QT Input Channel

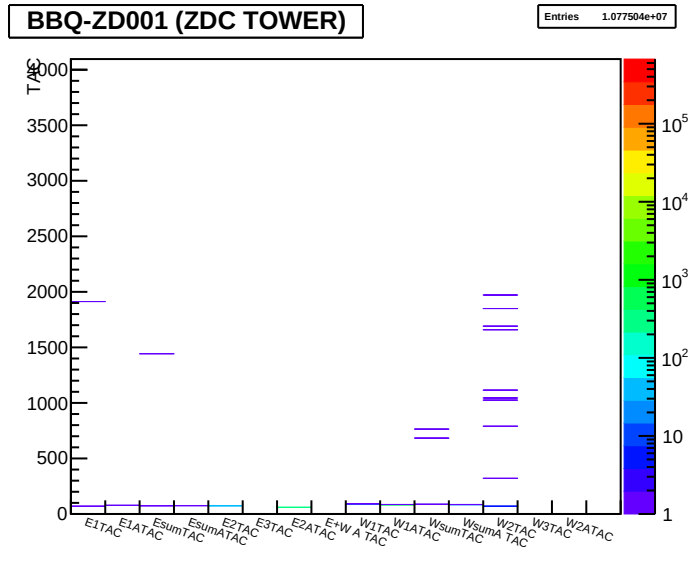

BBQ-VP001 East
Entries 1.077504e+07
ADC
QT Input Channel

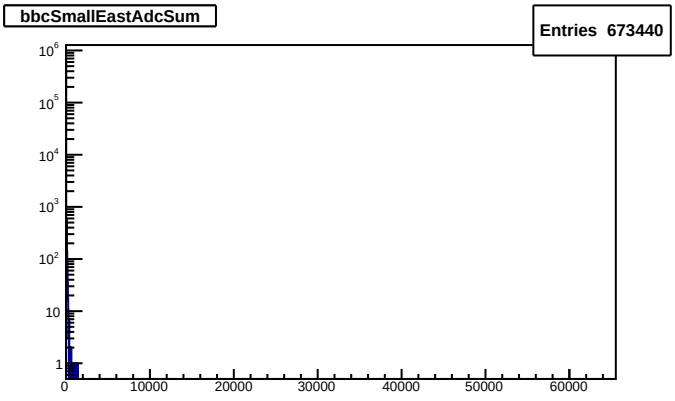

BBQ-VP001 East
Entries 1.077504e+07
TAC
QT Input Channel

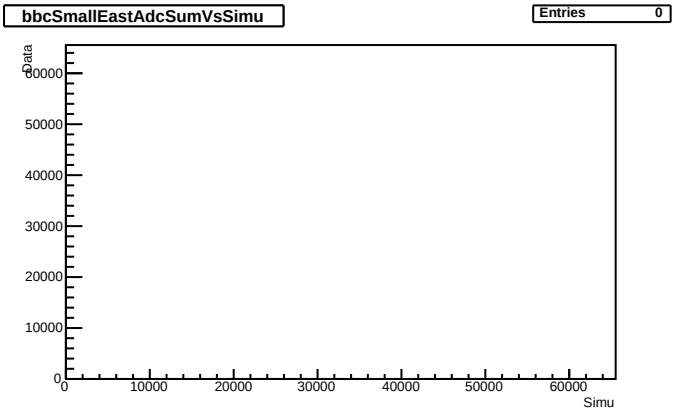

BBQ-VP002 West
Entries 1.077504e+07
ADC
QT Input Channel

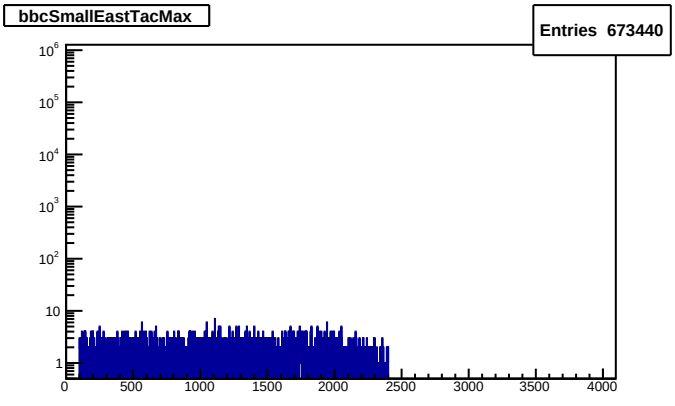

BBQ-VP002 West
Entries 1.077504e+07
TAC
QT Input Channel

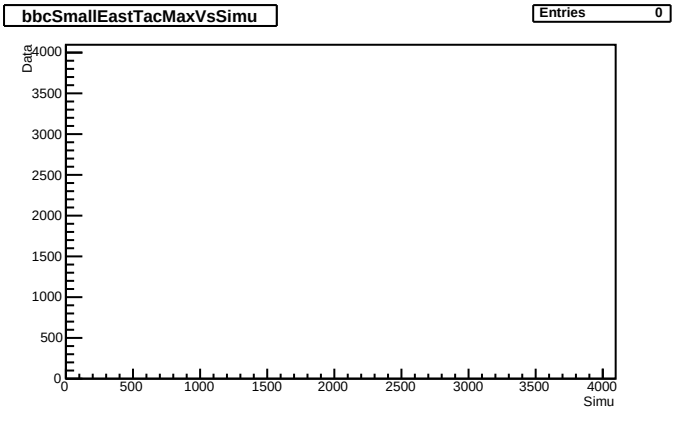

BBQ-ZD001 (ZDC TOWER)
Entries 1.077504e+07
ADC

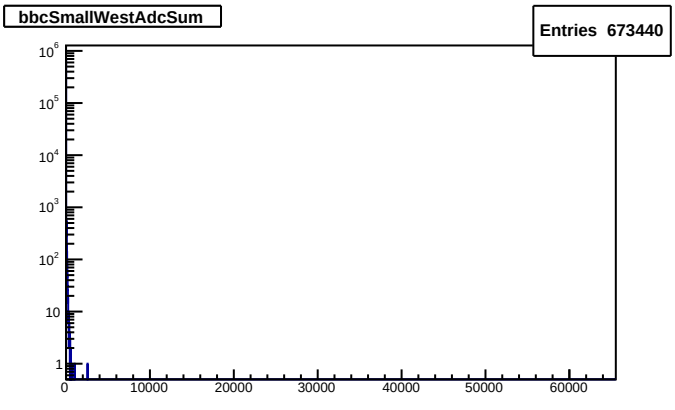

BBQ-ZD001 (ZDC TOWER)
Entries 1.077504e+07
TAC

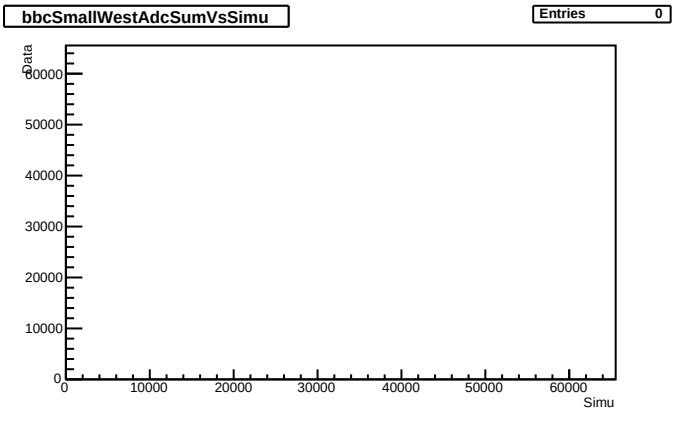

bbcSmallEastAdcSum
Entries 673440

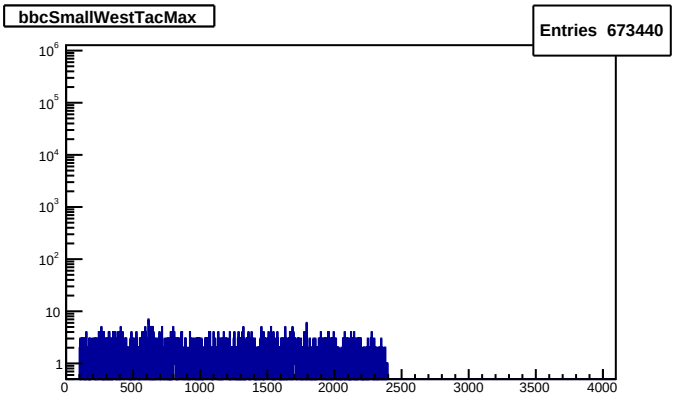

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

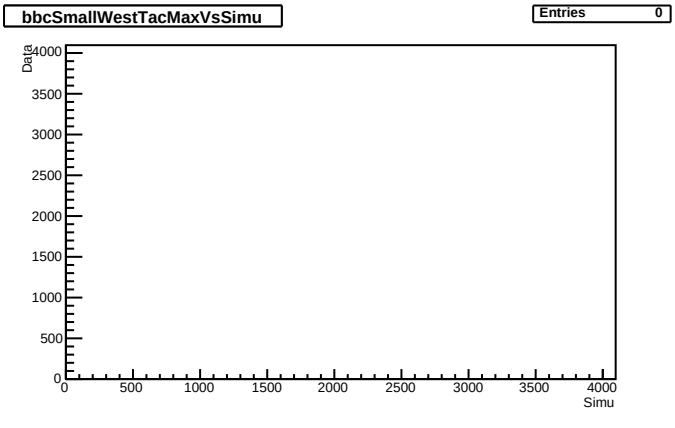

bbcSmallEastTacMax
Entries 673440

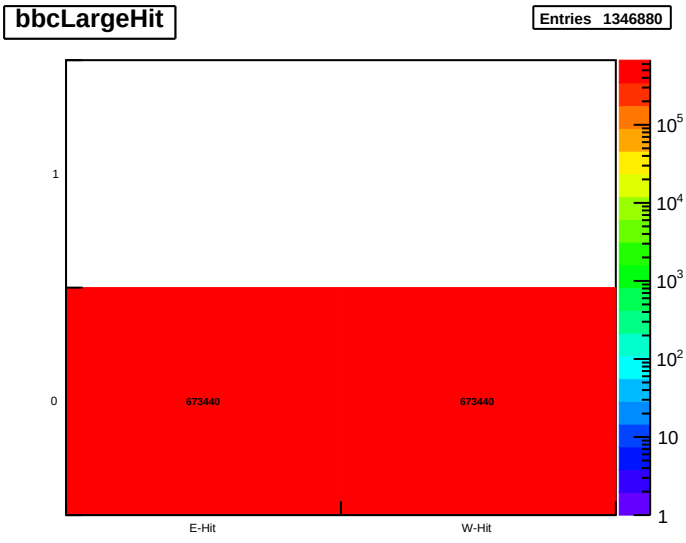

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

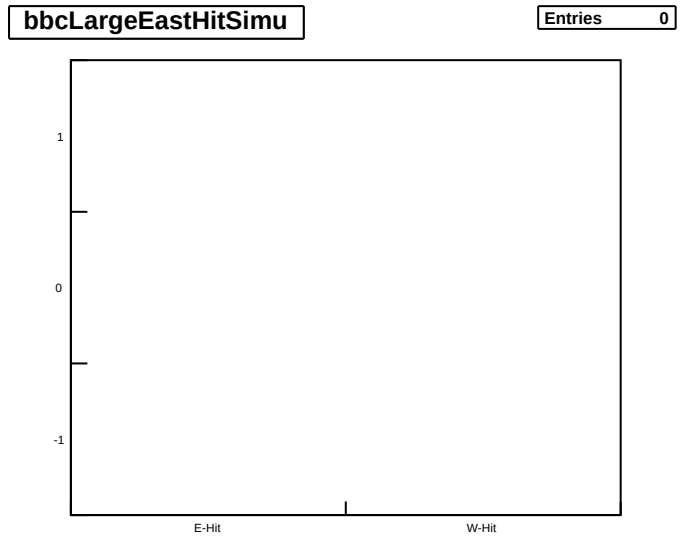

bbcSmallWestAdcSum
Entries 673440

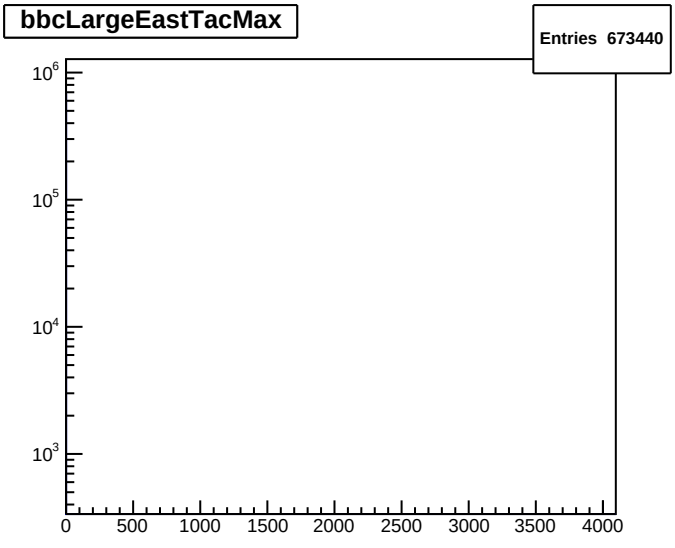

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

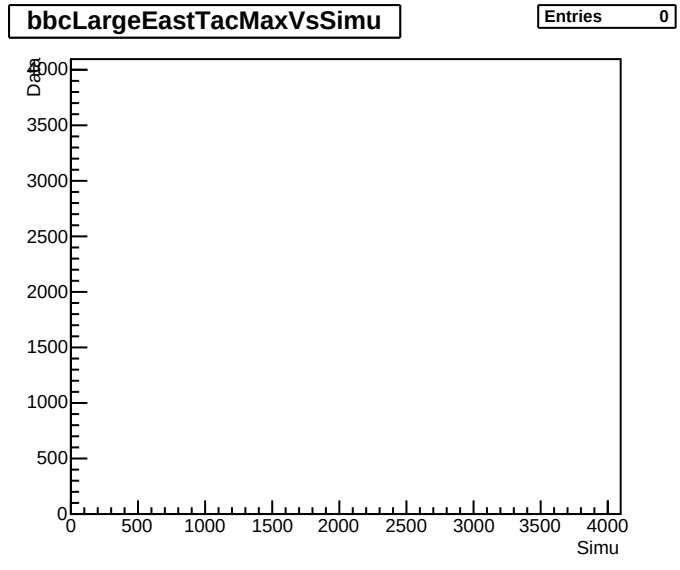

bbcSmallWestTacMax
Entries 673440

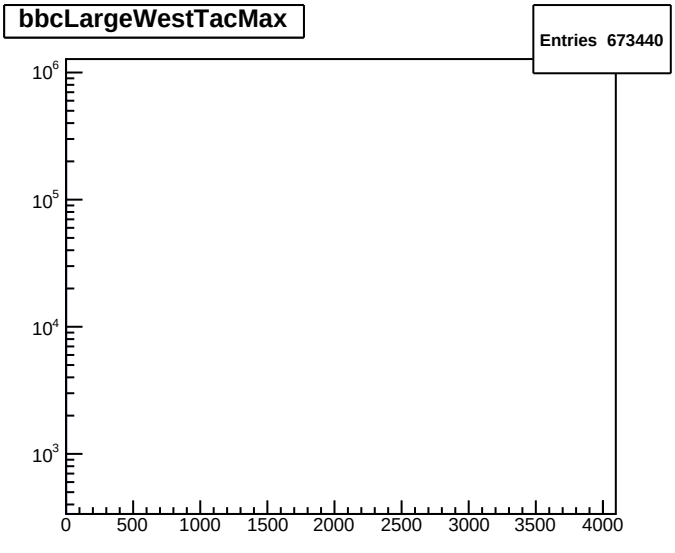

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

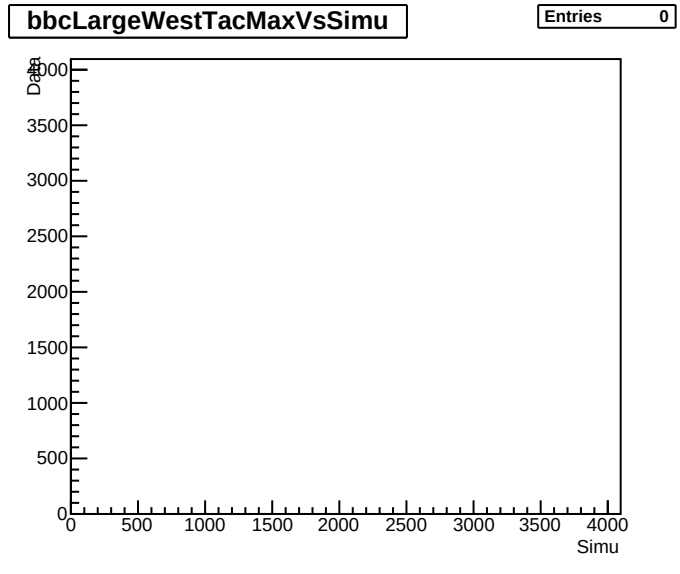

bbcLargeHit
Entries 1346880
673440
673440
E-Hit
W-Hit

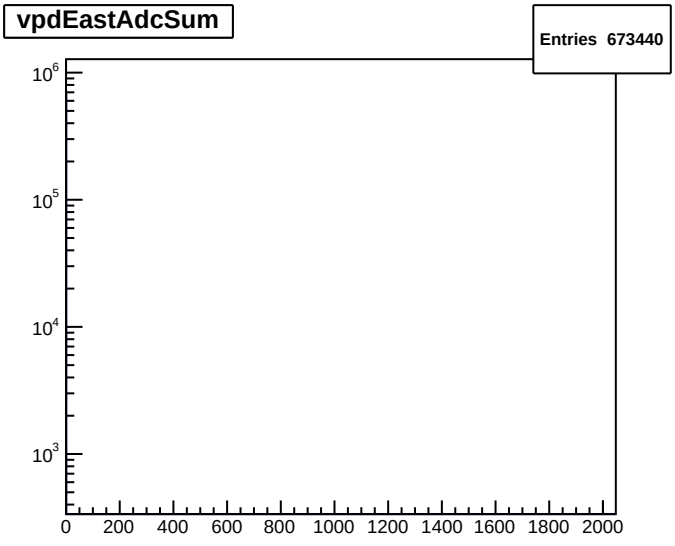

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

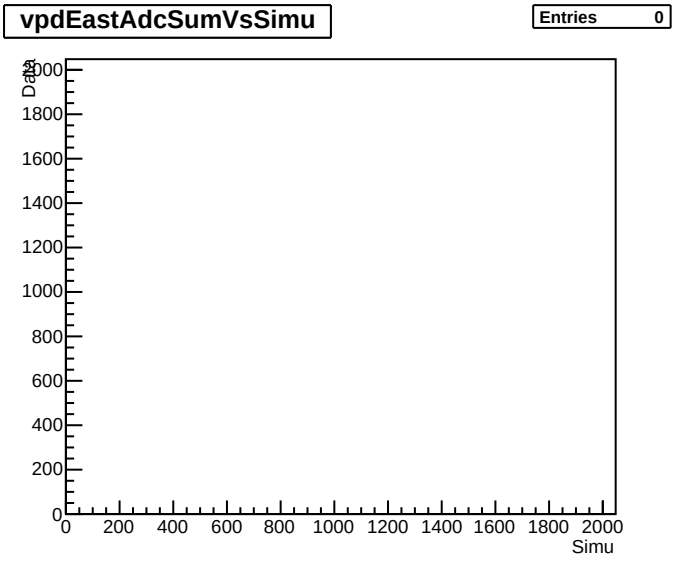

bbcLargeEastTacMax
Entries 673440

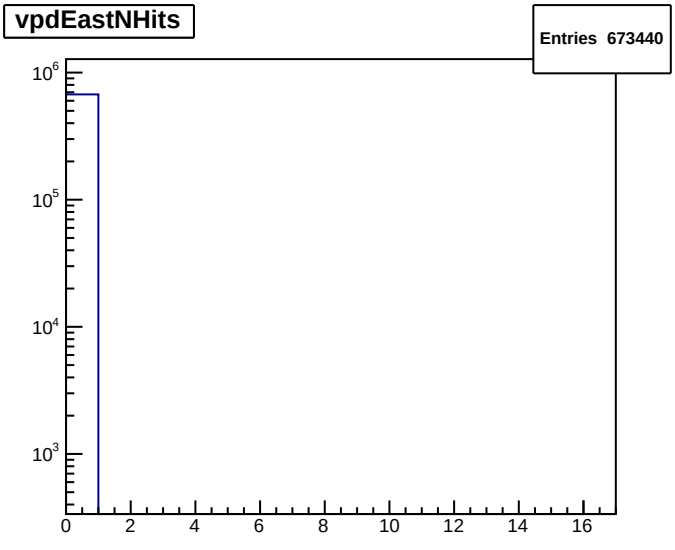

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

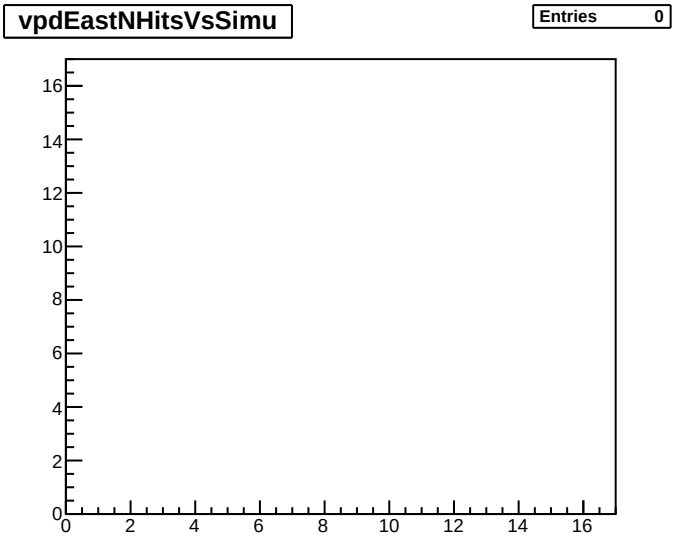

bbcLargeWestTacMax
Entries 673440

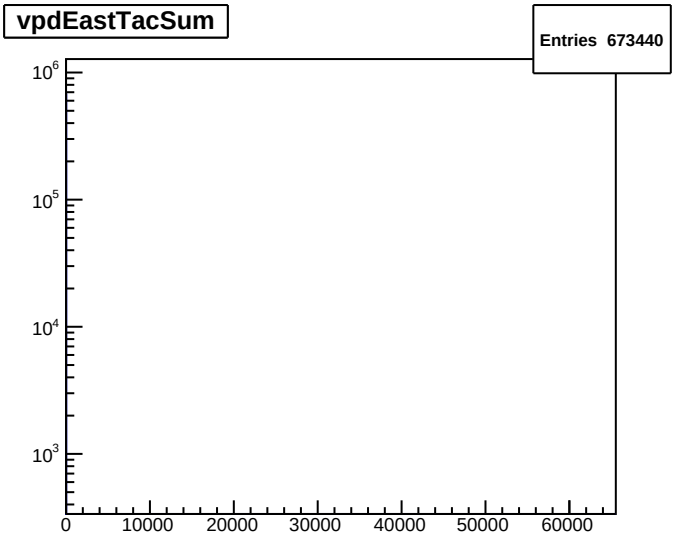

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

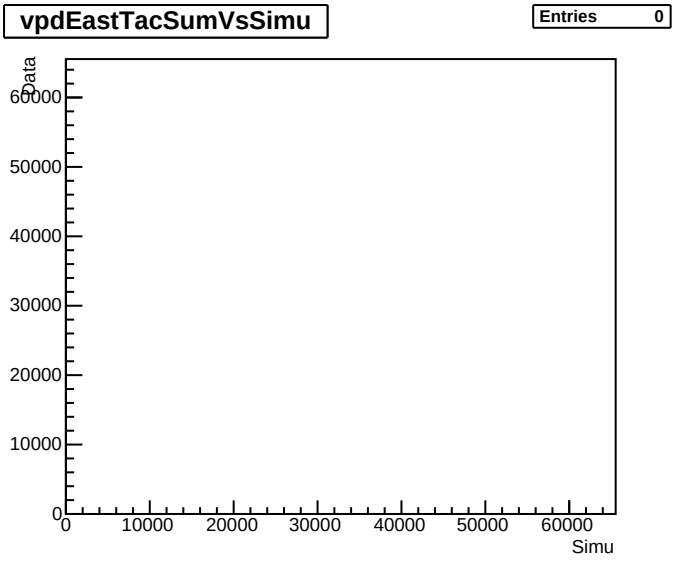

vpdEastAdcSum
Entries 673440
10^6
10^5
10^4
10^3
0 200 400 600 800 1000 1200 1400 1600 1800 2000

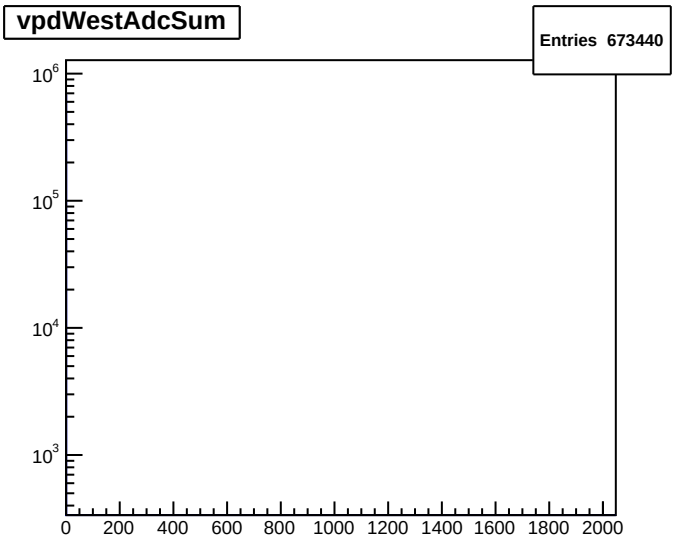

vpdEastAdcSumVsSimu
Entries 0
Data
2000 1800 1600 1400 1200 1000 800 600 400 200 0
0 200 400 600 800 1000 1200 1400 1600 1800 2000
Simu

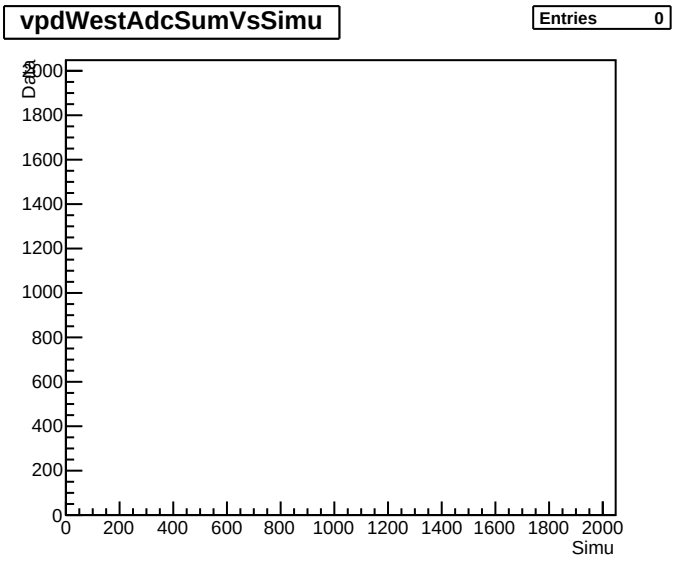

vpdEastNHits
Entries 673440
10^6
10^5
10^4
10^3
0 2 4 6 8 10 12 14 16

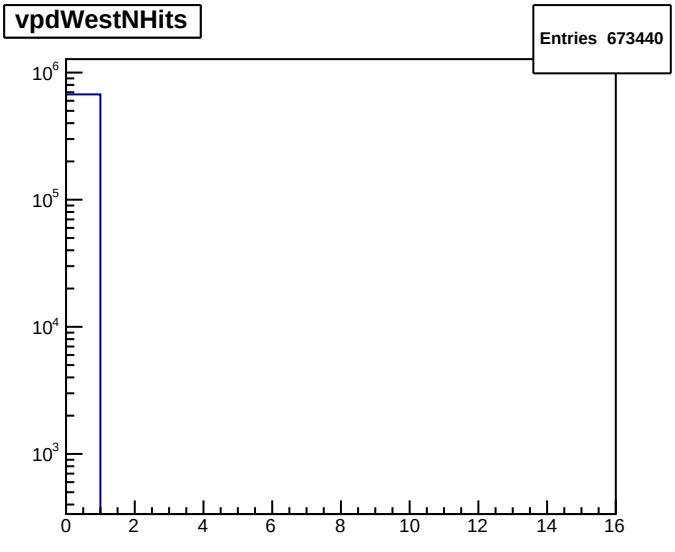

vpdEastNHitsVsSimu
Entries 0
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16

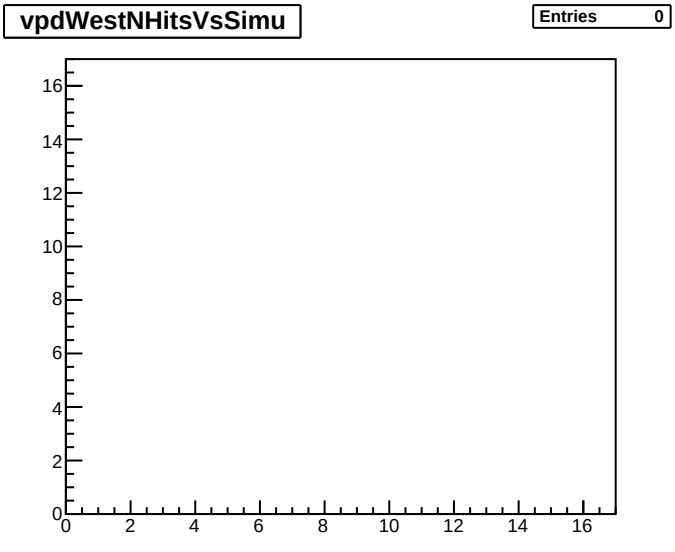

vpdEastTacSum
Entries 673440
10^6
10^5
10^4
10^3
0 10000 20000 30000 40000 50000 60000

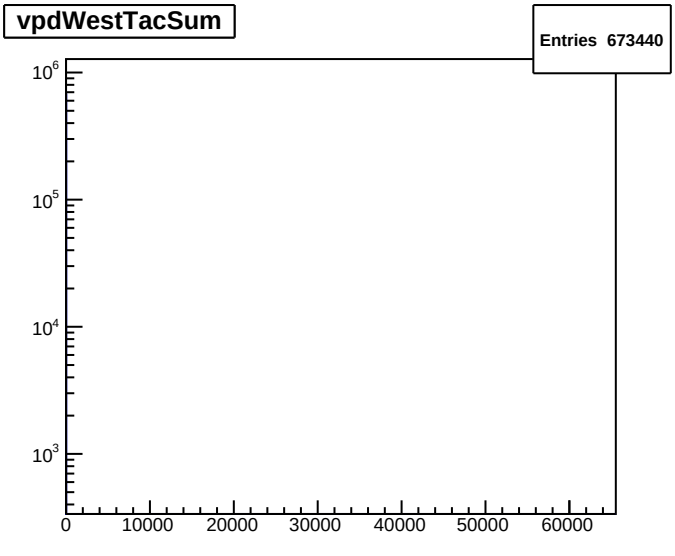

vpdEastTacSumVsSimu
Entries 0
Data
60000 50000 40000 30000 20000 10000 0
0 10000 20000 30000 40000 50000 60000
Simu

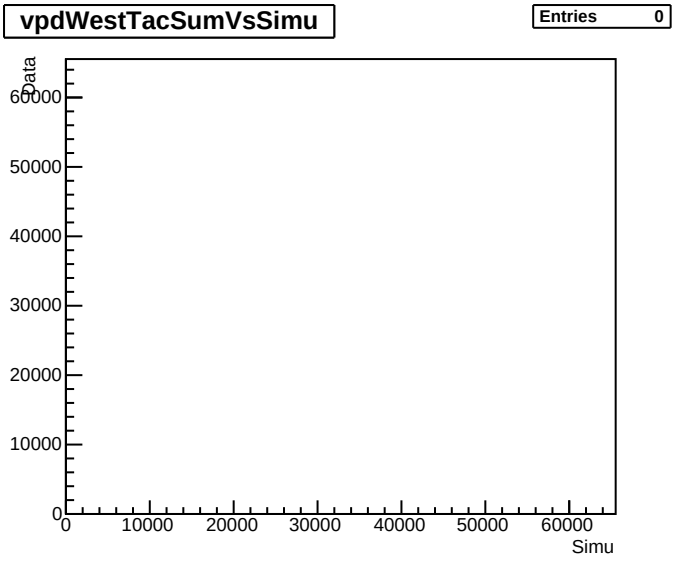

vpdWestAdcSum
Entries 673440
10^6
10^5
10^4
10^3
0 200 400 600 800 1000 1200 1400 1600 1800 2000

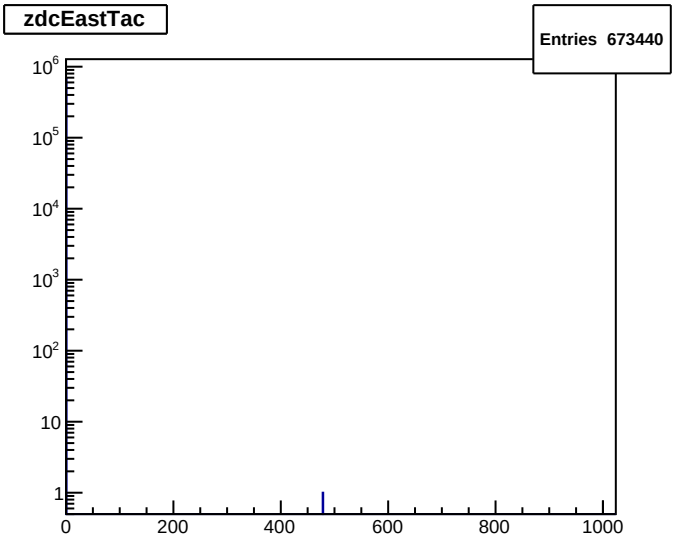

vpdWestAdcSumVsSimu
Entries 0
Data
2000 1800 1600 1400 1200 1000 800 600 400 200 0
0 200 400 600 800 1000 1200 1400 1600 1800 2000
Simu

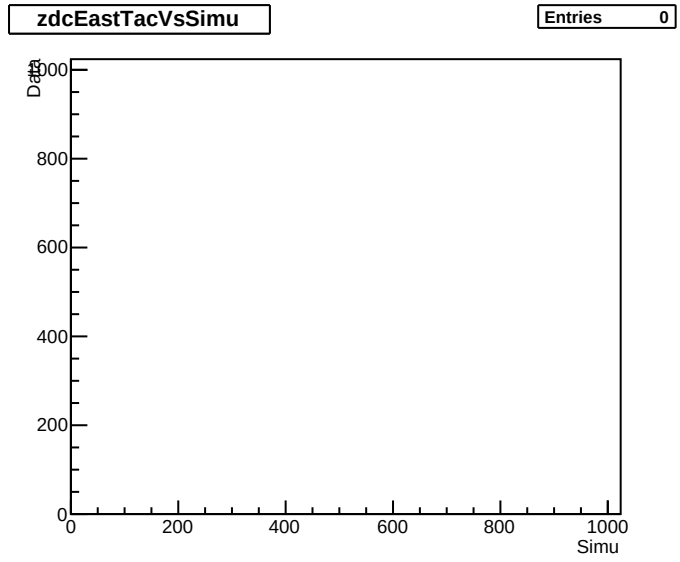

vpdWestNHits
Entries 673440
10^6
10^5
10^4
10^3
0 2 4 6 8 10 12 14 16

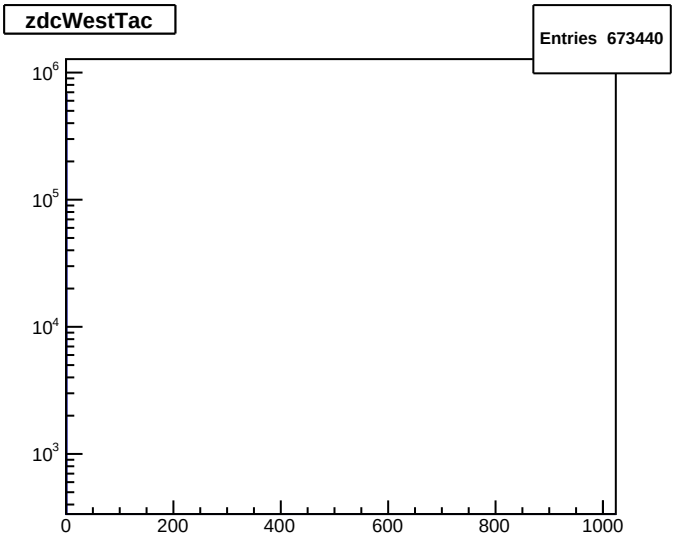

vpdWestNHitsVsSimu
Entries 0
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16

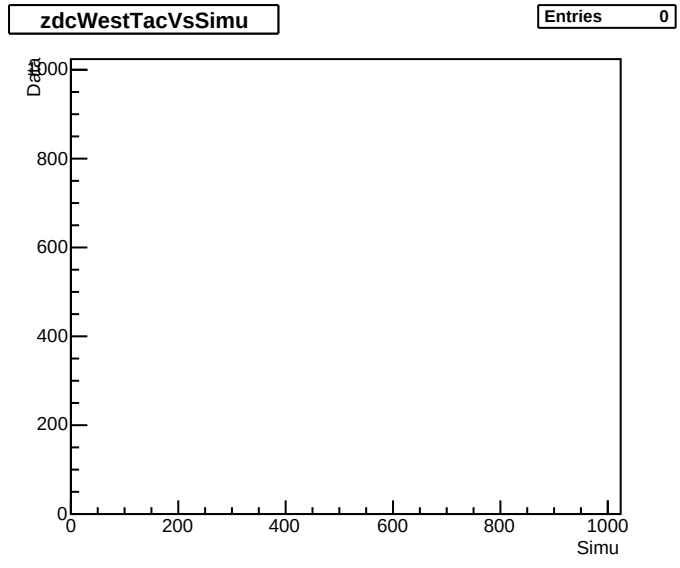

vpdWestTacSum
Entries 673440
10^6
10^5
10^4
10^3
0 10000 20000 30000 40000 50000 60000

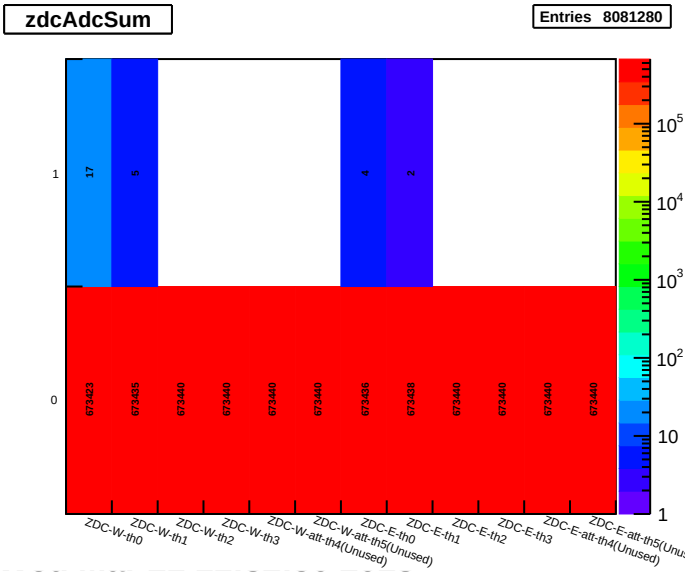

vpdWestTacSumVsSimu
Entries 0
Data
60000 50000 40000 30000 20000 10000 0
0 10000 20000 30000 40000 50000 60000
Simu

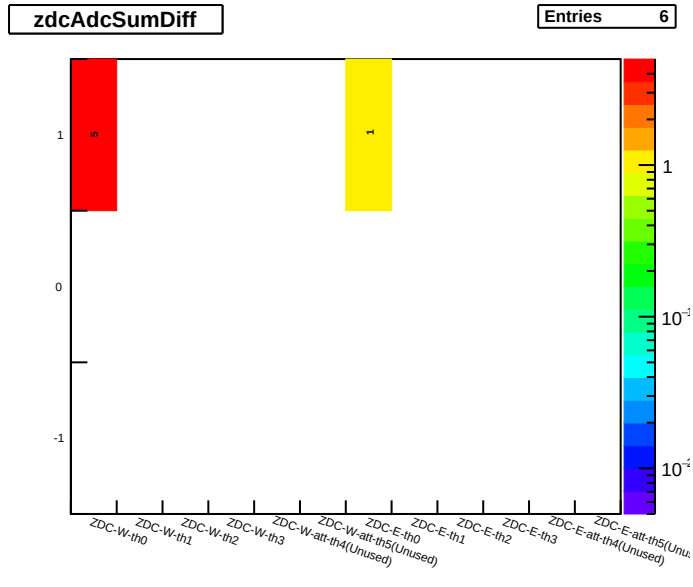

zdcEastTac
Entries 673440

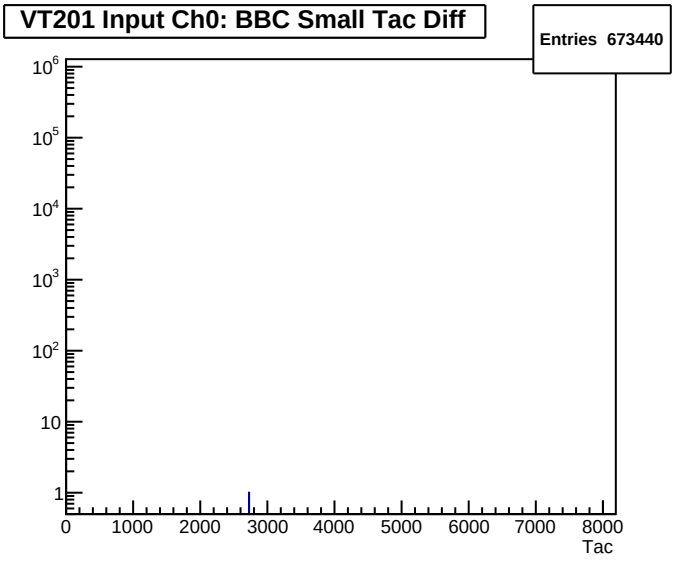

zdcEastTacVsSimu
Entries 0
Data
Simu

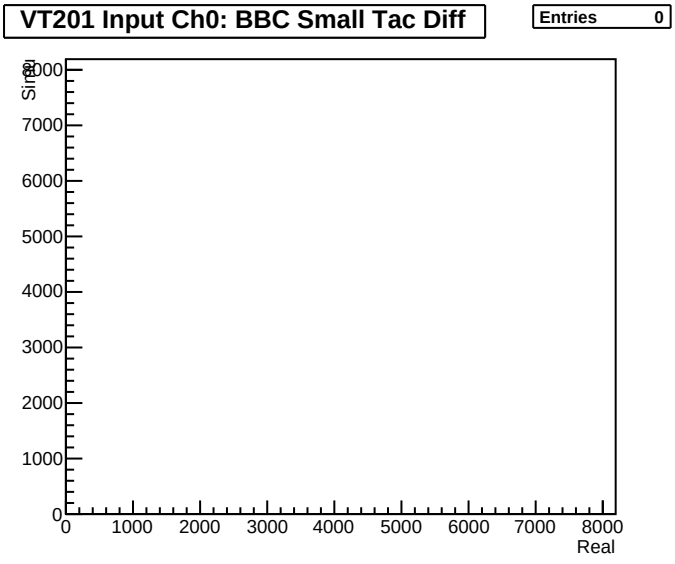

zdcWestTac
Entries 673440

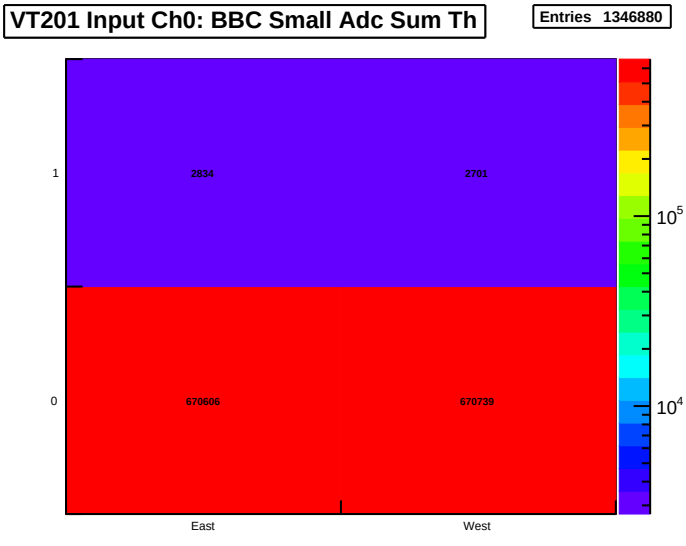

zdcWestTacVsSimu
Entries 0
Data
Simu

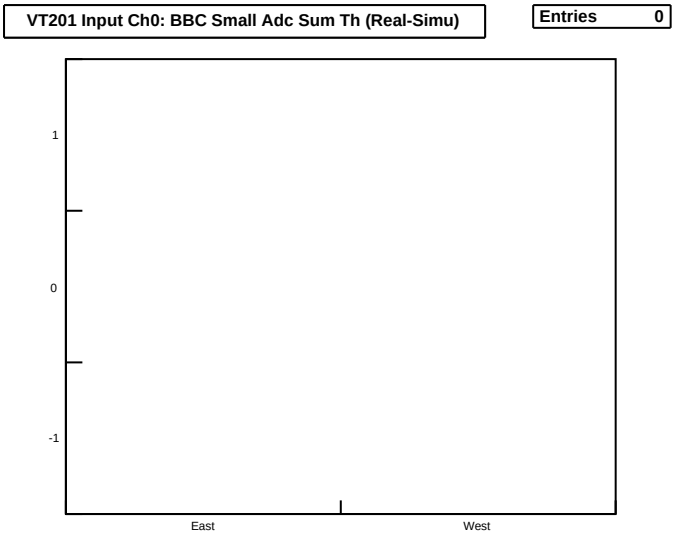

zdcAdcSum
Entries 8081280
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

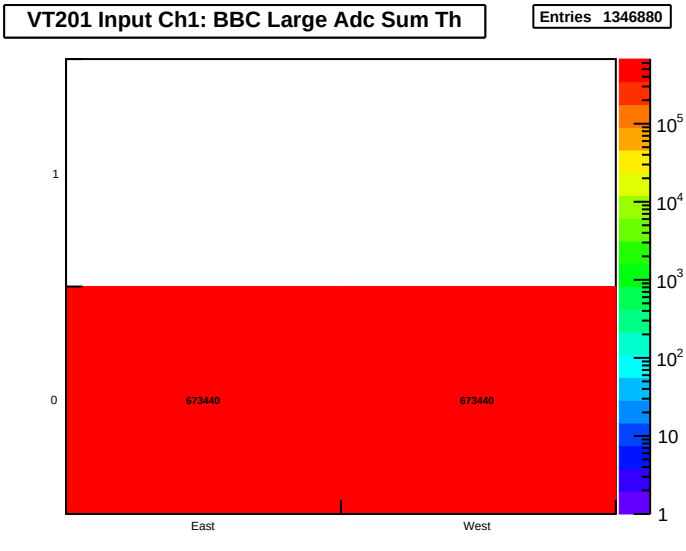

zdcAdcSumDiff
Entries 6
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

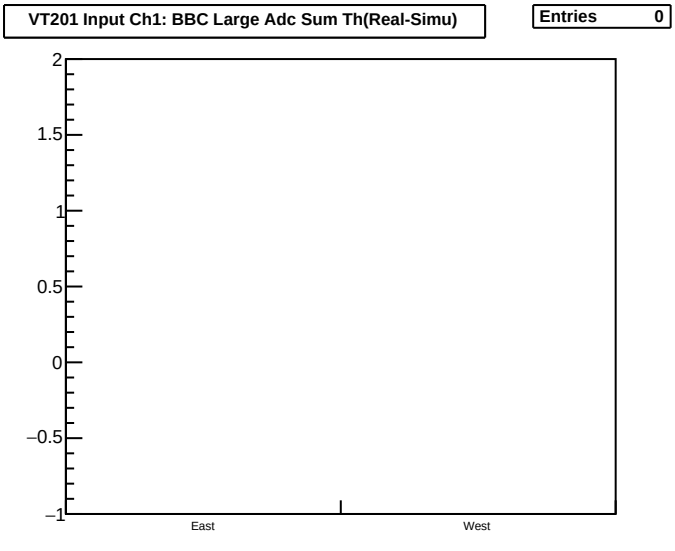

VT201 Input Ch0: BBC Small Tac Diff
Entries 673440
Tac

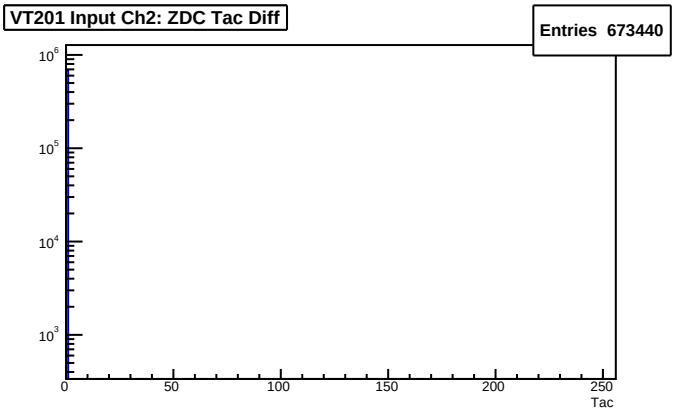

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

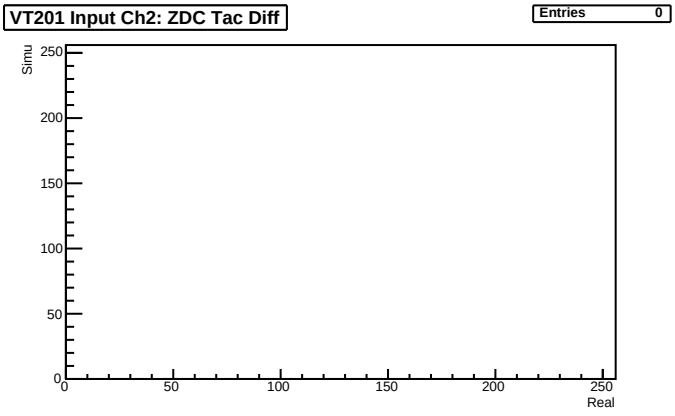

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 1346880
2834
2701
670606
670739
East
West

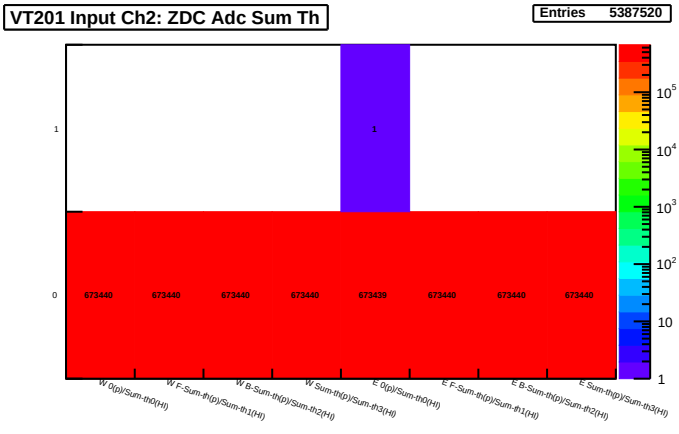

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

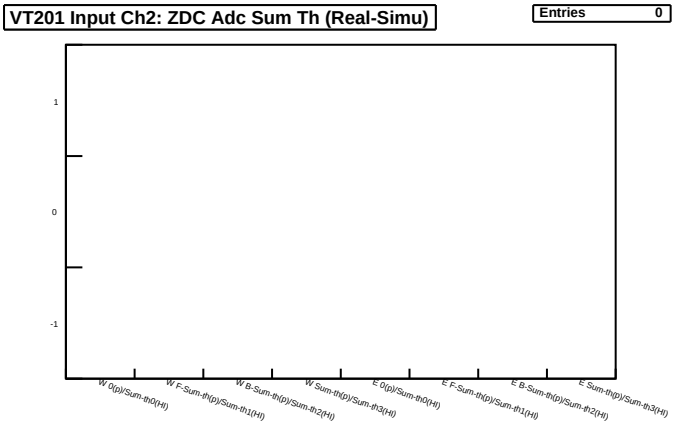

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 1346880
673440
673440
East
West

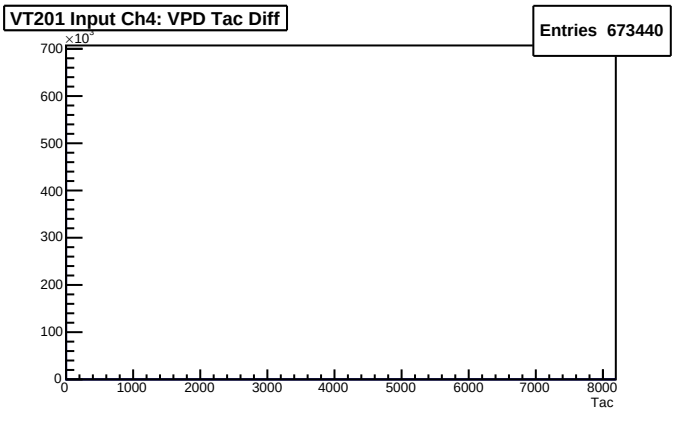

VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

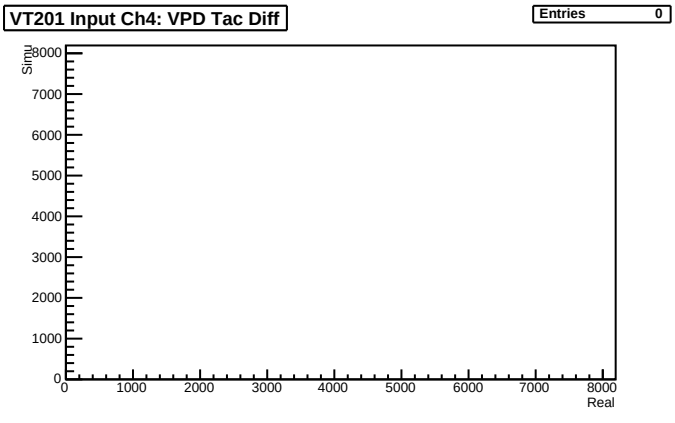

VT201 Input Ch2: ZDC Tac Diff
Entries 673440
Tac

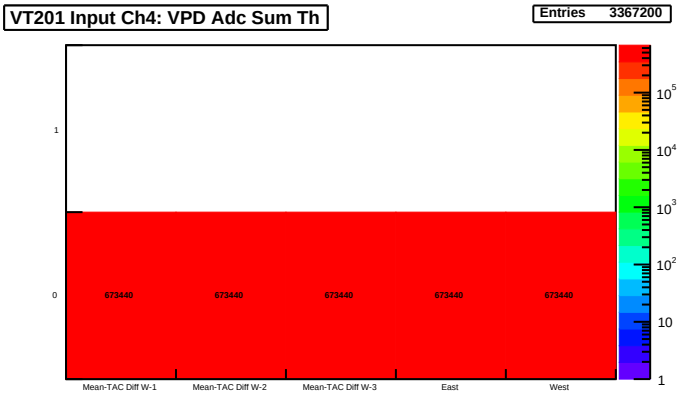

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

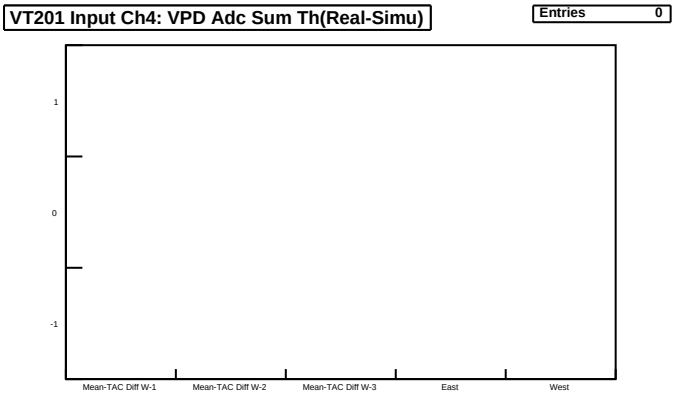

VT201 Input Ch2: ZDC Adc Sum Th
Entries 5387520
673440
673440
673440
673440
673439
673440
673440
673440
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

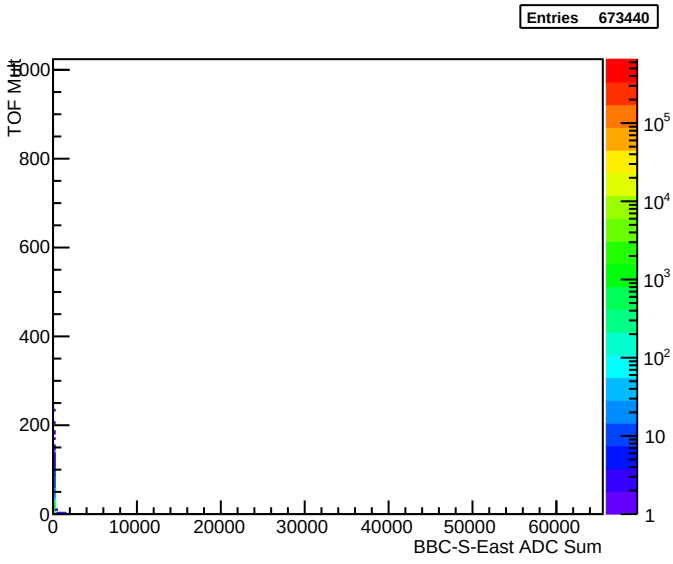

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

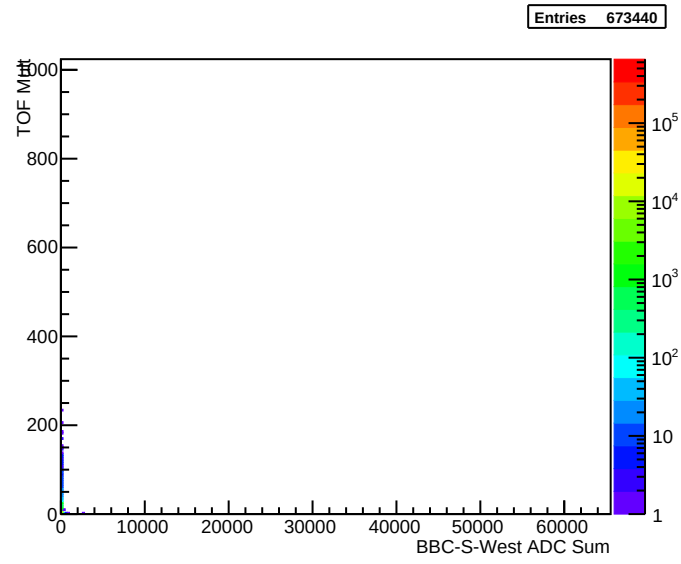

VT201 Input Ch4: VPD Tac Diff
Entries 673440
Tac

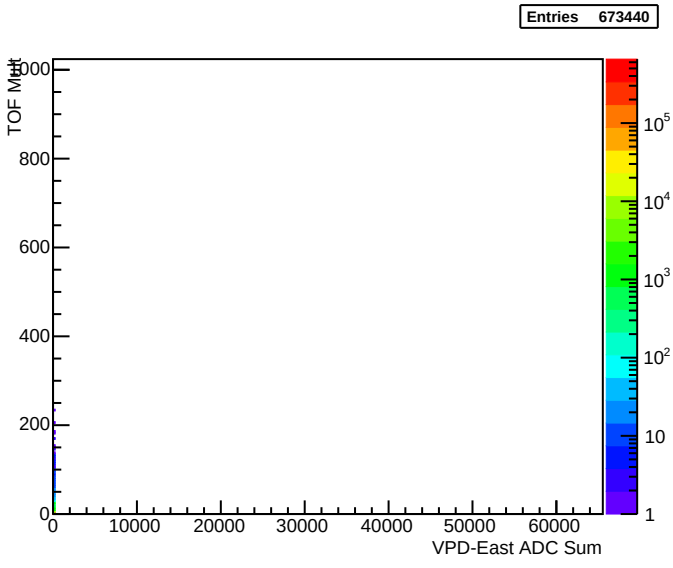

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

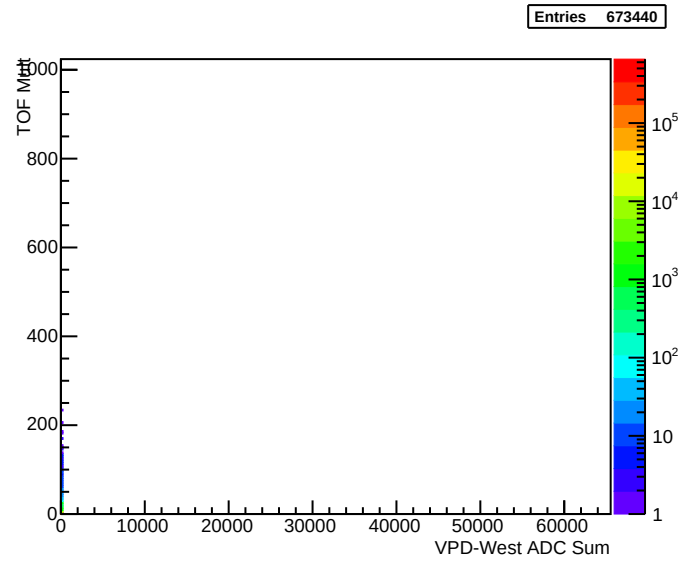

VT201 Input Ch4: VPD Adc Sum Th
Entries 3367200
673440
673440
673440
673440
673440
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

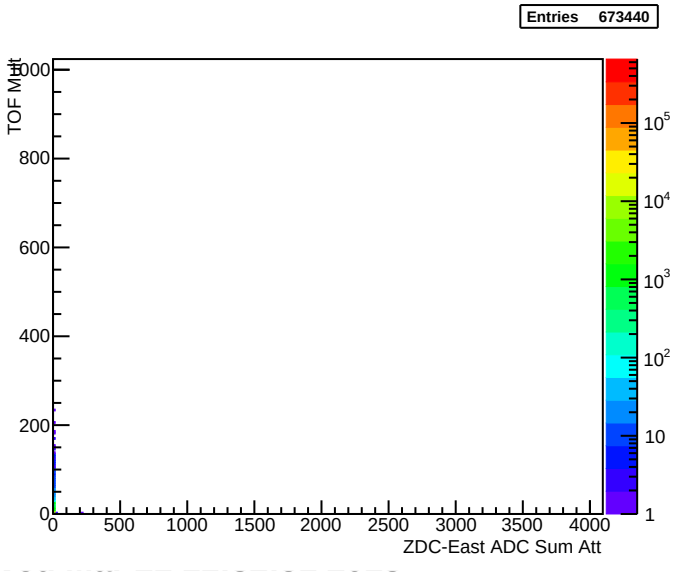

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

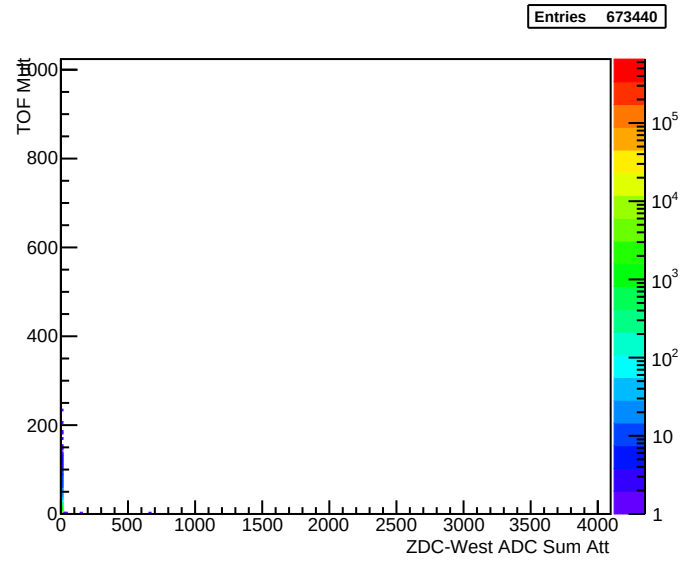

Entries 673440
TOF Mult
BBC-S-East ADC Sum

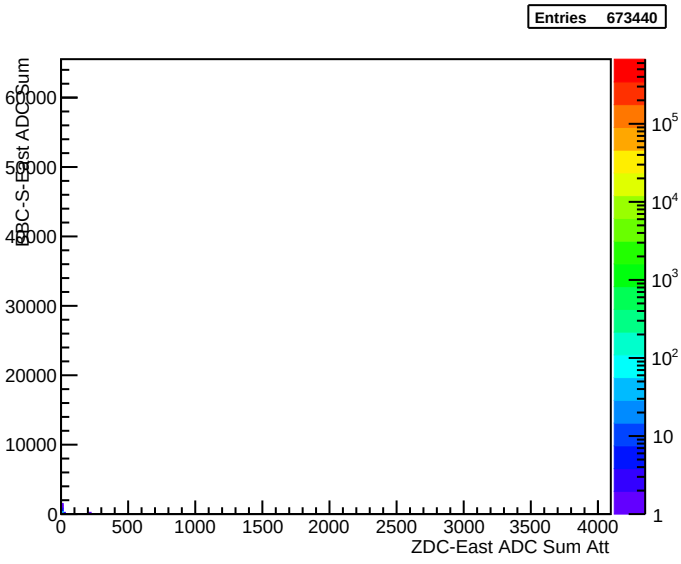

Entries 673440
TOF Mult
BBC-S-West ADC Sum

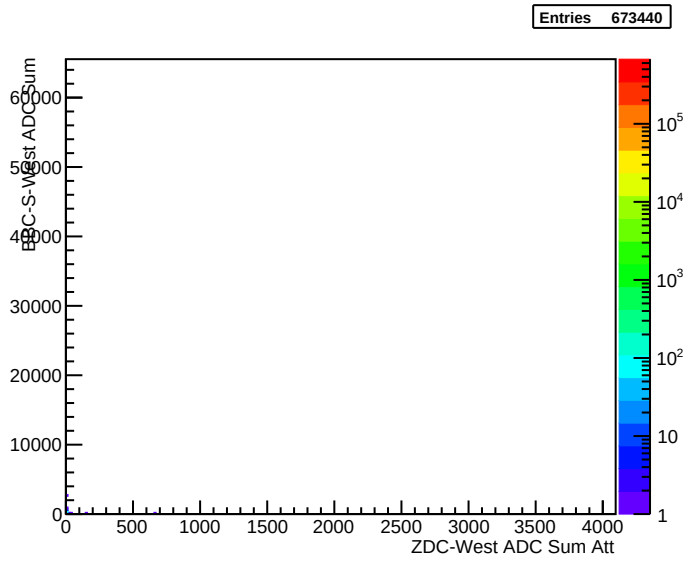

Entries 673440
TOF Mult
VPD-East ADC Sum

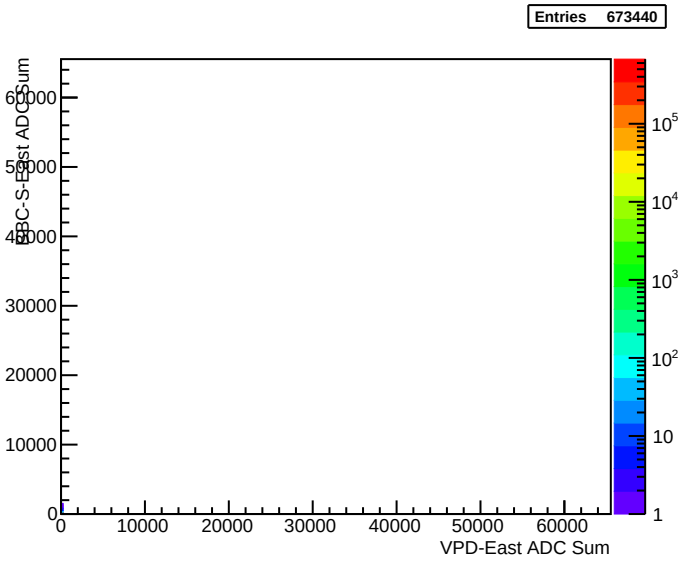

Entries 673440
TOF Mult
VPD-West ADC Sum

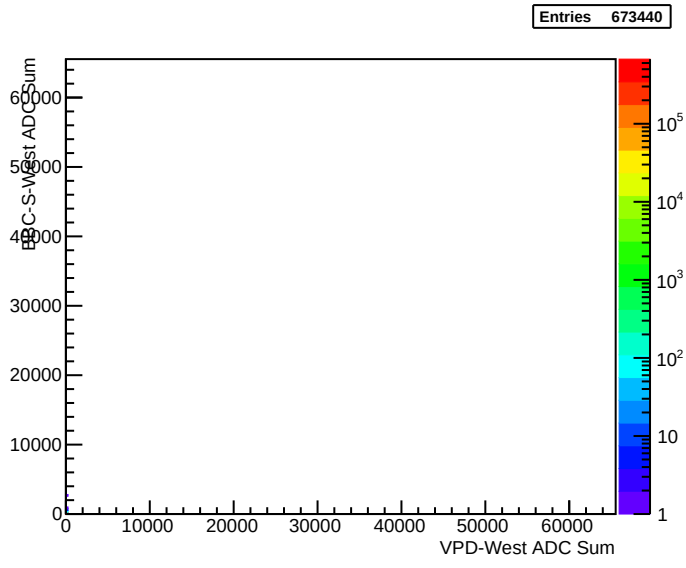

Entries 673440
TOF Mult
ZDC-East ADC Sum Att

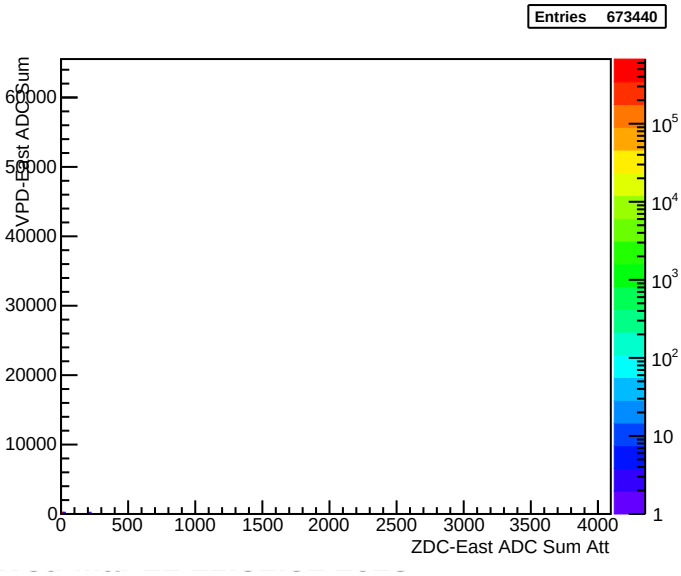

Entries 673440
TOF Mult
ZDC-West ADC Sum Att

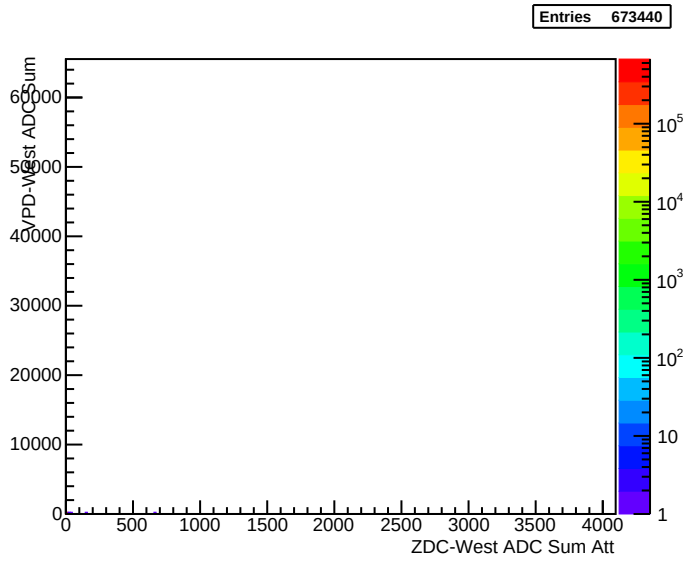

Entries 673440
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

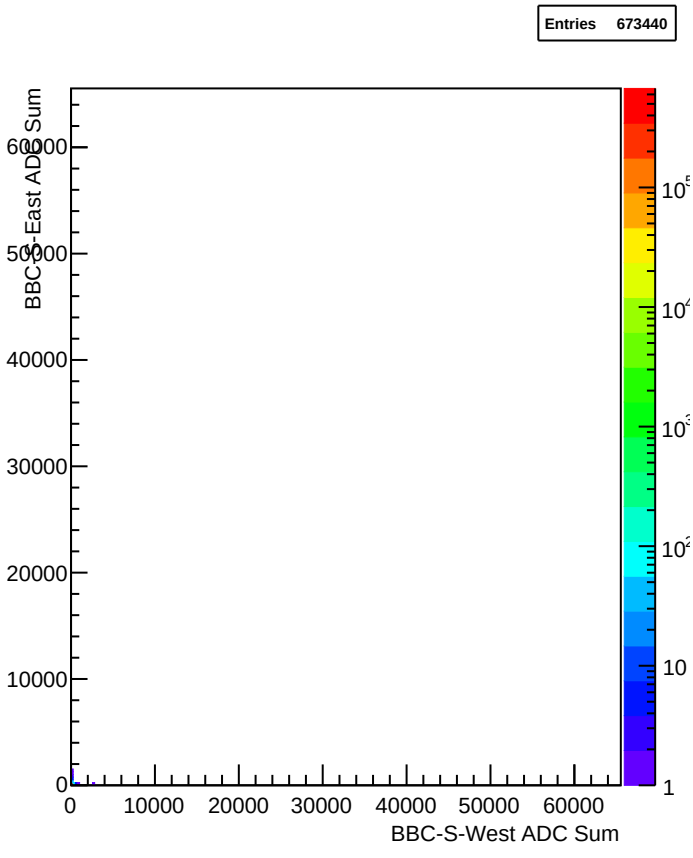

Entries 673440
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

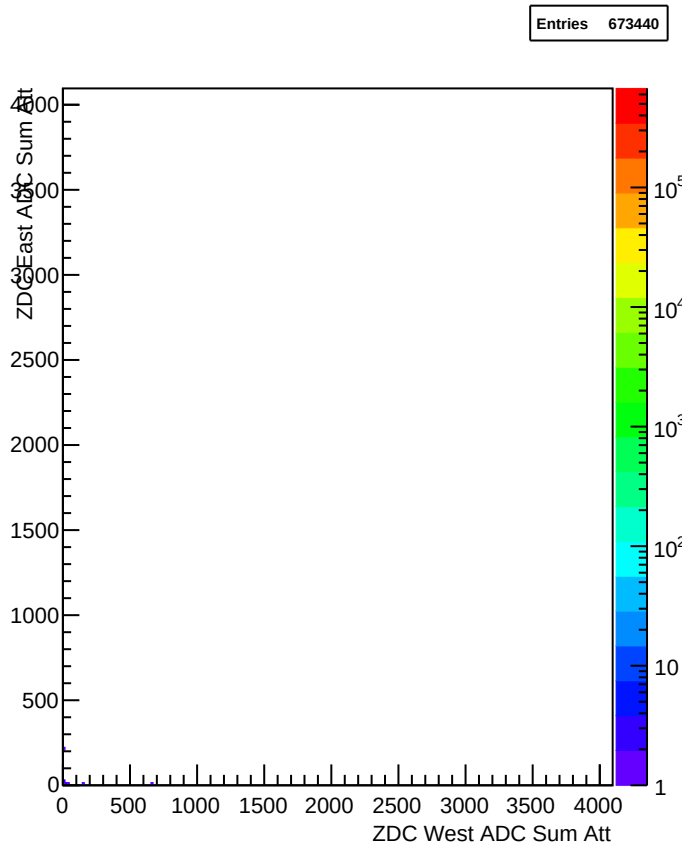

Entries 673440
BBC-S-East ADC Sum
VPD-East ADC Sum

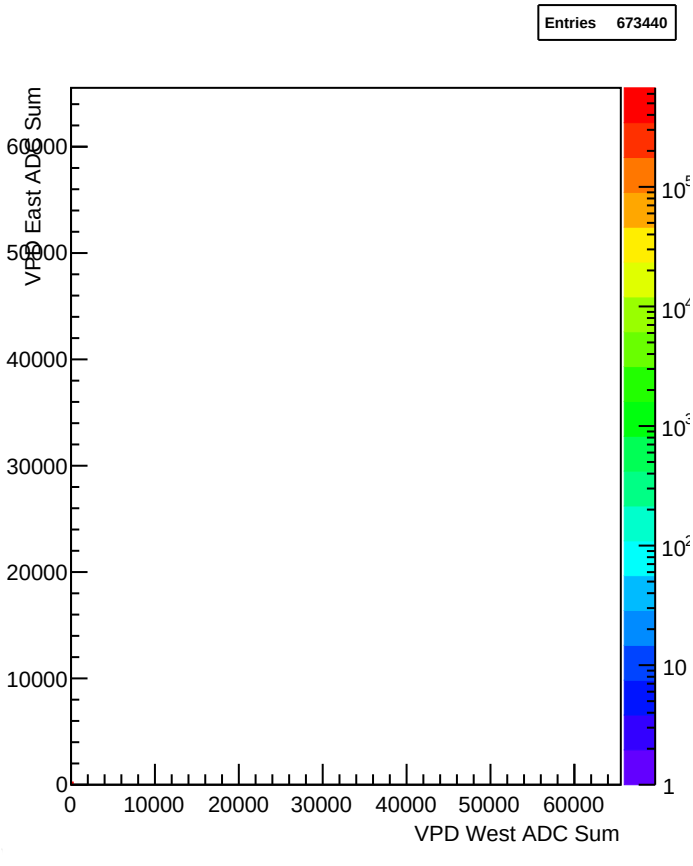

Entries 673440
BBC-S-West ADC Sum
VPD-West ADC Sum

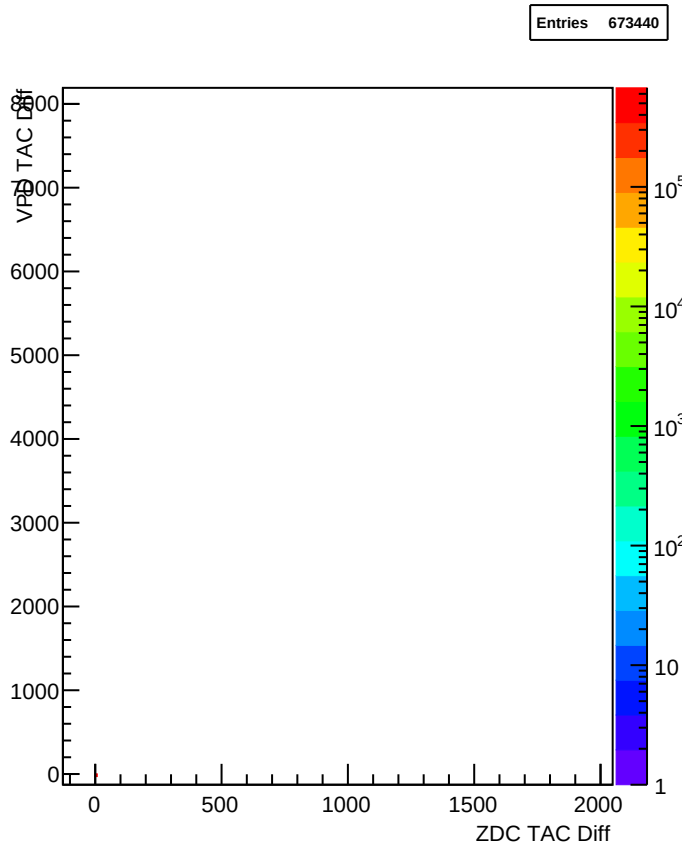

Entries 673440
VPD-East ADC Sum
ZDC-East ADC Sum Att

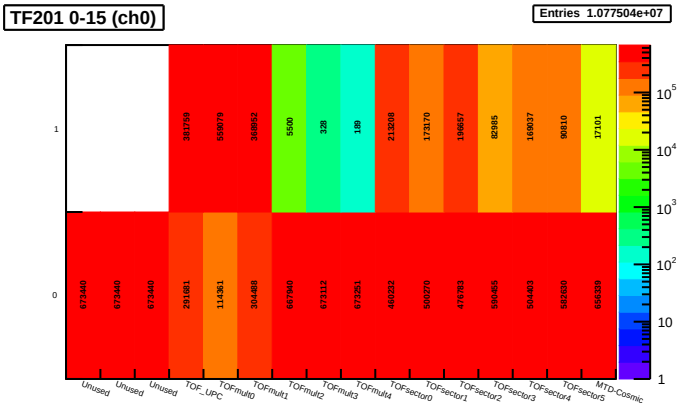

Entries 673440
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 673440
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 673440
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 673440
VPD East ADC Sum
VPD West ADC Sum


Entries 673440
VPD TAC Diff
ZDC TAC Diff


TF201 0-15 (ch0)
Entries 1.077504e+07
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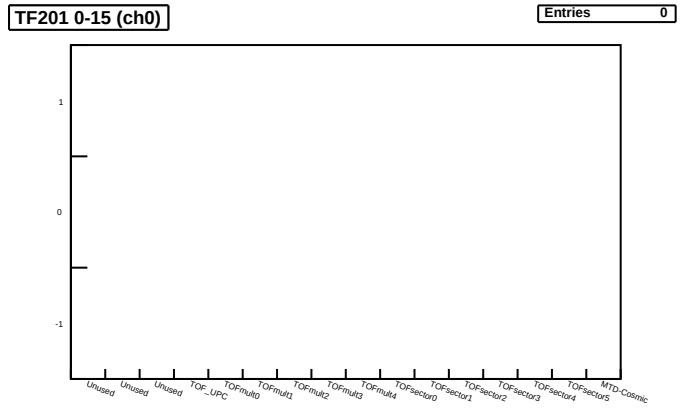
381759 559079 368952 5500 328 189 213208 173170 196657 82985 169137 90810 17101
673440 673440 673440 291681 114361 304488 667940 672112 673251 449232 500270 476783 590465 504403 582630 656339
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic
1 10 10^2 10^3 10^4 10^5


TF201 0-15 (ch0)
Entries 0
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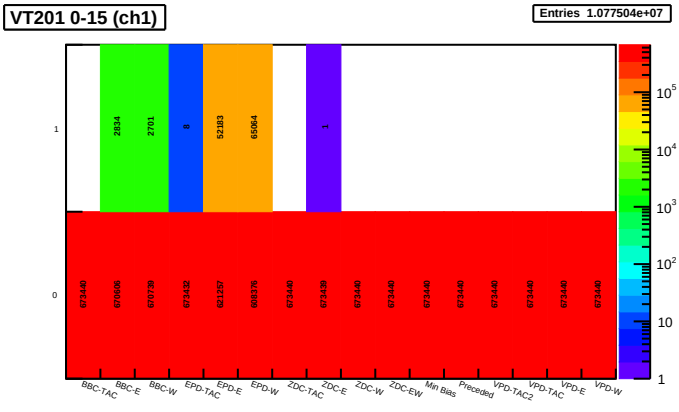
Unused Unused Unused TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
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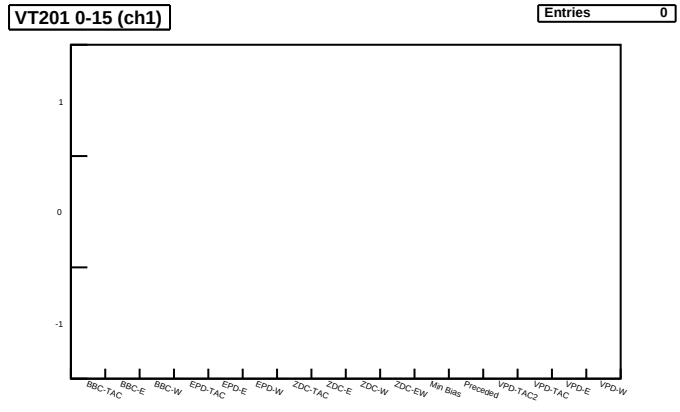
2834 2701 8 52183 65064 1
67440 67606 67739 67432 61257 60876 67440 67439 67440 67440 67440 67440 67440 67440 67440 67440
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W
1 10 10^2 10^3 10^4 10^5


VT201 0-15 (ch1)
Entries 0
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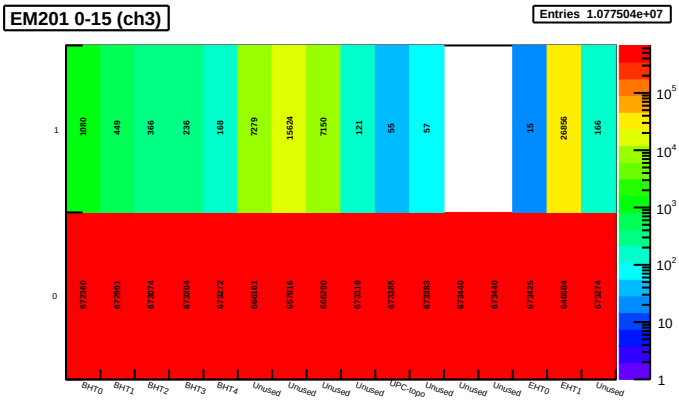
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 1.077504e+07
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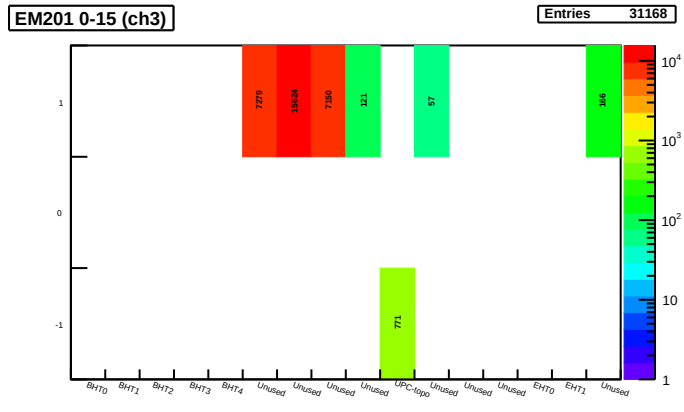
0
1080 449 366 236 168 7279 15624 7150 121 55 57 15 26856 166
672860 673991 673074 672604 672272 660161 657816 666290 673319 673385 673383 673440 673440 673425 646584 672274
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
1 10 10^2 10^3 10^4 10^5


EM201 0-15 (ch3)
Entries 31168
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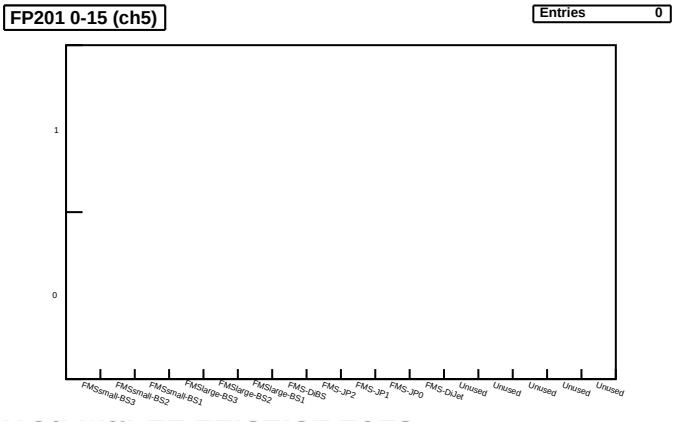
0
-1
7279 15624 7150 121 57 166
771
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
1 10 10^2 10^3 10^4


FP201 0-15 (ch5)
Entries 0
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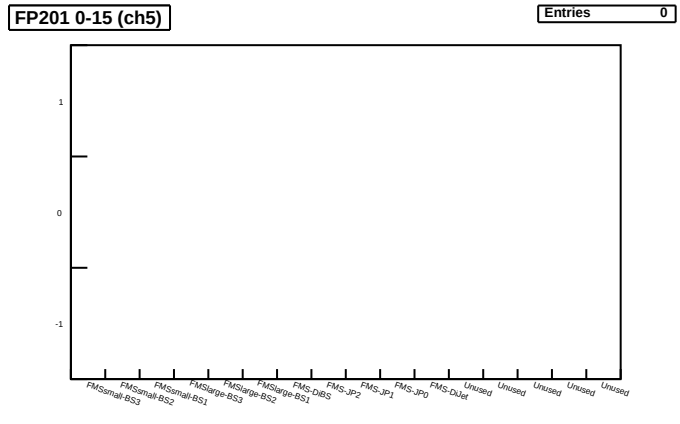
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused


FP201 0-15 (ch5)
Entries 0
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0
-1
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused