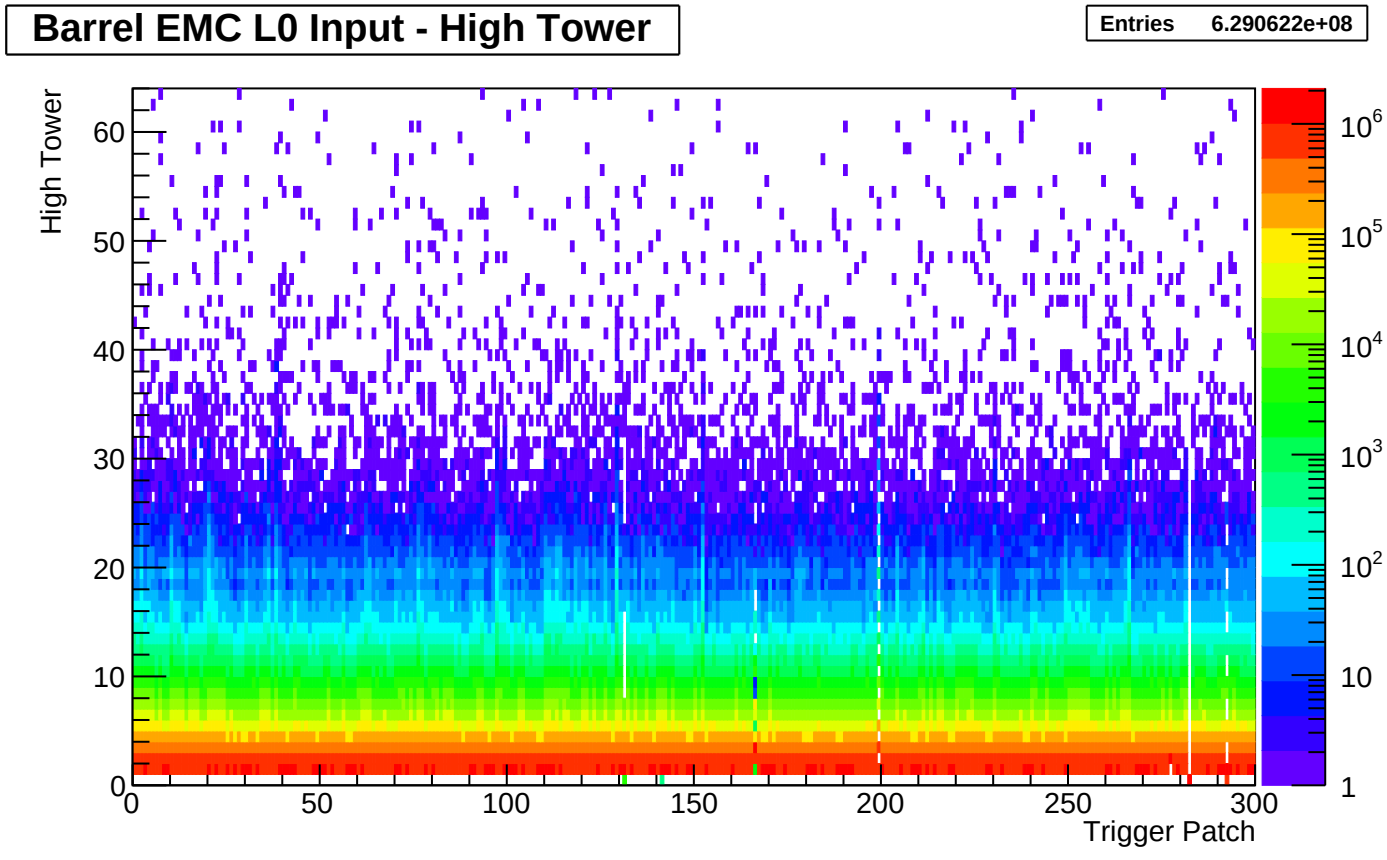
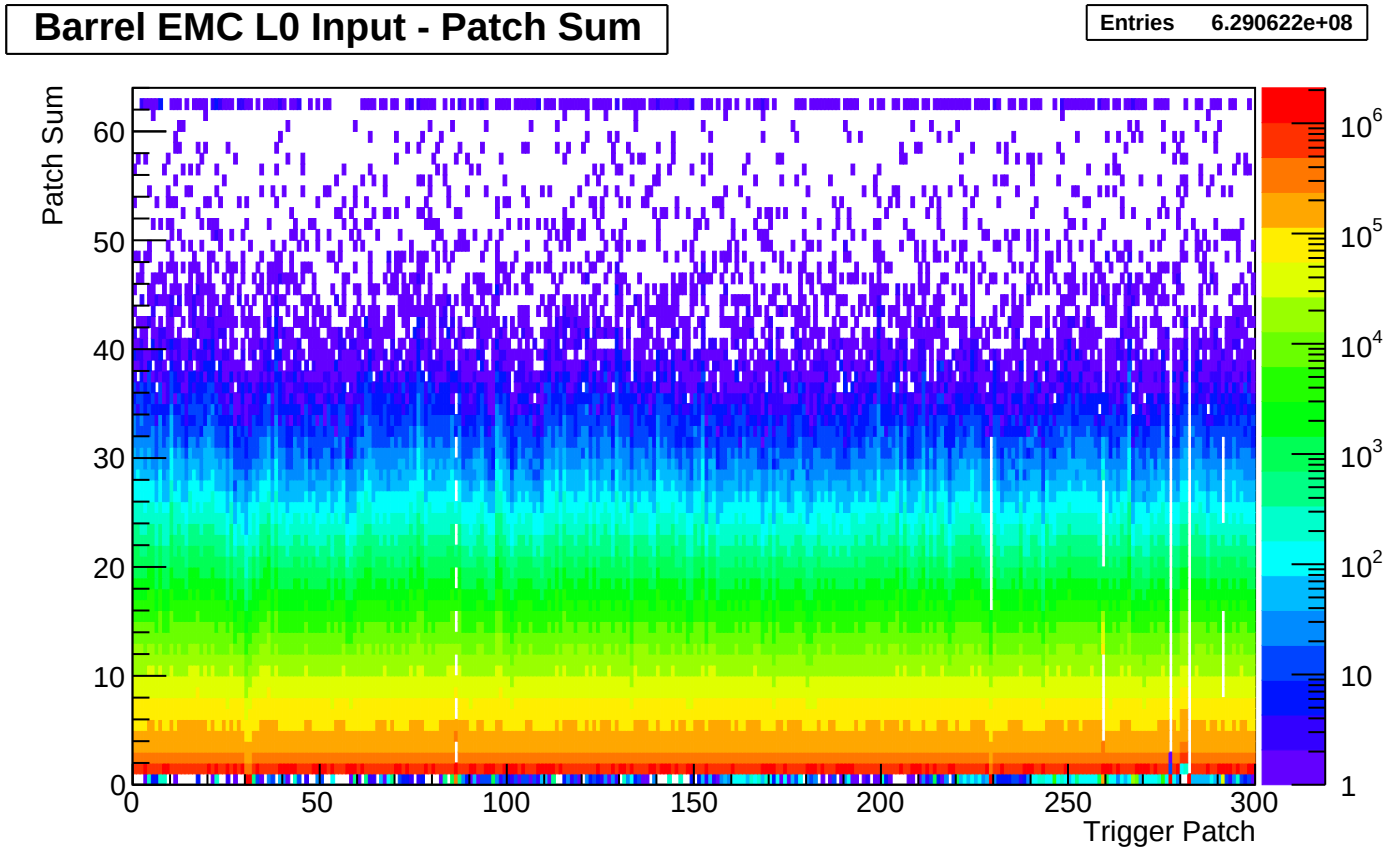
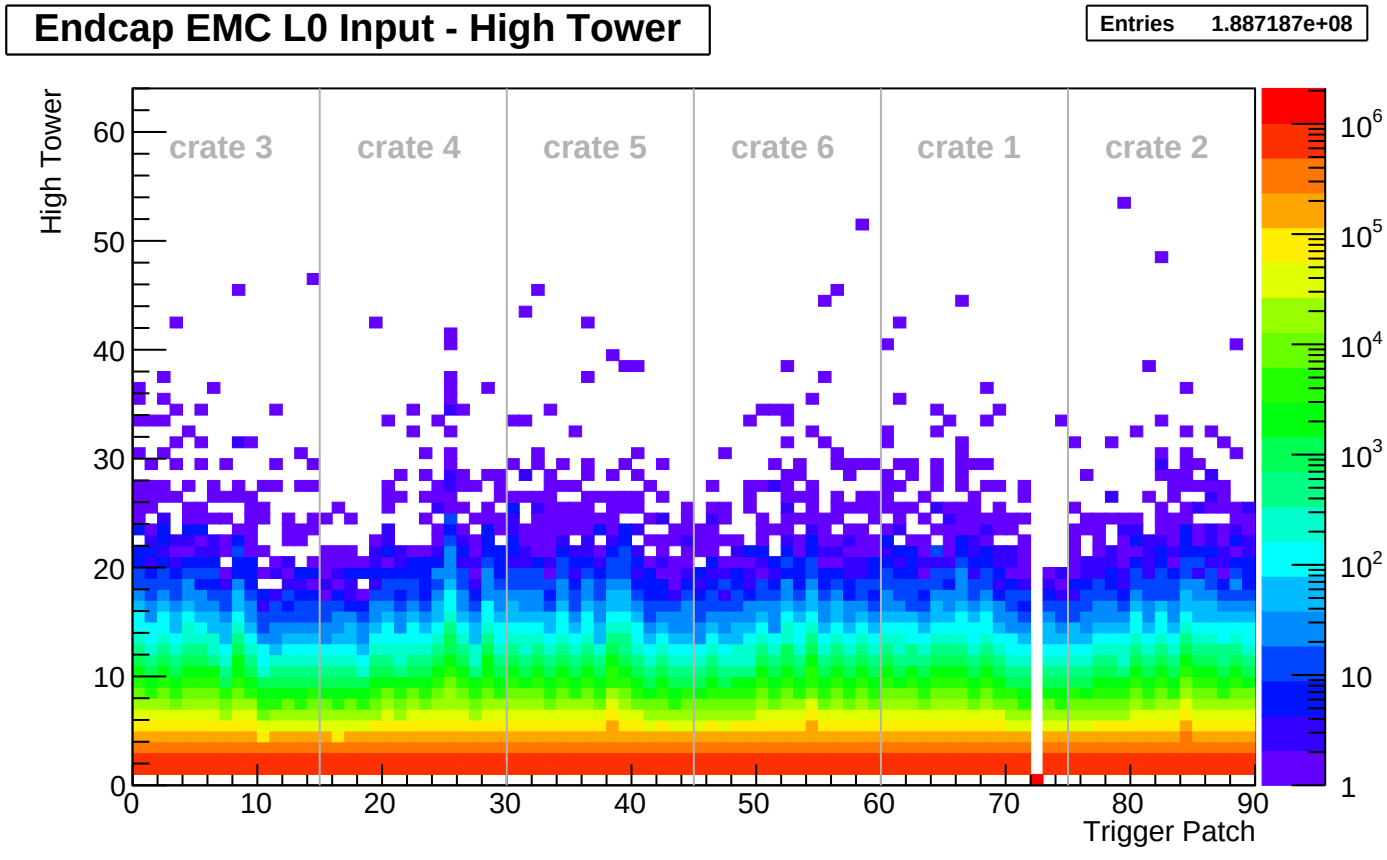
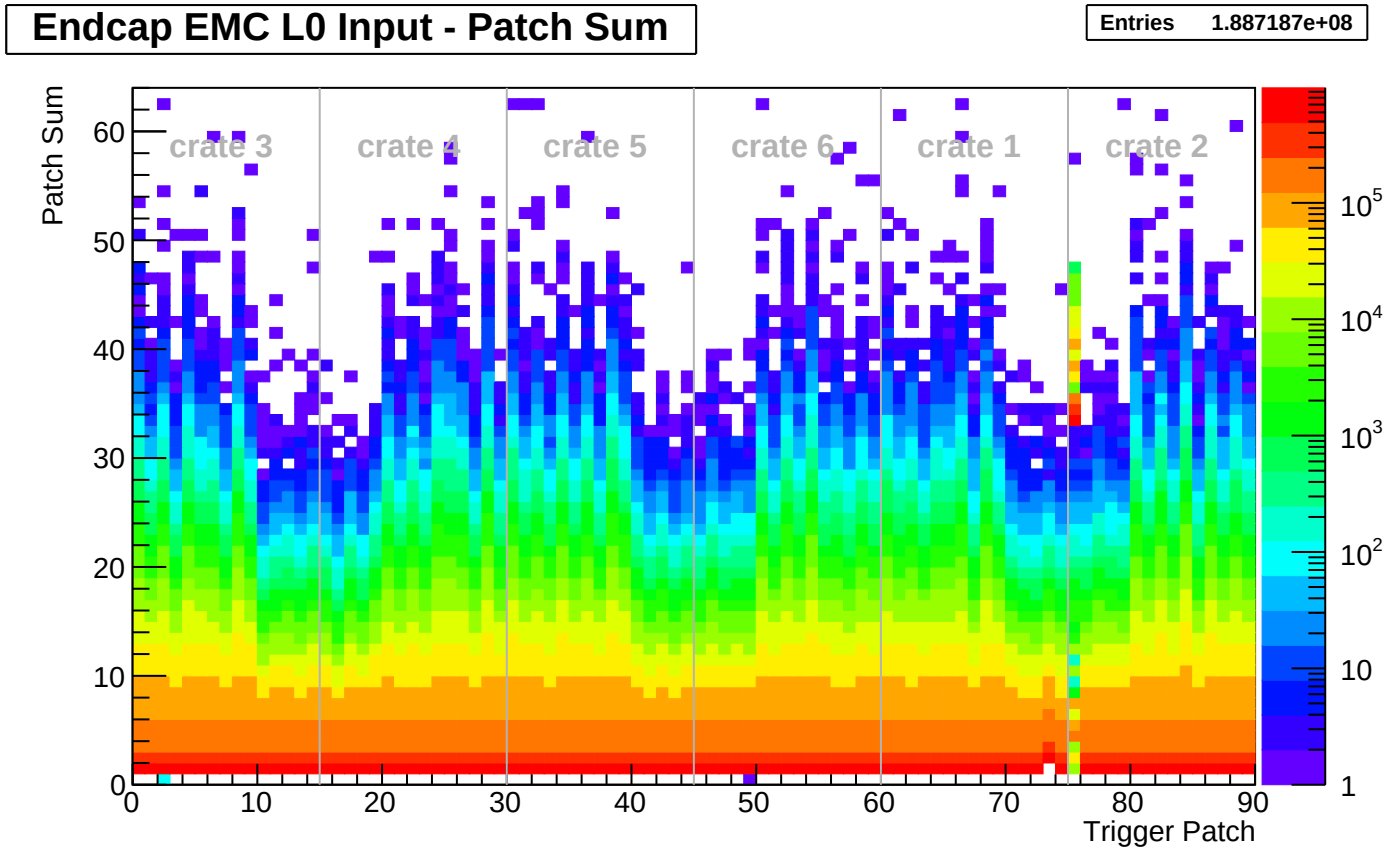
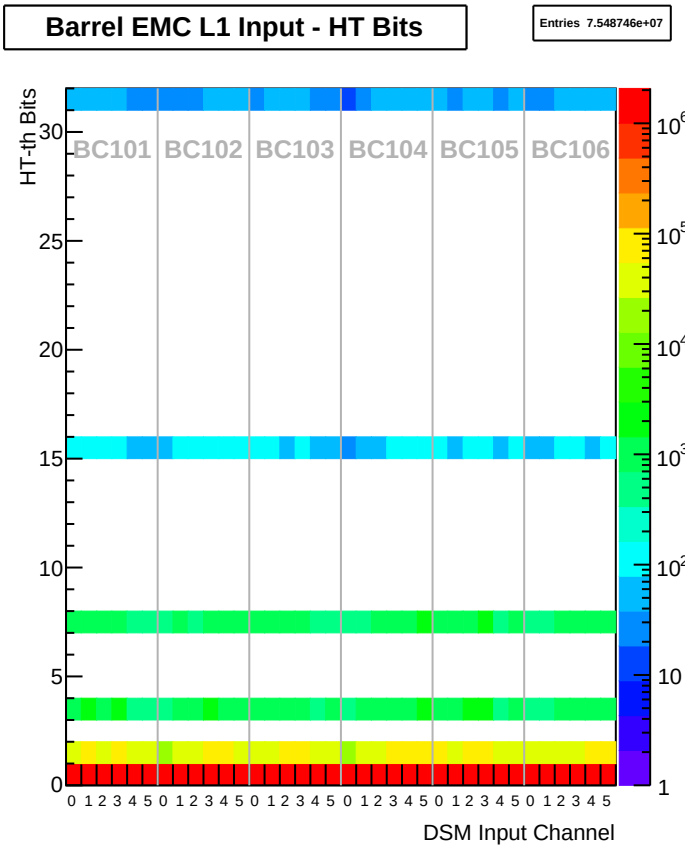
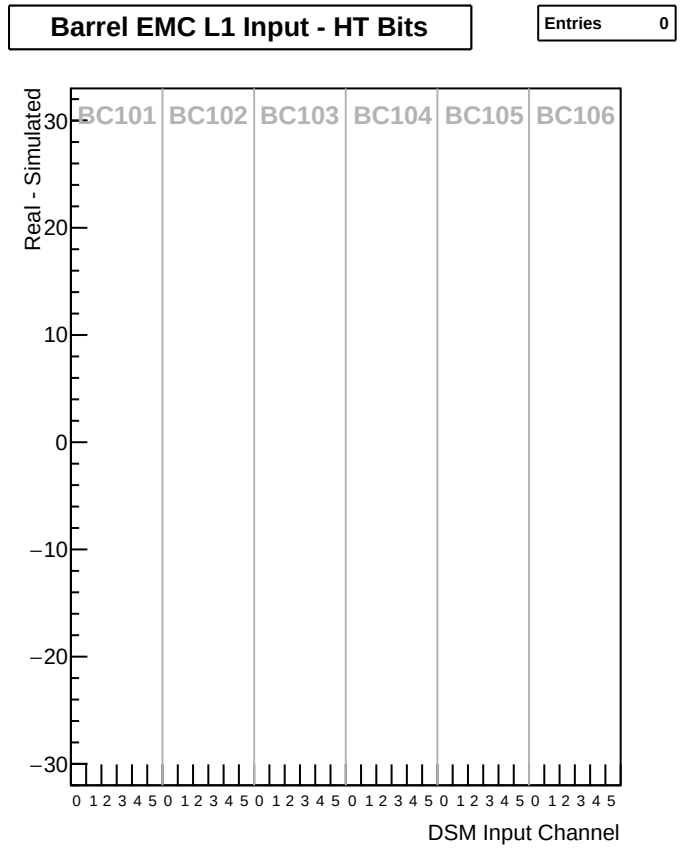

Barrel EMC L0 Input - High Tower
Entries 6.290622e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

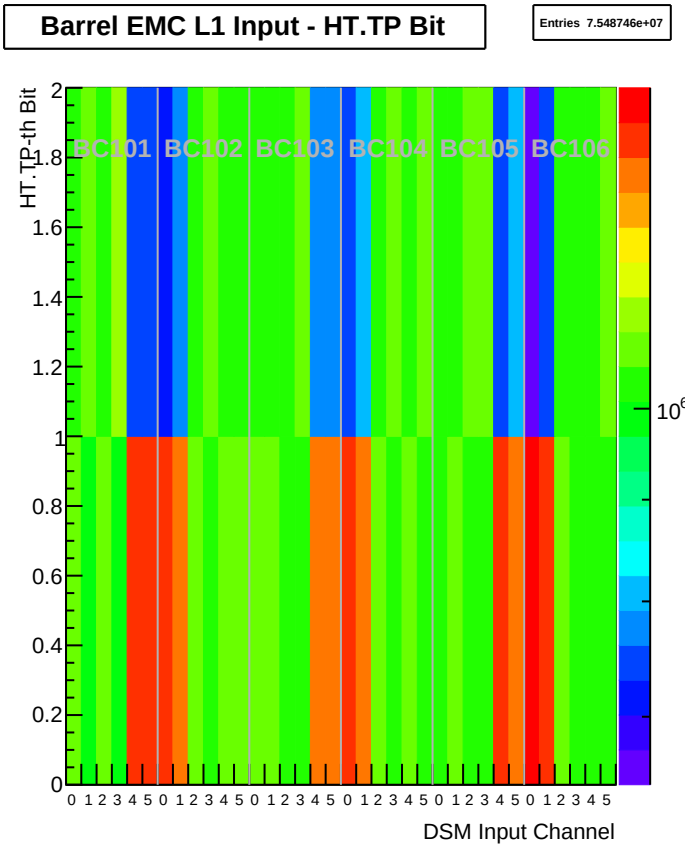

Barrel EMC L0 Input - Patch Sum
Entries 6.290622e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

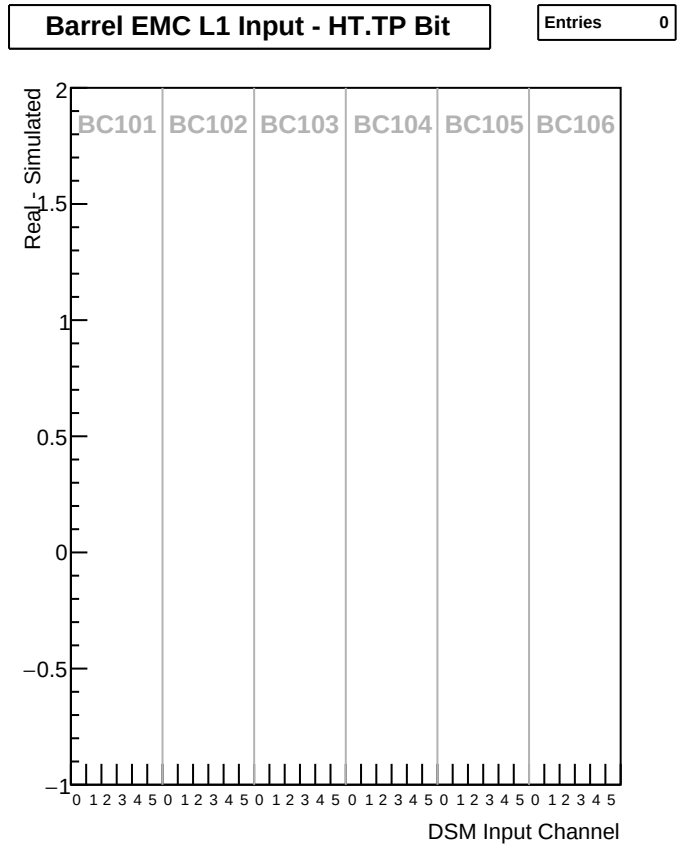

Endcap EMC L0 Input - High Tower
Entries 1.887187e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

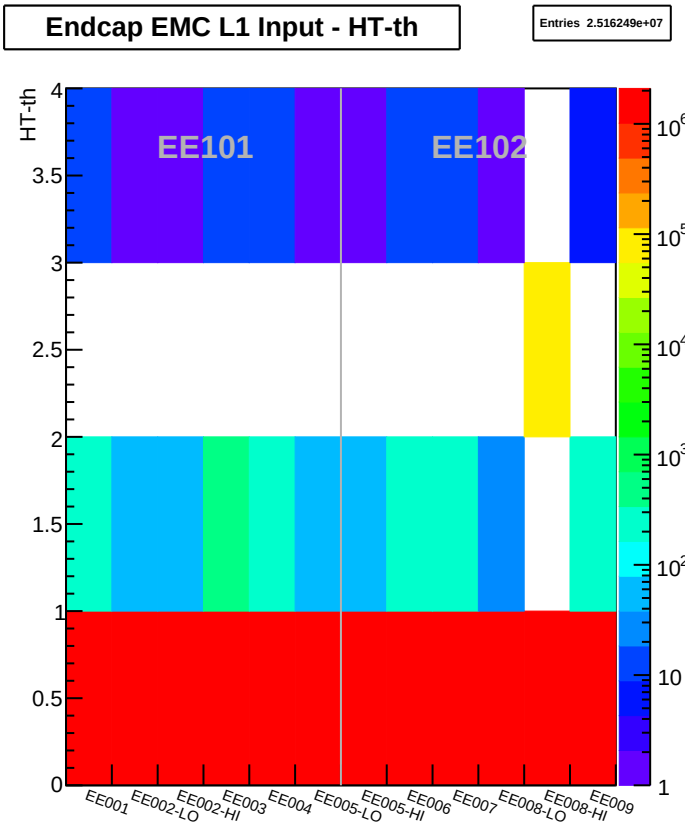

Endcap EMC L0 Input - Patch Sum
Entries 1.887187e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

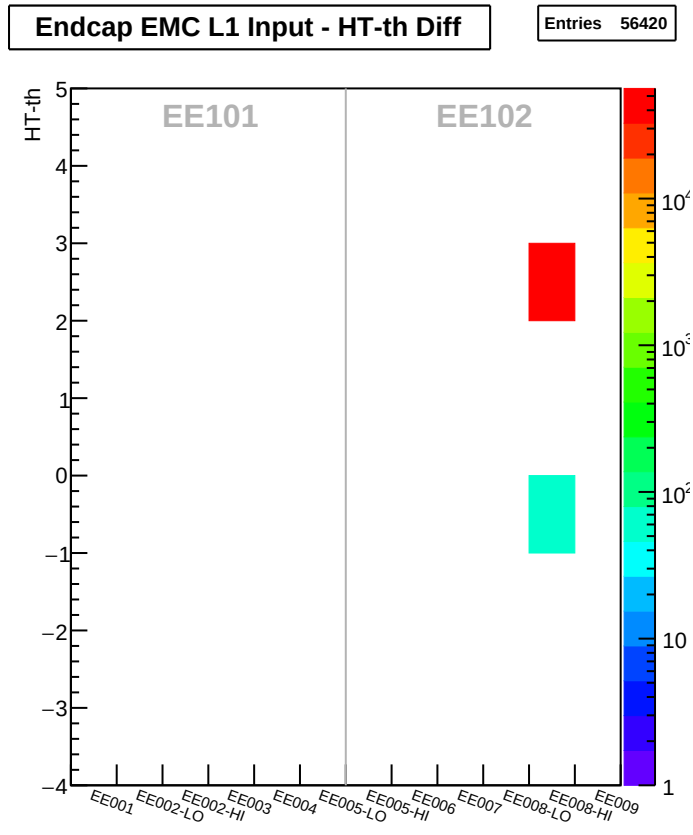

Barrel EMC L1 Input - HT Bits
Entries 7.548746e+07
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

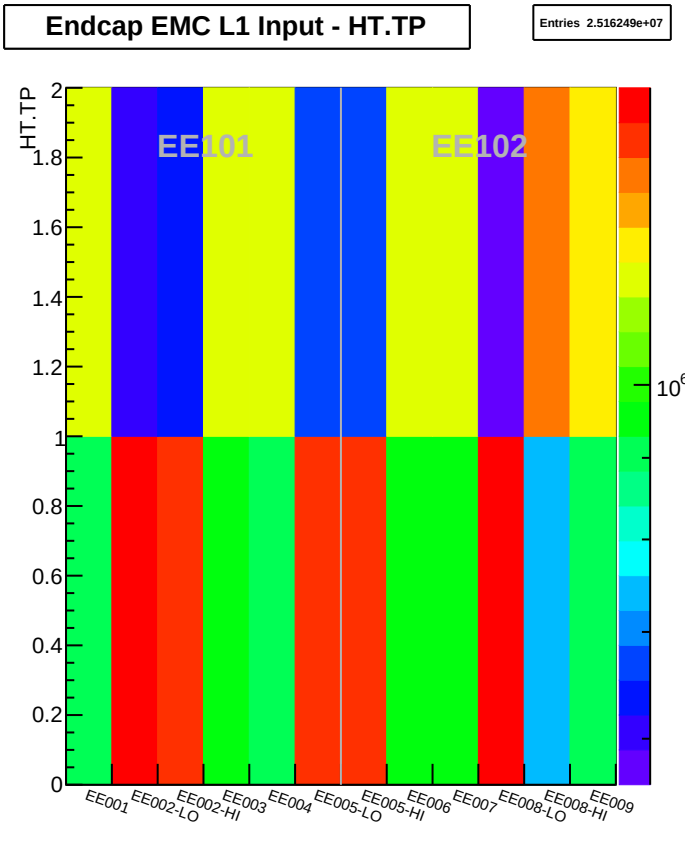

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

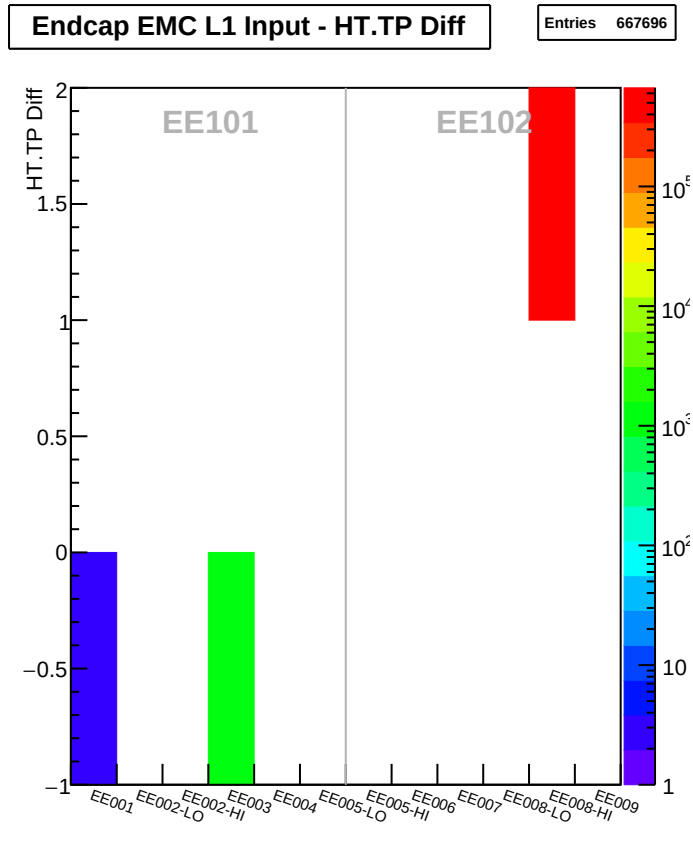

Barrel EMC L1 Input - HT.TP Bit
Entries 7.548746e+07
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

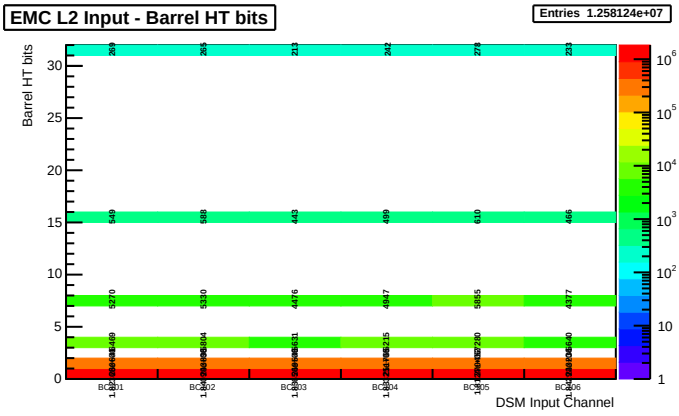

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

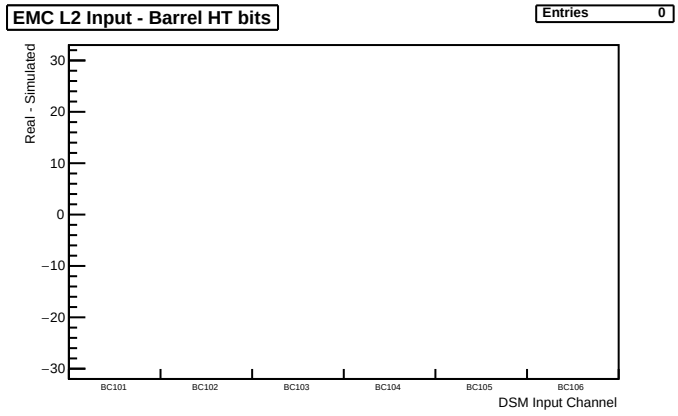

Endcap EMC L1 Input - HT-th
Entries 2.516249e+07
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

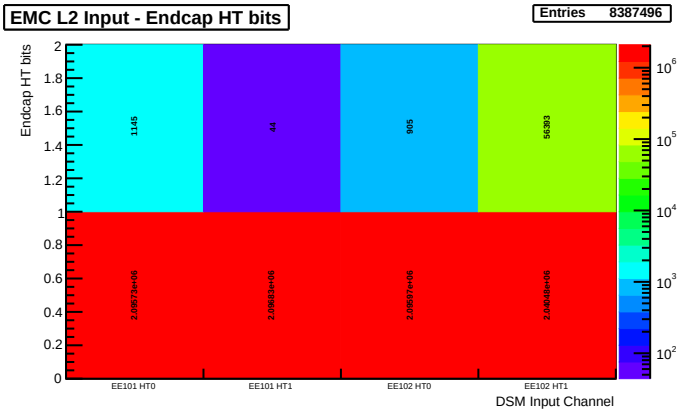

Endcap EMC L1 Input - HT-th Diff
Entries 56420
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

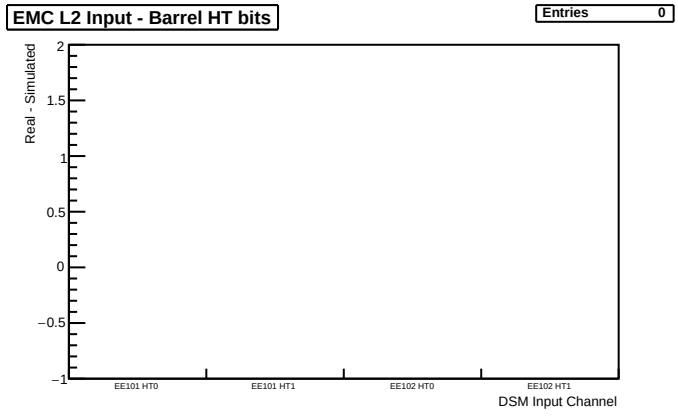

Endcap EMC L1 Input - HT.TP
Entries 2.516249e+07
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

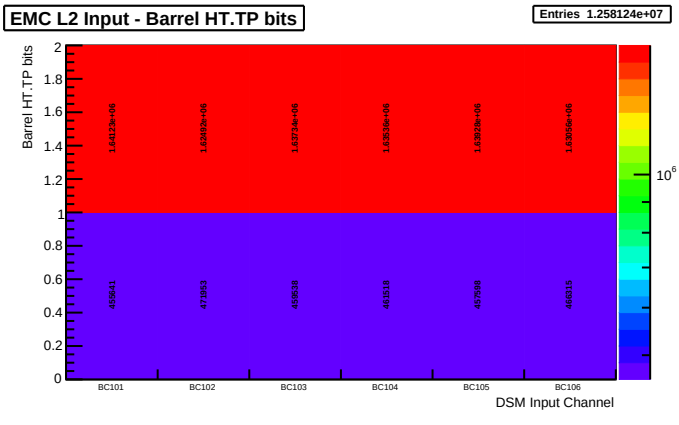

Endcap EMC L1 Input - HT.TP Diff
Entries 667696
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

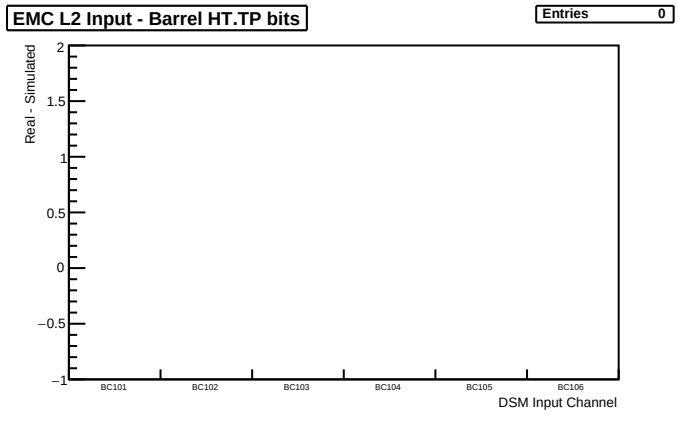

EMC L2 Input - Barrel HT bits
Entries 1.258124e+07
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

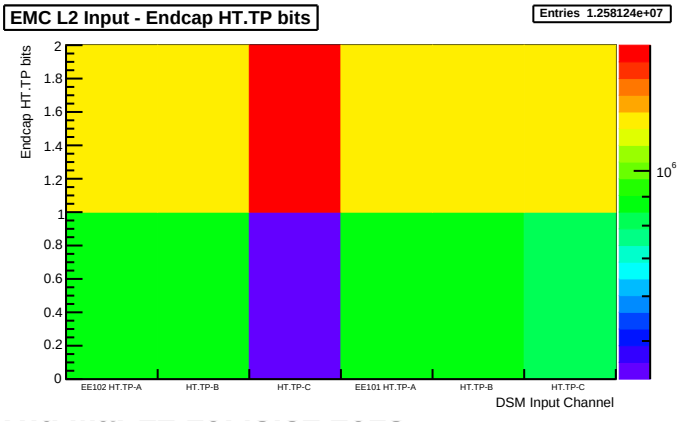

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

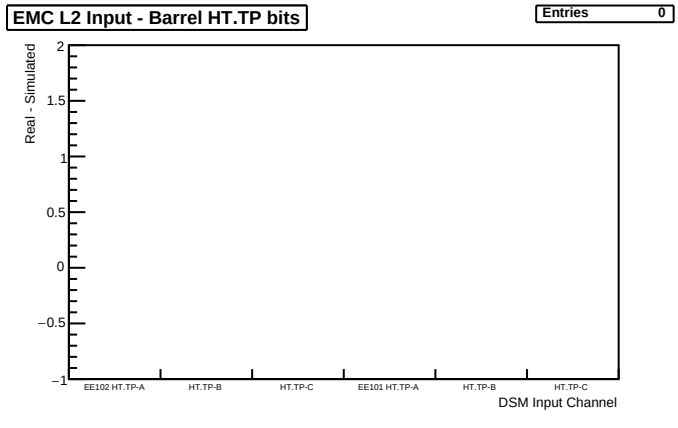

EMC L2 Input - Endcap HT bits
Entries 8387496
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

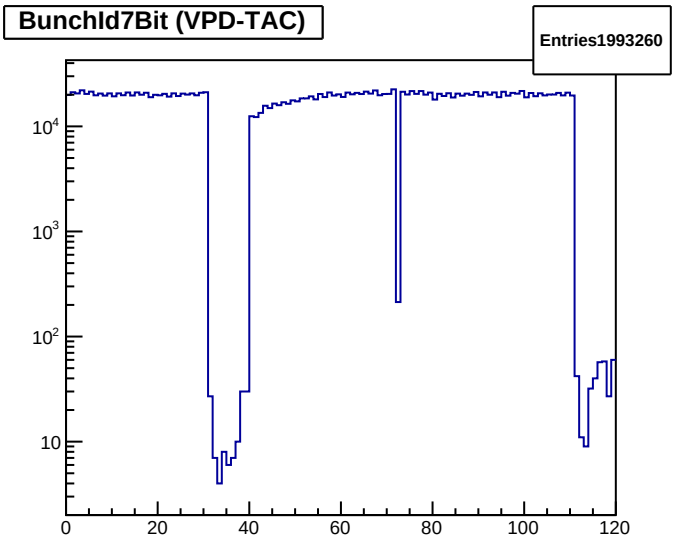

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

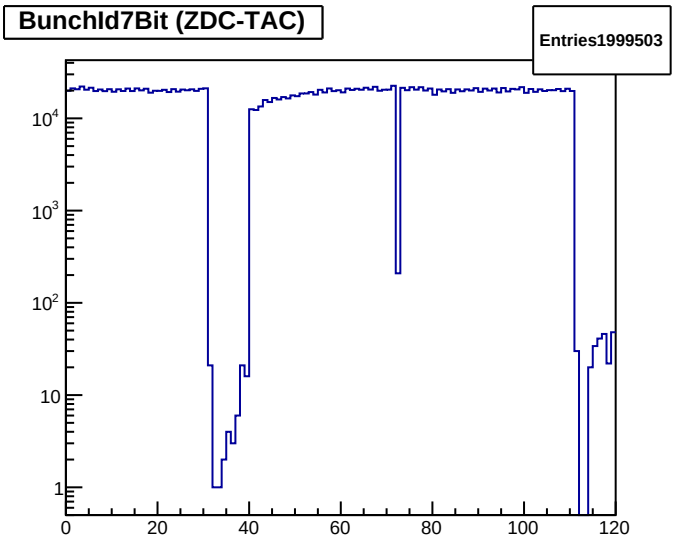

EMC L2 Input - Barrel HT.TP bits
Entries 1.258124e+07
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

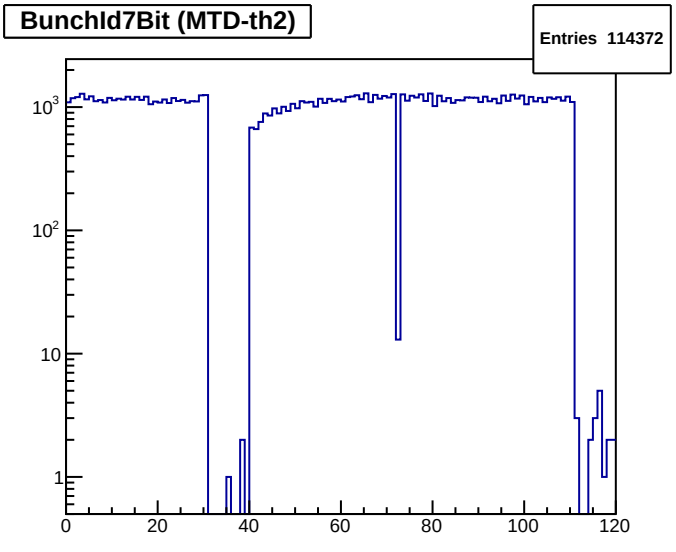

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

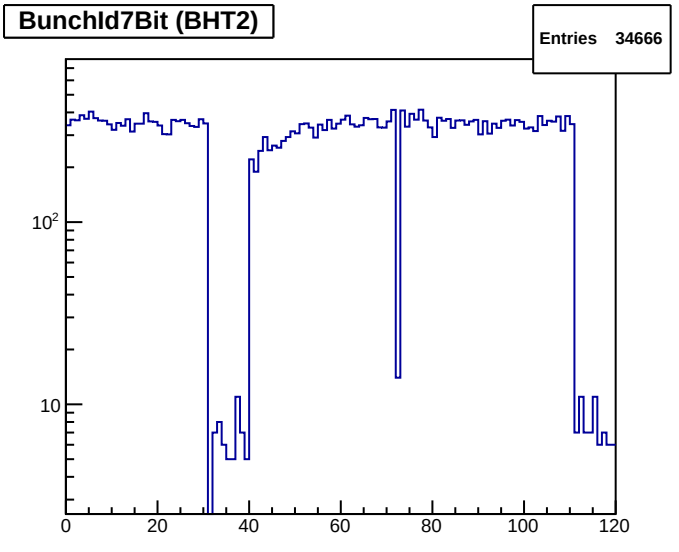

EMC L2 Input - Endcap HT.TP bits
Entries 1.258124e+07
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

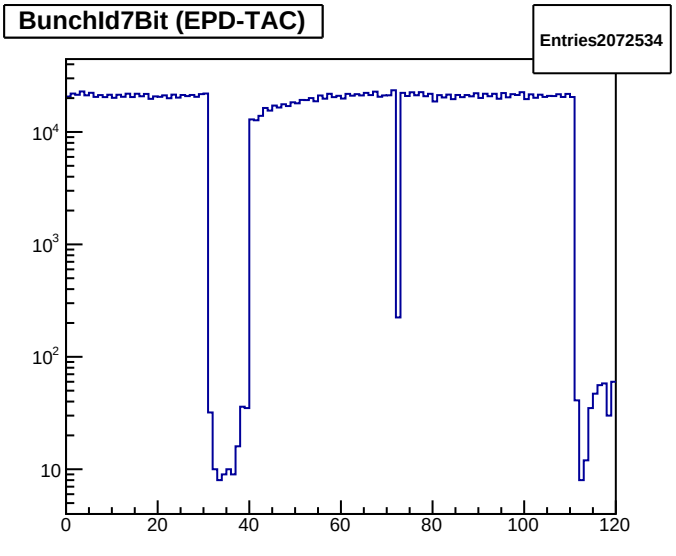

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

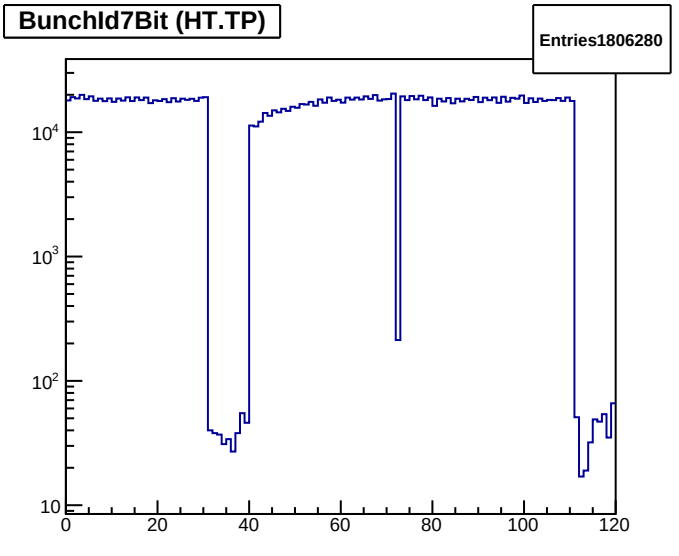

BunchId7Bit (VPD-TAC)
Entries1993260

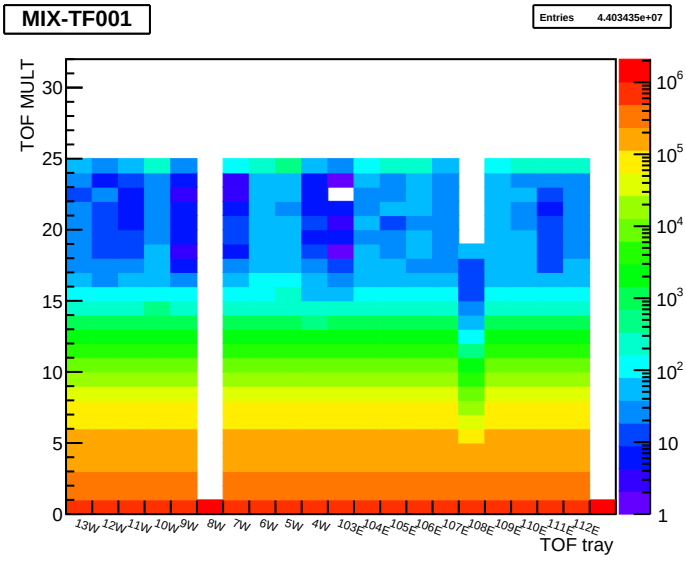

BunchId7Bit (ZDC-TAC)
Entries1999503

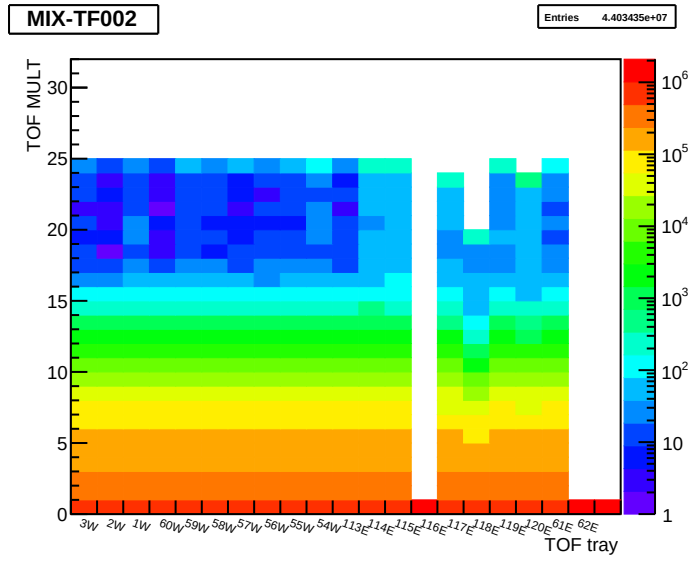

BunchId7Bit (MTD-th2)
Entries 114372

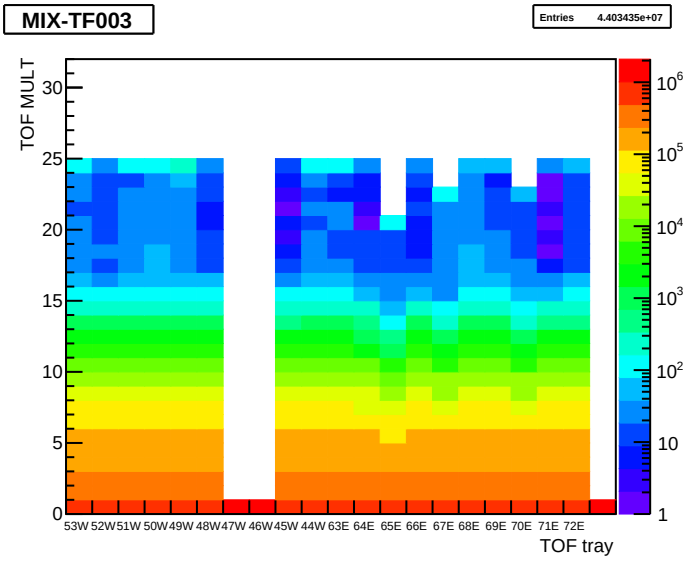

BunchId7Bit (BHT2)
Entries 34666

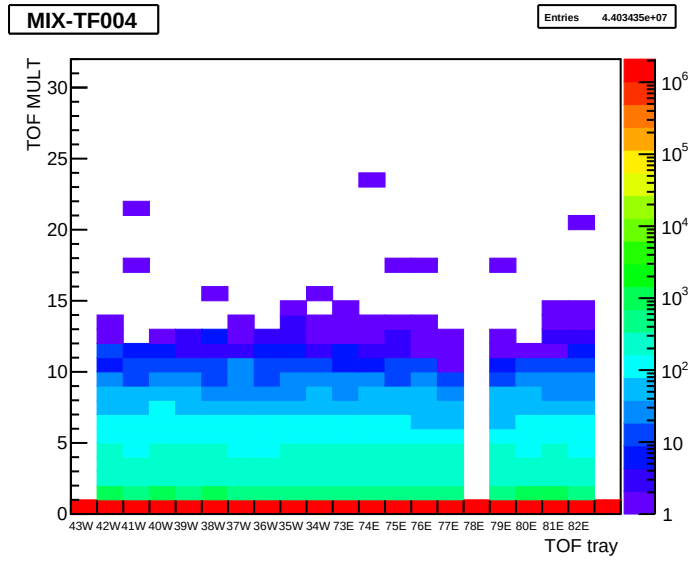

BunchId7Bit (EPD-TAC)
Entries2072534

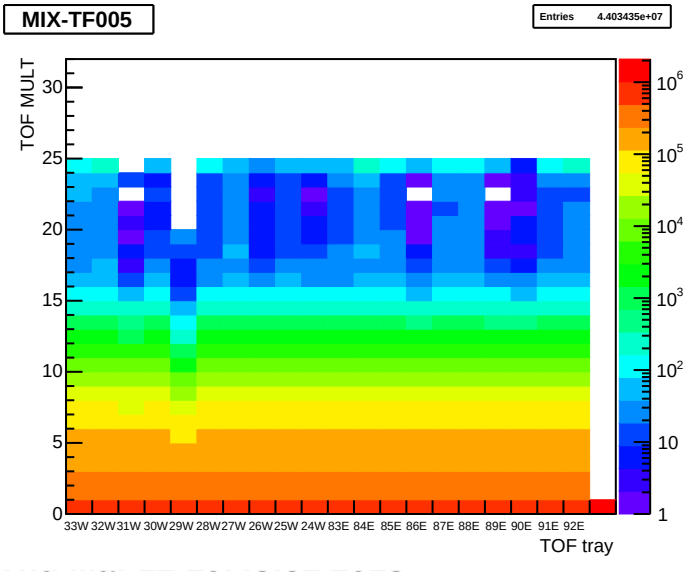

BunchId7Bit (HT.TP)
Entries1806280

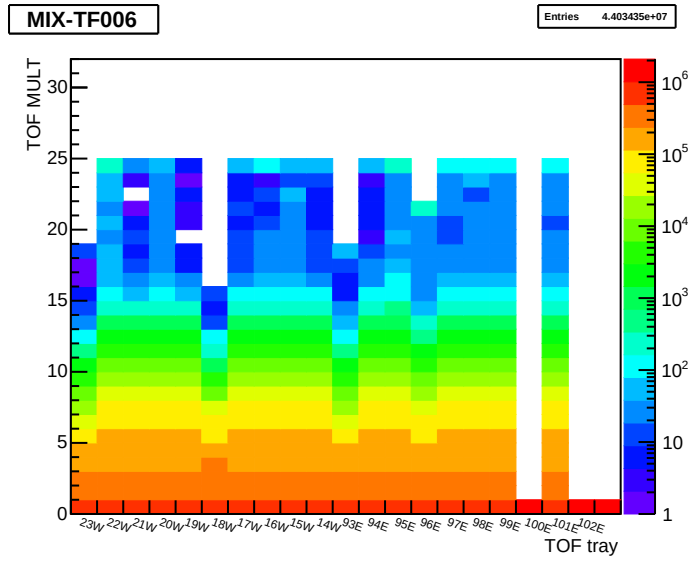

MIX-TF001
Entries 4.403435e+07
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

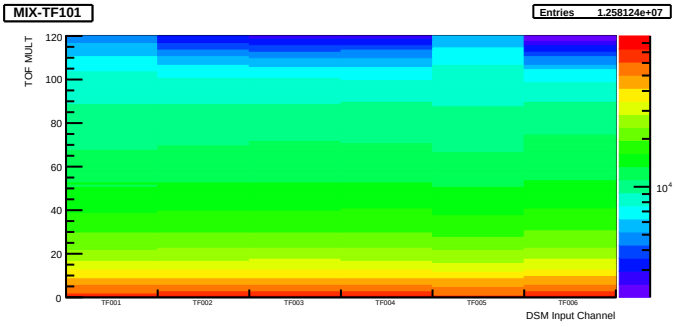

MIX-TF002
Entries 4.403435e+07
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

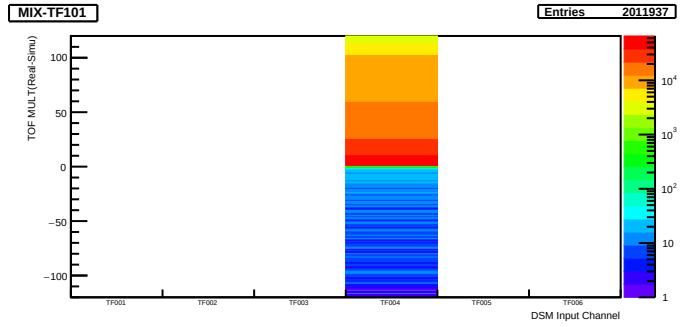

MIX-TF003
Entries 4.403435e+07
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

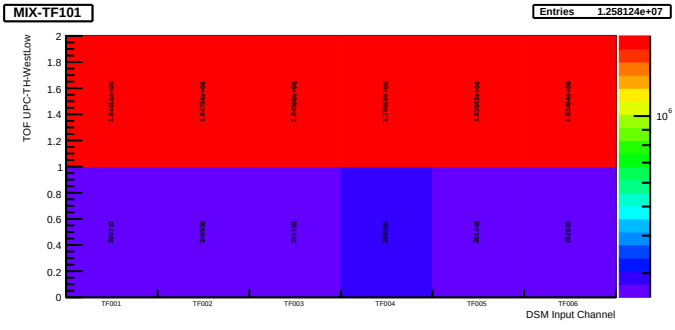

MIX-TF004
Entries 4.403435e+07
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

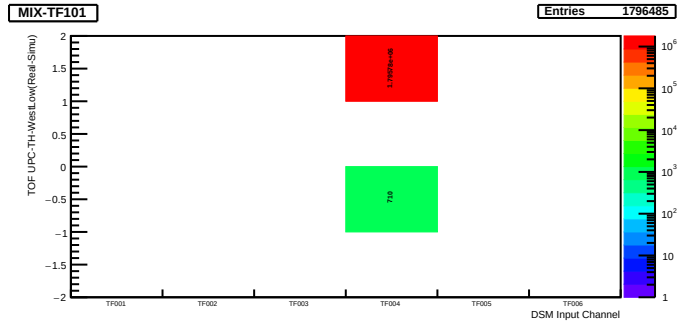

MIX-TF005
Entries 4.403435e+07
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

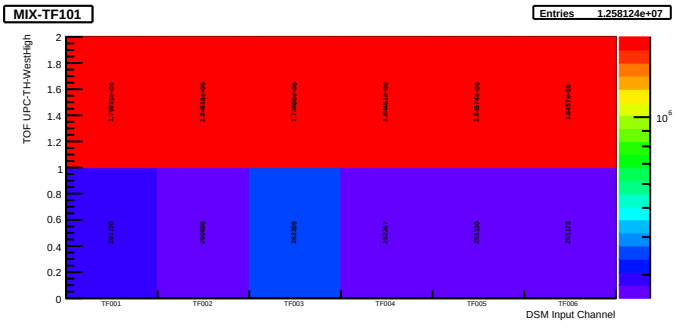

MIX-TF006
Entries 4.403435e+07
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

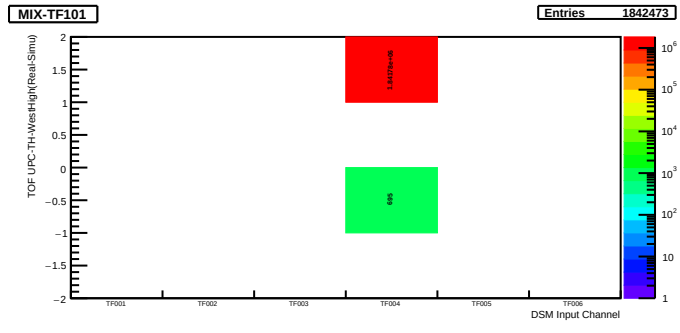

MIX-TF101
Entries 1.258124e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

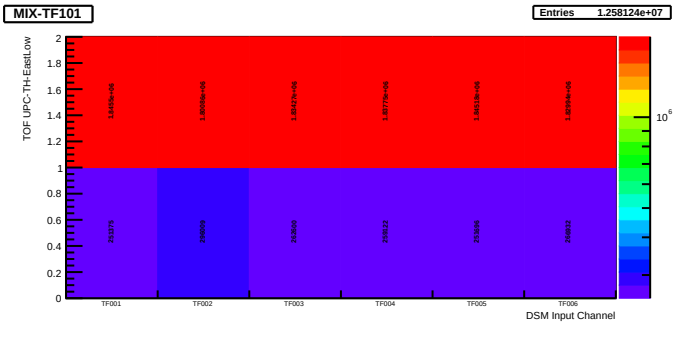

MIX-TF101
Entries 2011937
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

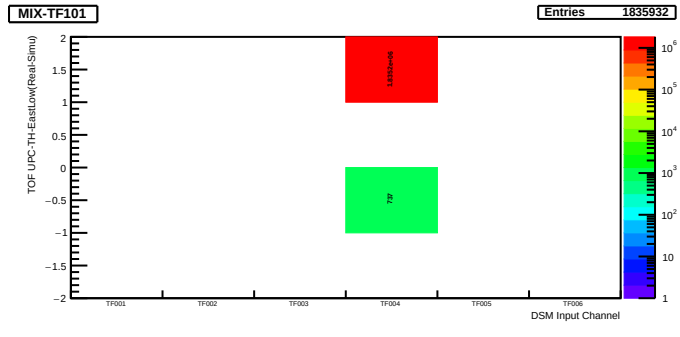

MIX-TF101
Entries 1.258124e+07
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

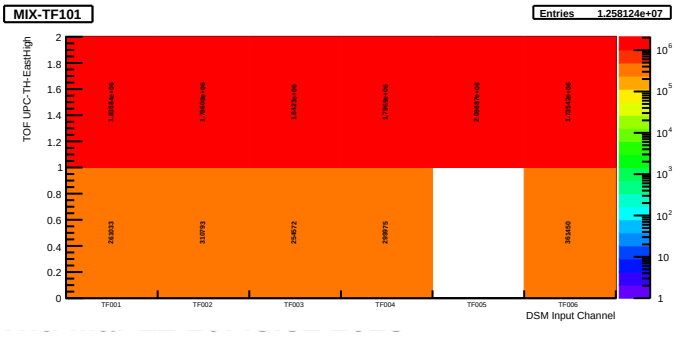

MIX-TF101
Entries 1796485
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

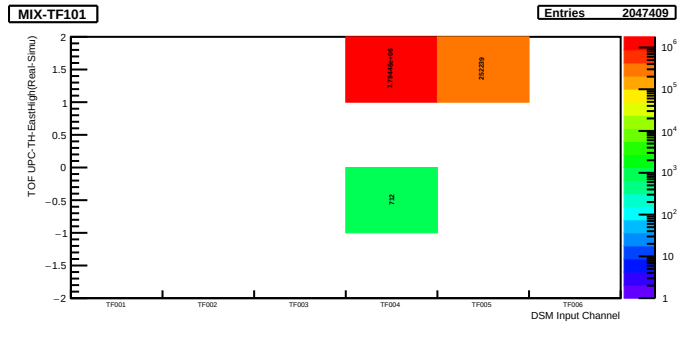

MIX-TF101
Entries 1.258124e+07
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

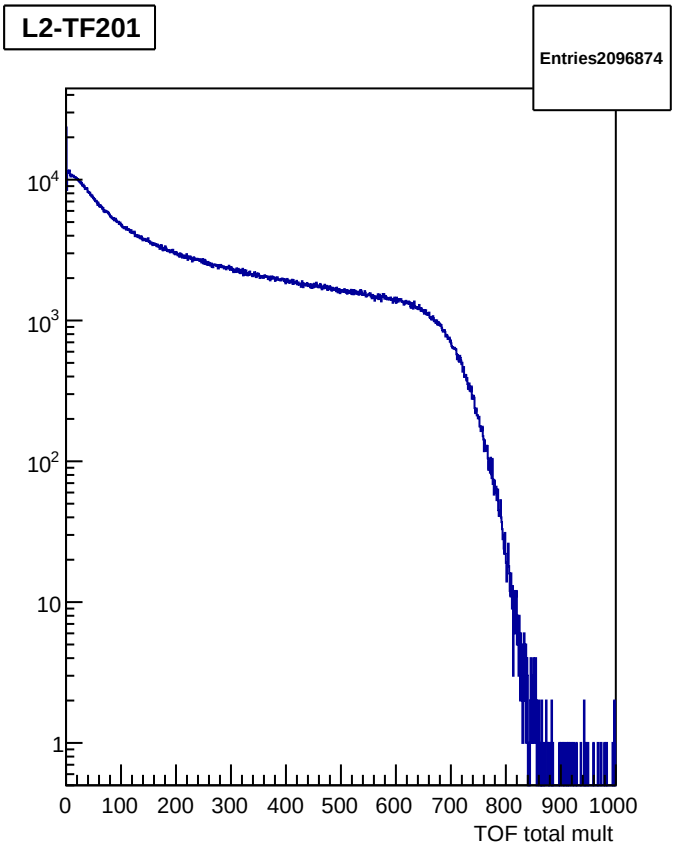

MIX-TF101
Entries 1842473
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

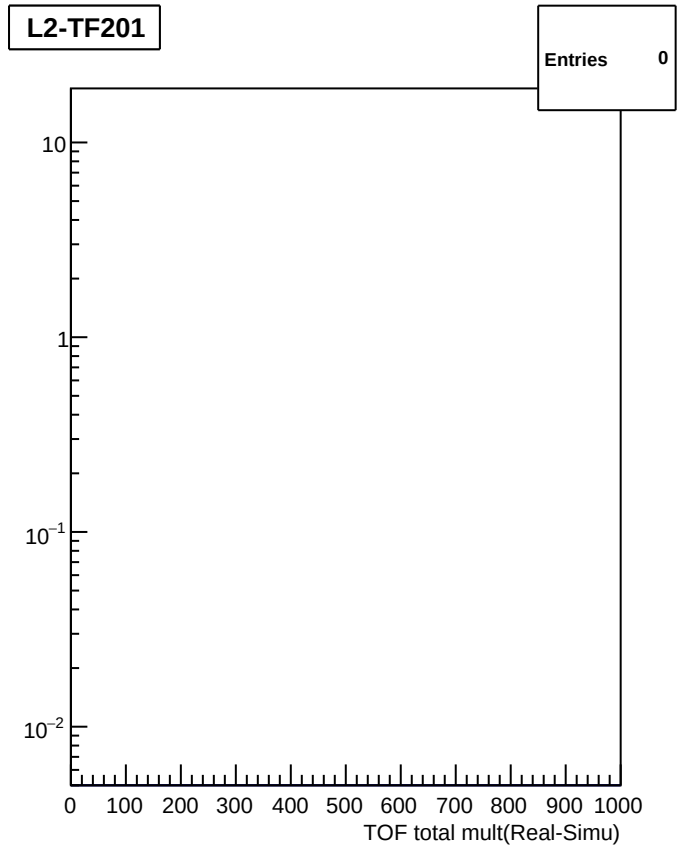

MIX-TF101
Entries 1.258124e+07
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

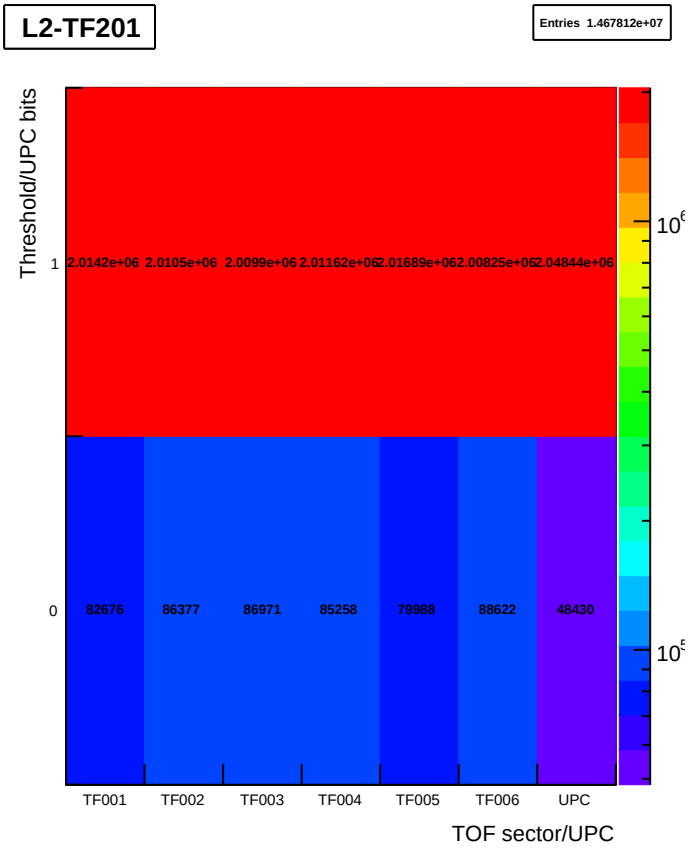

MIX-TF101
Entries 1835932
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

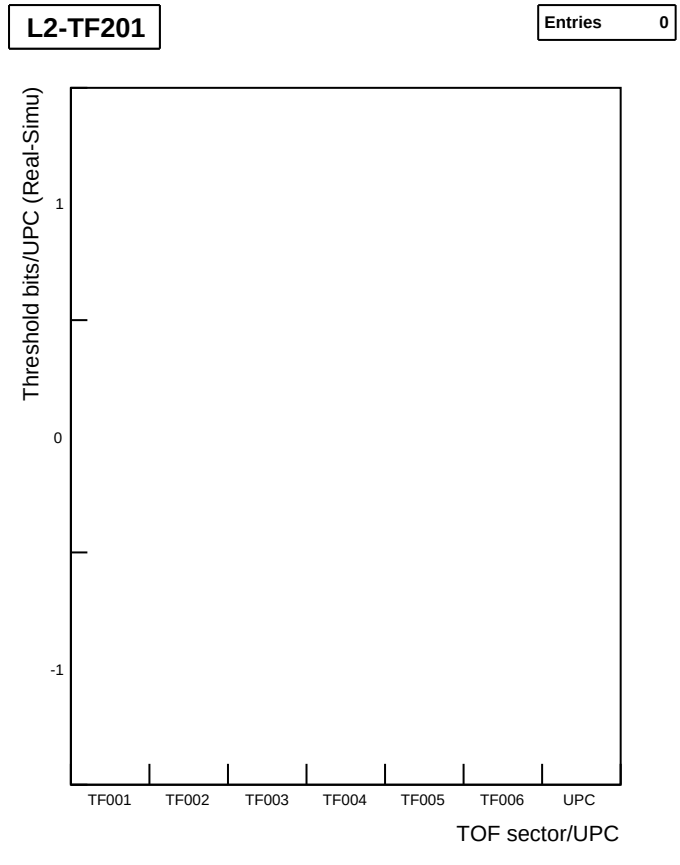

MIX-TF101
Entries 1.258124e+07
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

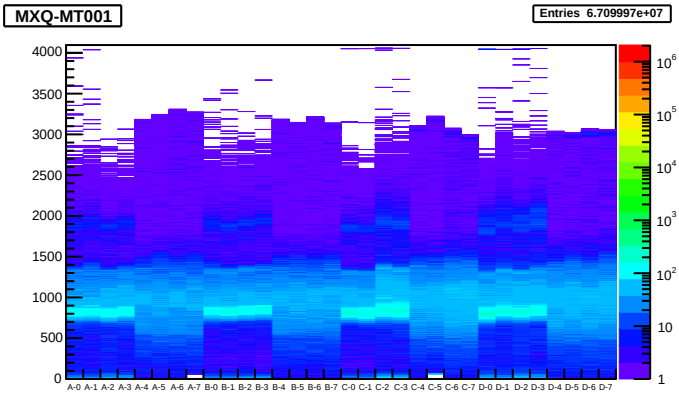

MIX-TF101
Entries 2047409
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

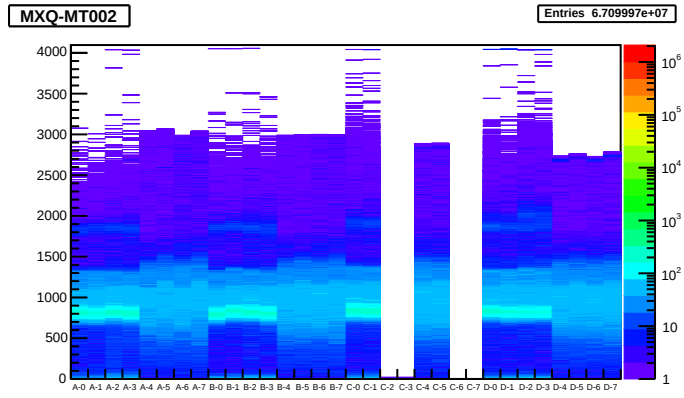

L2-TF201
Entries2096874
TOF total mult

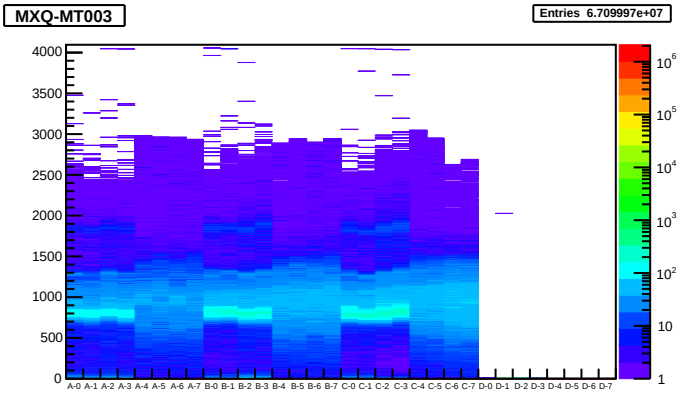

L2-TF201
Entries 0
TOF total mult(Real-Simu)

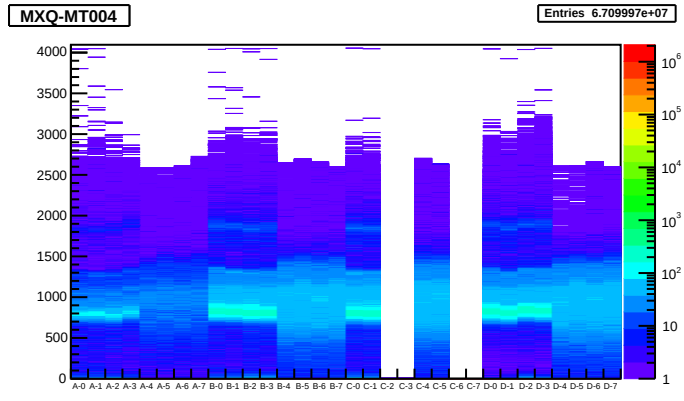

L2-TF201
Entries 1.467812e+07
Threshold/UPC bits
2.0142e+06 2.0105e+06 2.0099e+06 2.01162e+06 2.01689e+06 2.00825e+06 2.04844e+06
82676 86377 86971 85258 79988 88622 48430
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

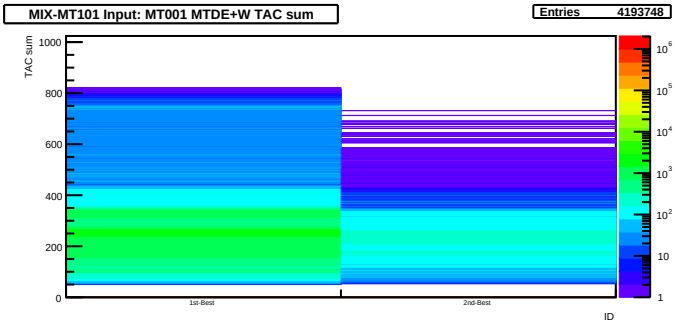

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

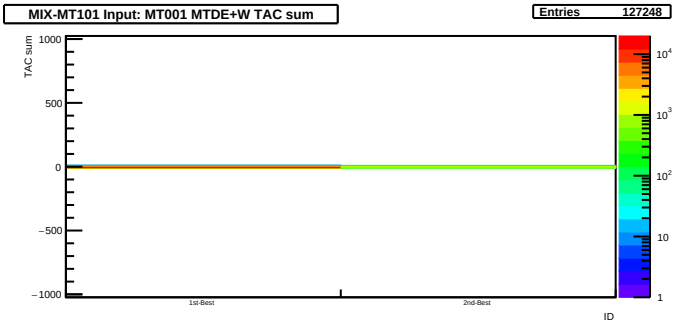

MXQ-MT001
Entries 6.709997e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1

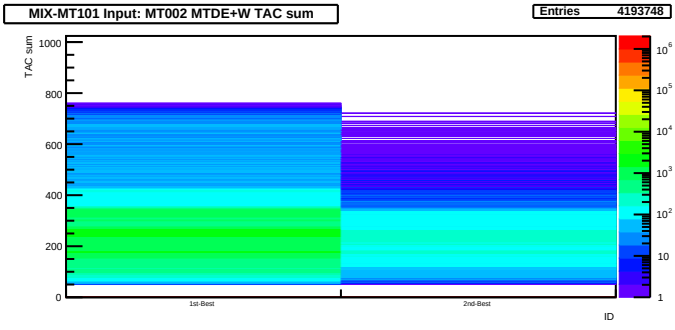

MXQ-MT002
Entries 6.709997e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1

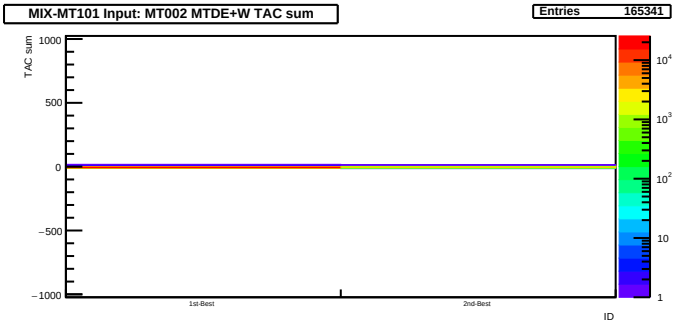

MXQ-MT003
Entries 6.709997e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1

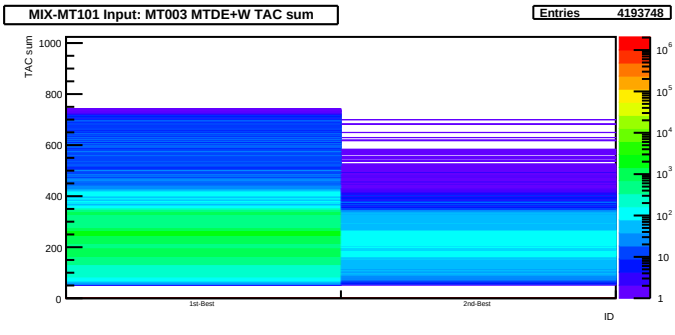

MXQ-MT004
Entries 6.709997e+07
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1

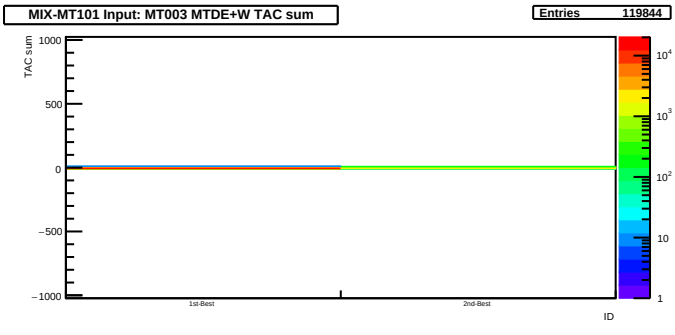

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 4193748
TAC sum
1st-Best
2nd-Best
ID

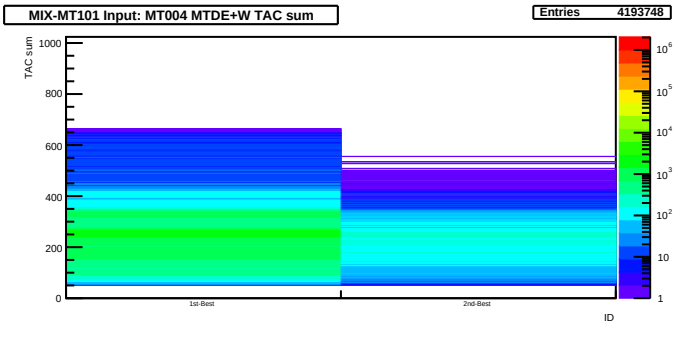

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 127248
TAC sum
1st-Best
2nd-Best
ID

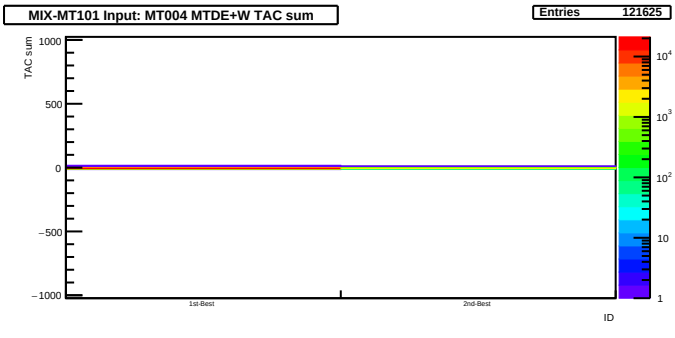

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 4193748
TAC sum
1st-Best
2nd-Best
ID

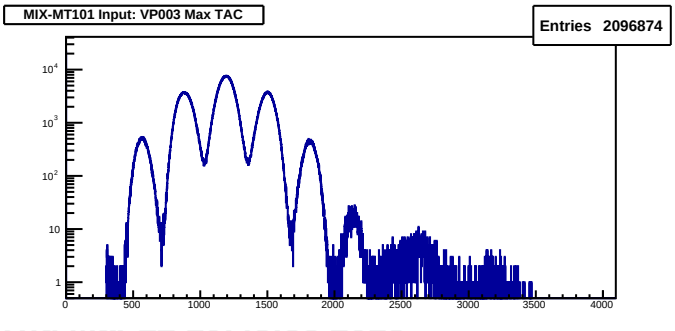

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 165341
TAC sum
1st-Best
2nd-Best
ID

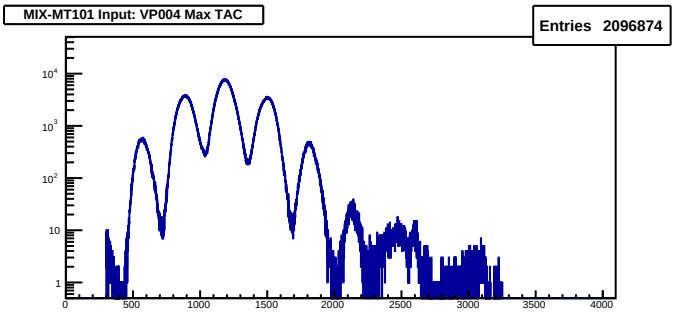

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 4193748
TAC sum
1st-Best
2nd-Best
ID

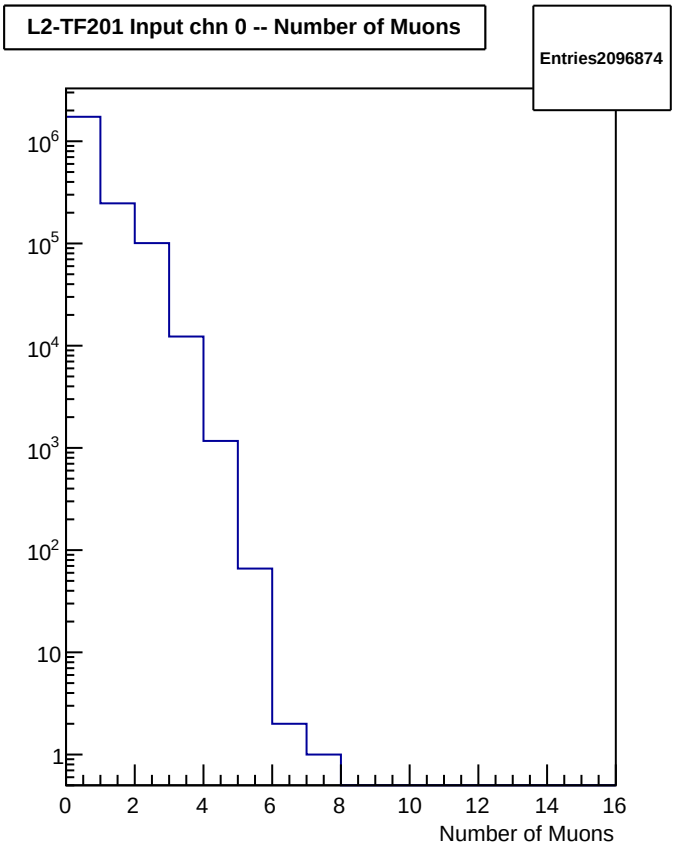

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 119844
TAC sum
1st-Best
2nd-Best
ID

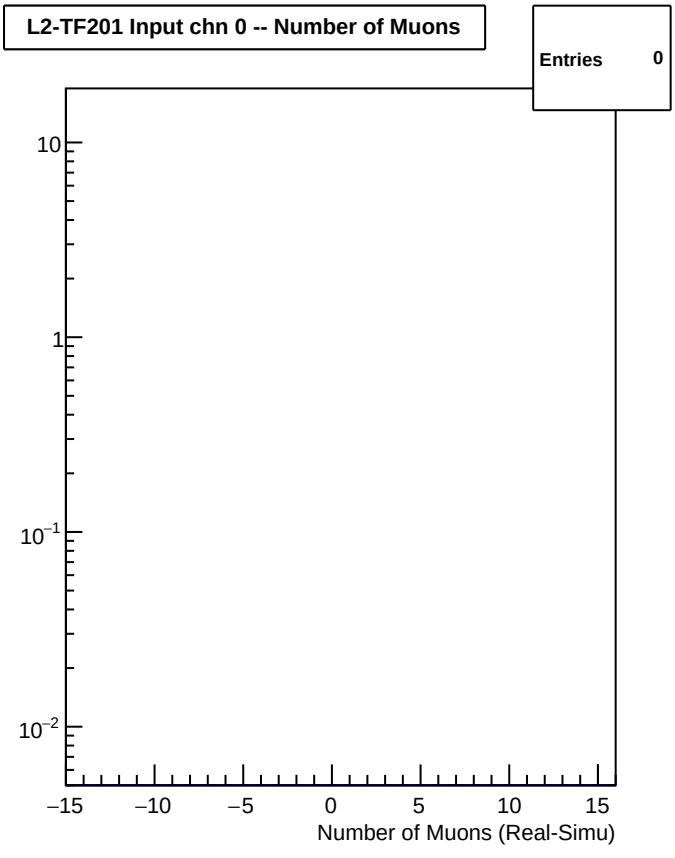

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 4193748
TAC sum
1st-Best
2nd-Best
ID

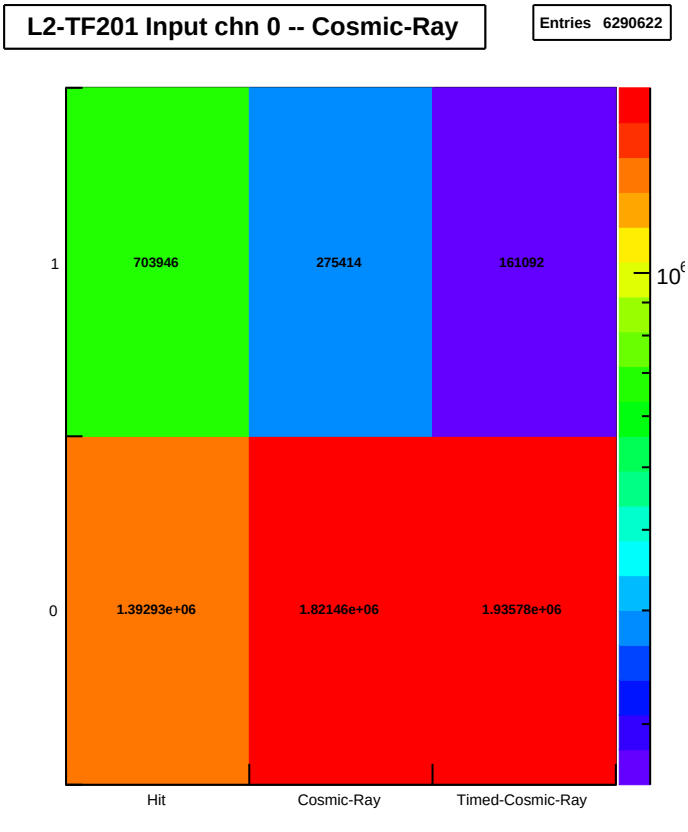

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 121625
TAC sum
1st-Best
2nd-Best
ID

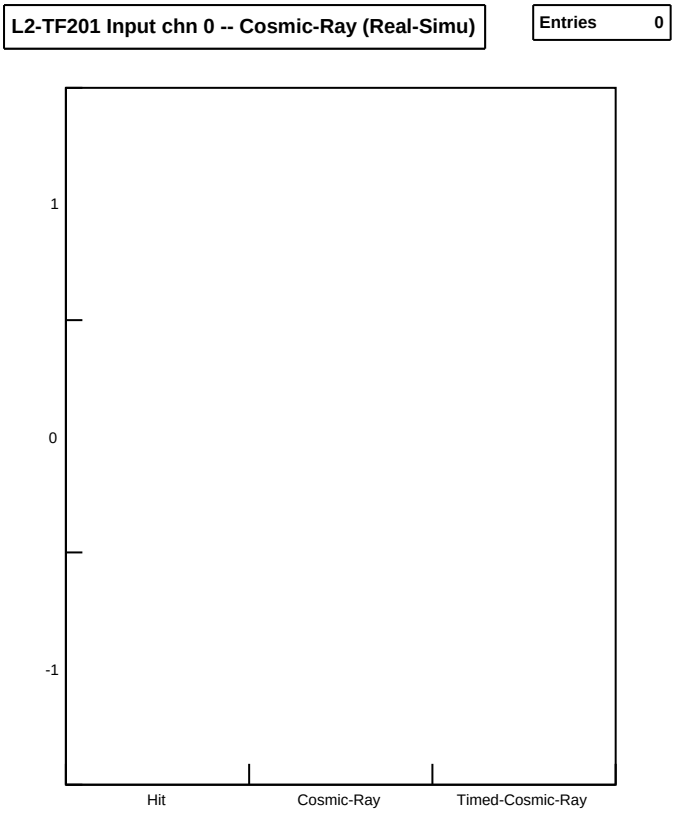

MIX-MT101 Input: VP003 Max TAC
Entries 2096874

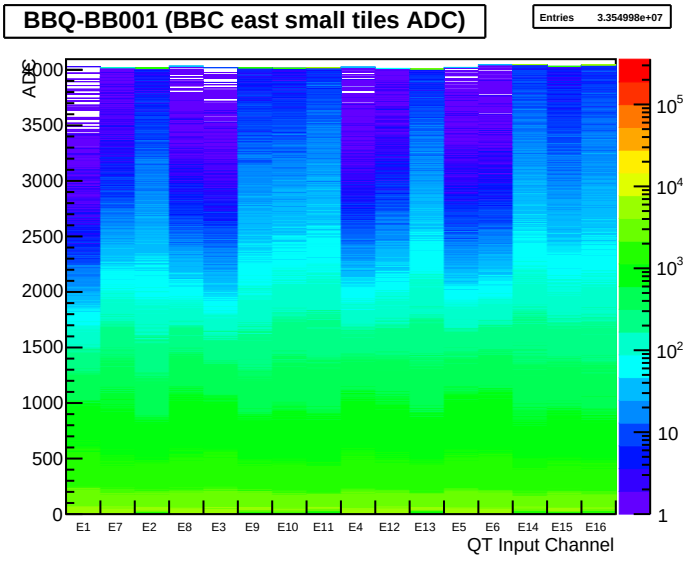

MIX-MT101 Input: VP004 Max TAC
Entries 2096874

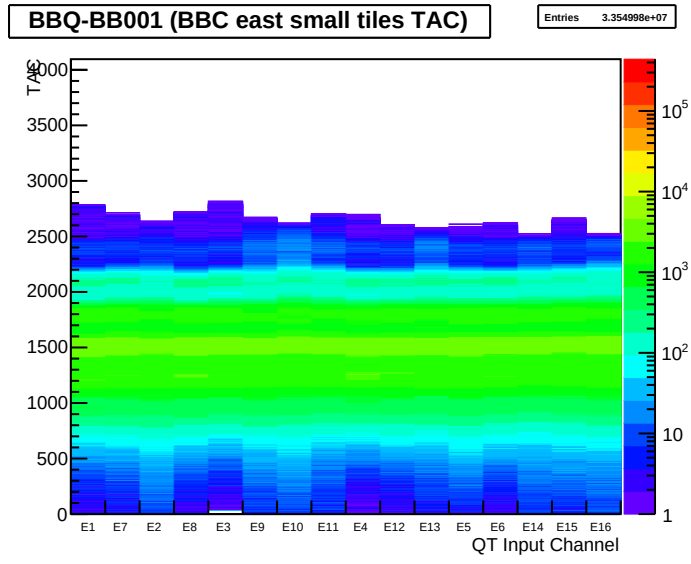

L2-TF201 Input chn 0 -- Number of Muons
Entries2096874
Number of Muons

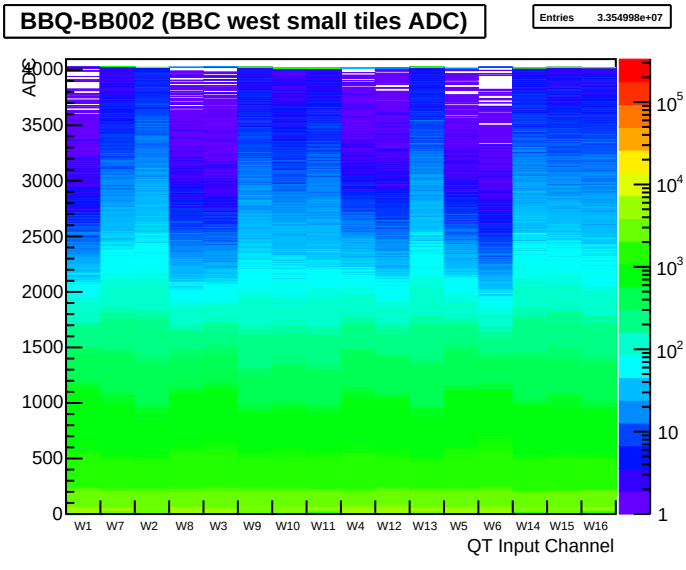

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

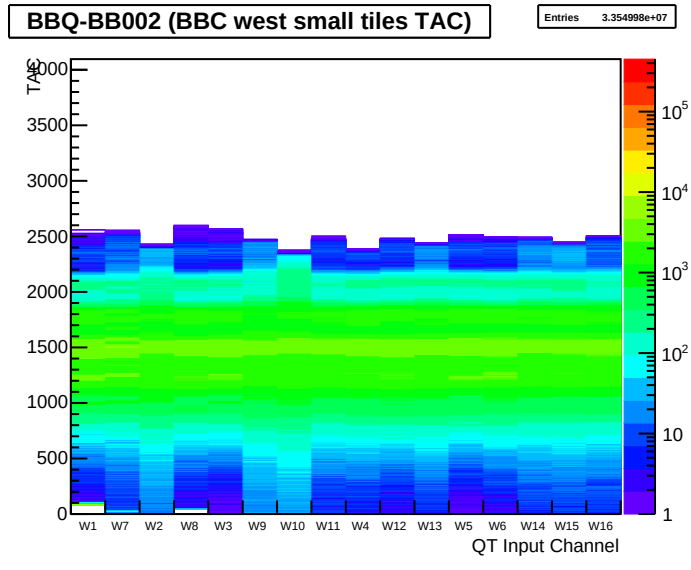

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 6290622
703946
275414
161092
1.39293e+06
1.82146e+06
1.93578e+06
Hit
Cosmic-Ray
Timed-Cosmic-Ray

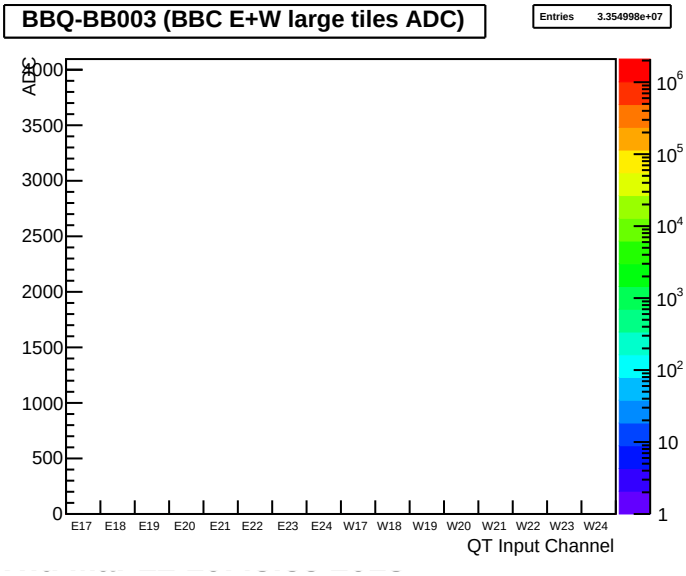

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

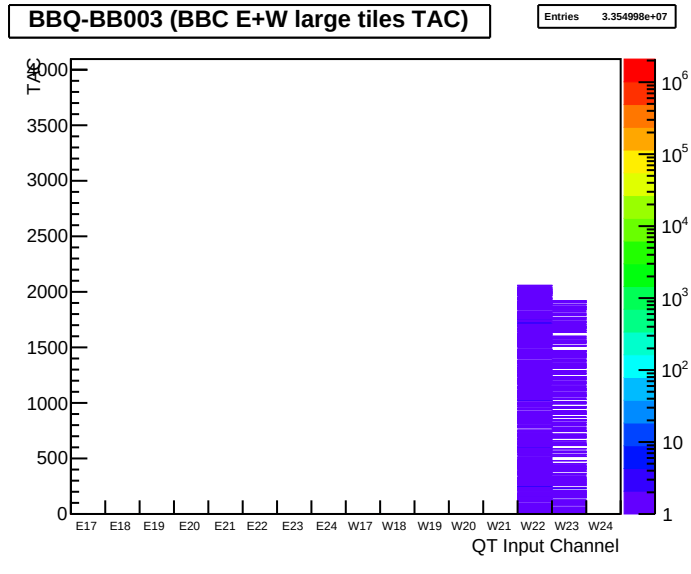

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.354998e+07
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

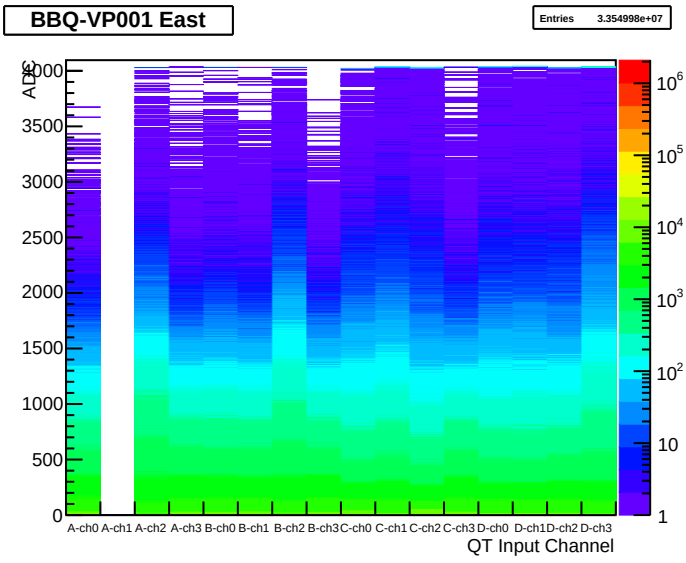

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.354998e+07
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

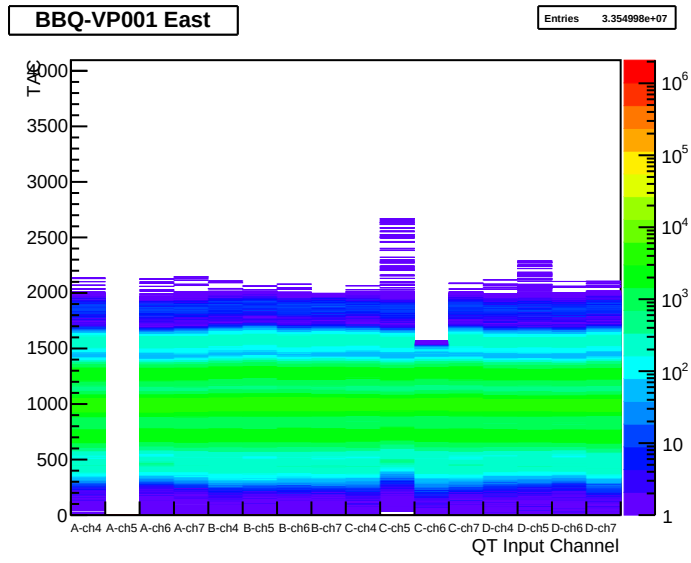

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.354998e+07
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

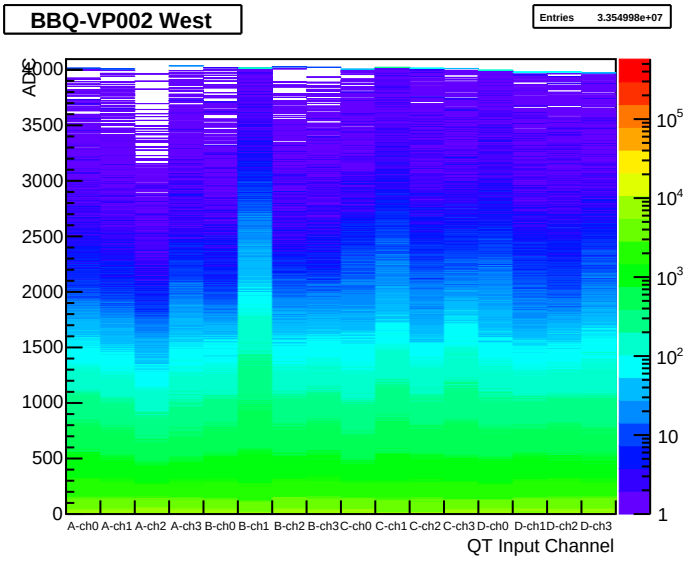

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.354998e+07
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

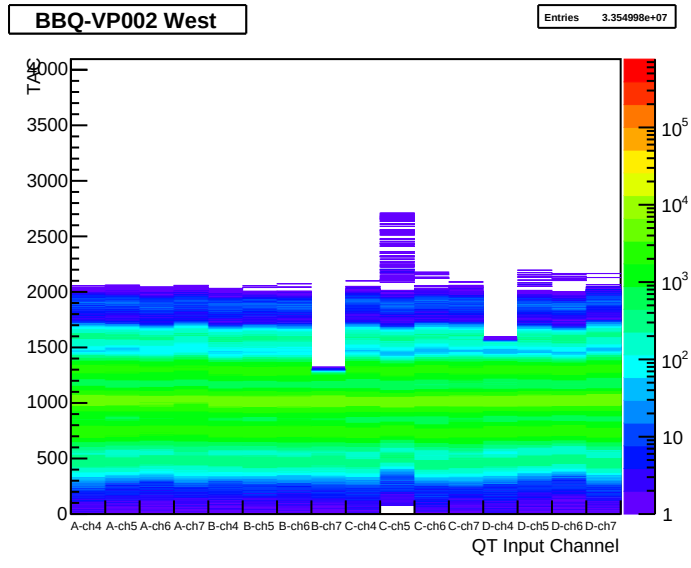

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.354998e+07
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

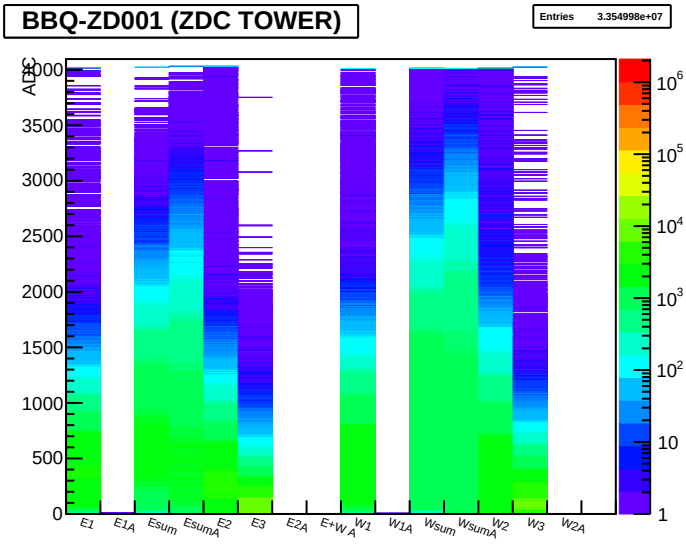

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.354998e+07
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

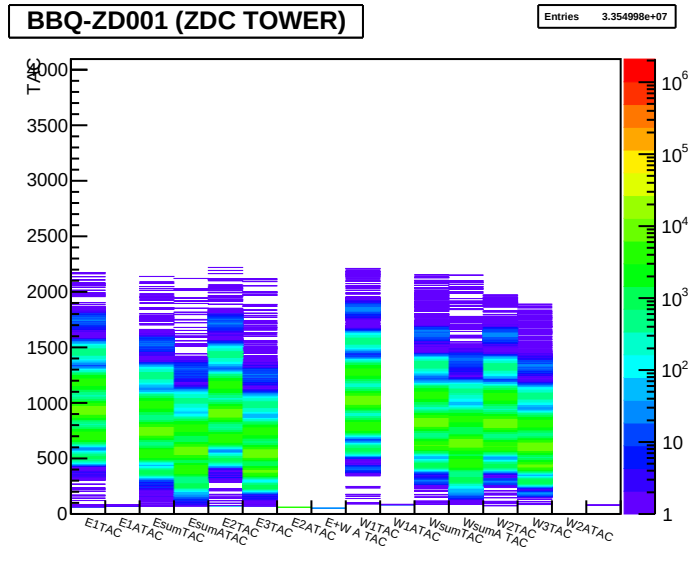

BBQ-VP001 East
Entries 3.354998e+07
ADC
A-ch0 A-ch1 A-ch2 A-ch3 B-ch0 B-ch1 B-ch2 B-ch3 C-ch0 C-ch1 C-ch2 C-ch3 D-ch0 D-ch1 D-ch2 D-ch3
QT Input Channel

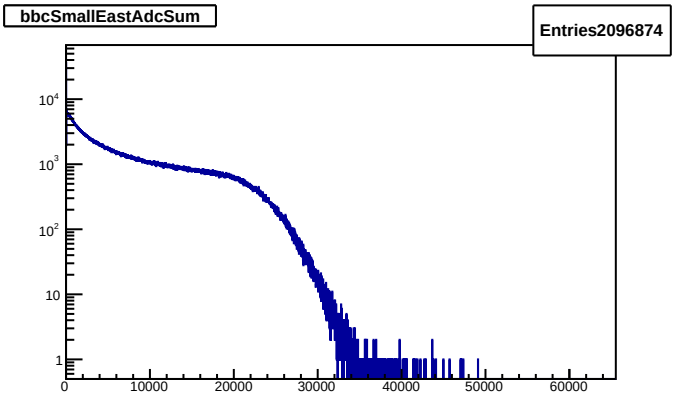

BBQ-VP001 East
Entries 3.354998e+07
TAC
A-ch4 A-ch5 A-ch6 A-ch7 B-ch4 B-ch5 B-ch6 B-ch7 C-ch4 C-ch5 C-ch6 C-ch7 D-ch4 D-ch5 D-ch6 D-ch7
QT Input Channel

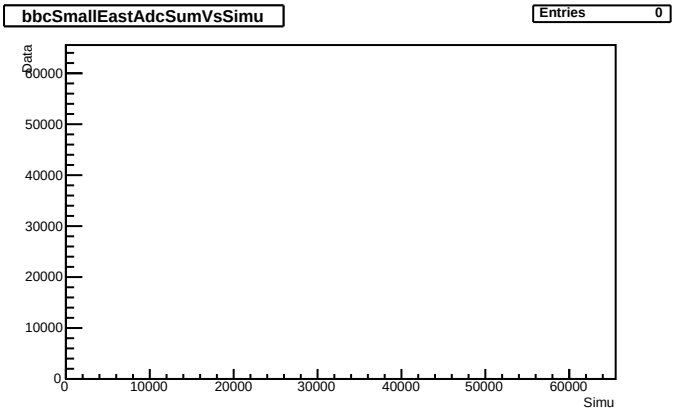

BBQ-VP002 West
Entries 3.354998e+07
ADC
A-ch0 A-ch1 A-ch2 A-ch3 B-ch0 B-ch1 B-ch2 B-ch3 C-ch0 C-ch1 C-ch2 C-ch3 D-ch0 D-ch1 D-ch2 D-ch3
QT Input Channel

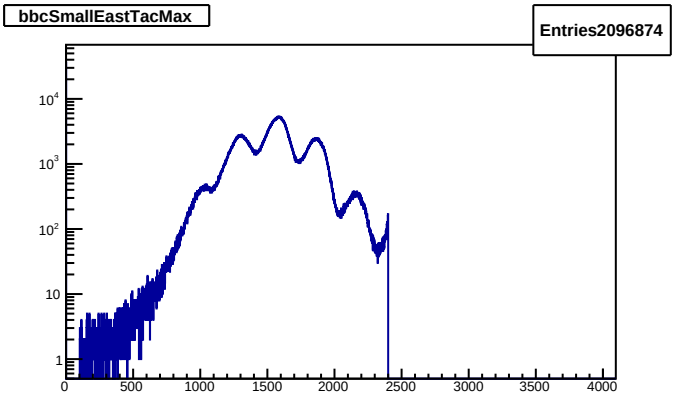

BBQ-VP002 West
Entries 3.354998e+07
TAC
A-ch4 A-ch5 A-ch6 A-ch7 B-ch4 B-ch5 B-ch6 B-ch7 C-ch4 C-ch5 C-ch6 C-ch7 D-ch4 D-ch5 D-ch6 D-ch7
QT Input Channel

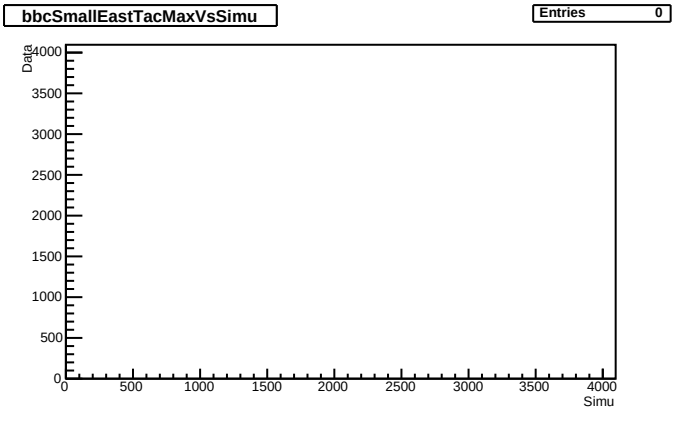

BBQ-ZD001 (ZDC TOWER)
Entries 3.354998e+07
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

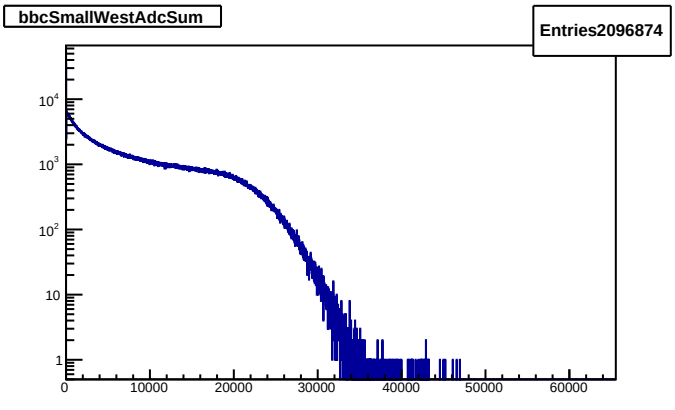

BBQ-ZD001 (ZDC TOWER)
Entries 3.354998e+07
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

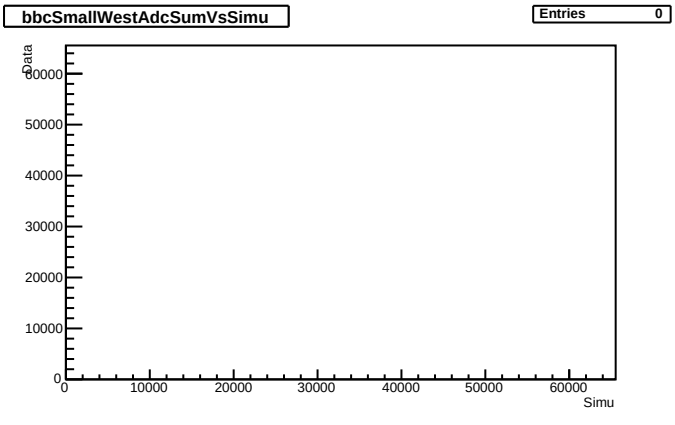

bbcSmallEastAdcSum
Entries2096874

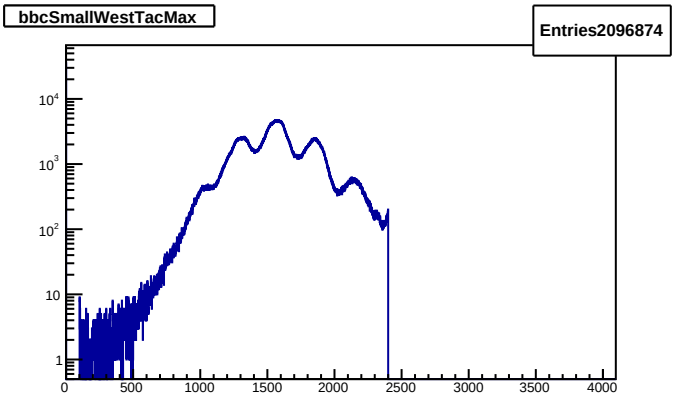

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

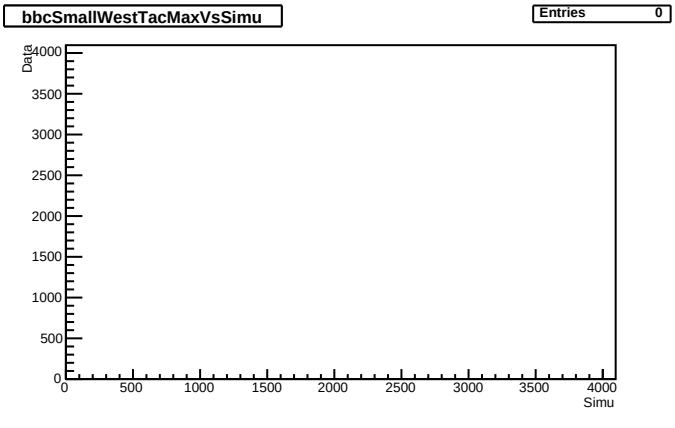

bbcSmallEastTacMax
Entries2096874

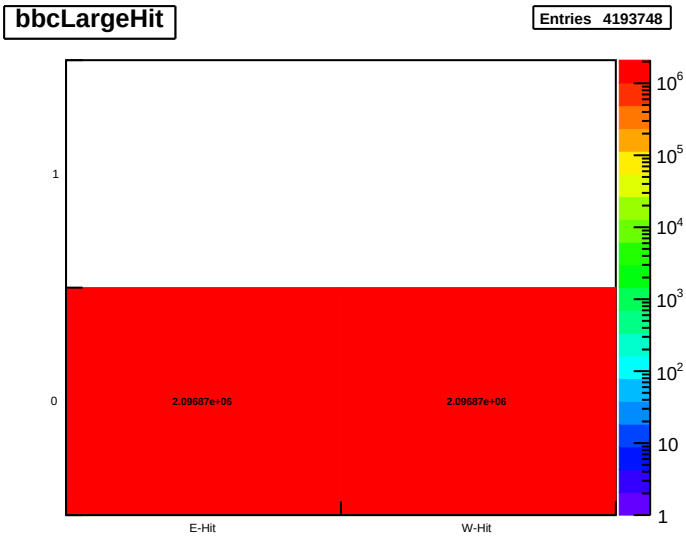

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

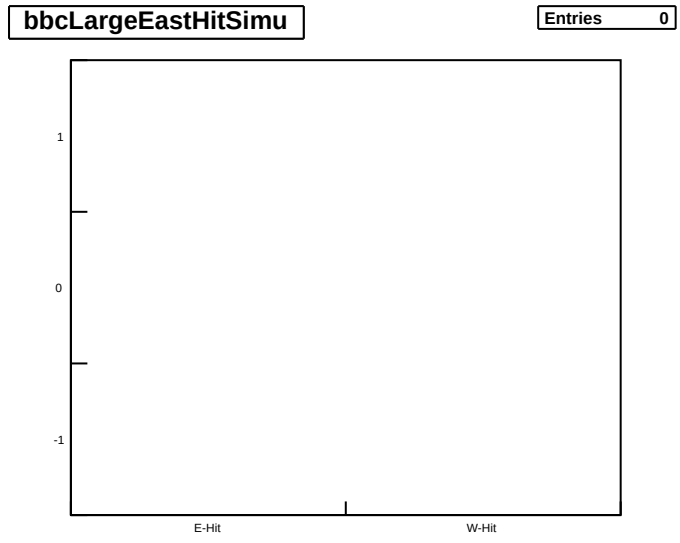

bbcSmallWestAdcSum
Entries2096874

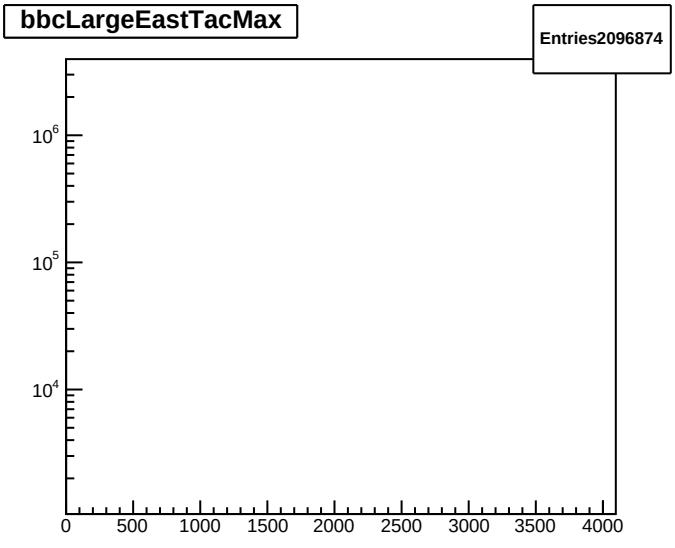

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

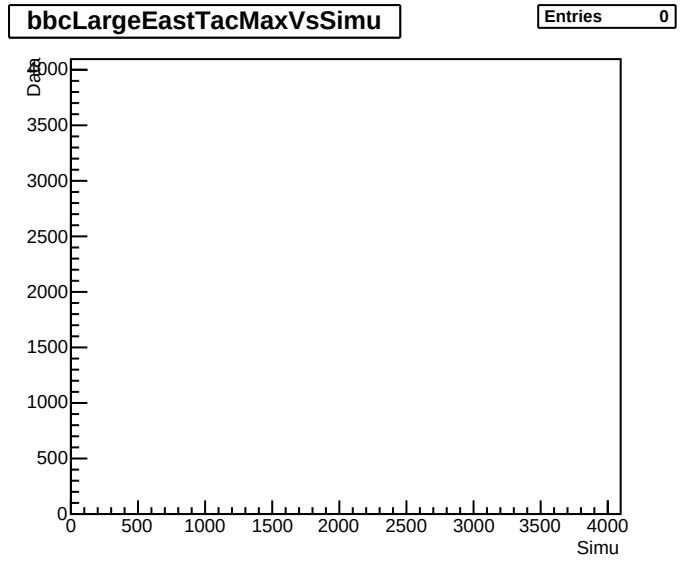

bbcSmallWestTacMax
Entries2096874

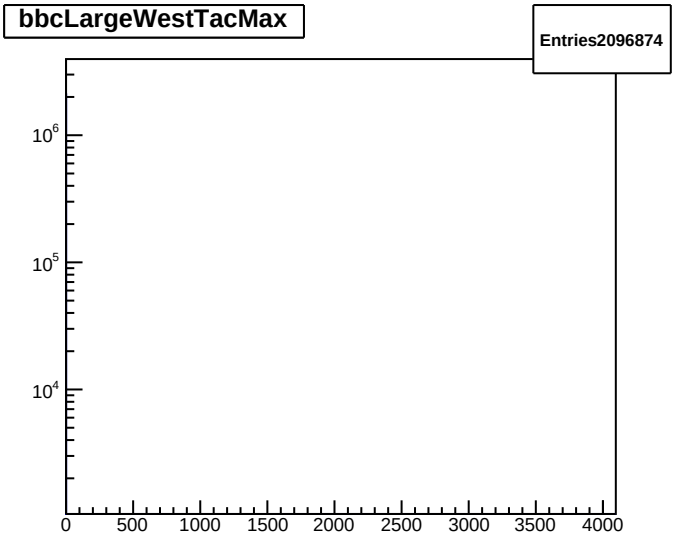

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

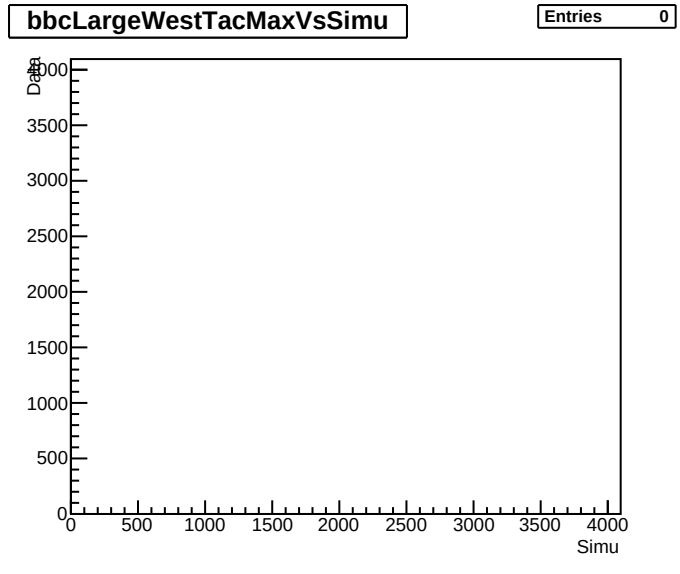

bbcLargeHit
Entries 4193748
2.09687e+06
2.09687e+06
E-Hit
W-Hit

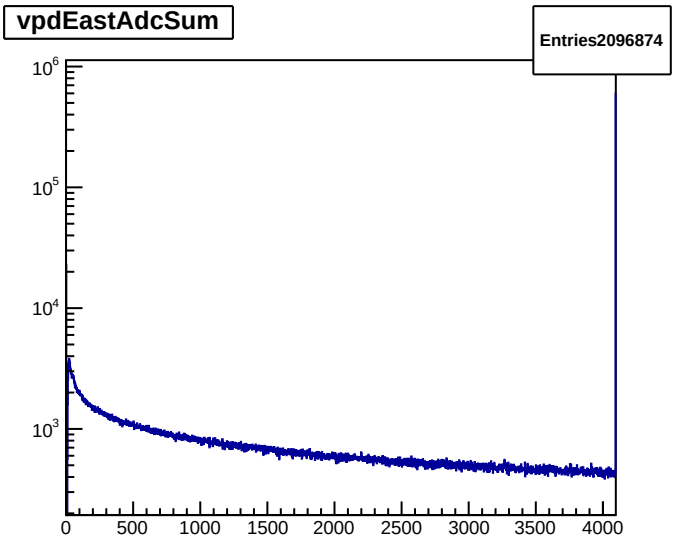

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

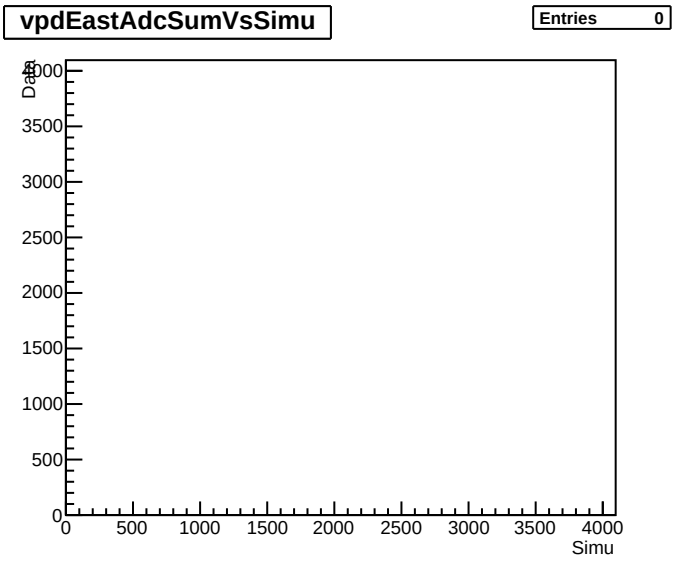

bbcLargeEastTacMax
Entries2096874

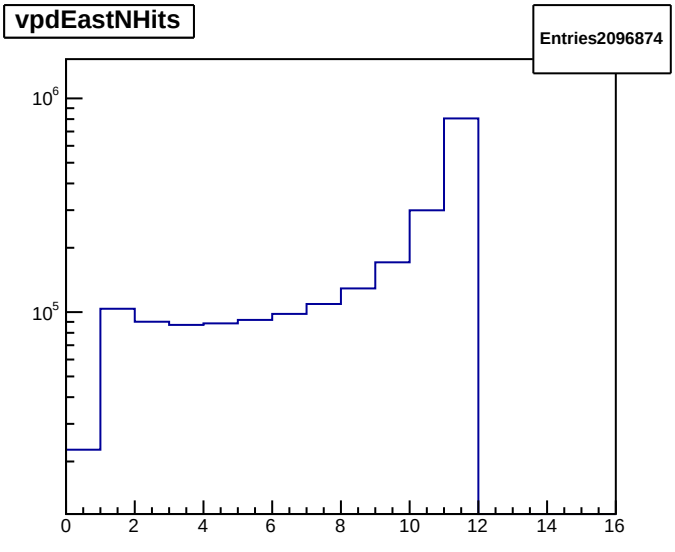

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

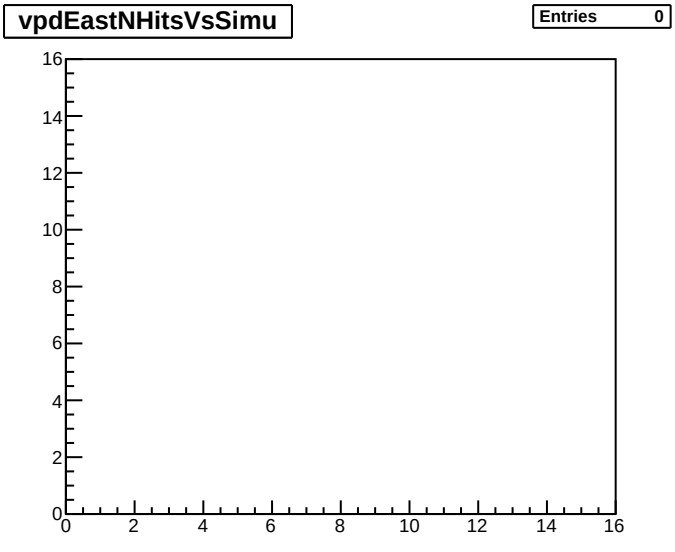

bbcLargeWestTacMax
Entries2096874

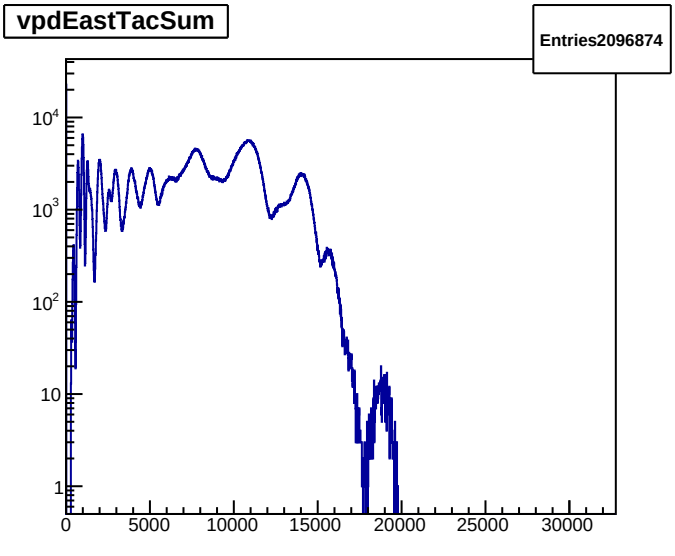

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

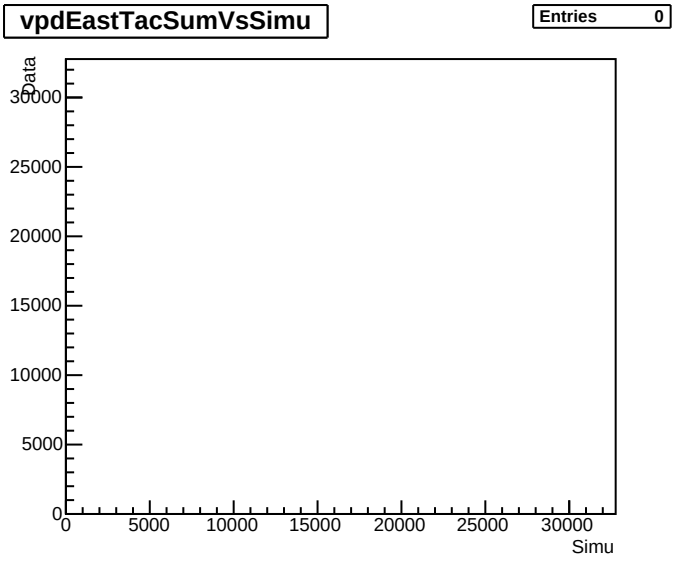

vpdEastAdcSum
Entries2096874

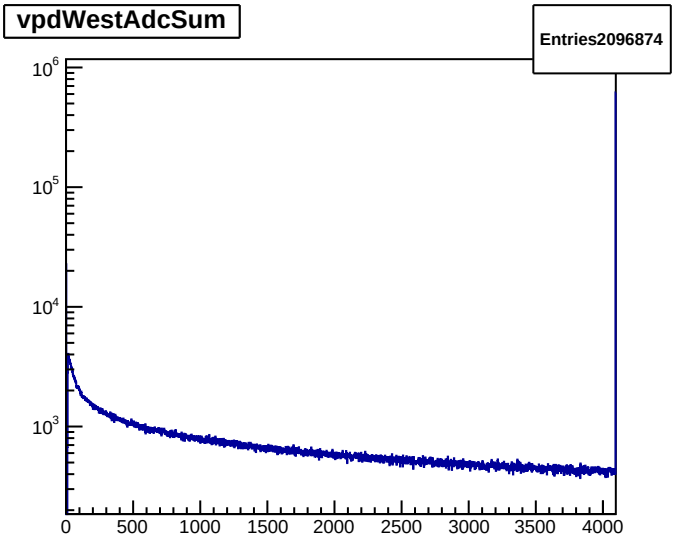

vpdEastAdcSumVsSimu
Entries 0
Data
Simu

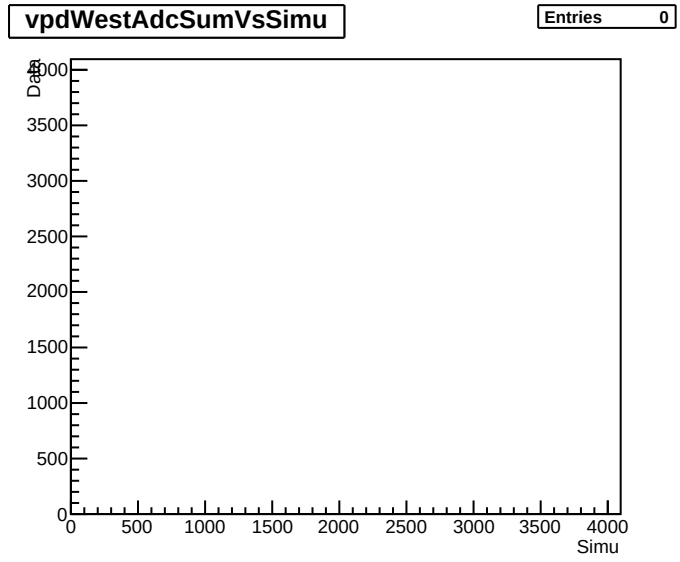

vpdEastNHits
Entries2096874

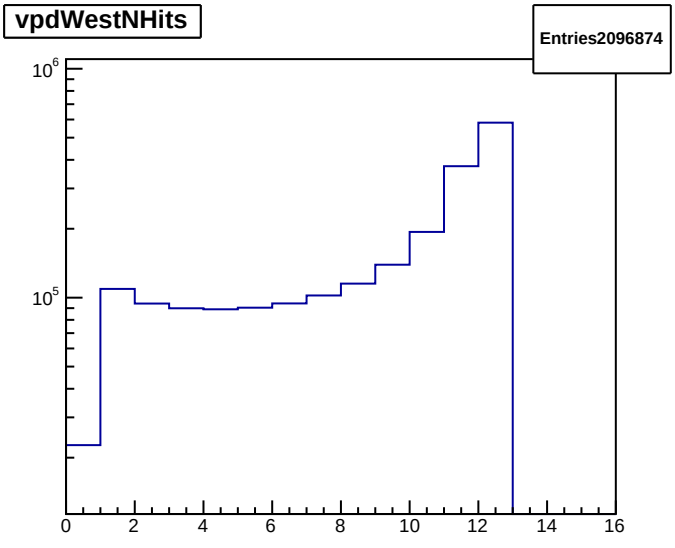

vpdEastNHitsVsSimu
Entries 0

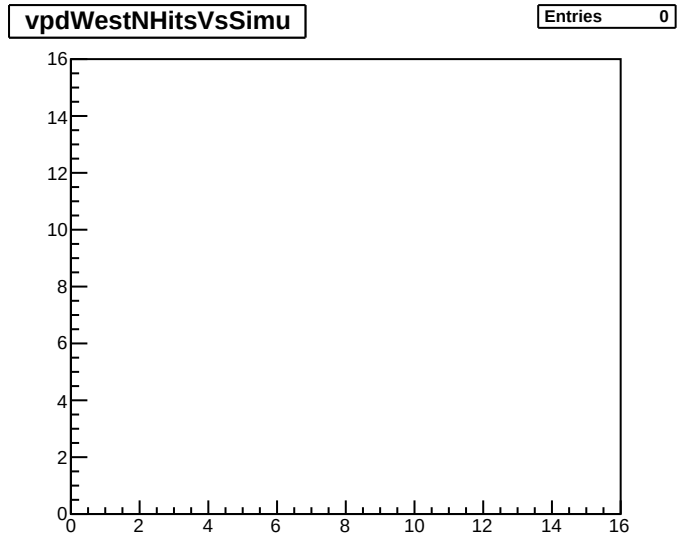

vpdEastTacSum
Entries2096874

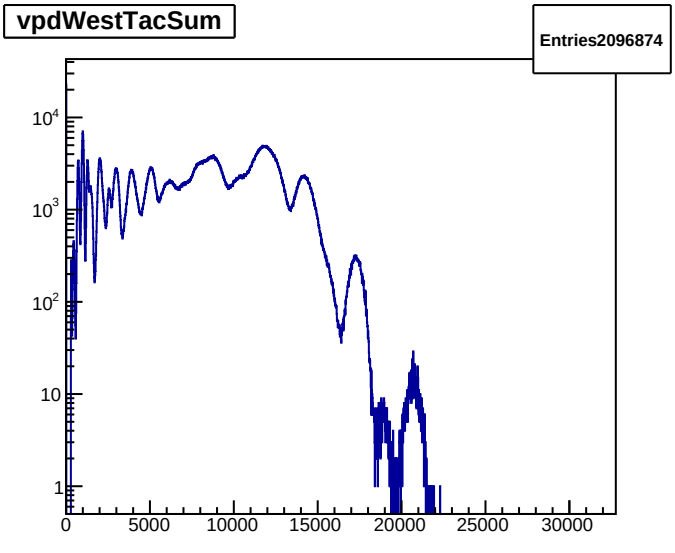

vpdEastTacSumVsSimu
Entries 0
Data
Simu

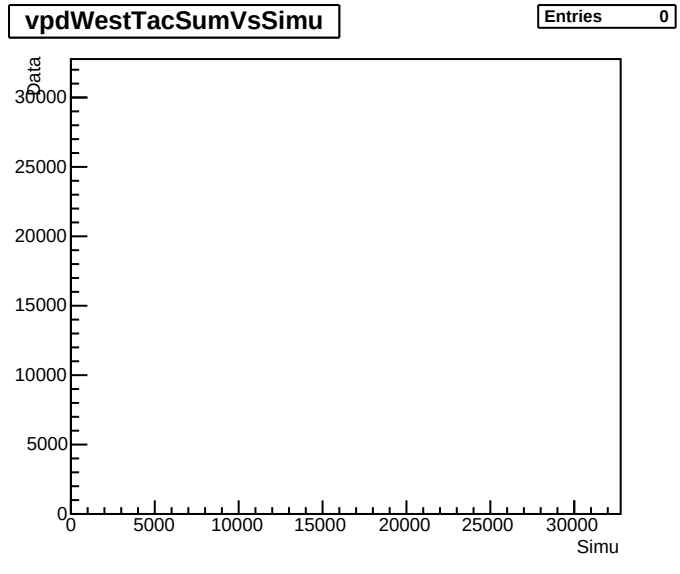

vpdWestAdcSum
Entries2096874
10^6
10^5
10^4
10^3
0 500 1000 1500 2000 2500 3000 3500 4000

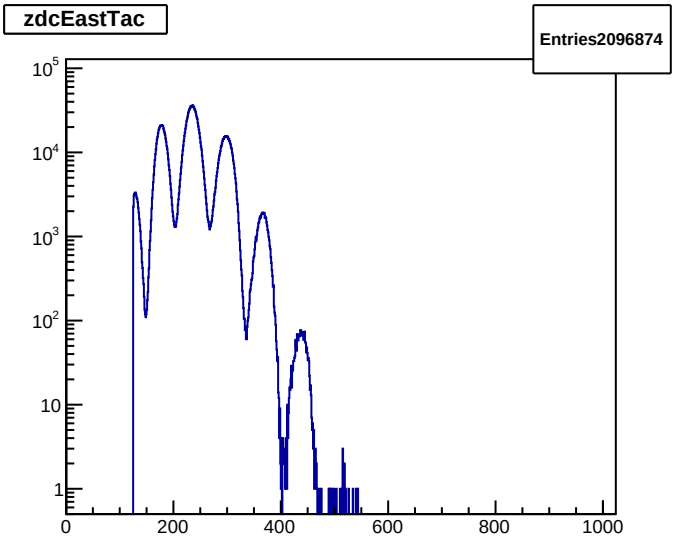

vpdWestAdcSumVsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

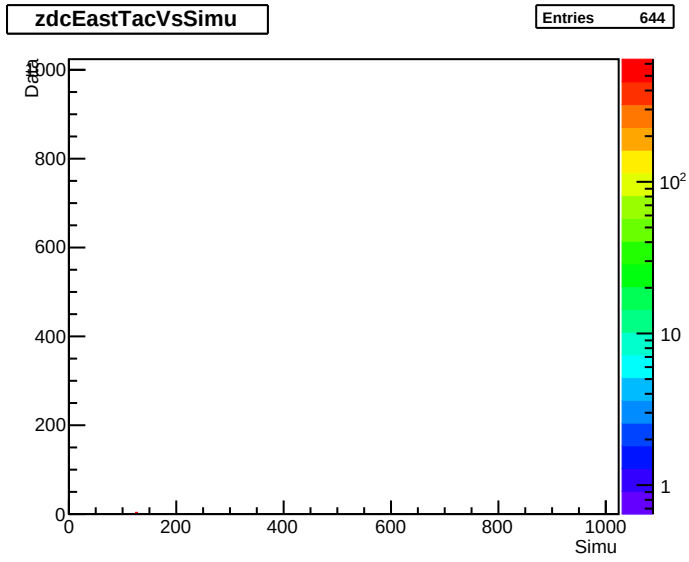

vpdWestNHits
Entries2096874
10^6
10^5
0 2 4 6 8 10 12 14 16

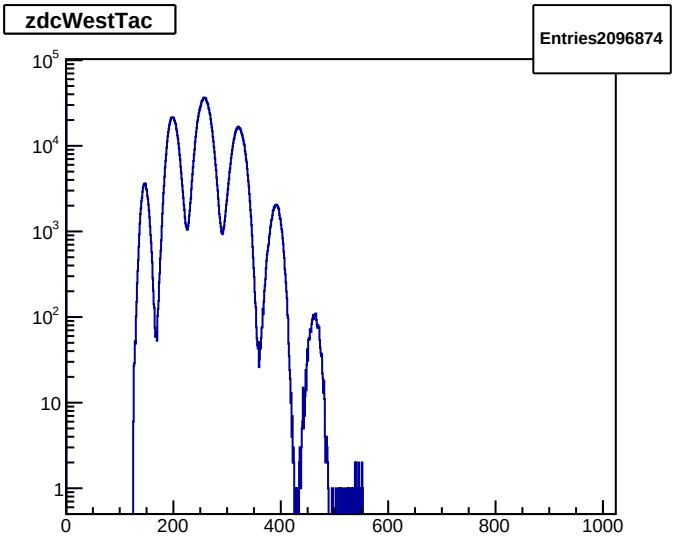

vpdWestNHitsVsSimu
Entries 0
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16

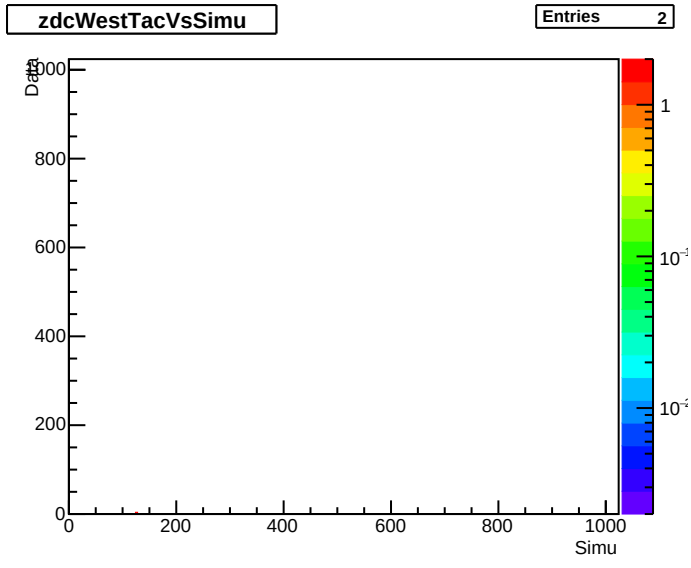

vpdWestTacSum
Entries2096874
10^4
10^3
10^2
10
1
0 5000 10000 15000 20000 25000 30000

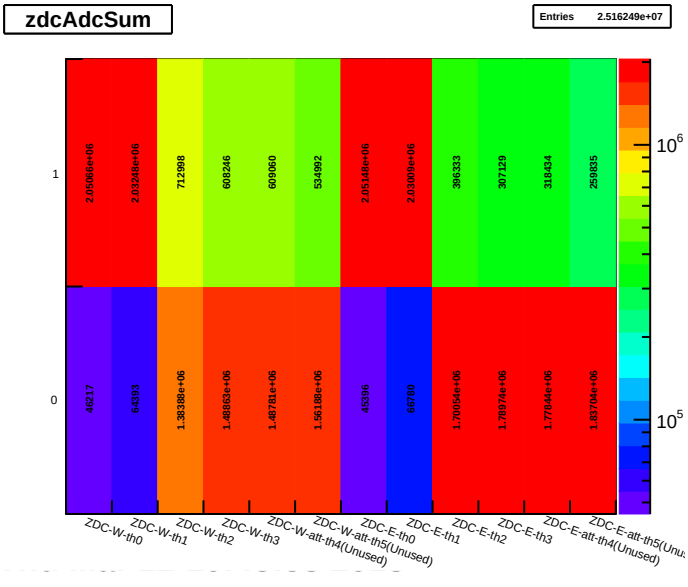

vpdWestTacSumVsSimu
Entries 0
Data
30000 25000 20000 15000 10000 5000 0
0 5000 10000 15000 20000 25000 30000
Simu

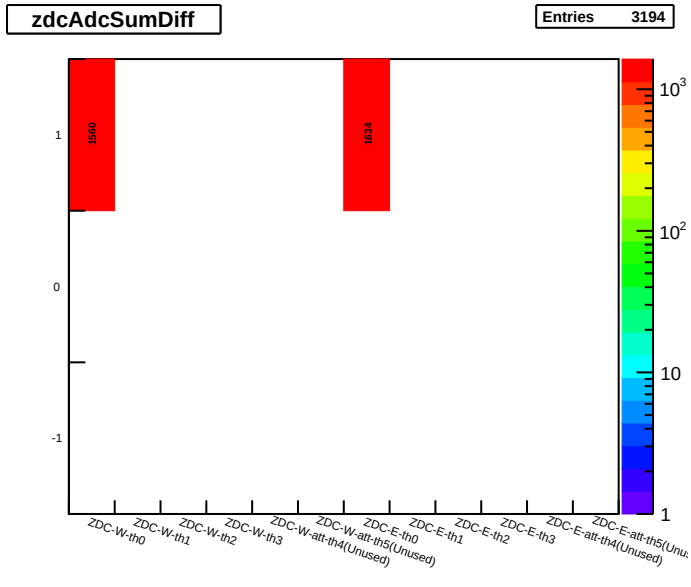

zdcEastTac
Entries2096874

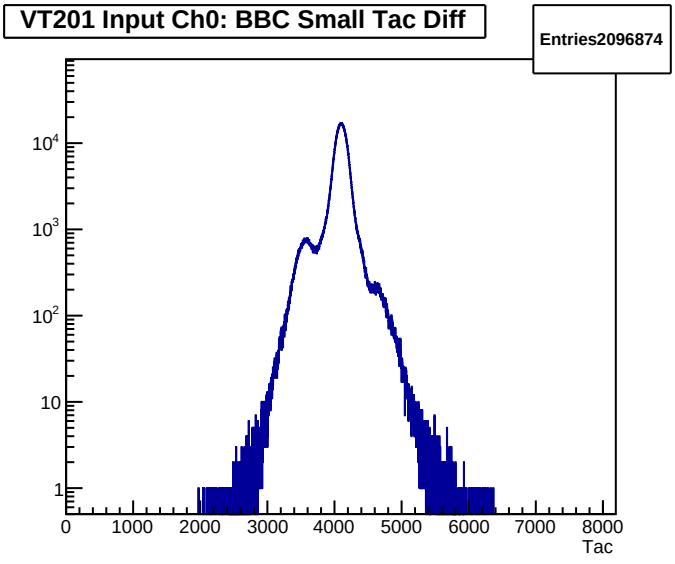

zdcEastTacVsSimu
Entries 644
Data
Simu

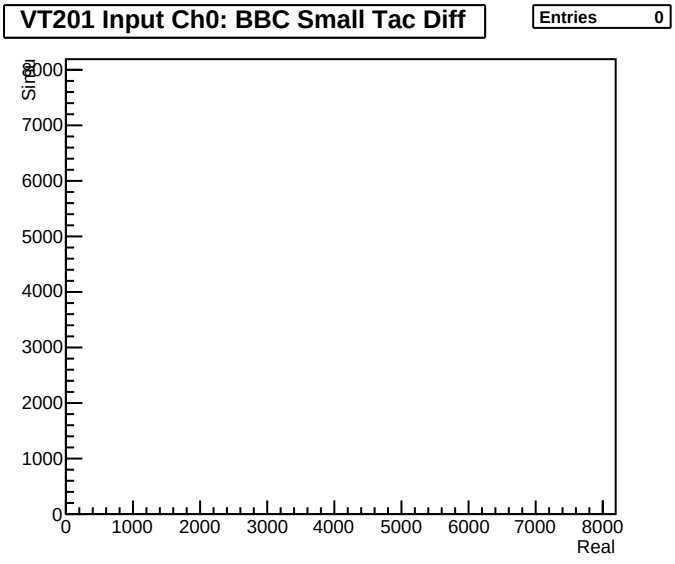

zdcWestTac
Entries2096874

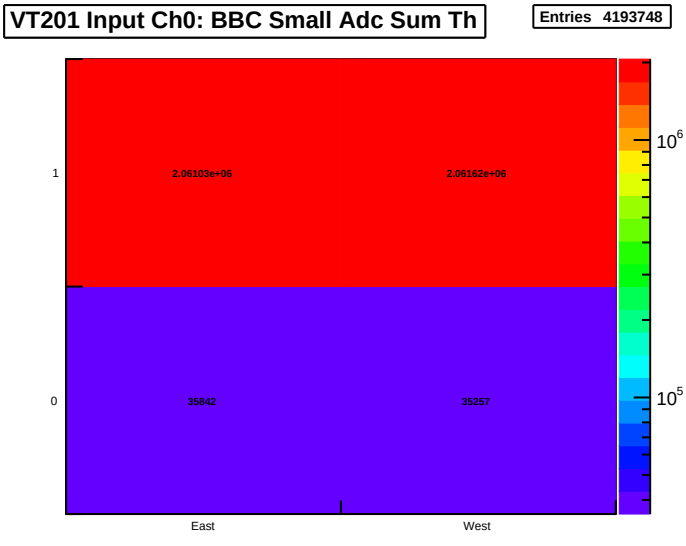

zdcWestTacVsSimu
Entries 2
Data
Simu

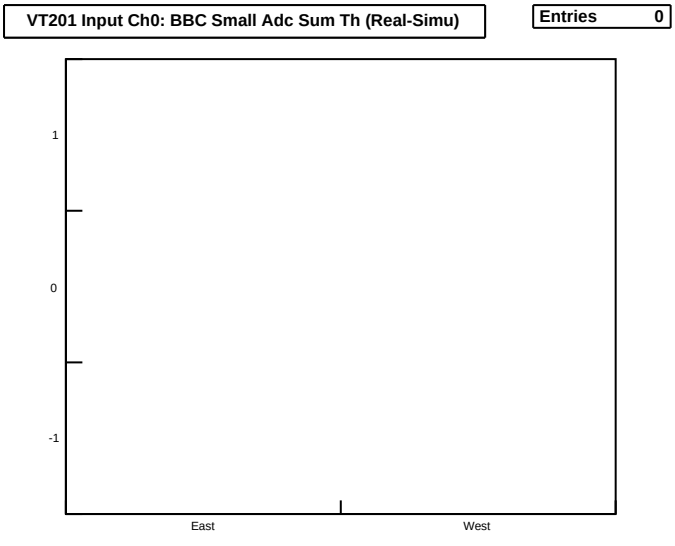

zdcAdcSum
Entries 2.516249e+07
2.05066e+06
2.03248e+06
712998
608246
609060
534992
2.05148e+06
2.03069e+06
396333
307129
318434
259835
46217
64093
1.36388e+06
1.48863e+06
1.49781e+06
1.56188e+06
45396
66780
1.70054e+06
1.78974e+06
1.77844e+06
1.83704e+06
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

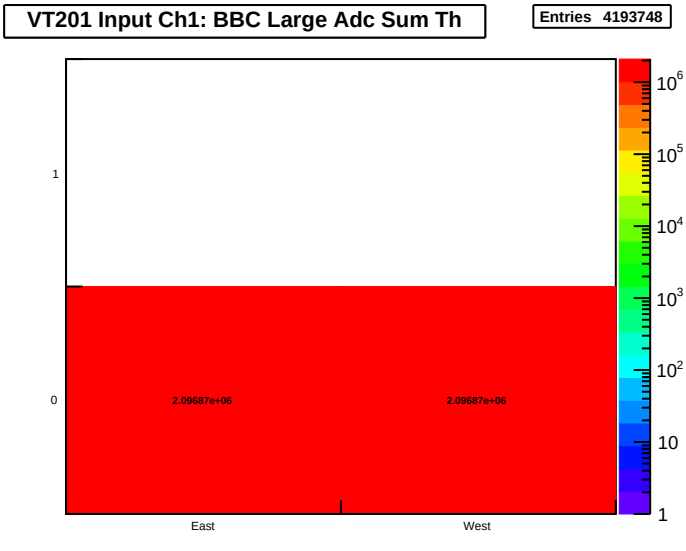

zdcAdcSumDiff
Entries 3194
1560
1634
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

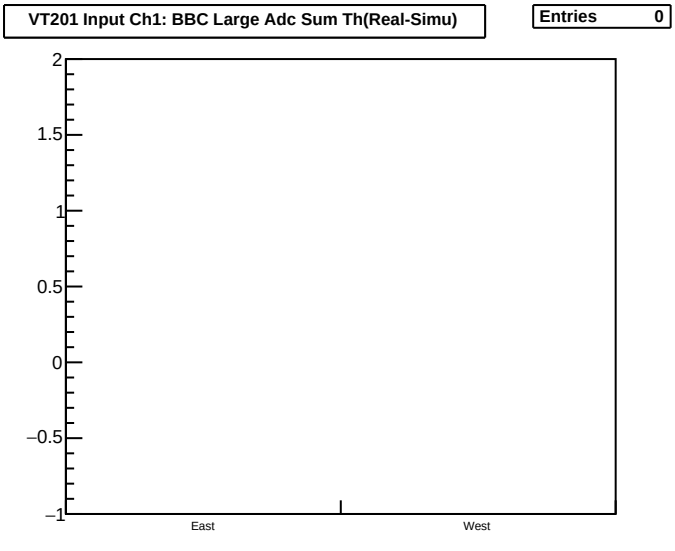

VT201 Input Ch0: BBC Small Tac Diff
Entries2096874
Tac

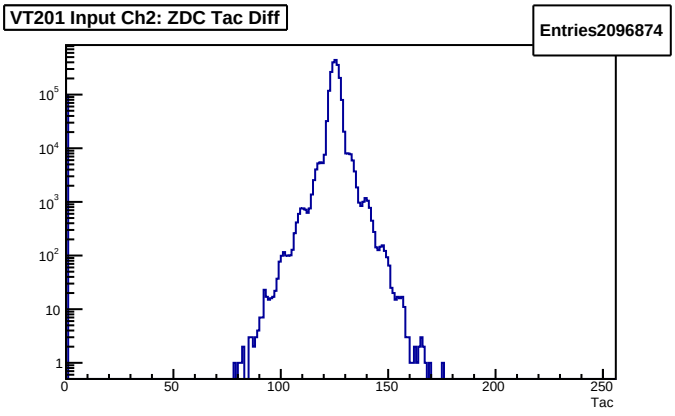

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

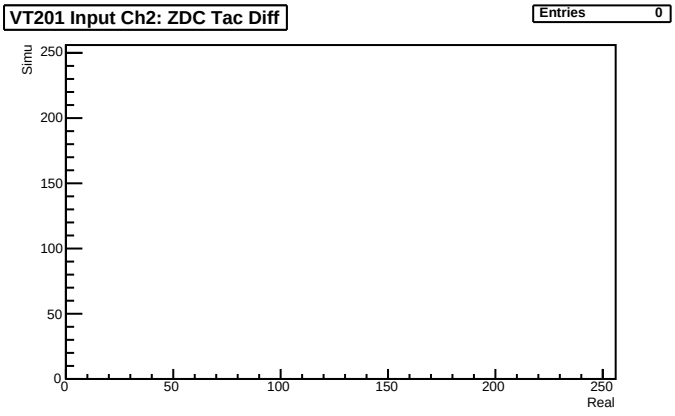

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 4193748
2.06103e+06
2.06162e+06
35842
35257
East
West

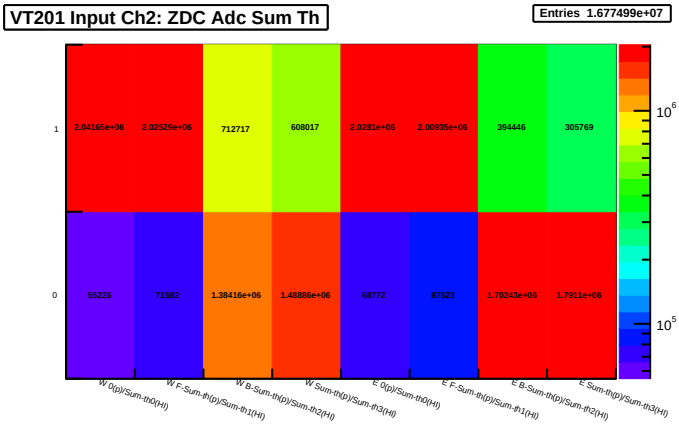

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

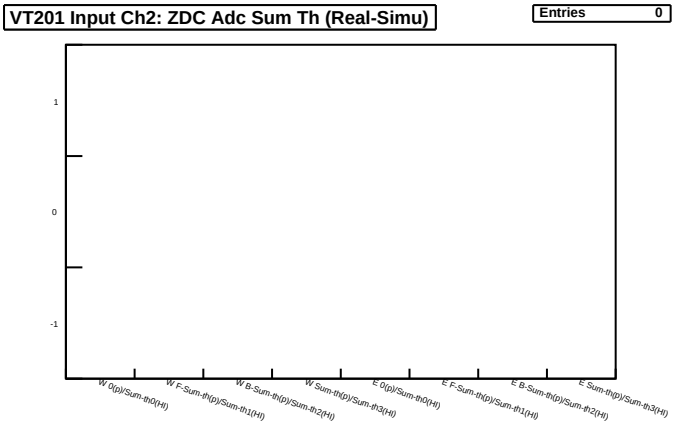

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 4193748
2.09687e+06
2.09687e+06
East
West

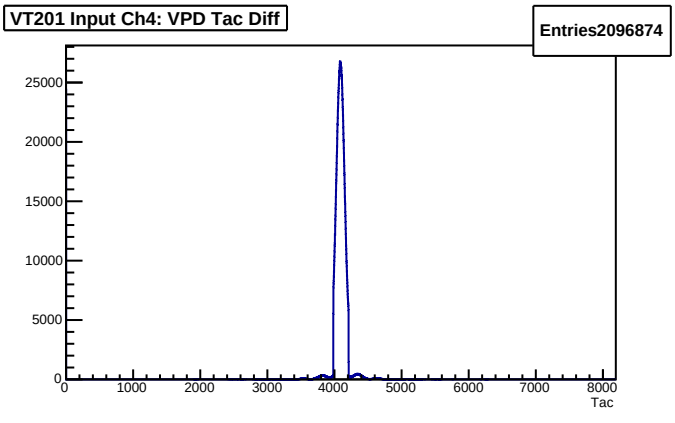

VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

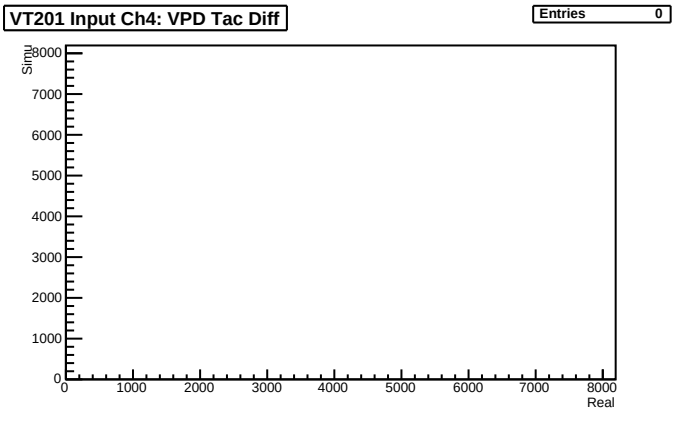

VT201 Input Ch2: ZDC Tac Diff
Entries2096874
Tac

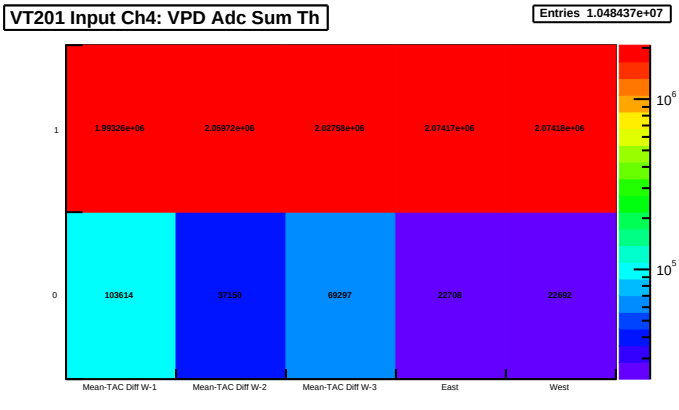

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

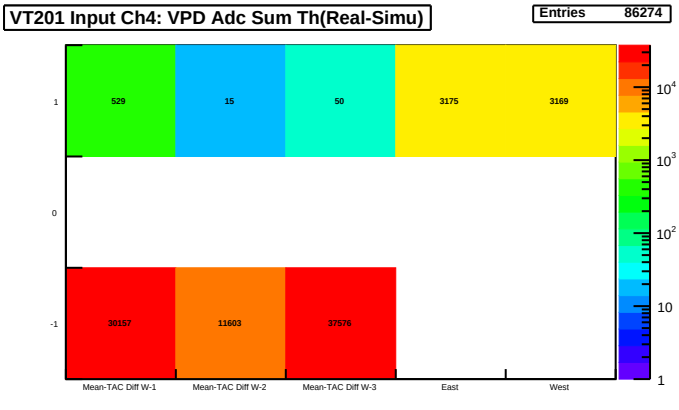

VT201 Input Ch2: ZDC Adc Sum Th
Entries 1.677499e+07
2.04165e+06
2.02529e+06
712717
608017
2.0281e+06
2.00935e+06
394446
305769
55226
71582
1.38416e+06
1.48886e+06
68772
87523
1.70243e+06
1.7911e+06
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

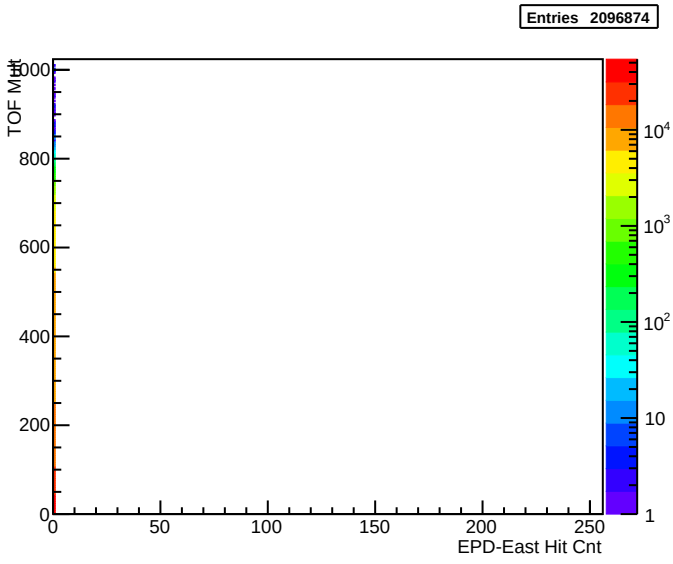

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

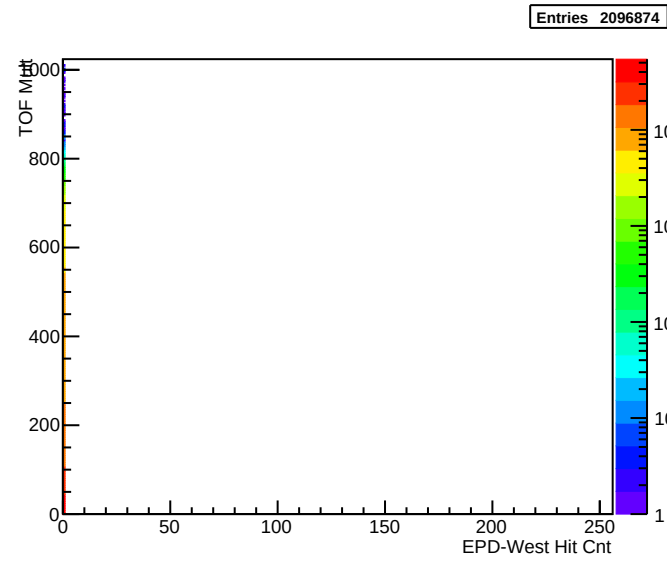

VT201 Input Ch4: VPD Tac Diff
Entries2096874
Tac

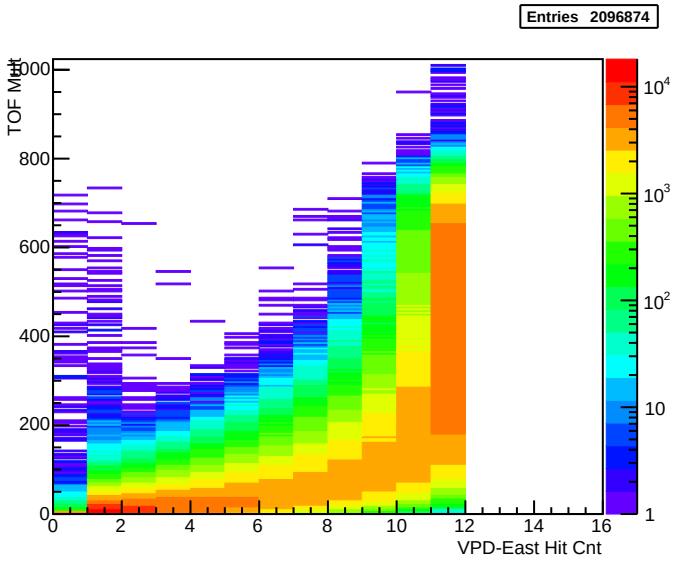

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

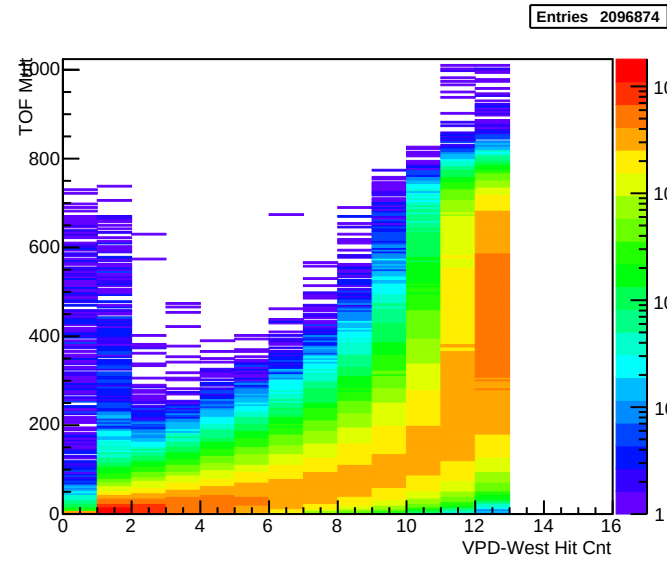

VT201 Input Ch4: VPD Adc Sum Th
Entries 1.048437e+07
1.99326e+06
2.05972e+06
2.02758e+06
2.07417e+06
2.07418e+06
103614
37150
69297
22708
22692
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

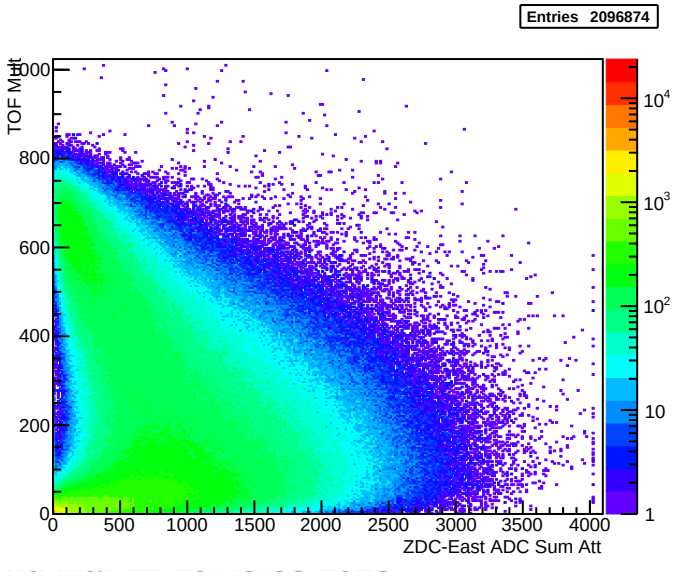

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 86274
529
15
50
3175
3169
30157
11603
37576
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

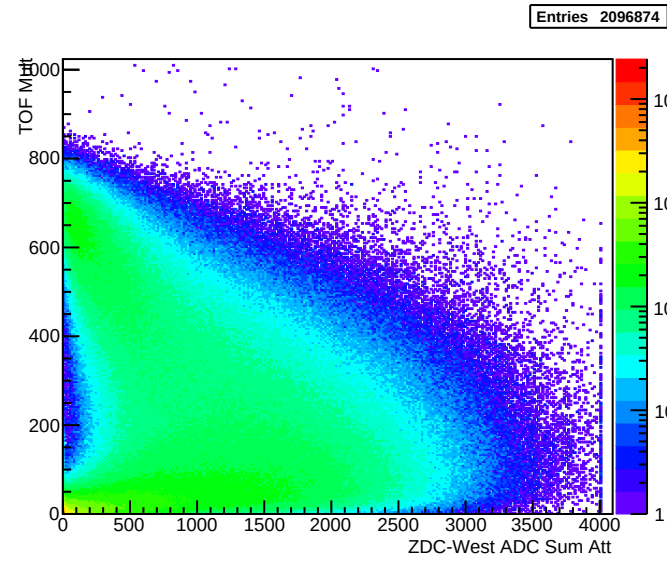

Entries 2096874
TOF Mult
EPD-East Hit Cnt

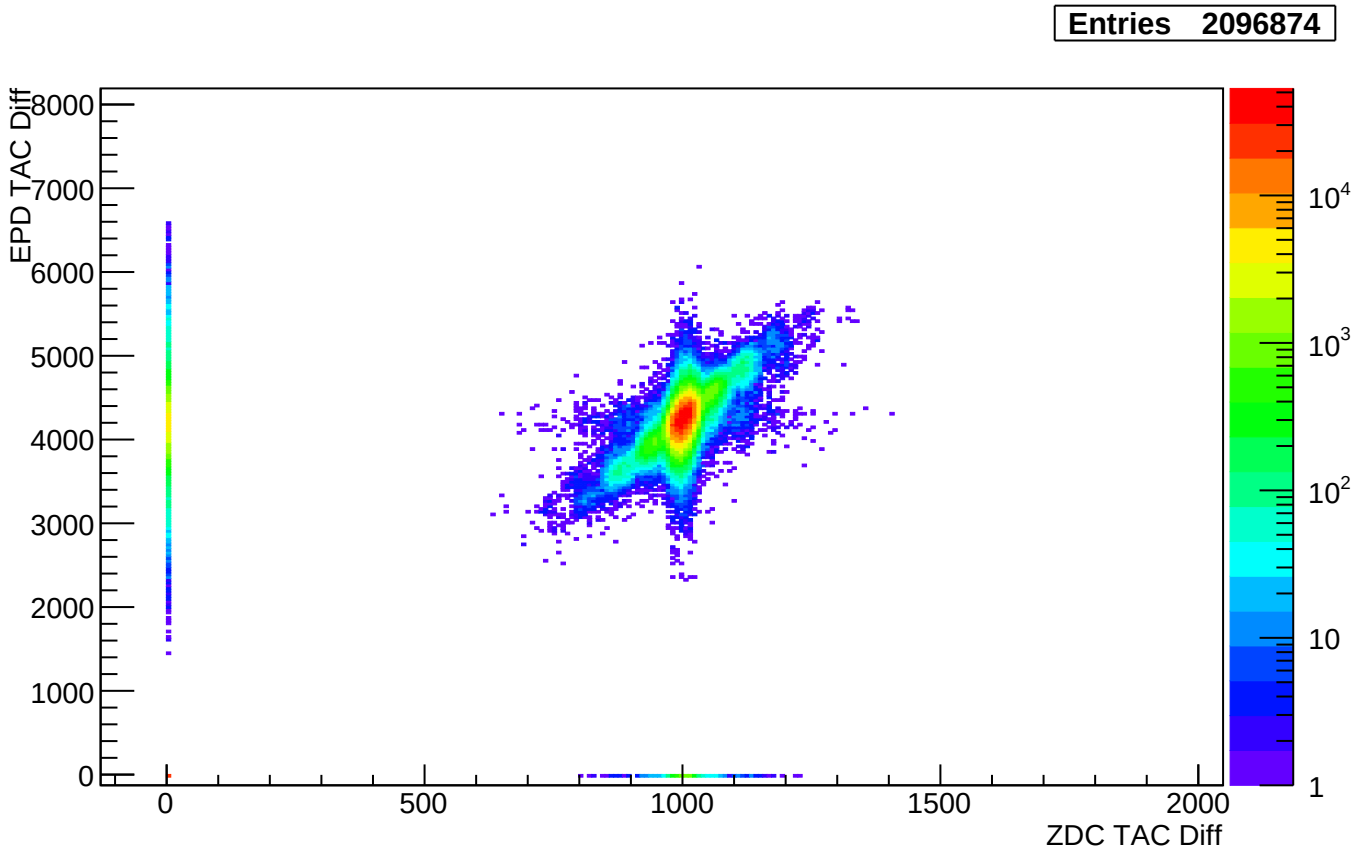

Entries 2096874
TOF Mult
EPD-West Hit Cnt

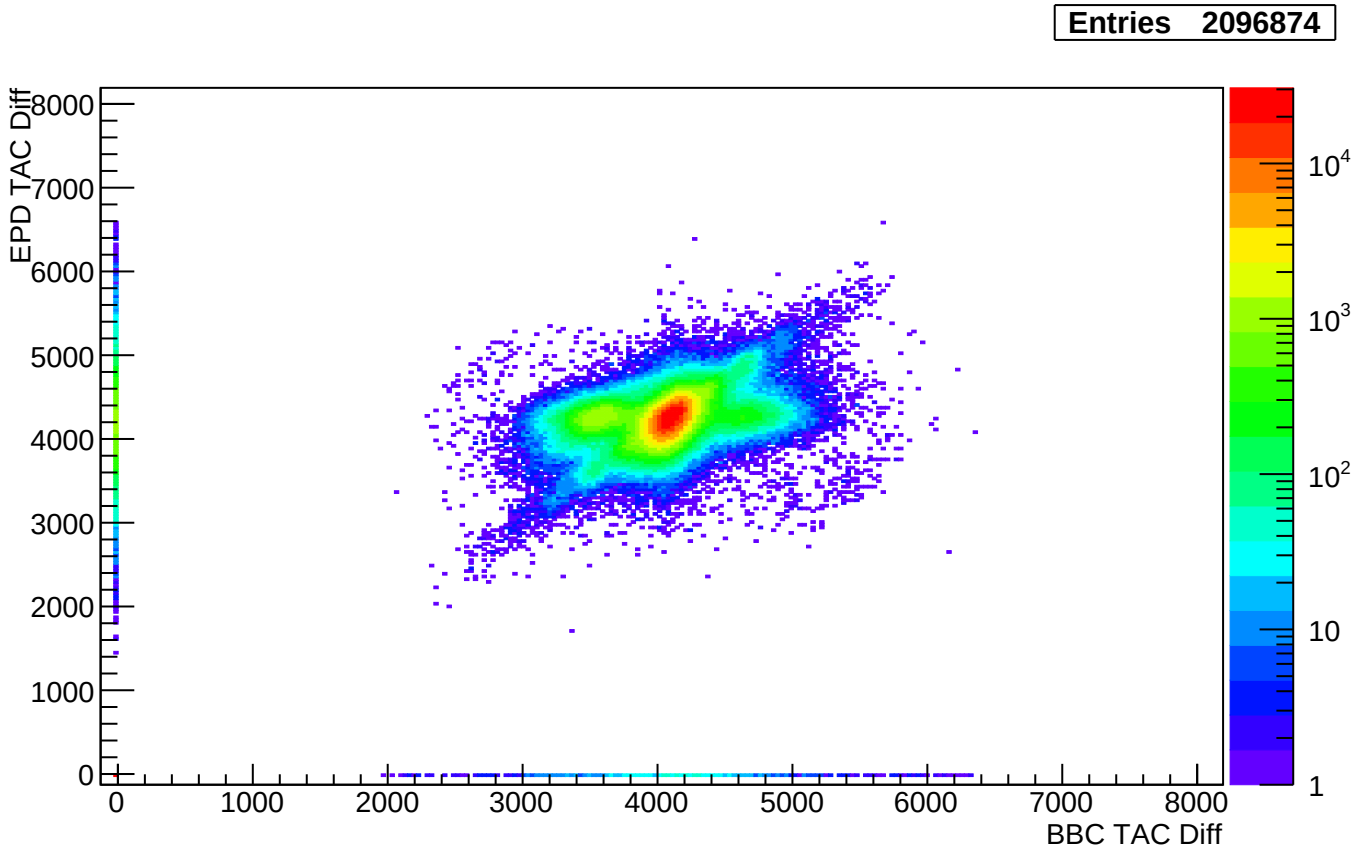

Entries 2096874
TOF Mult
VPD-East Hit Cnt

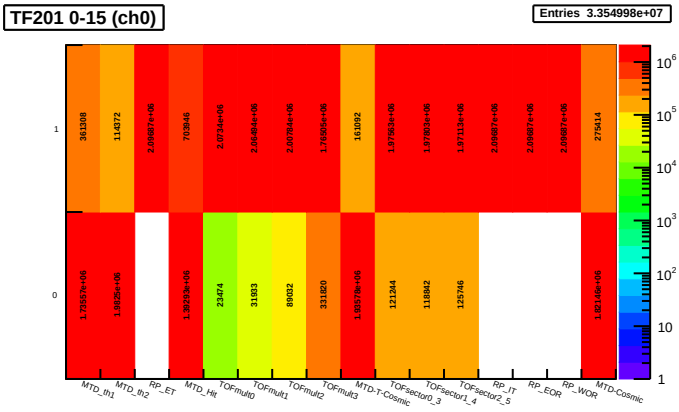

Entries 2096874
TOF Mult
VPD-West Hit Cnt

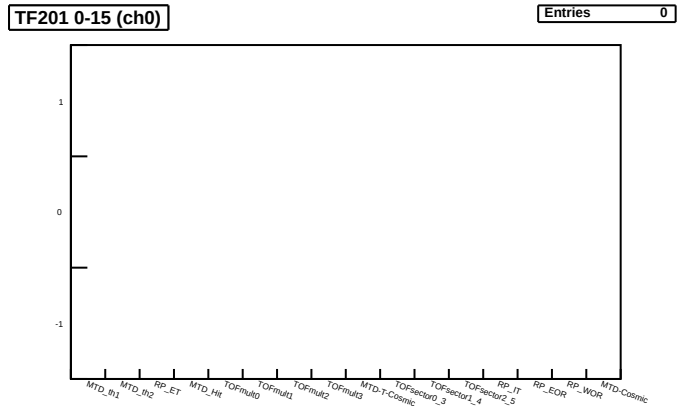

Entries 2096874
TOF Mult
ZDC-East ADC Sum Att

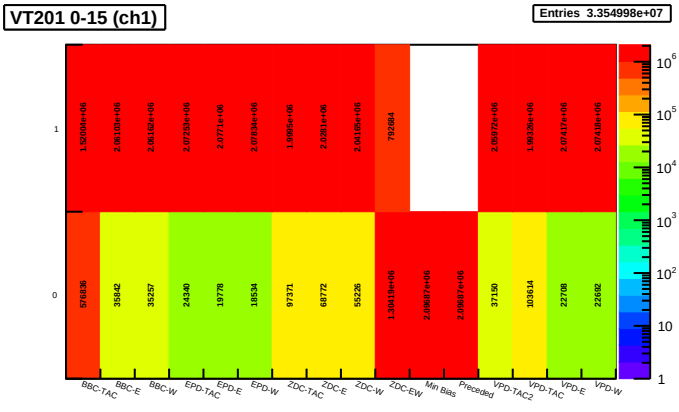

Entries 2096874
TOF Mult
ZDC-West ADC Sum Att


Entries 2096874
EPD TAC Diff
ZDC TAC Diff
0
500
1000
1500
2000
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2
10^3
10^4


Entries 2096874
EPD TAC Diff
BBC TAC Diff
0
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2
10^3
10^4


TF201 0-15 (ch0)
Entries 3.354998e+07
MTD_tfl1
MTD_tfl2
RP_-ET
MTD_Hit
TOFmult0
TOFmult1
TOFmult2
TOFmult3
MTD-T-Cosmic
TOFsector0_3
TOFsector1_4
TOFsector2_5
RP_IT
RP_-EOR
RP_-WOR
MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD_tfl1
MTD_tfl2
RP_-ET
MTD_Hit
TOFmult0
TOFmult1
TOFmult2
TOFmult3
MTD-T-Cosmic
TOFsector0_3
TOFsector1_4
TOFsector2_5
RP_IT
RP_-EOR
RP_-WOR
MTD-Cosmic


VT201 0-15 (ch1)
Entries 3.354998e+07
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
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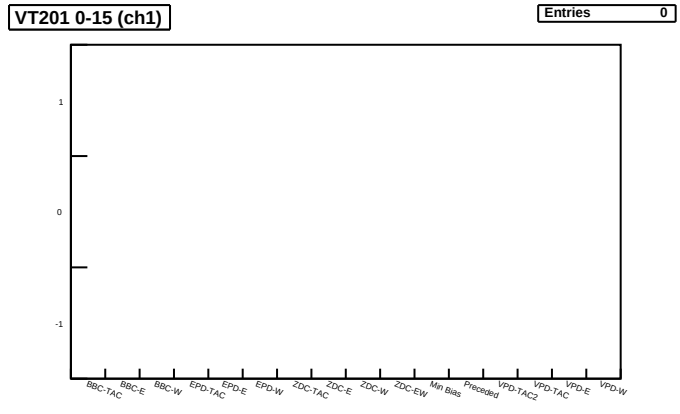
ZDC-E
ZDC-W
ZDC-EW
Min Bias
Preceded
VPD-TAC2
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 0
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BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
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ZDC-W
ZDC-EW
Min Bias
Preceded
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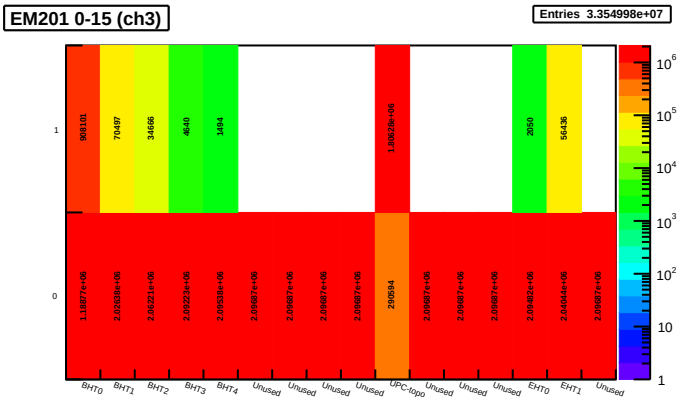
VPD-E
VPD-W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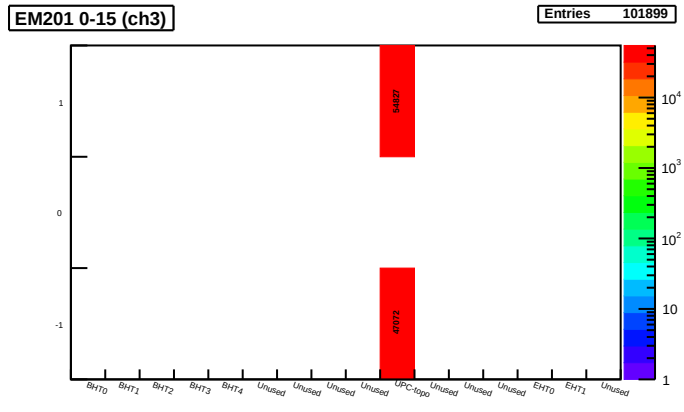
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1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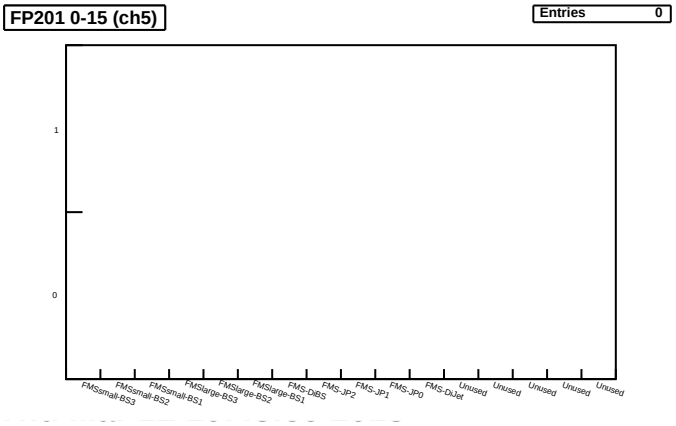
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1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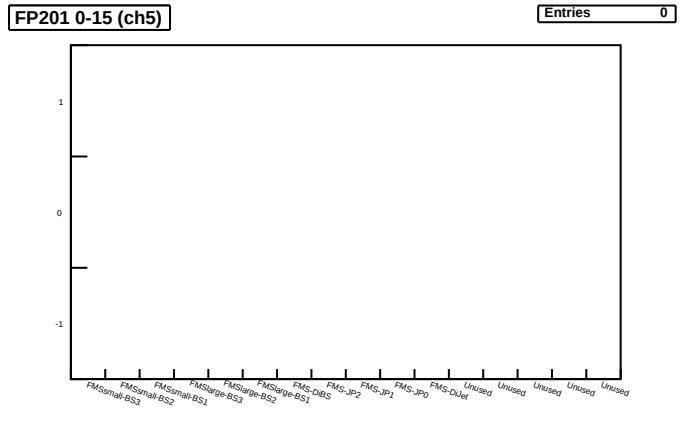
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused