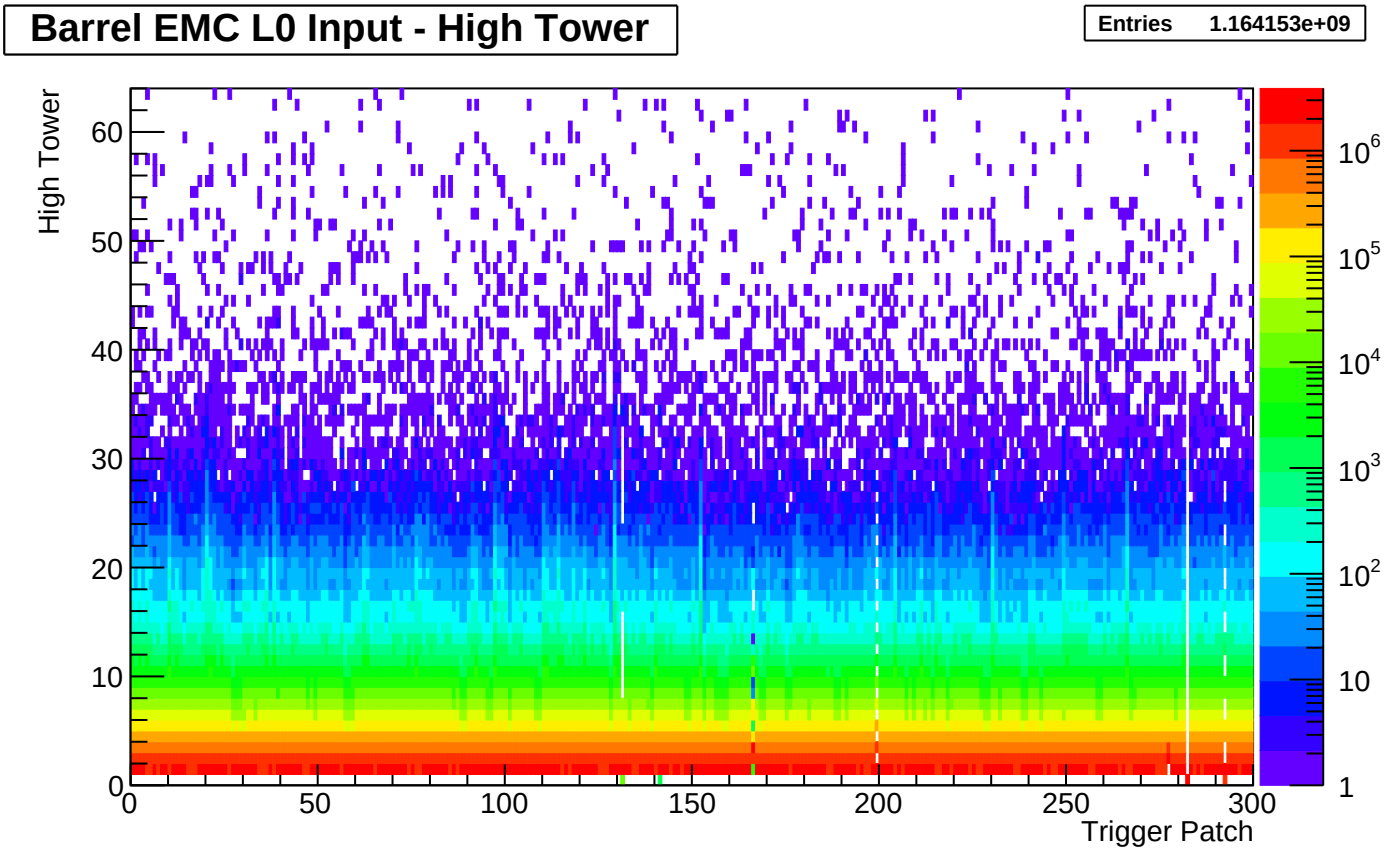
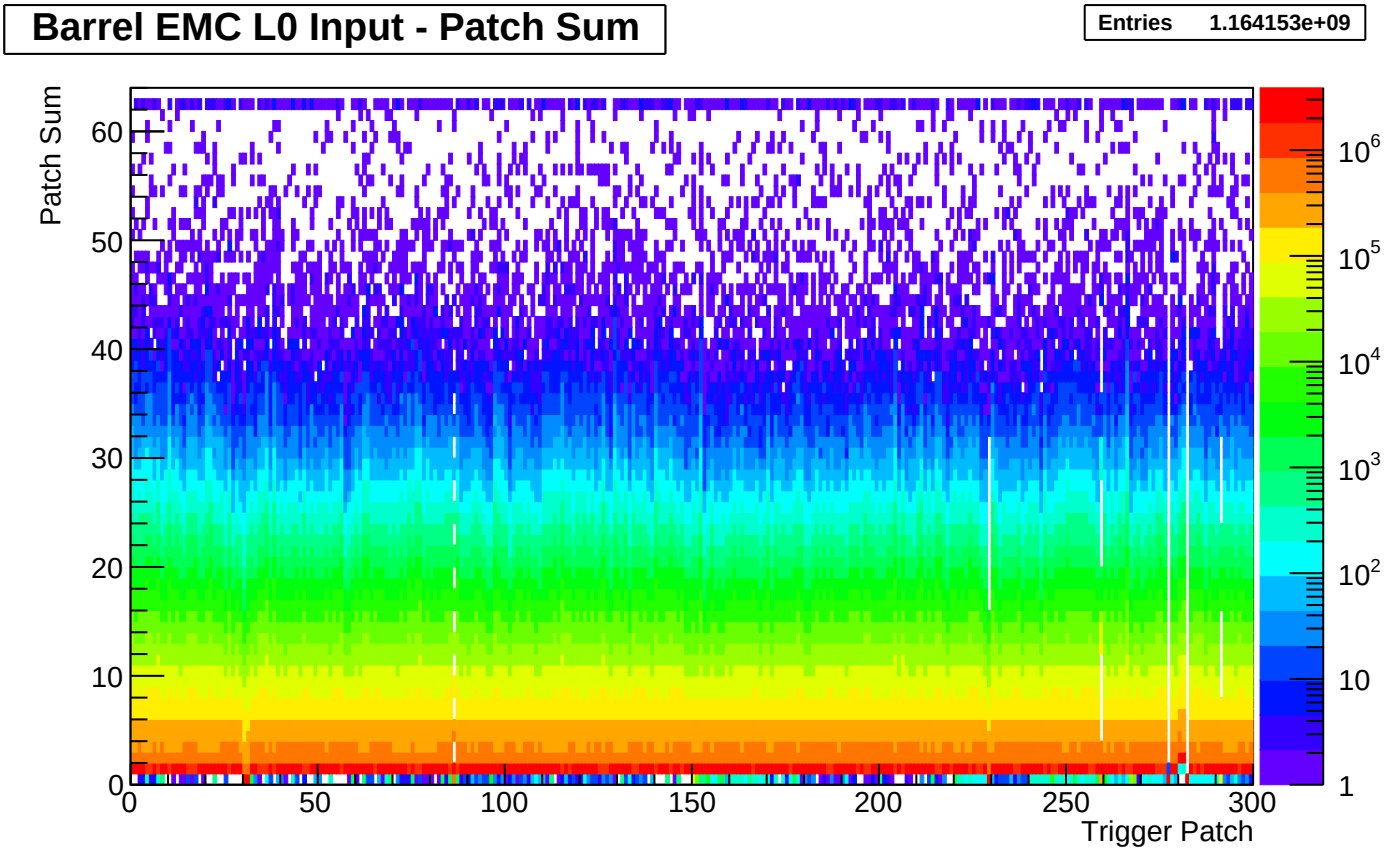
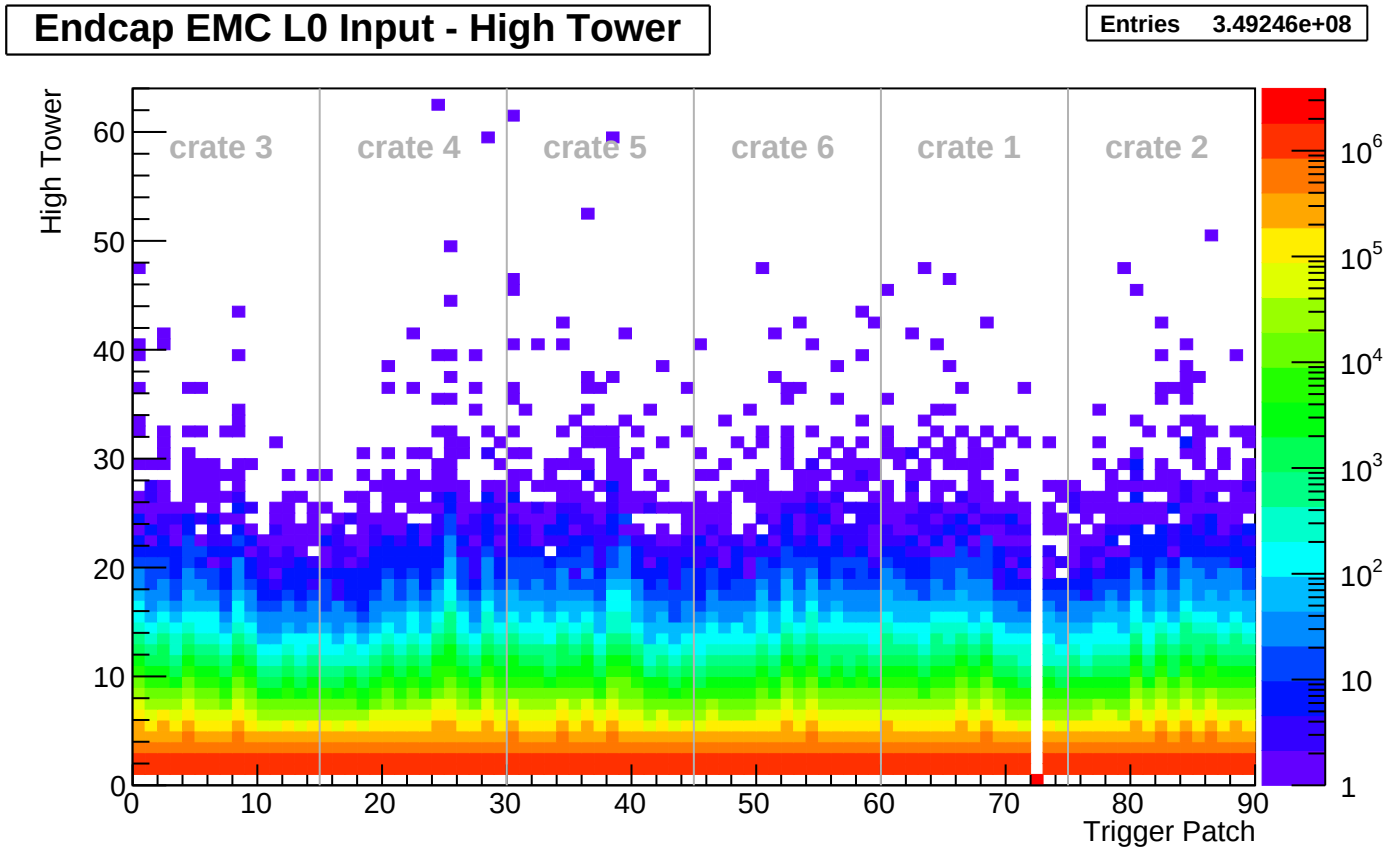
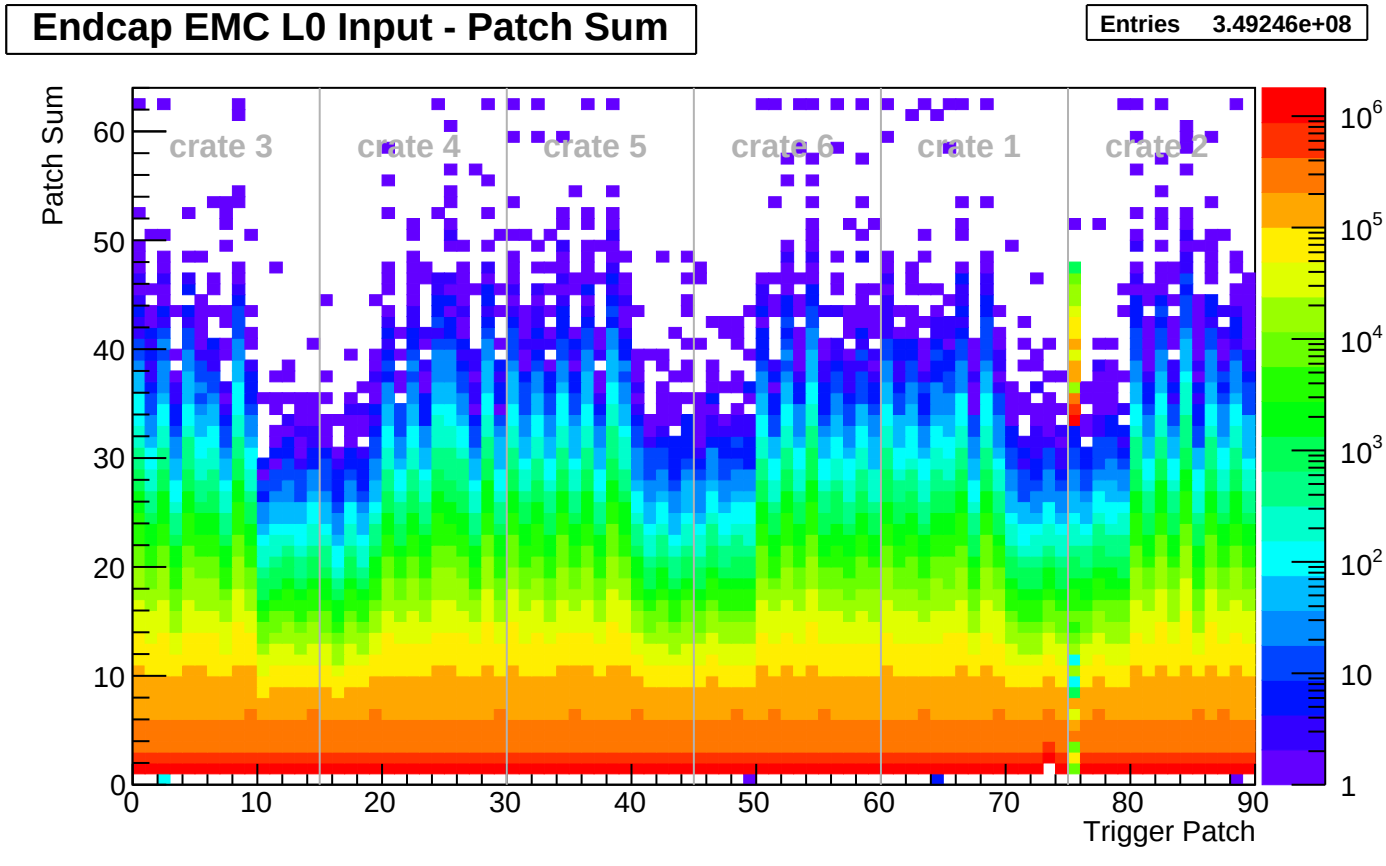
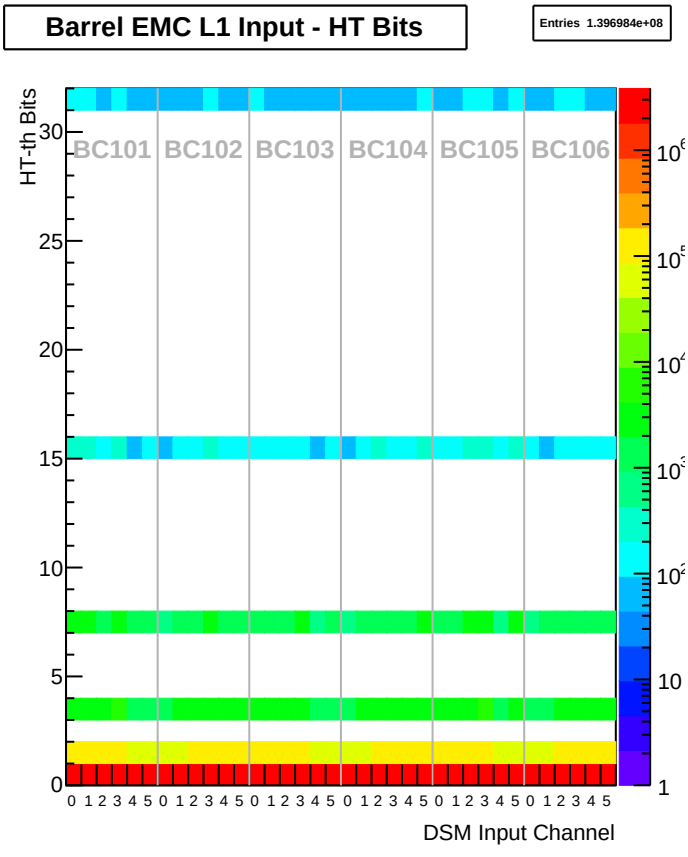
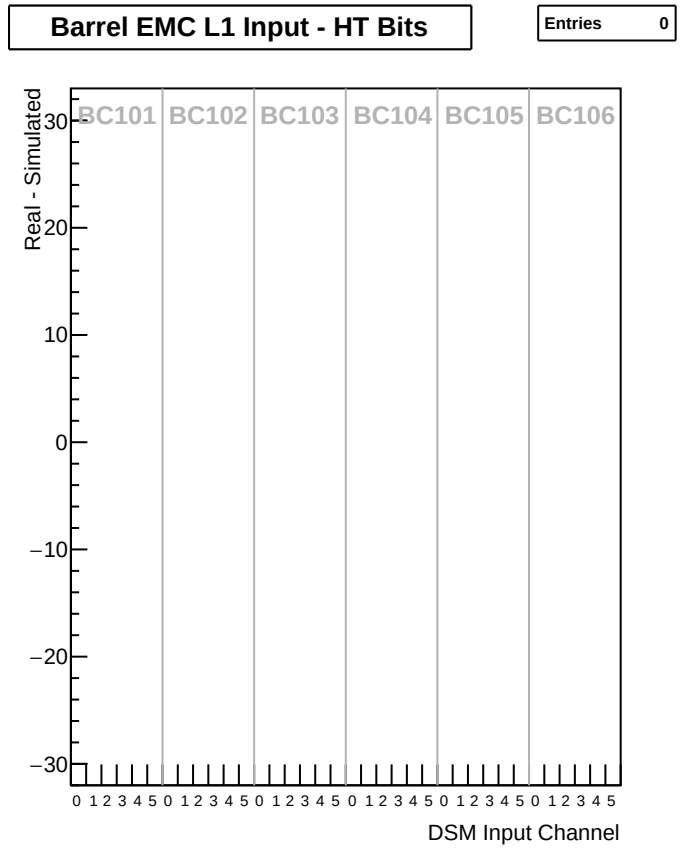

Barrel EMC L0 Input - High Tower
Entries 1.164153e+09
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

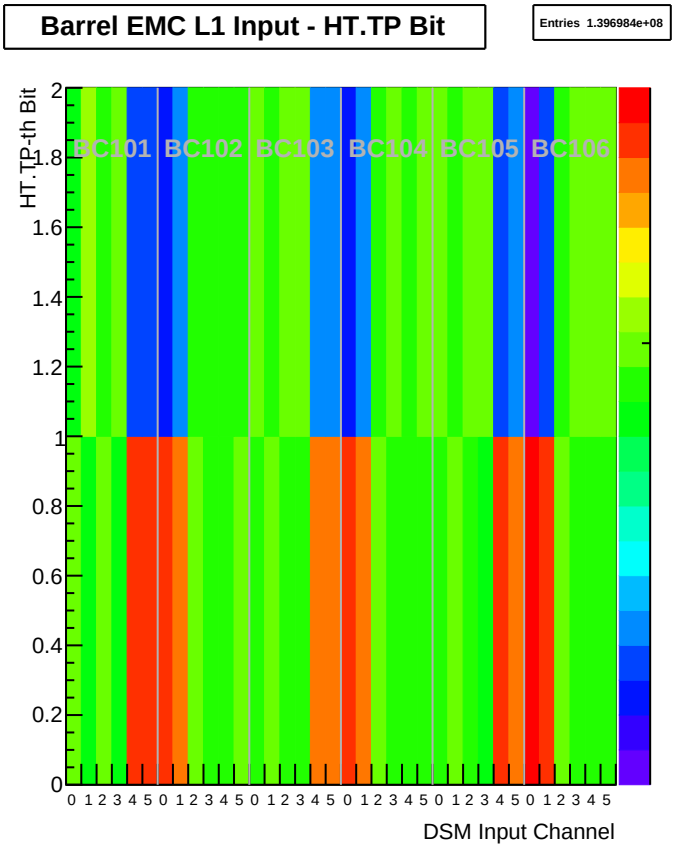

Barrel EMC L0 Input - Patch Sum
Entries 1.164153e+09
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

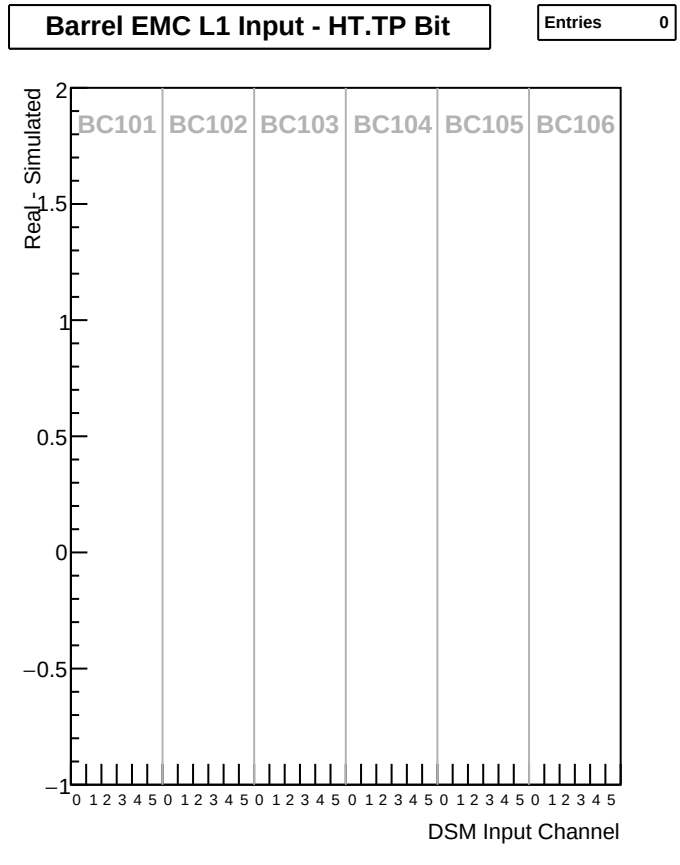

Endcap EMC L0 Input - High Tower
Entries 3.49246e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

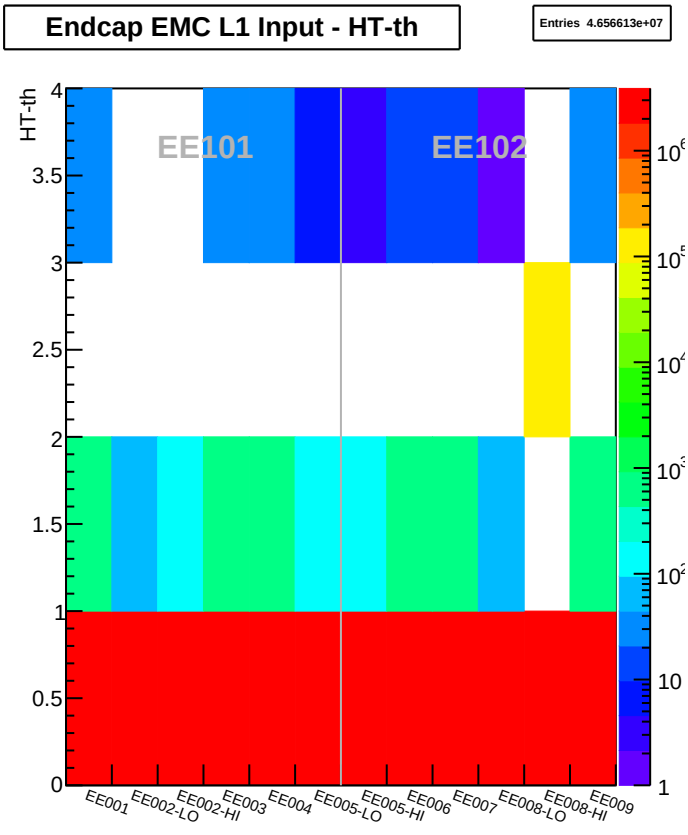

Endcap EMC L0 Input - Patch Sum
Entries 3.49246e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

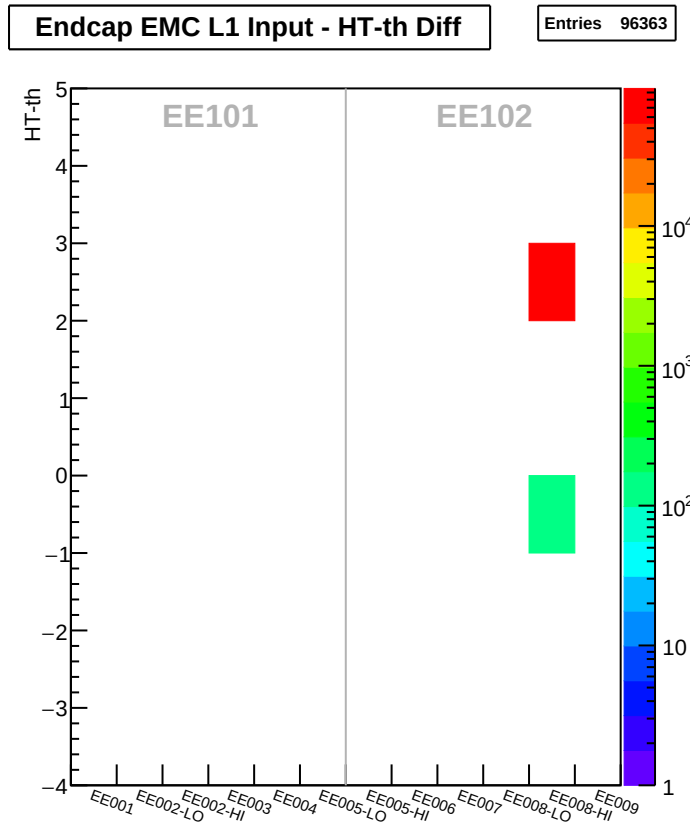

Barrel EMC L1 Input - HT Bits
Entries 1.396984e+08
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

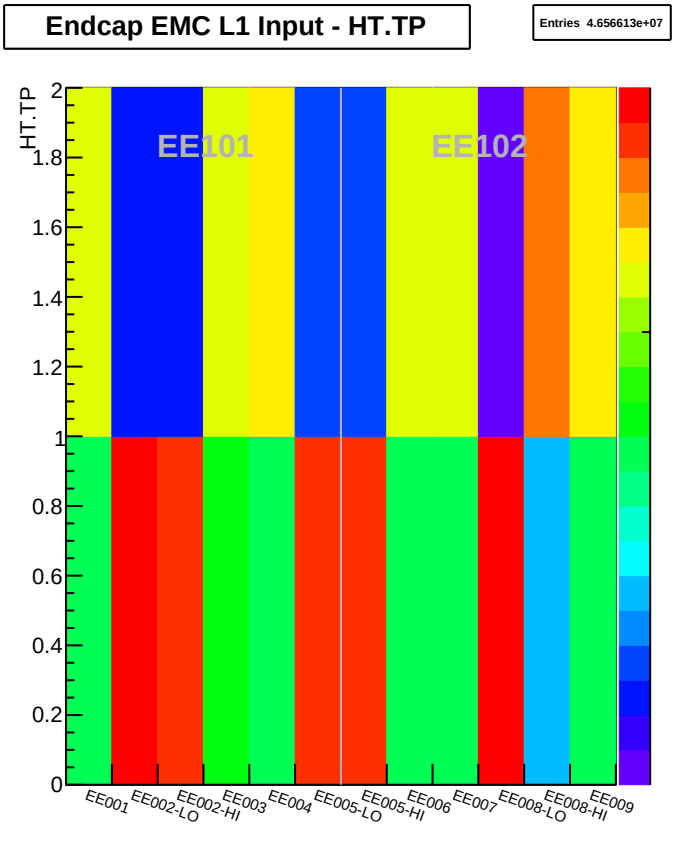

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

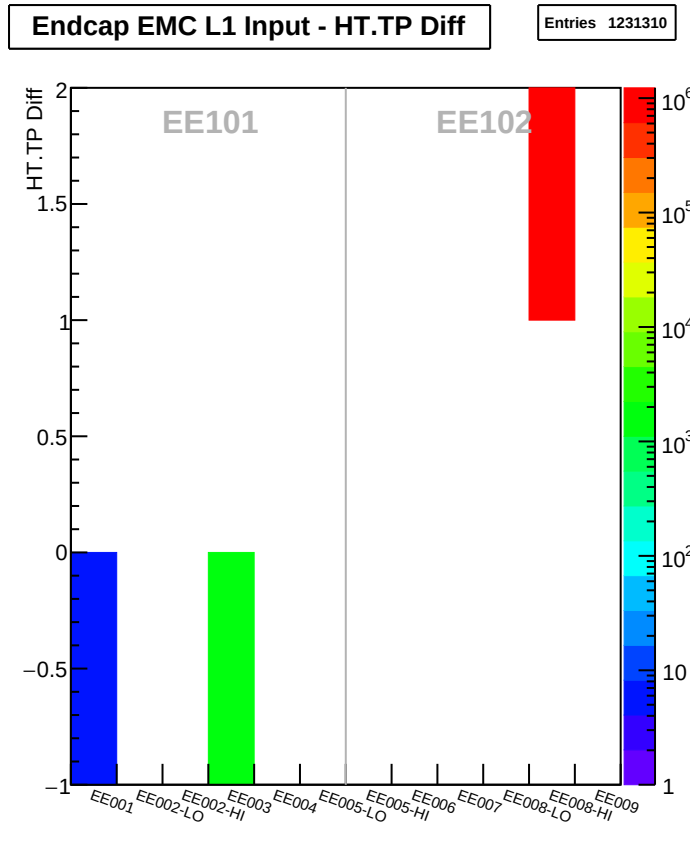

Barrel EMC L1 Input - HT.TP Bit
Entries 1.396984e+08
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

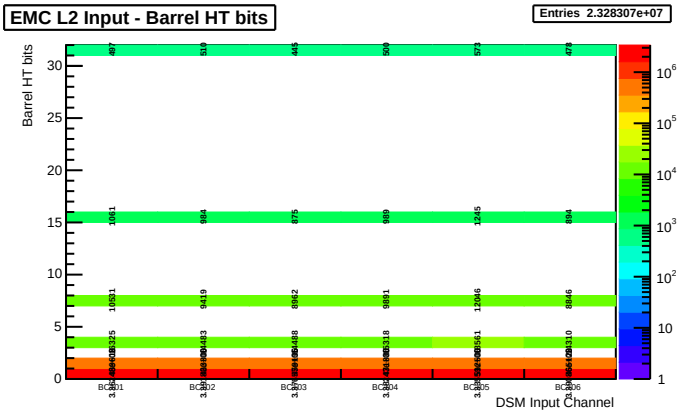

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

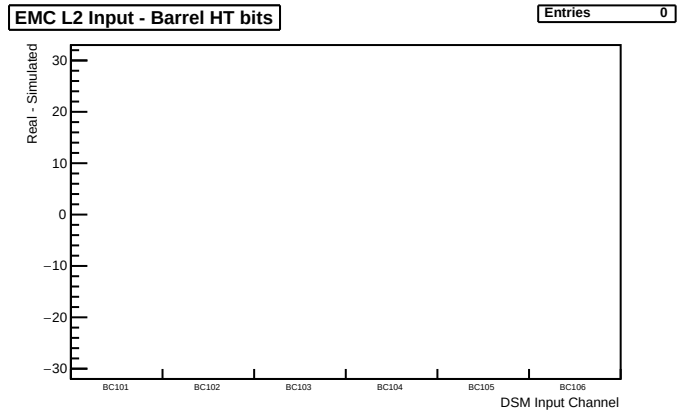

Endcap EMC L1 Input - HT-th
Entries 4.656613e+07
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

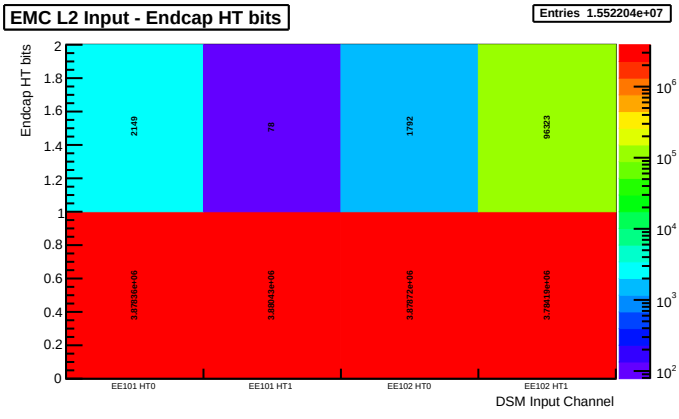

Endcap EMC L1 Input - HT-th Diff
Entries 96363
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

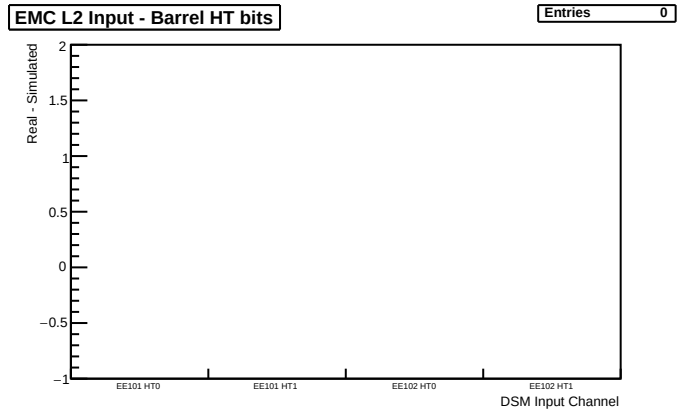

Endcap EMC L1 Input - HT.TP
Entries 4.656613e+07
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

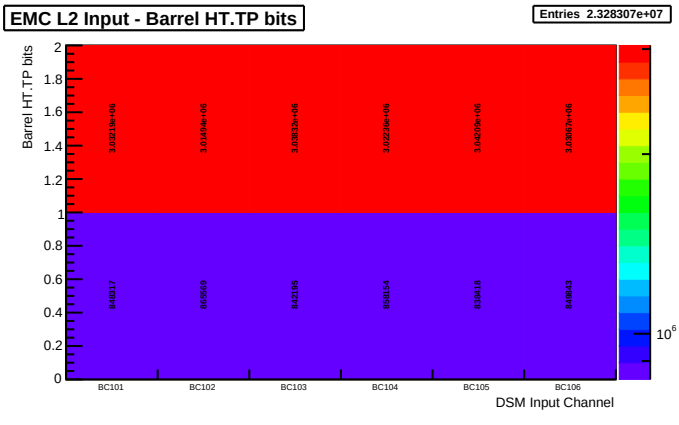

Endcap EMC L1 Input - HT.TP Diff
Entries 1231310
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

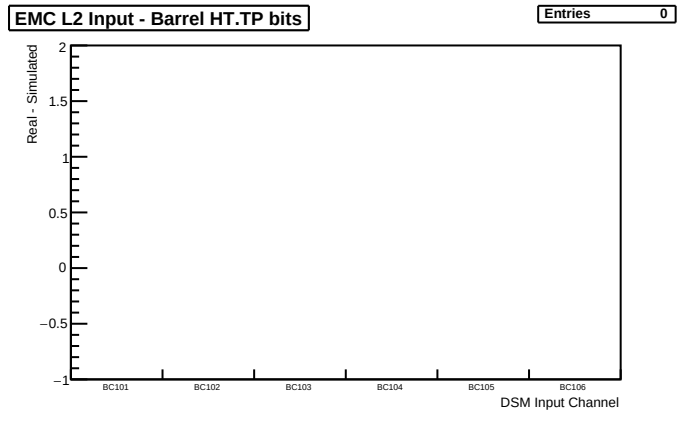

EMC L2 Input - Barrel HT bits
Entries 2.328307e+07
Barrel HT bits
DSM Input Channel

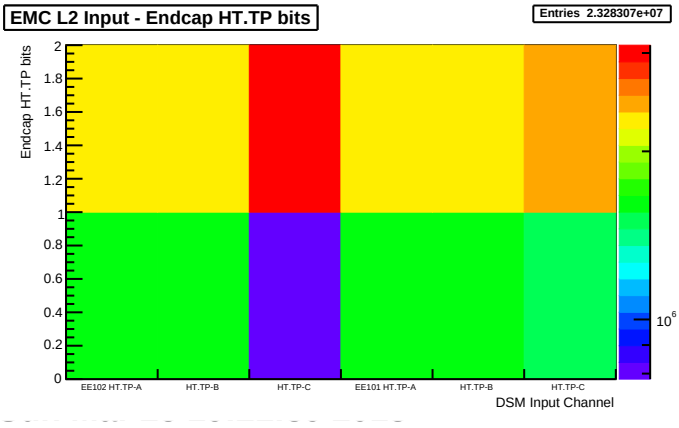

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel

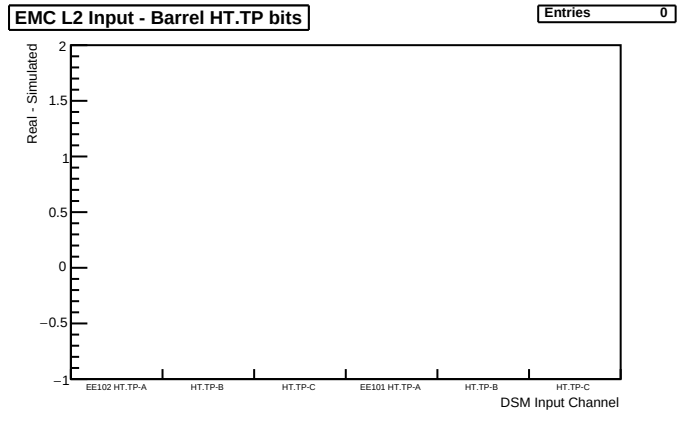

EMC L2 Input - Endcap HT bits
Entries 1.552204e+07
Endcap HT bits
DSM Input Channel

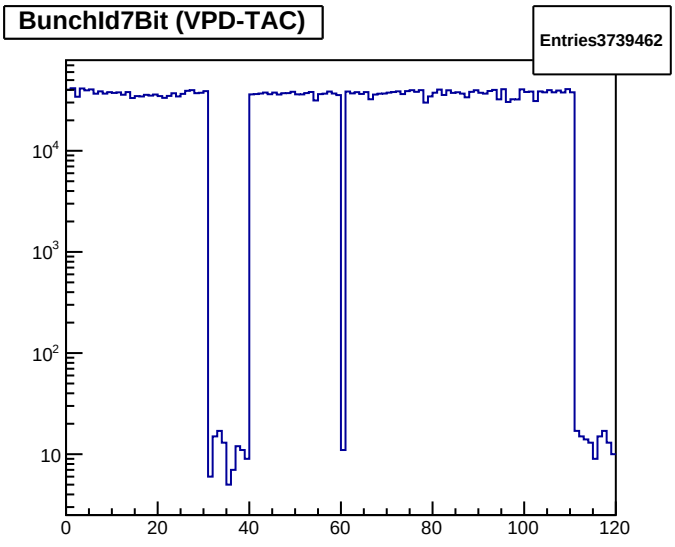

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel

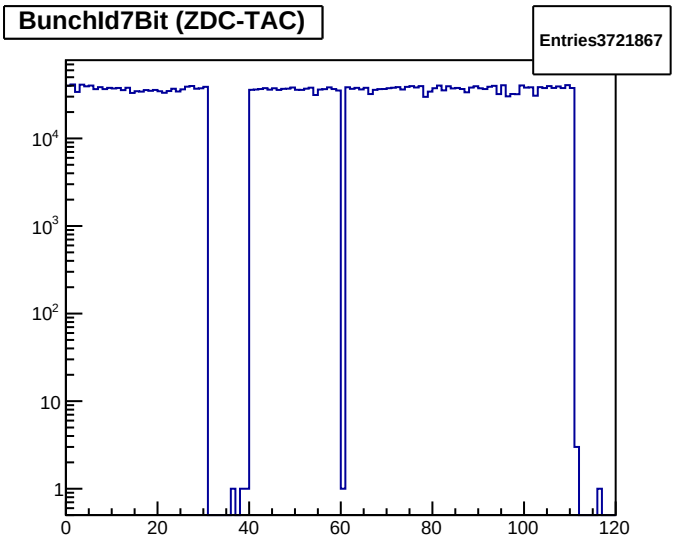

EMC L2 Input - Barrel HT.TP bits
Entries 2.328307e+07
Barrel HT.TP bits
DSM Input Channel

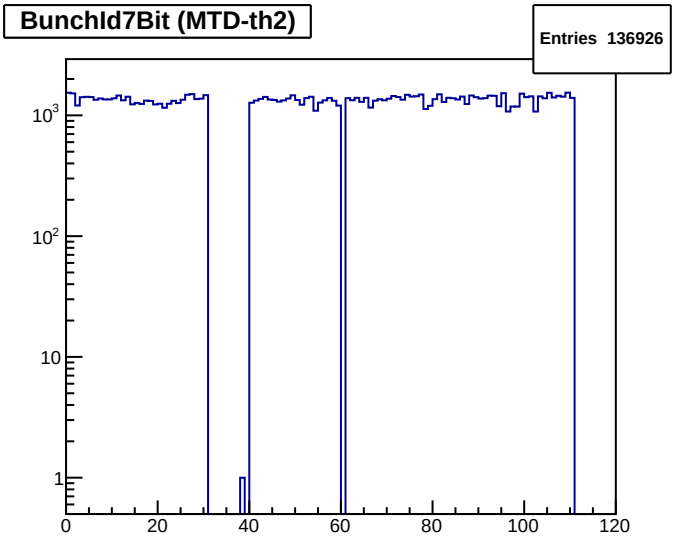

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel

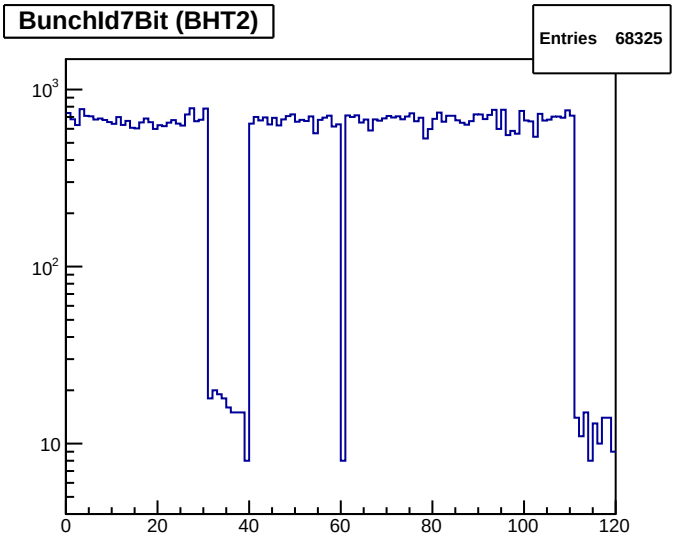

EMC L2 Input - Endcap HT.TP bits
Entries 2.328307e+07
Endcap HT.TP bits
DSM Input Channel

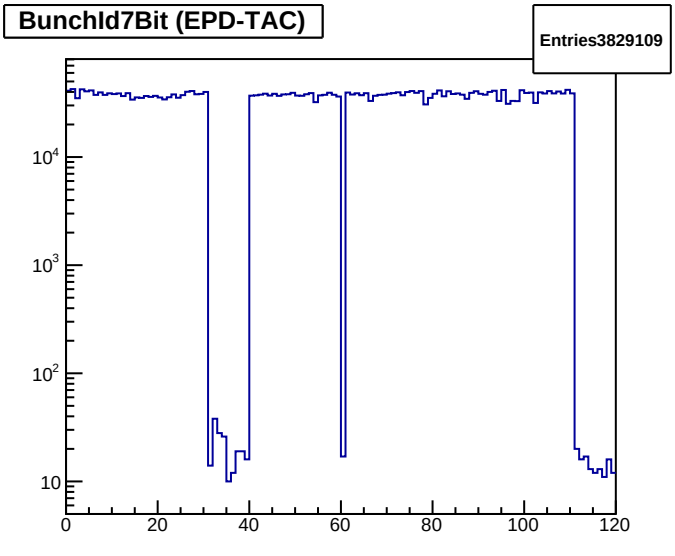

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel

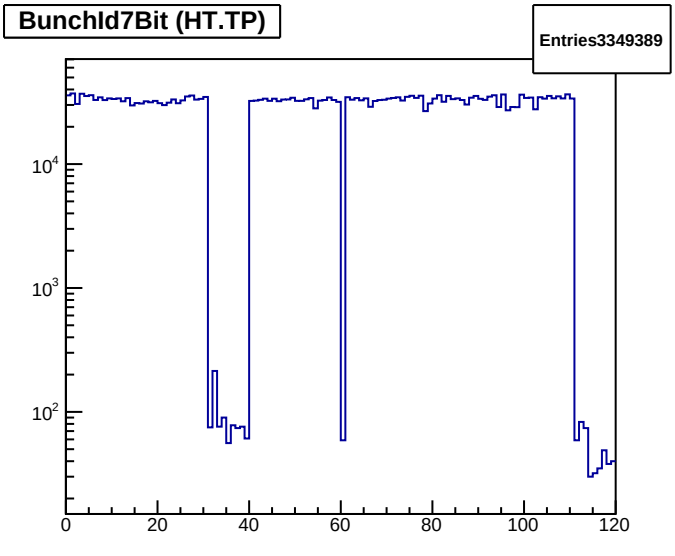

BunchId7Bit (VPD-TAC)
Entries3739462

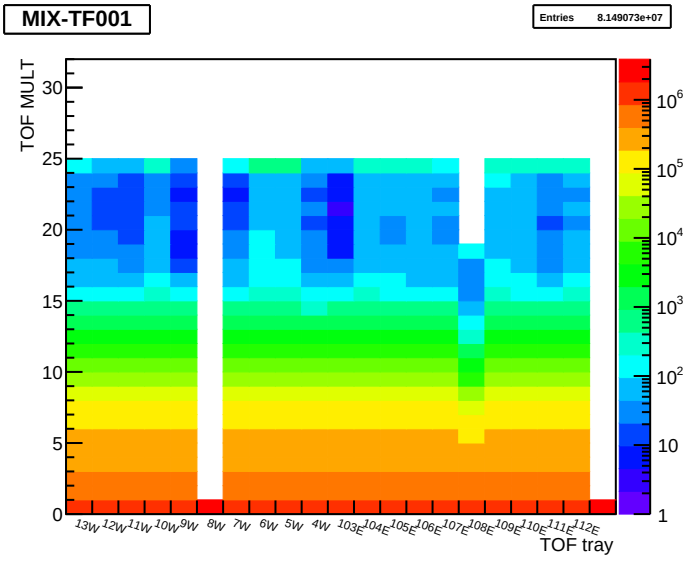

BunchId7Bit (ZDC-TAC)
Entries3721867

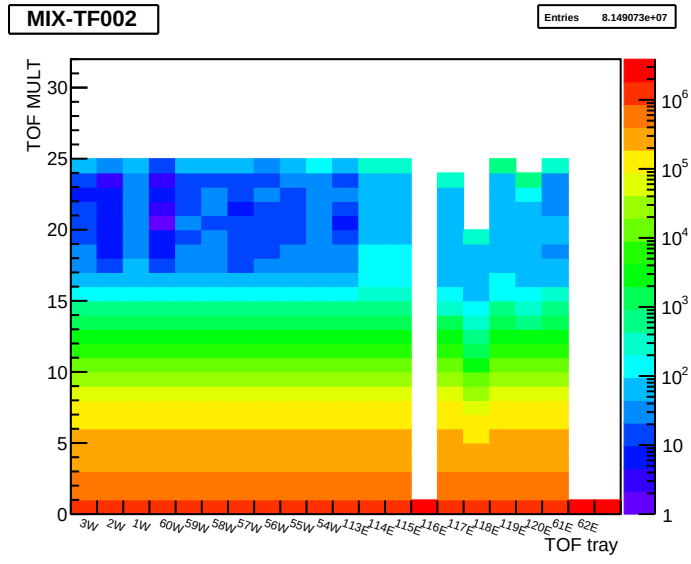

BunchId7Bit (MTD-th2)
Entries 136926

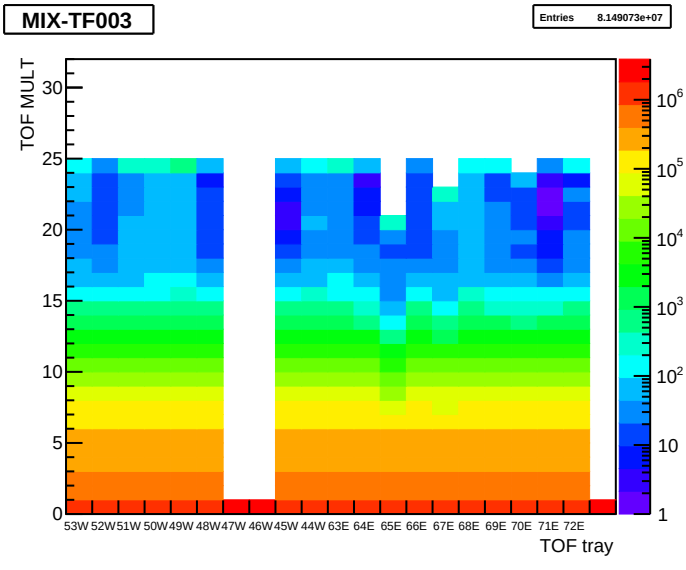

BunchId7Bit (BHT2)
Entries 68325

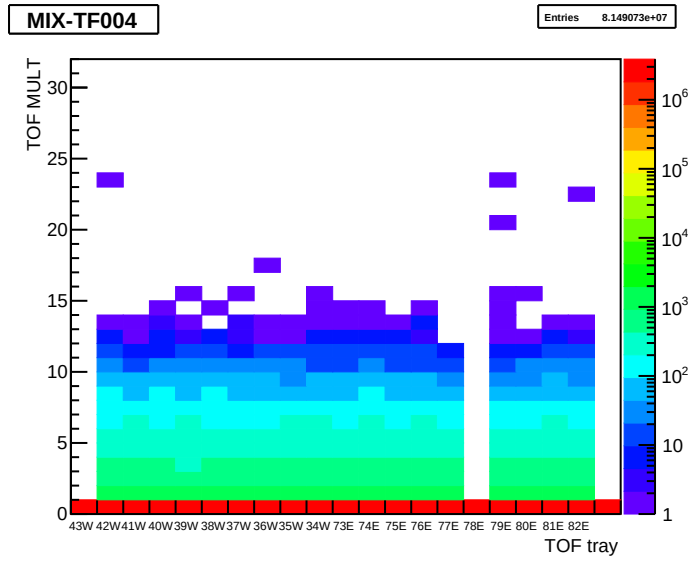

BunchId7Bit (EPD-TAC)
Entries3829109

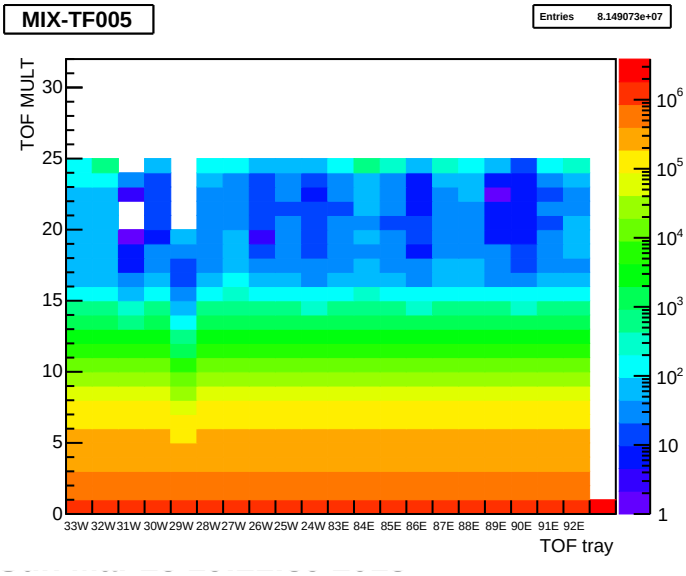

BunchId7Bit (HT.TP)
Entries3349389

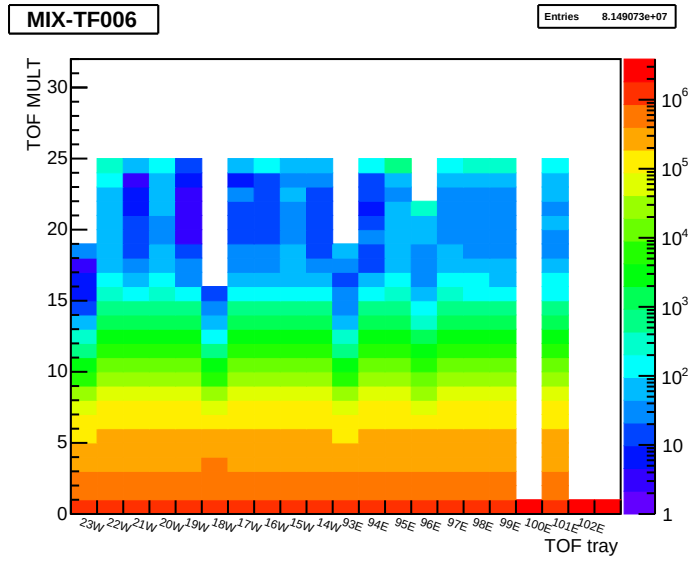

MIX-TF001
Entries 8.149073e+07
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

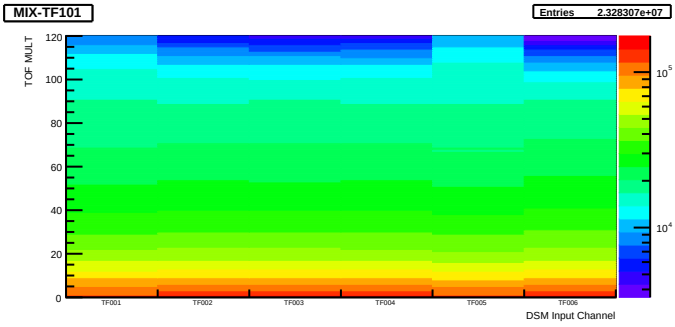

MIX-TF002
Entries 8.149073e+07
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

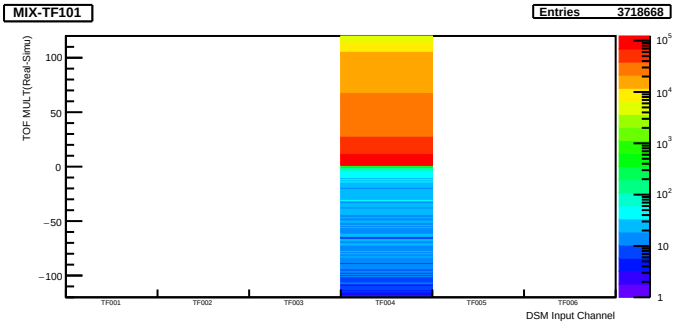

MIX-TF003
Entries 8.149073e+07
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

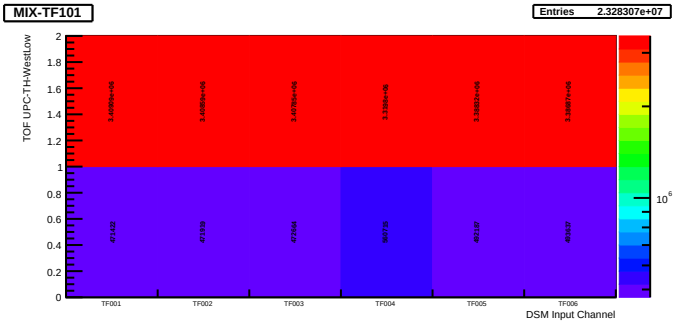

MIX-TF004
Entries 8.149073e+07
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

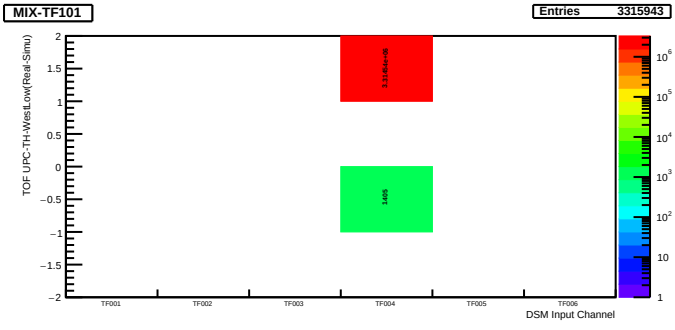

MIX-TF005
Entries 8.149073e+07
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

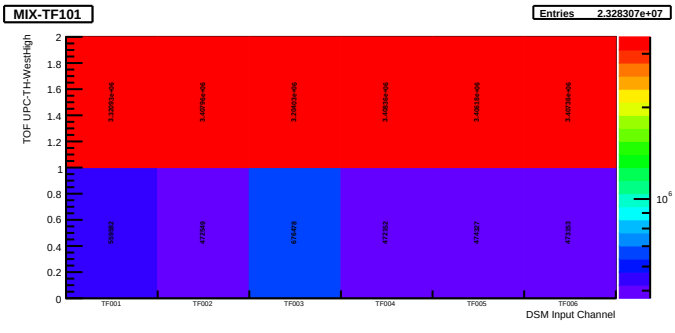

MIX-TF006
Entries 8.149073e+07
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

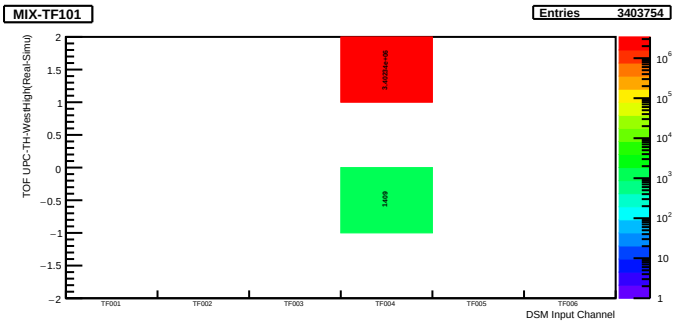

MIX-TF101
Entries 2.328307e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

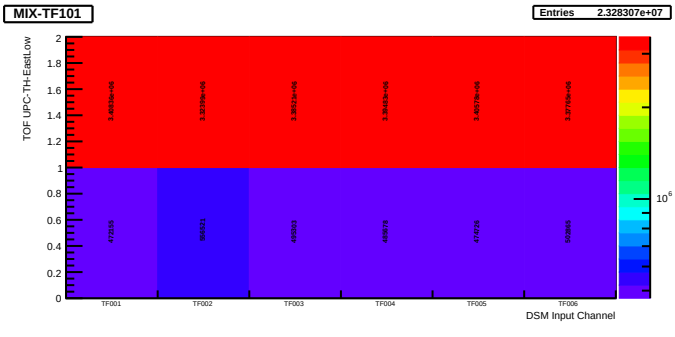

MIX-TF101
Entries 3718668
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

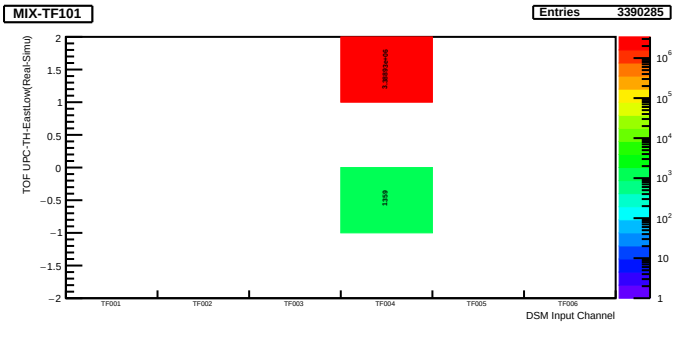

MIX-TF101
Entries 2.328307e+07
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

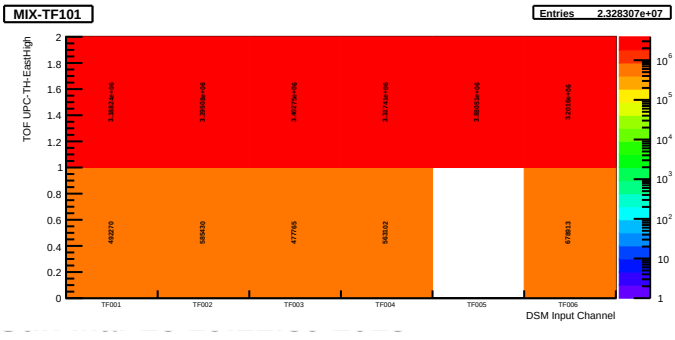

MIX-TF101
Entries 3315943
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

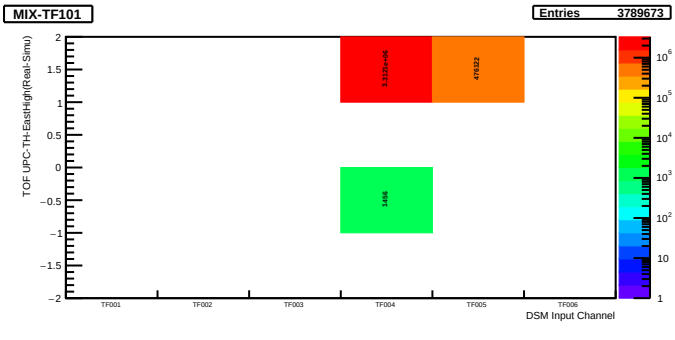

MIX-TF101
Entries 2.328307e+07
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

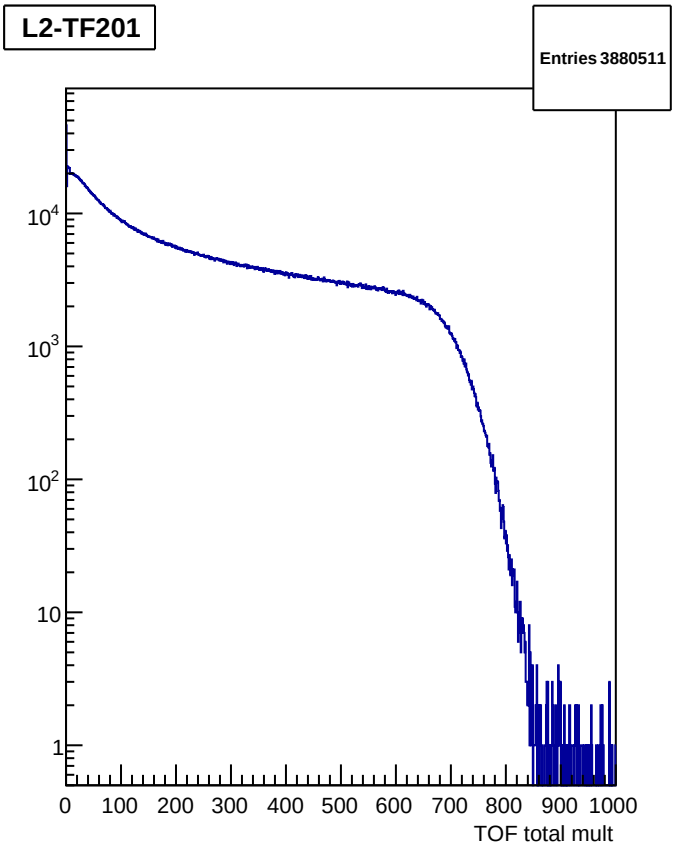

MIX-TF101
Entries 3403754
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

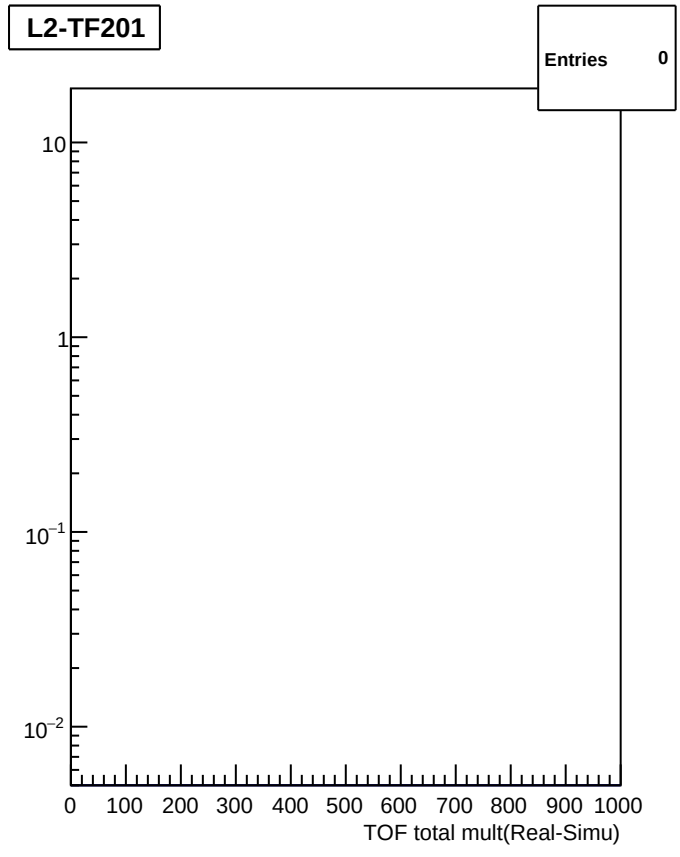

MIX-TF101
Entries 2.328307e+07
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

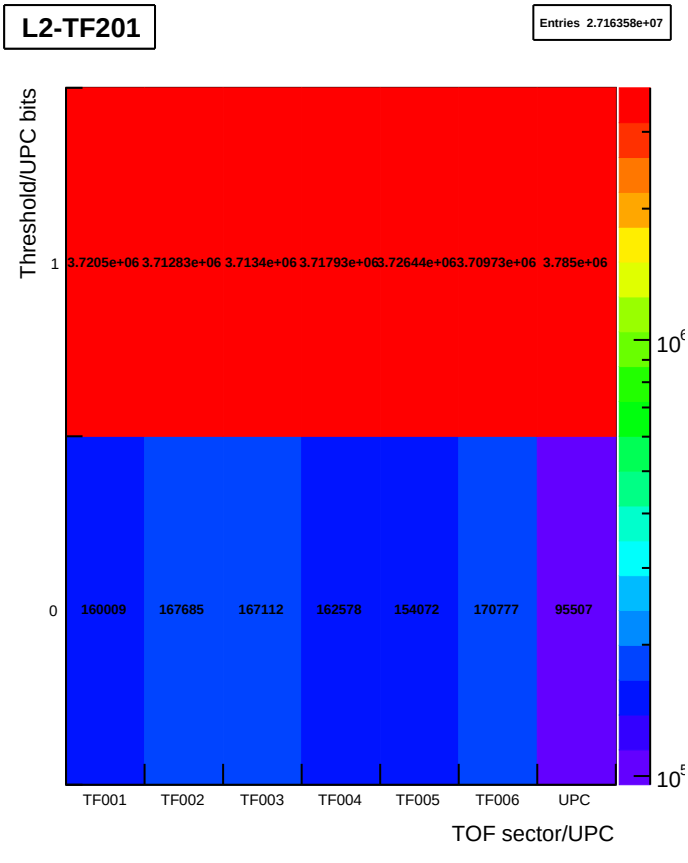

MIX-TF101
Entries 3390285
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

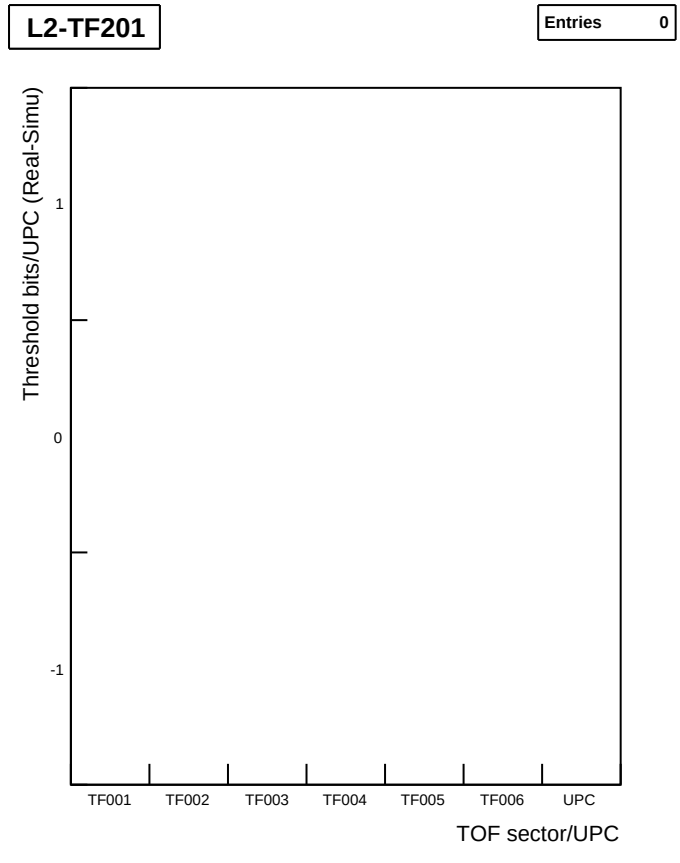

MIX-TF101
Entries 2.328307e+07
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

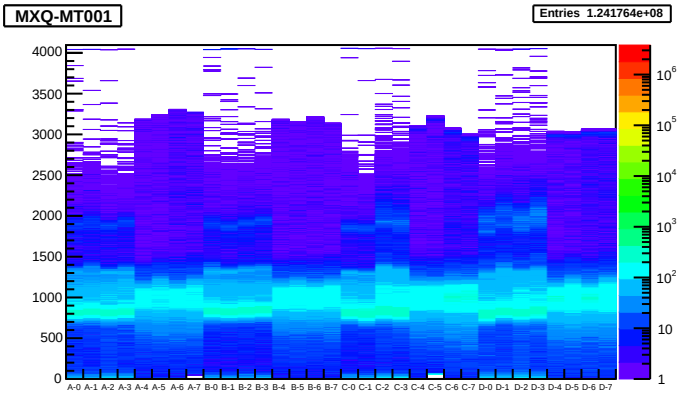

MIX-TF101
Entries 3789673
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

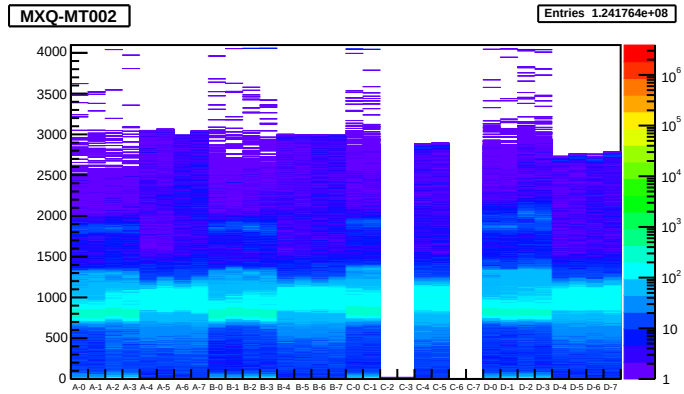

L2-TF201
Entries 3880511
TOF total mult

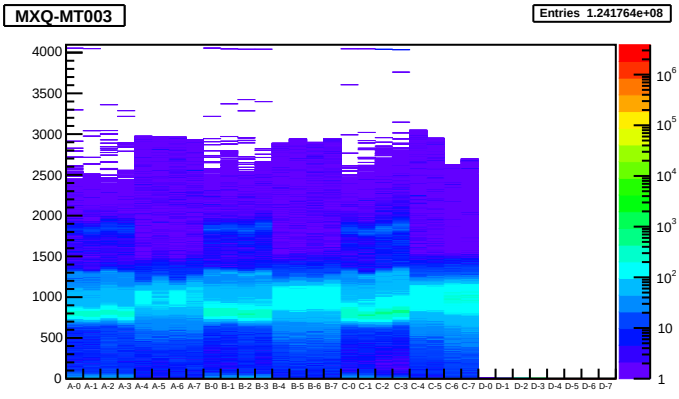

L2-TF201
Entries 0
TOF total mult(Real-Simu)

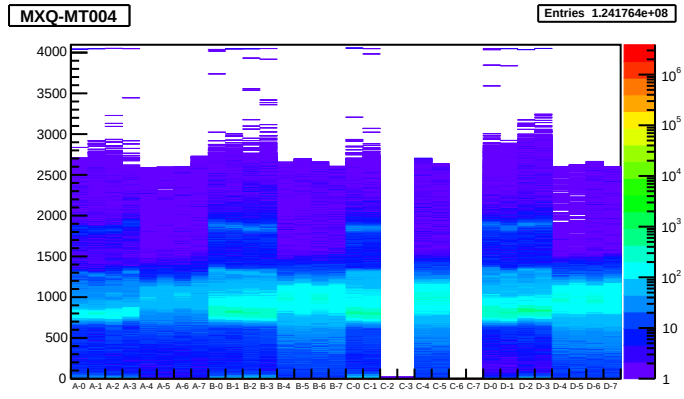

L2-TF201
Entries 2.716358e+07
Threshold/UPC bits
3.7205e+06
3.71283e+06
3.7134e+06
3.71793e+06
3.72644e+06
3.70973e+06
3.785e+06
160009
167685
167112
162578
154072
170777
95507
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

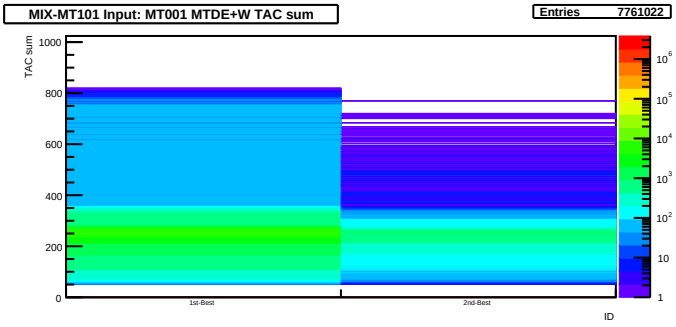

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

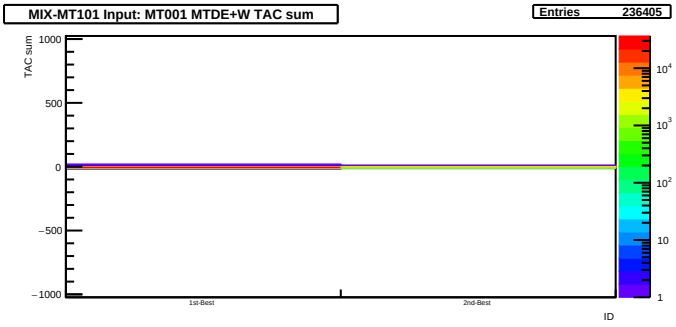

MXQ-MT001
Entries 1.241764e+08
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1

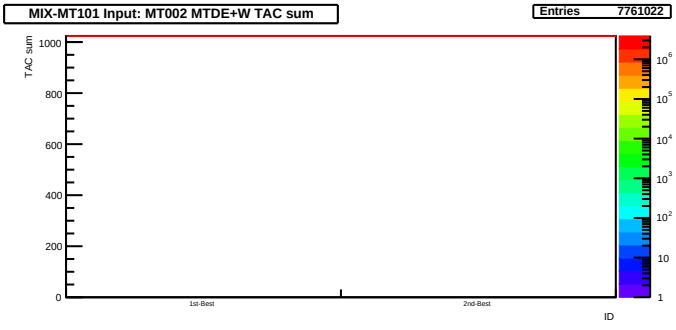

MXQ-MT002
Entries 1.241764e+08
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1

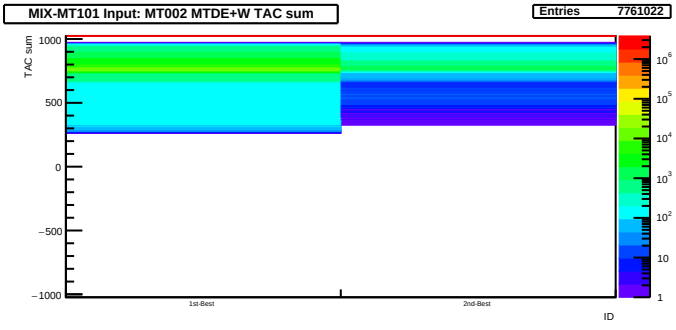

MXQ-MT003
Entries 1.241764e+08
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1

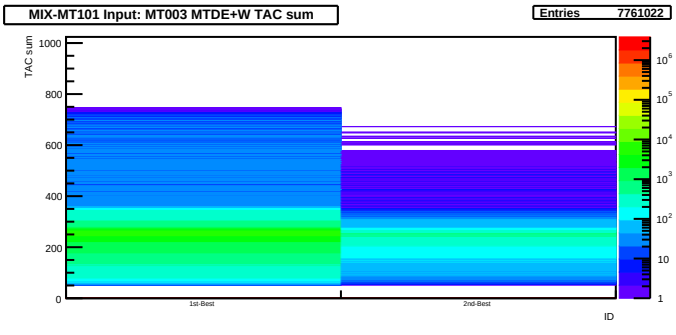

MXQ-MT004
Entries 1.241764e+08
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1

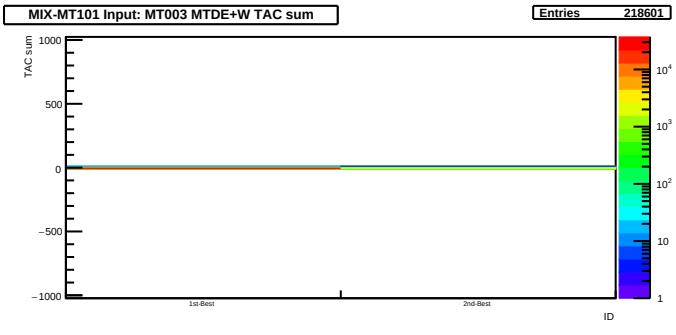

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 7761022
TAC sum
1st-Best
2nd-Best
ID

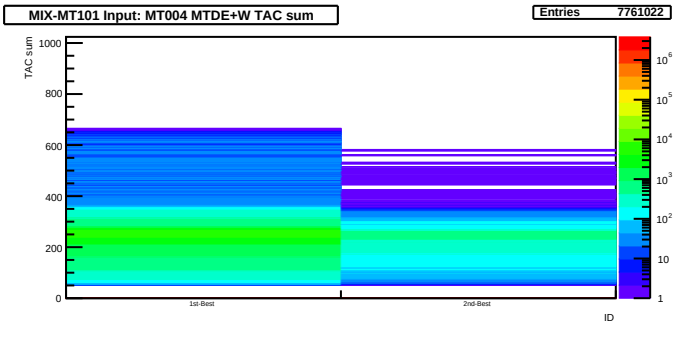

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 236405
TAC sum
1st-Best
2nd-Best
ID

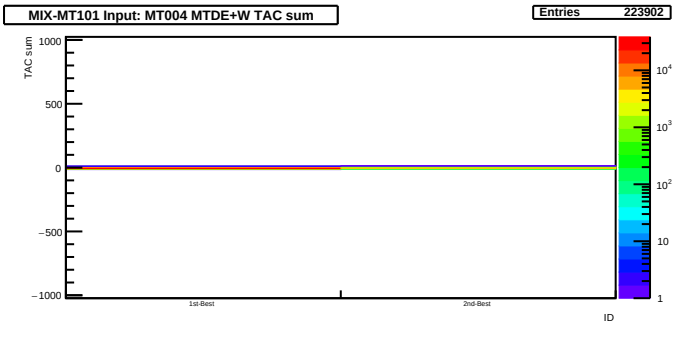

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 7761022
TAC sum
1st-Best
2nd-Best
ID

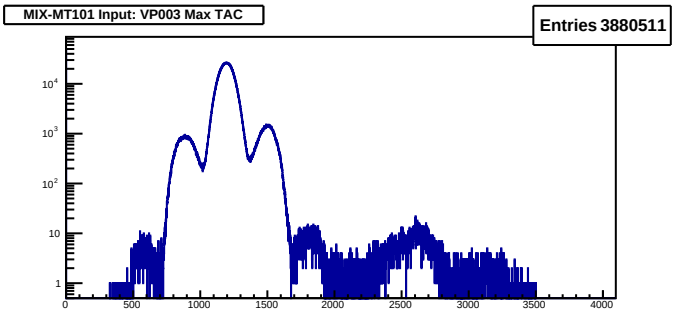

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 7761022
TAC sum
1st-Best
2nd-Best
ID

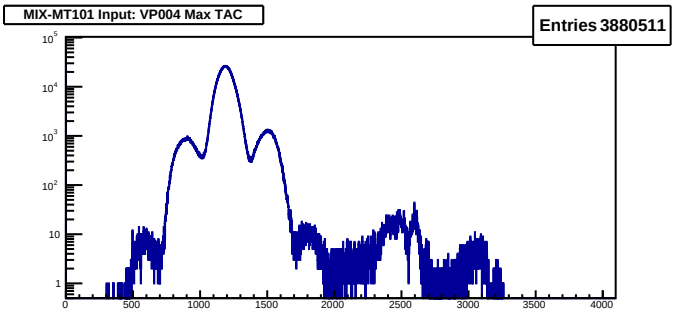

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 7761022
TAC sum
1st-Best
2nd-Best
ID

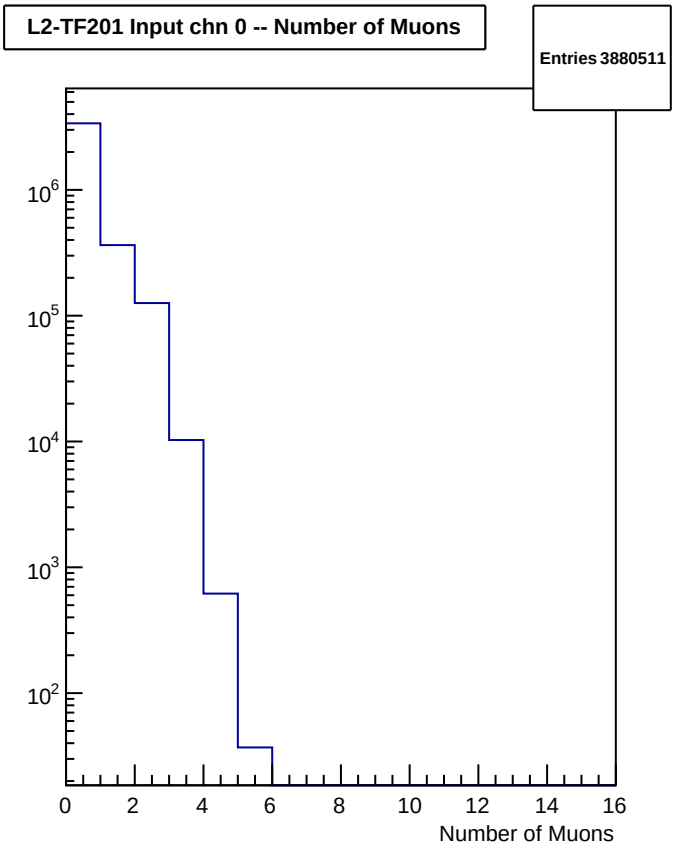

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 218601
TAC sum
1st-Best
2nd-Best
ID

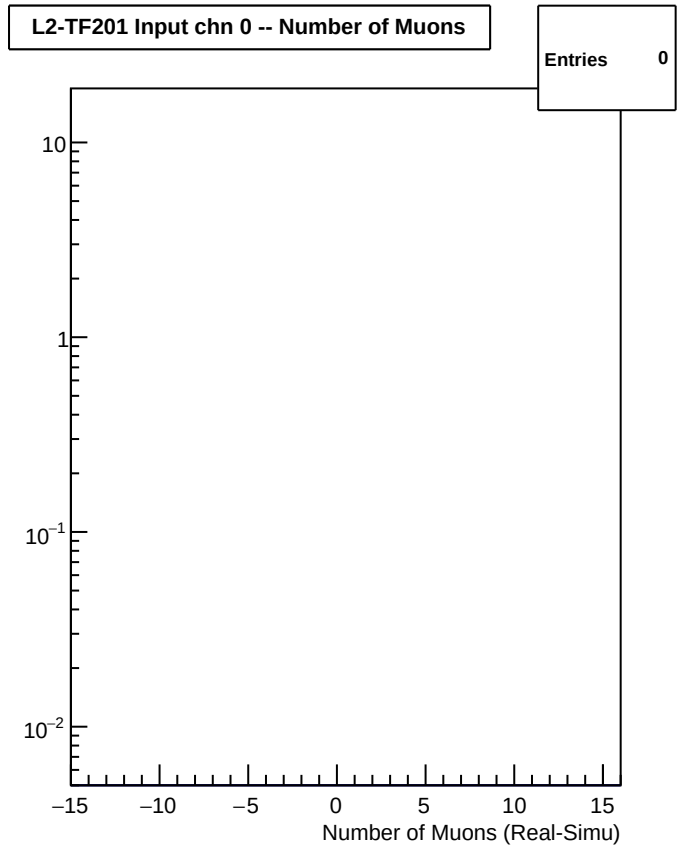

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 7761022
TAC sum
1st-Best
2nd-Best
ID

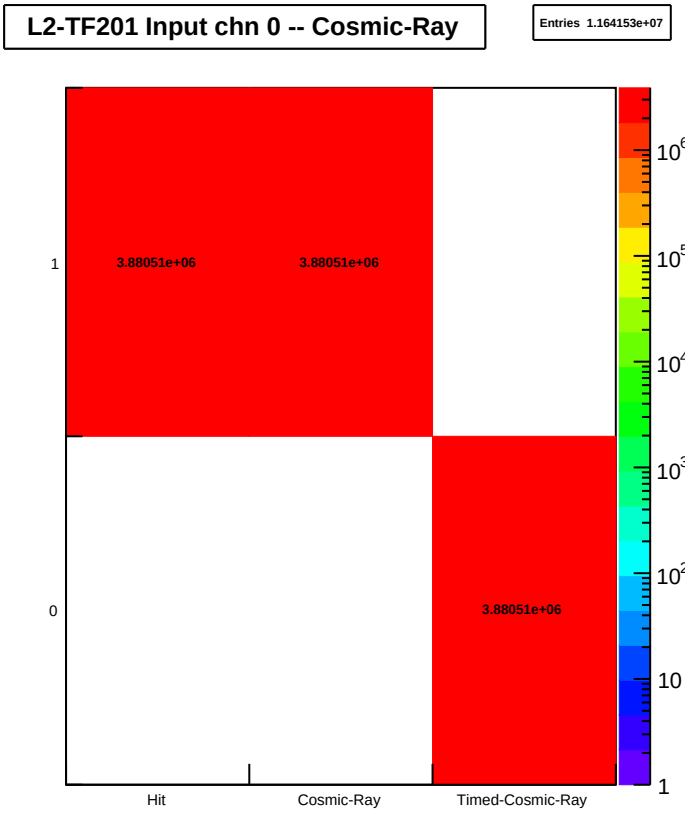

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 223902
TAC sum
1st-Best
2nd-Best
ID

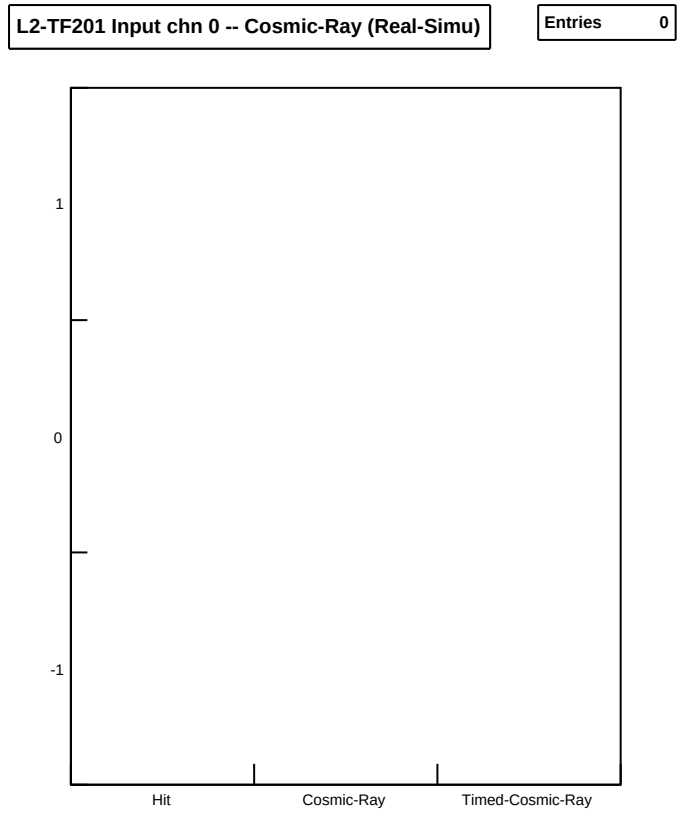

MIX-MT101 Input: VP003 Max TAC
Entries 3880511

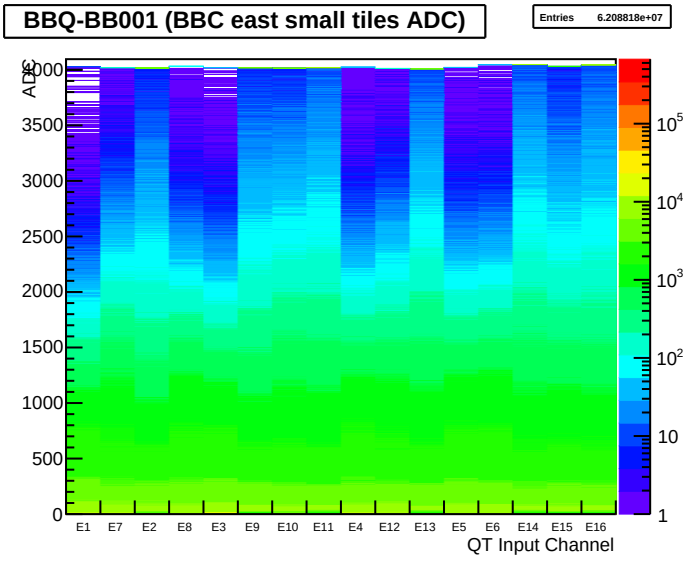

MIX-MT101 Input: VP004 Max TAC
Entries 3880511

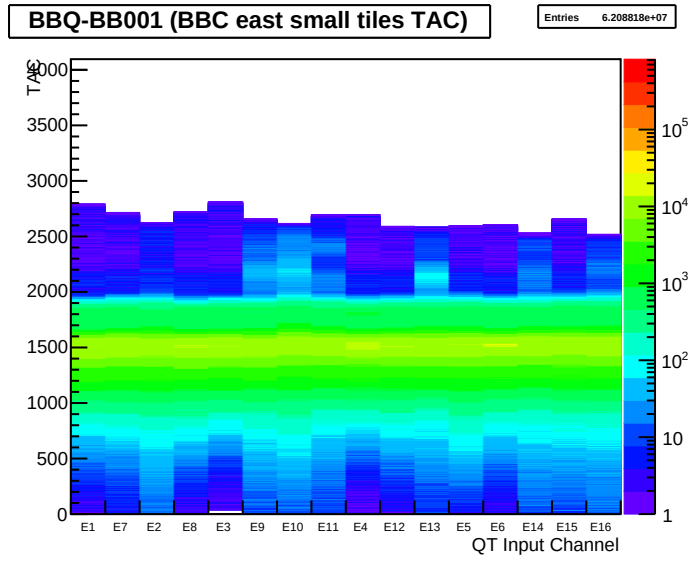

L2-TF201 Input chn 0 -- Number of Muons
Entries 3880511
Number of Muons

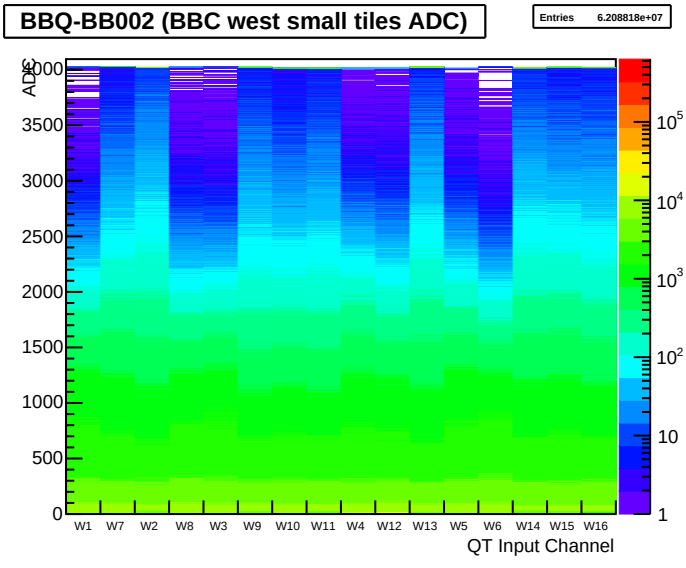

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

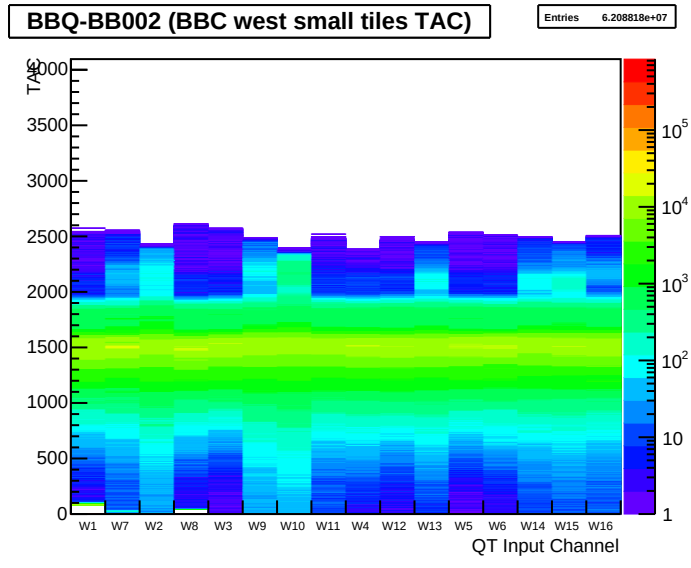

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 1.164153e+07
3.88051e+06
3.88051e+06
3.88051e+06
Hit
Cosmic-Ray
Timed-Cosmic-Ray

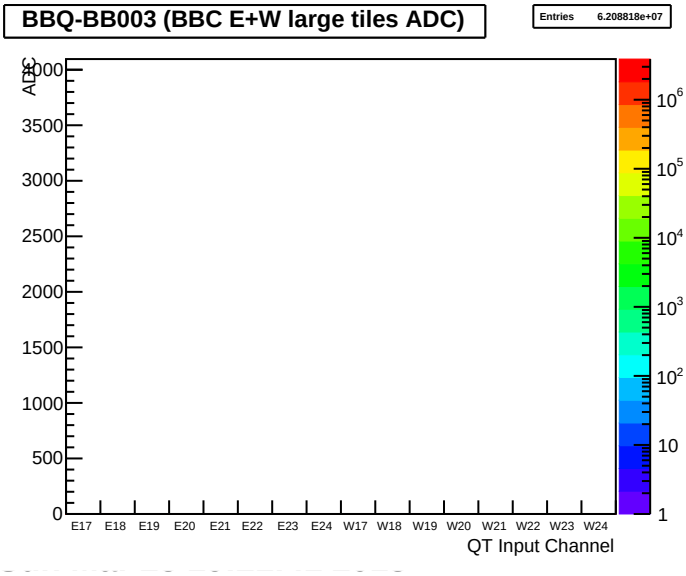

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

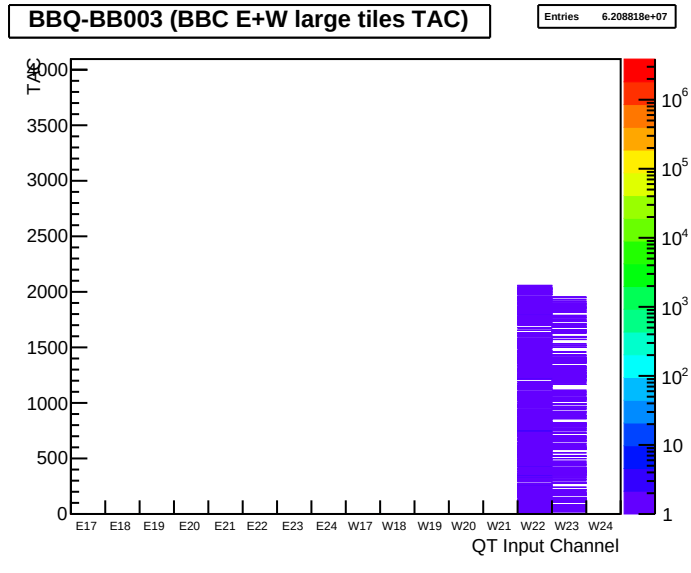

BBQ-BB001 (BBC east small tiles ADC)
Entries 6.208818e+07
ADC
QT Input Channel

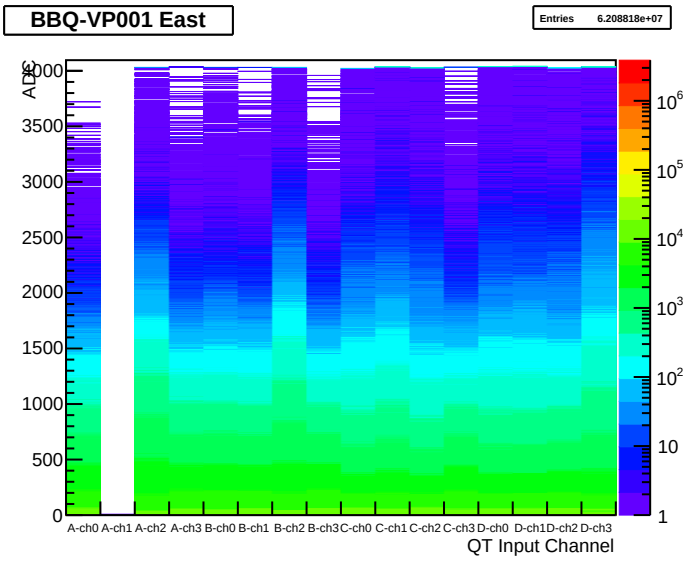

BBQ-BB001 (BBC east small tiles TAC)
Entries 6.208818e+07
TAC
QT Input Channel

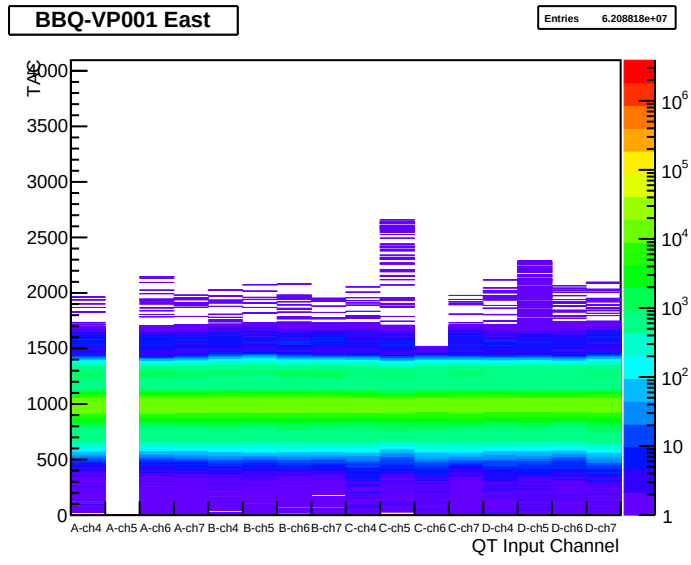

BBQ-BB002 (BBC west small tiles ADC)
Entries 6.208818e+07
ADC
QT Input Channel

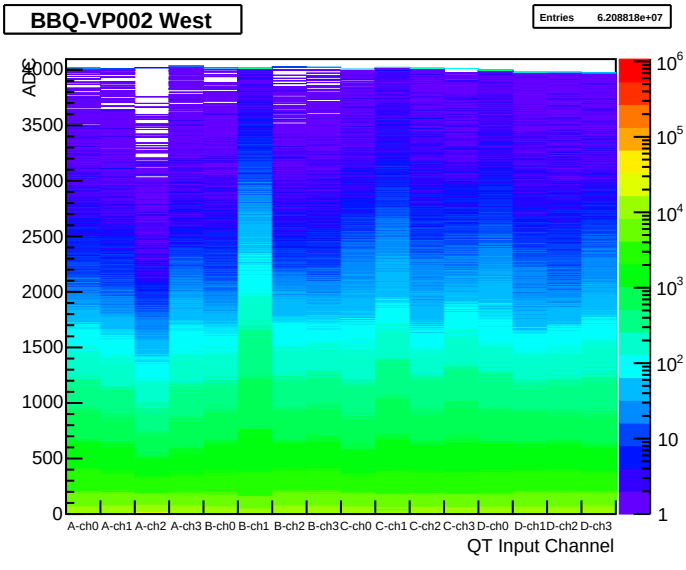

BBQ-BB002 (BBC west small tiles TAC)
Entries 6.208818e+07
TAC
QT Input Channel

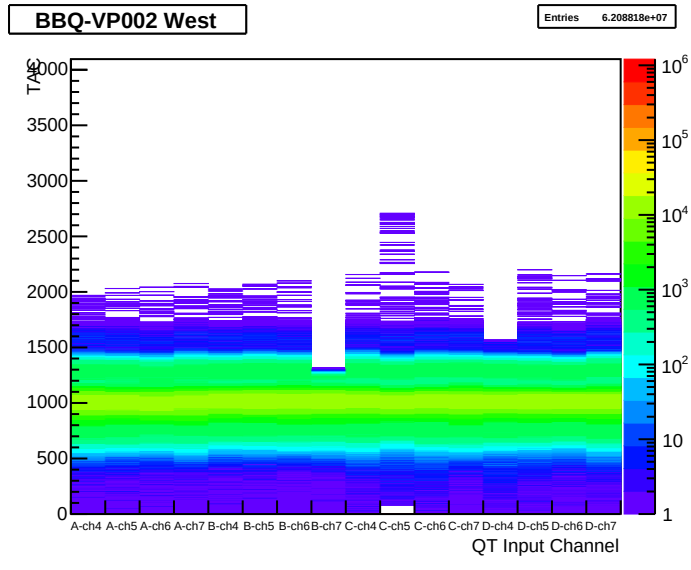

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6.208818e+07
ADC
QT Input Channel

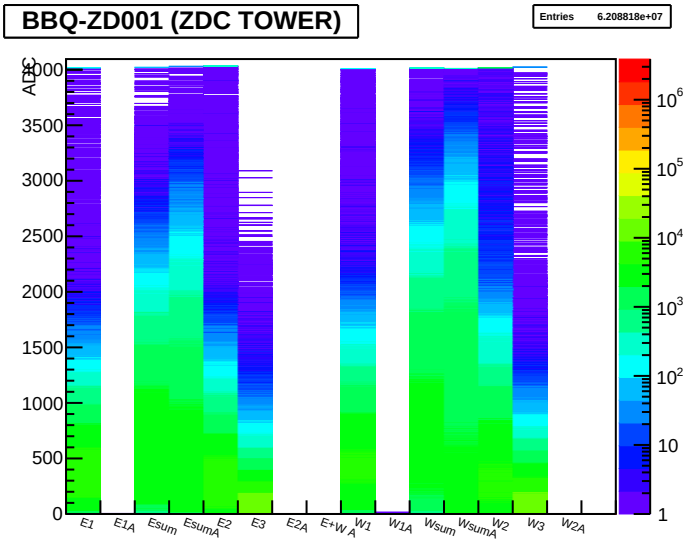

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6.208818e+07
TAC
QT Input Channel

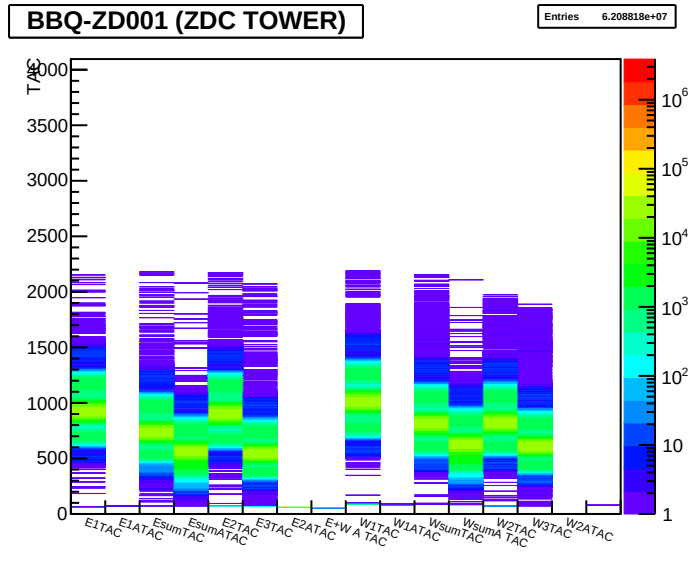

BBQ-VP001 East
Entries 6.208818e+07
ADC
QT Input Channel

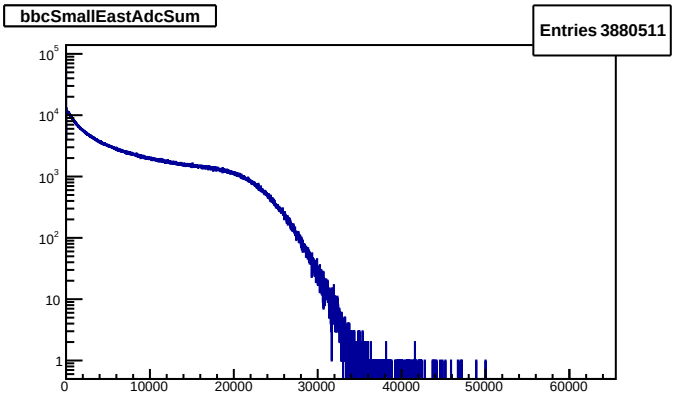

BBQ-VP001 East
Entries 6.208818e+07
TAC
QT Input Channel

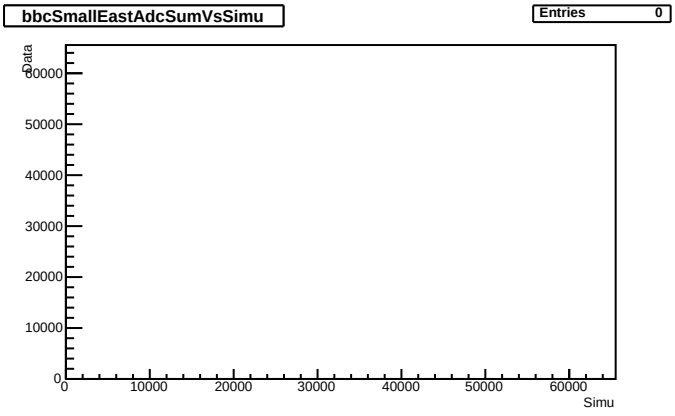

BBQ-VP002 West
Entries 6.208818e+07
ADC
QT Input Channel

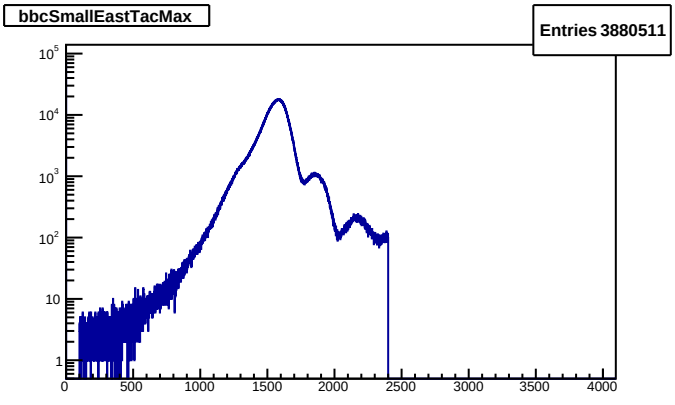

BBQ-VP002 West
Entries 6.208818e+07
TAC
QT Input Channel

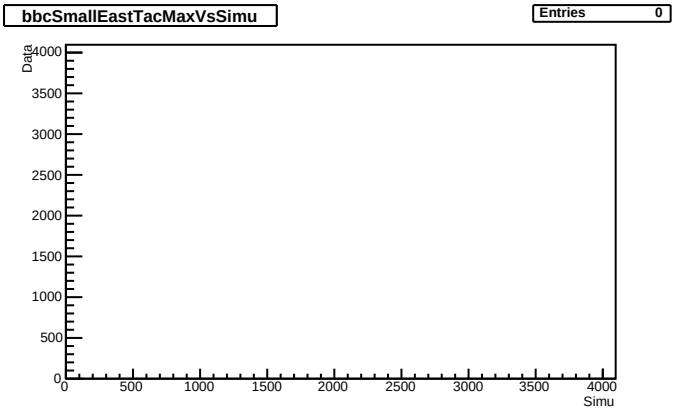

BBQ-ZD001 (ZDC TOWER)
Entries 6.208818e+07
ADC

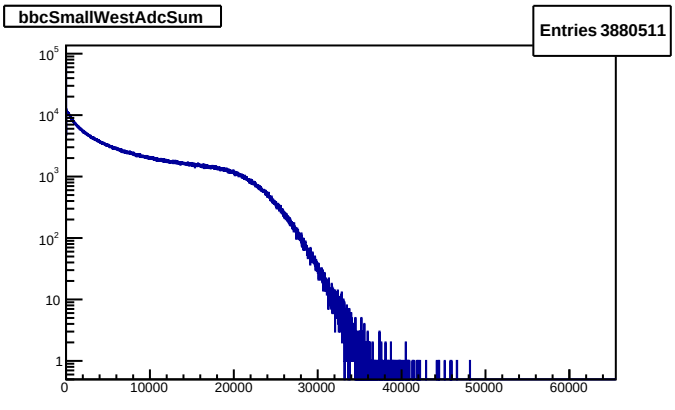

BBQ-ZD001 (ZDC TOWER)
Entries 6.208818e+07
TAC

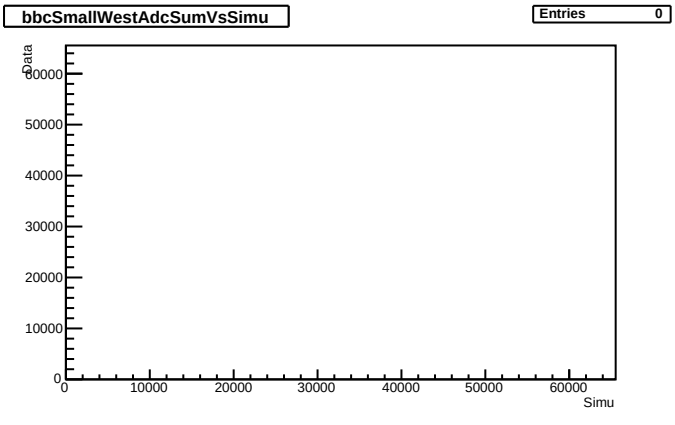

bbcSmallEastAdcSum
Entries 3880511

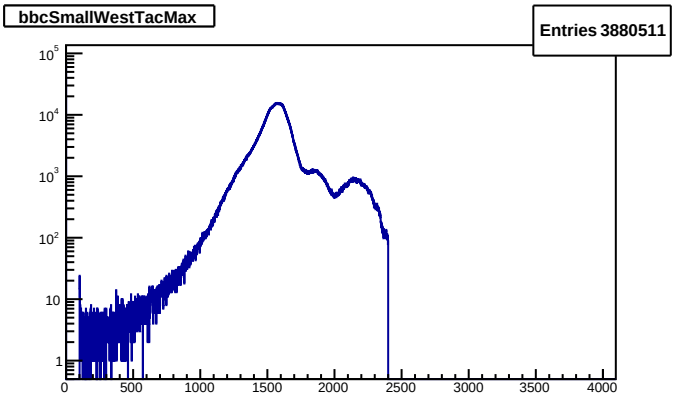

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

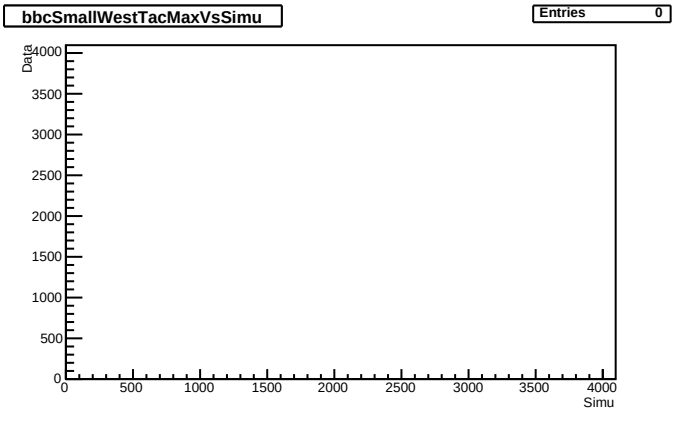

bbcSmallEastTacMax
Entries 3880511

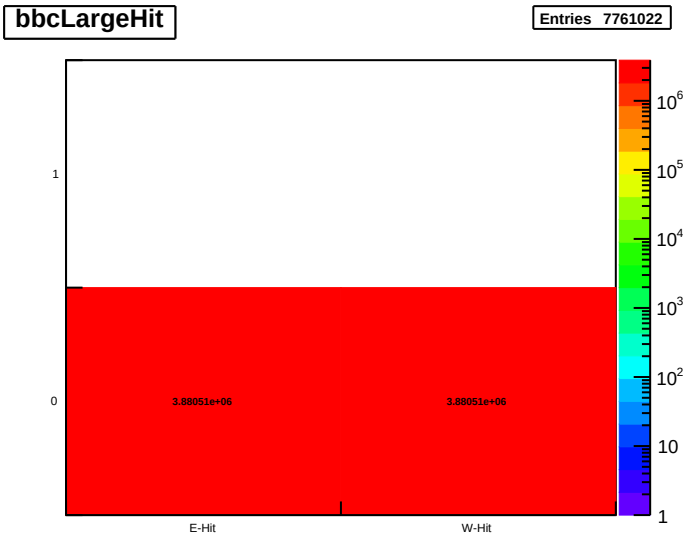

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

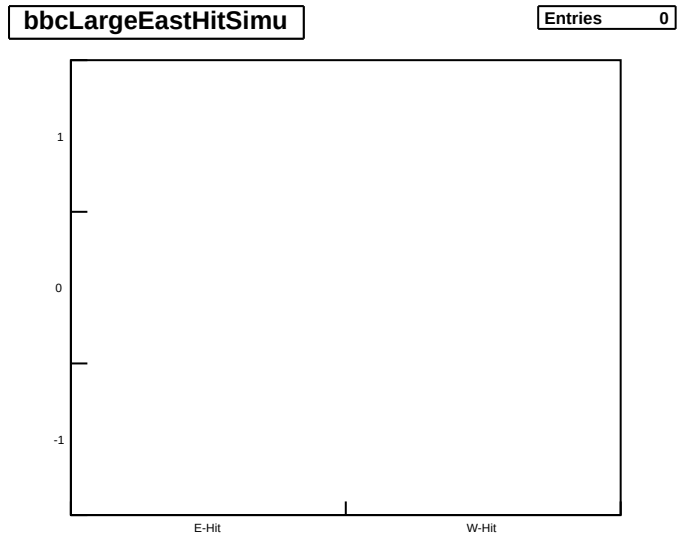

bbcSmallWestAdcSum
Entries 3880511

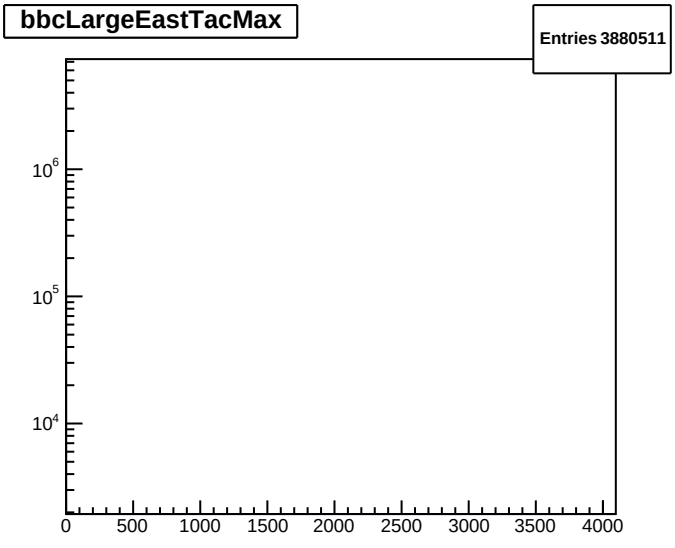

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

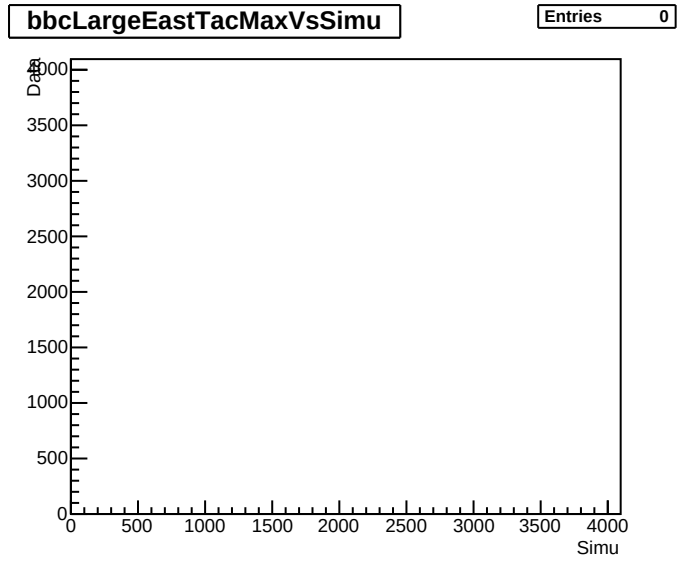

bbcSmallWestTacMax
Entries 3880511

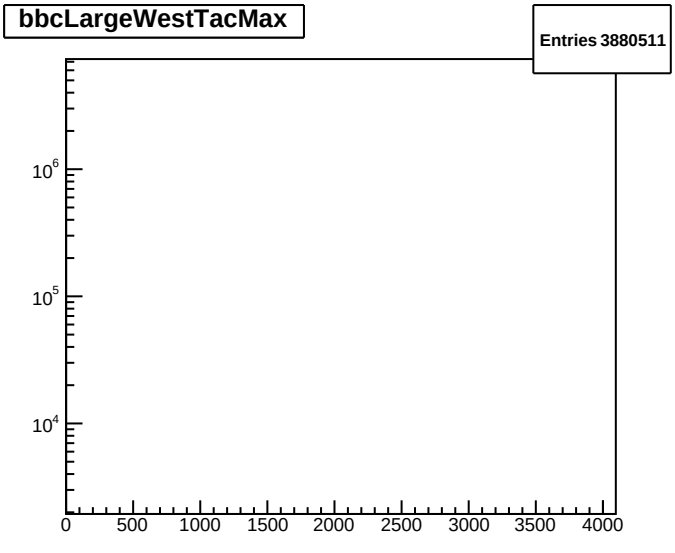

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

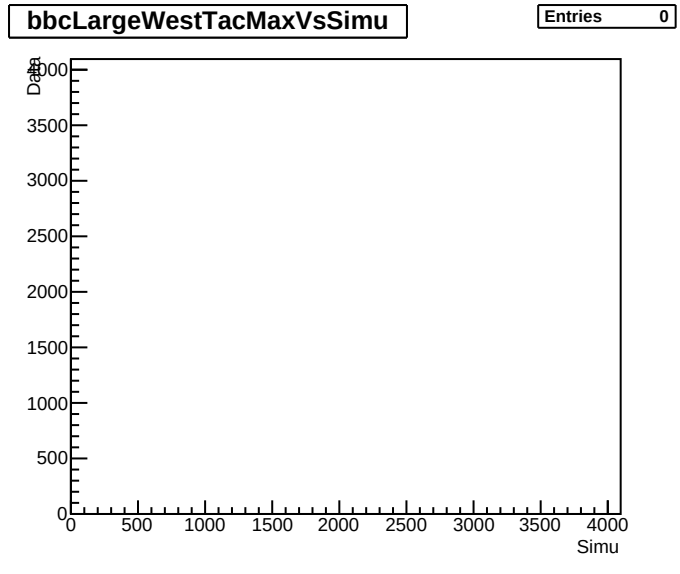

bbcLargeHit
Entries 7761022
3.88051e+06
3.88051e+06
E-Hit
W-Hit

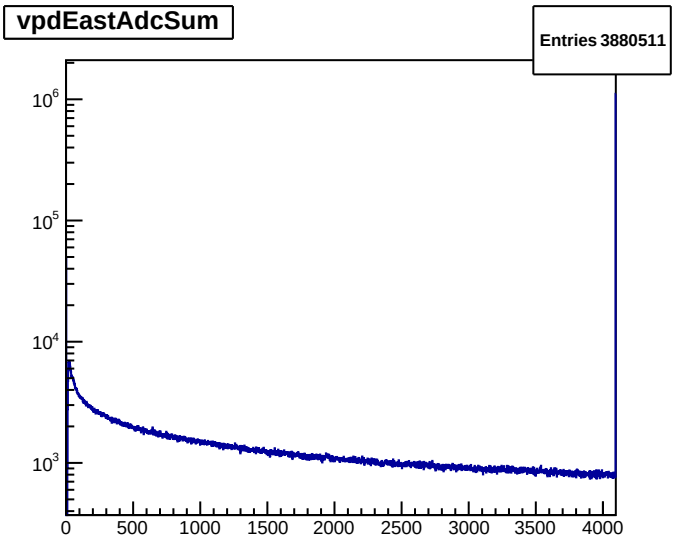

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

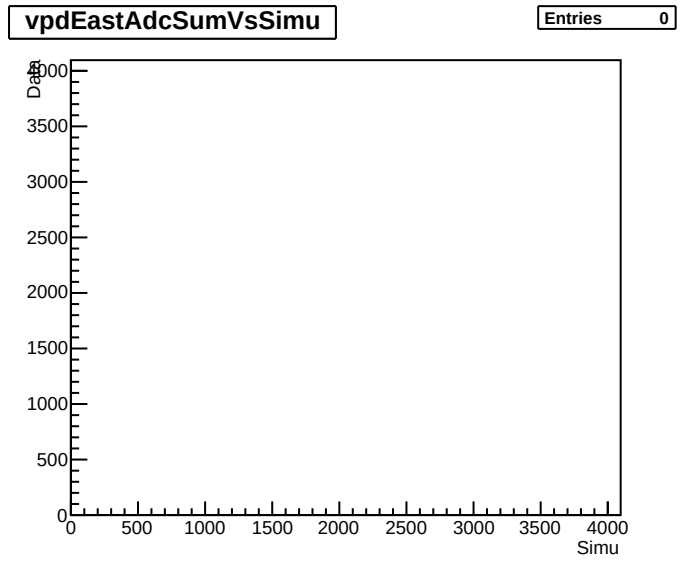

bbcLargeEastTacMax
Entries 3880511

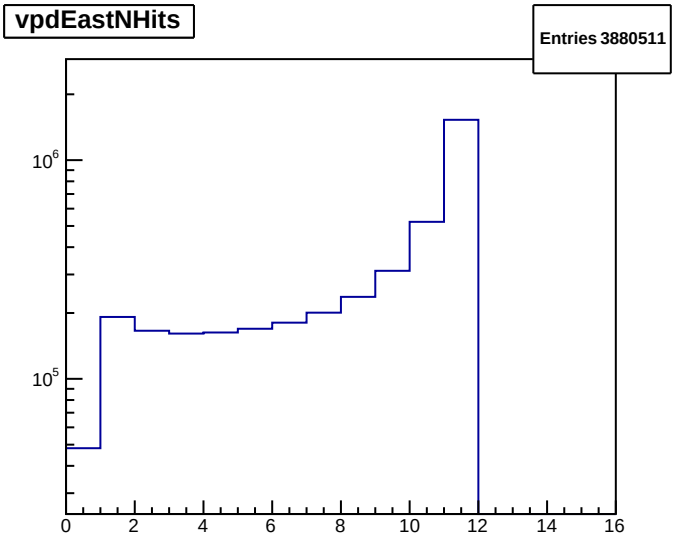

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

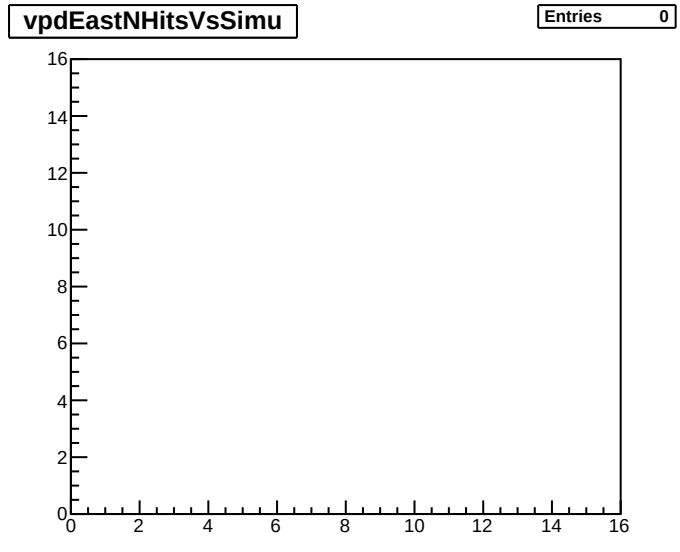

bbcLargeWestTacMax
Entries 3880511

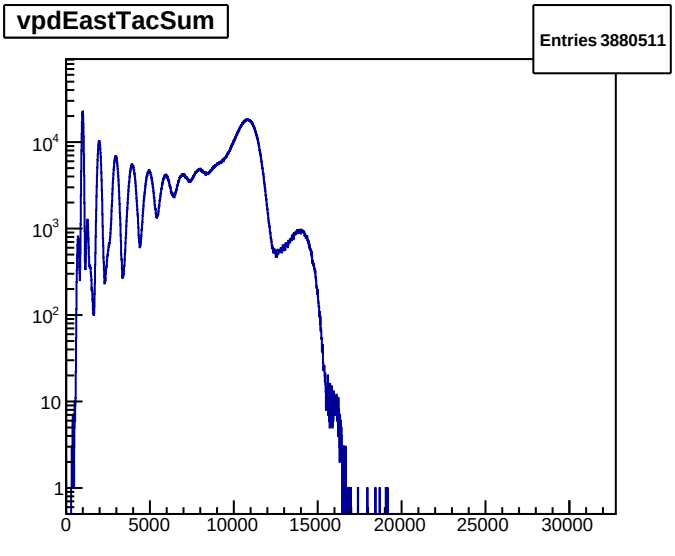

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

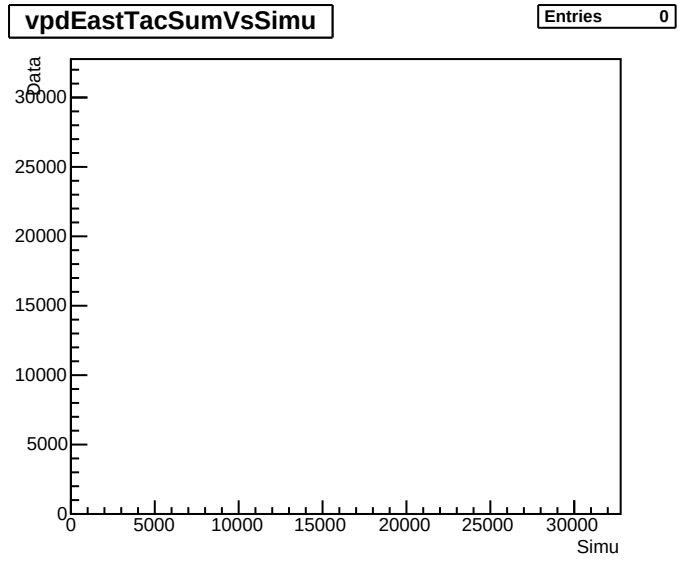

vpdEastAdcSum
Entries 3880511

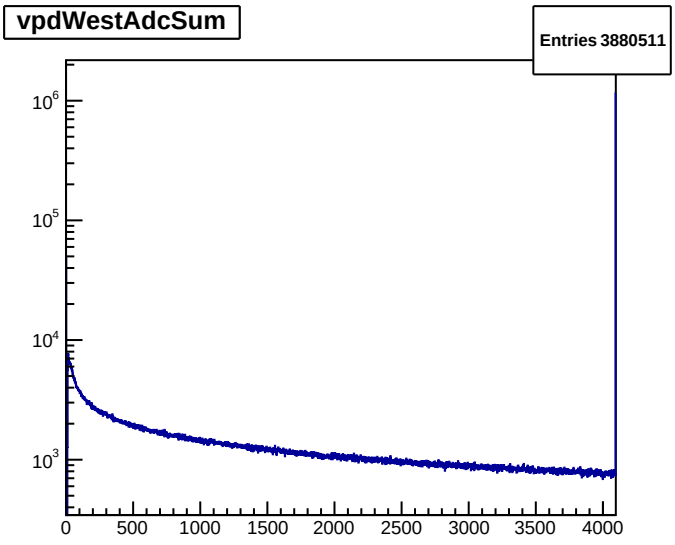

vpdEastAdcSumVsSimu
Entries 0
Data
Simu

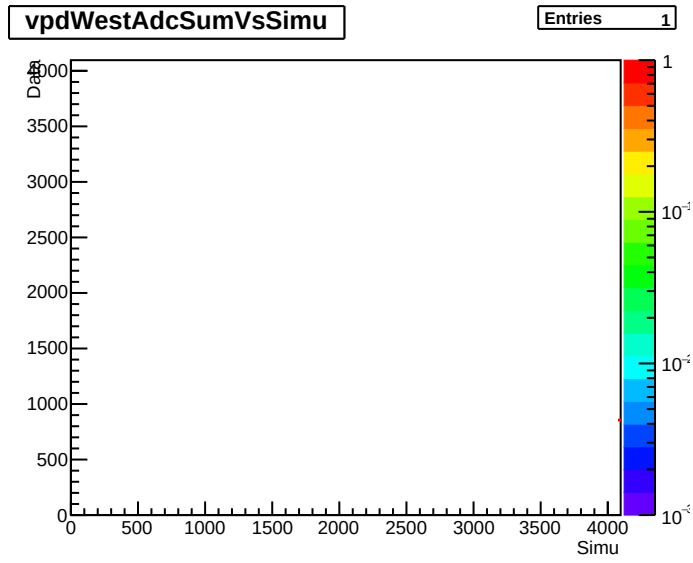

vpdEastNHits
Entries 3880511

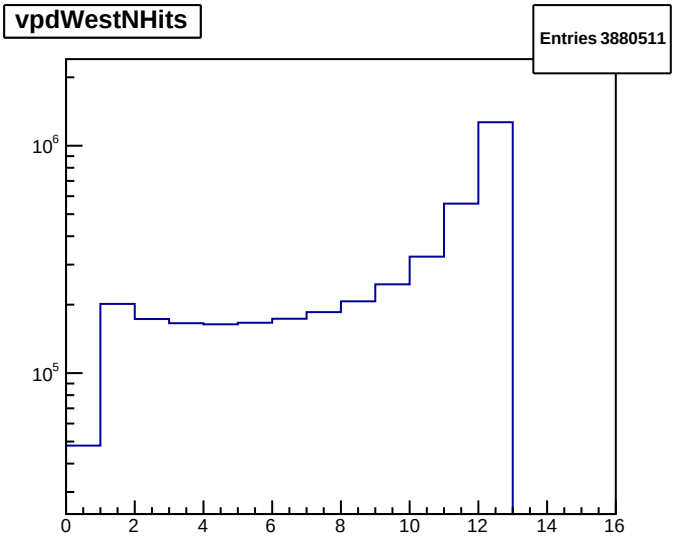

vpdEastNHitsVsSimu
Entries 0

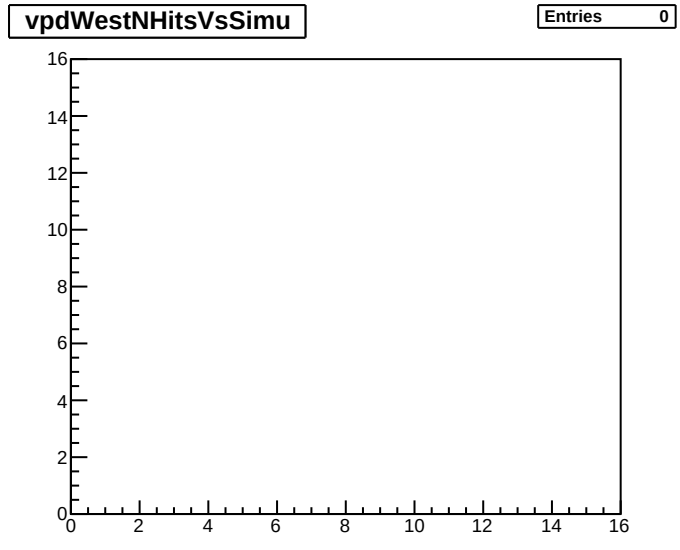

vpdEastTacSum
Entries 3880511

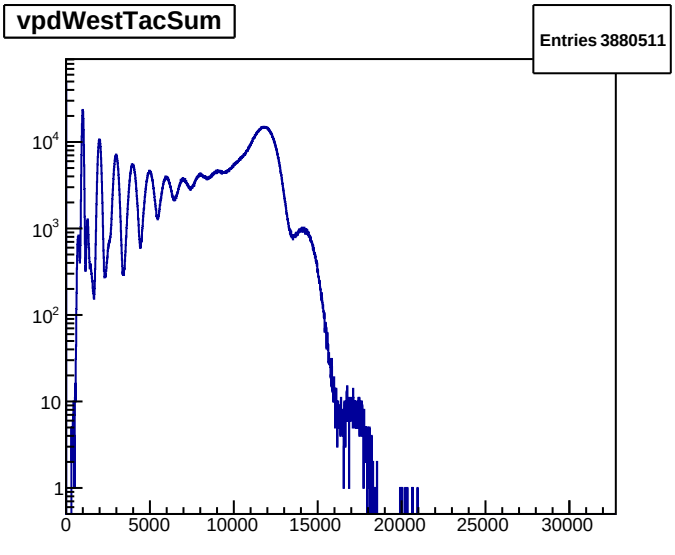

vpdEastTacSumVsSimu
Entries 0
Data
Simu

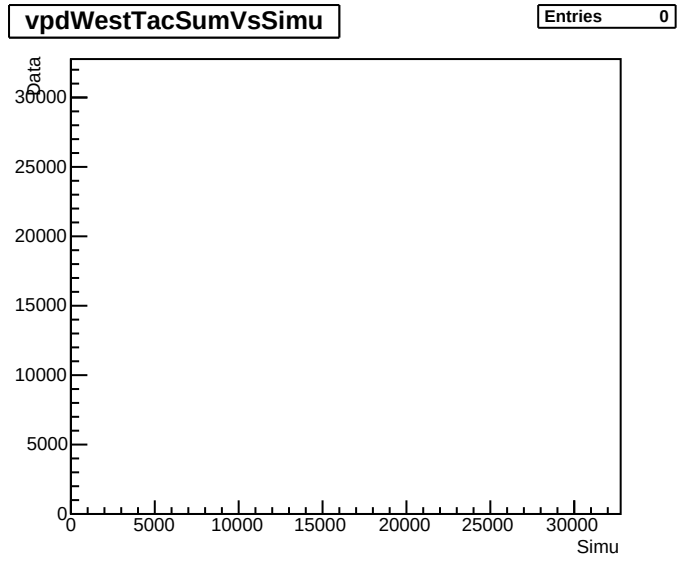

vpdWestAdcSum
Entries 3880511

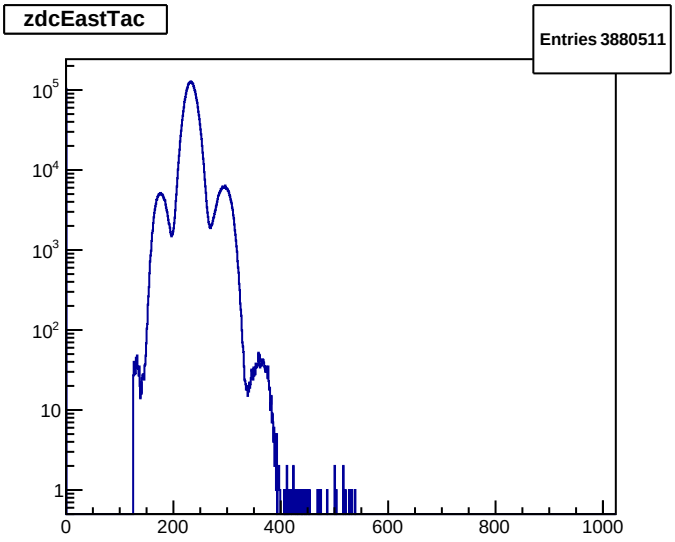

vpdWestAdcSumVsSimu
Entries 1
Data
Simu

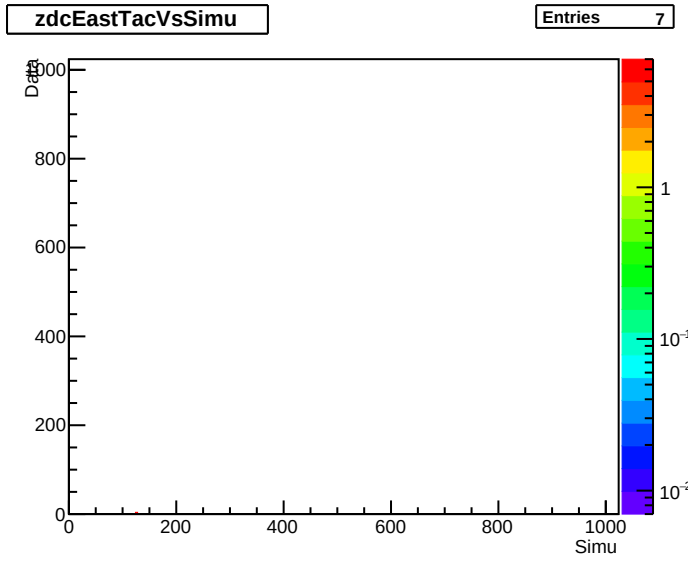

vpdWestNHits
Entries 3880511

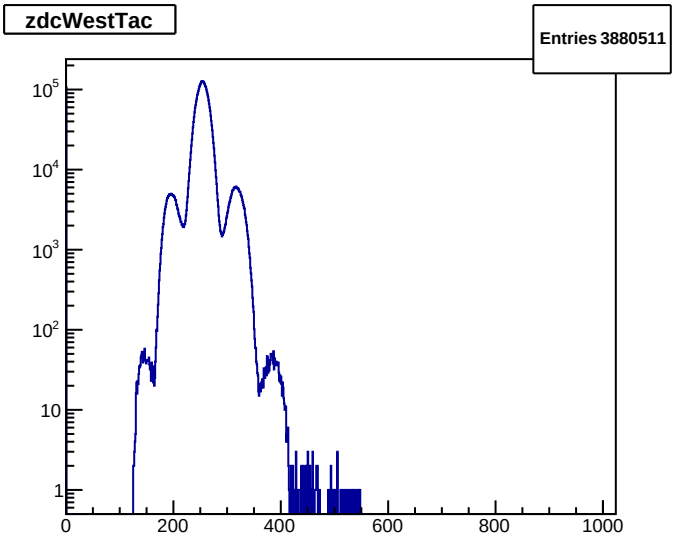

vpdWestNHitsVsSimu
Entries 0

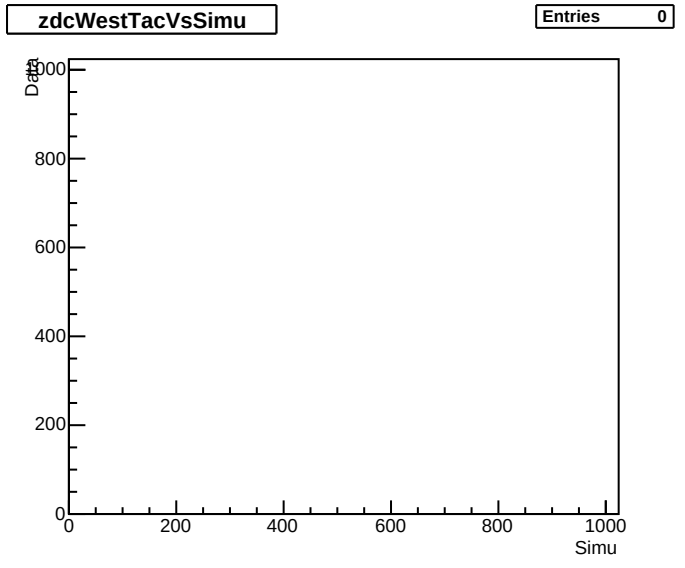

vpdWestTacSum
Entries 3880511

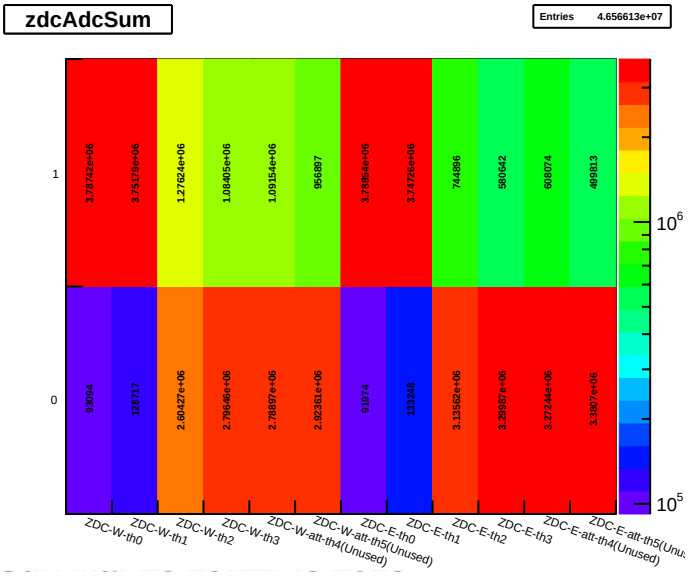

vpdWestTacSumVsSimu
Entries 0
Data
Simu

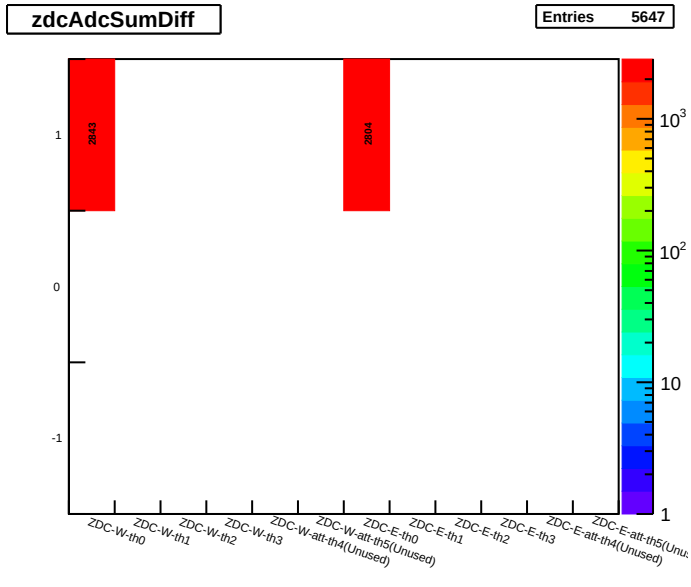

zdcEastTac
Entries 3880511

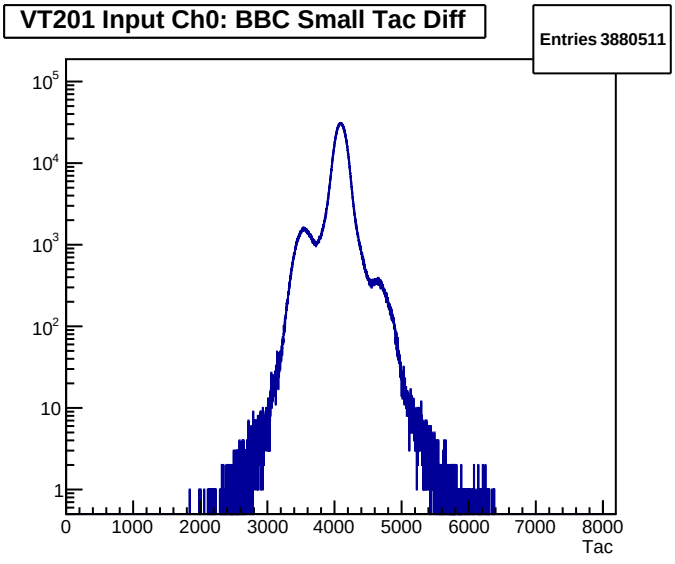

zdcEastTacVsSimu
Entries 7
Data
Simu

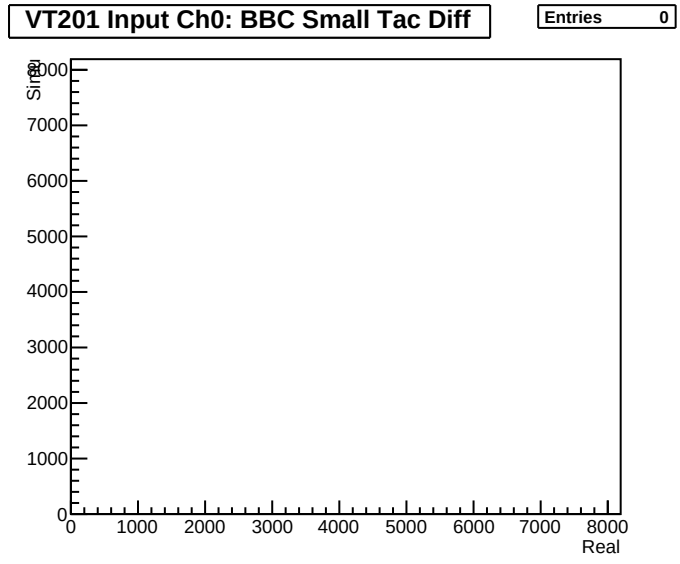

zdcWestTac
Entries 3880511

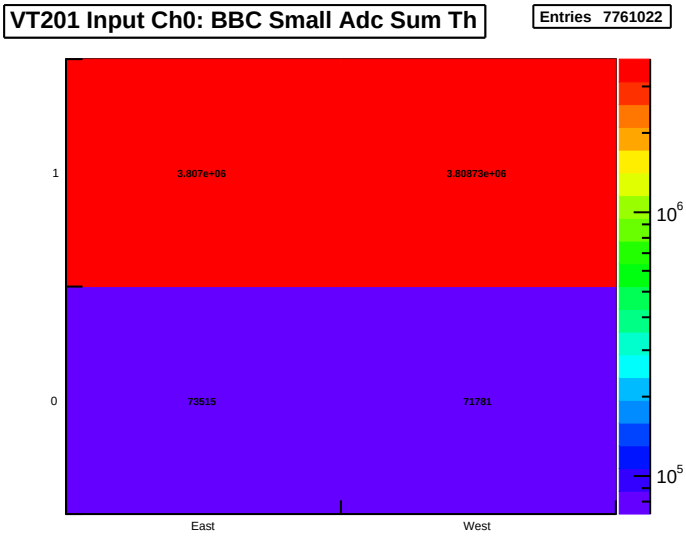

zdcWestTacVsSimu
Entries 0
Data
Simu

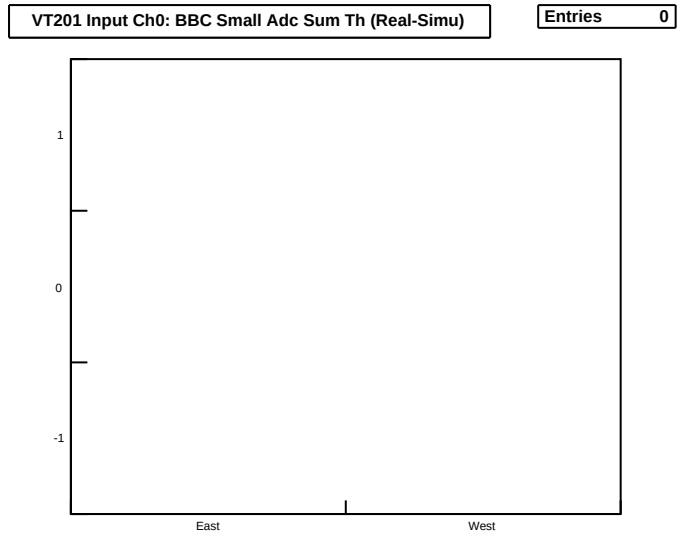

zdcAdcSum
Entries 4.656613e+07
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unus

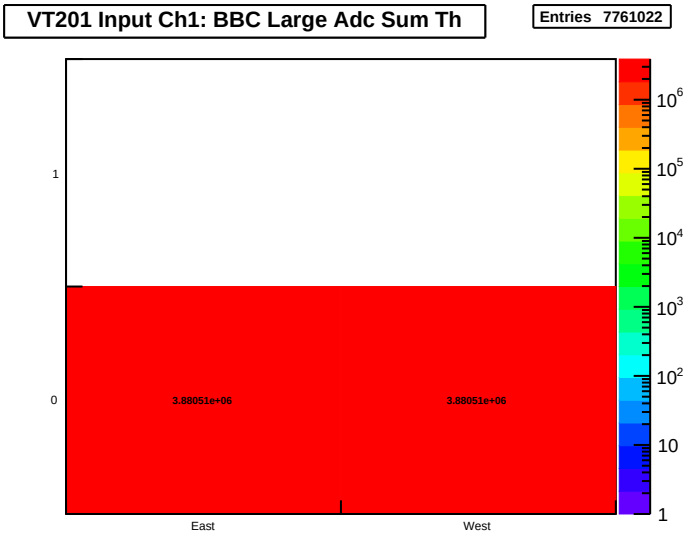

zdcAdcSumDiff
Entries 5647
2843
2804
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unus

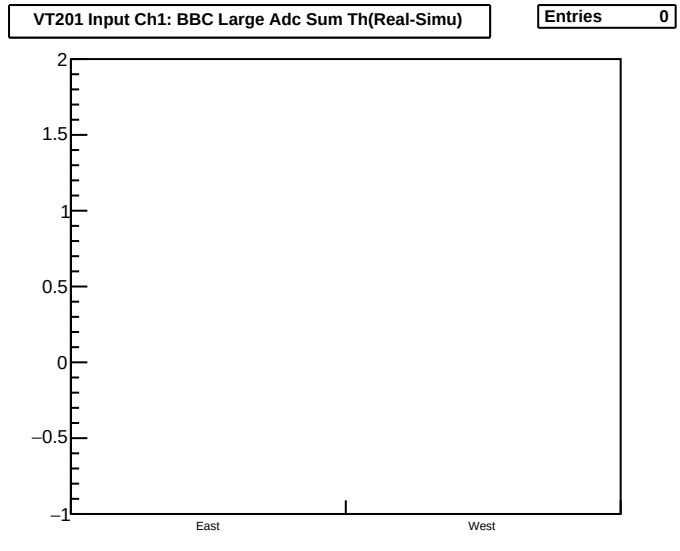

VT201 Input Ch0: BBC Small Tac Diff
Entries 3880511
Tac

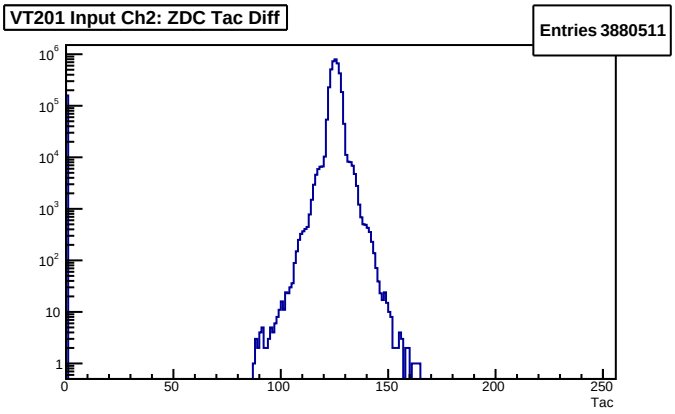

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

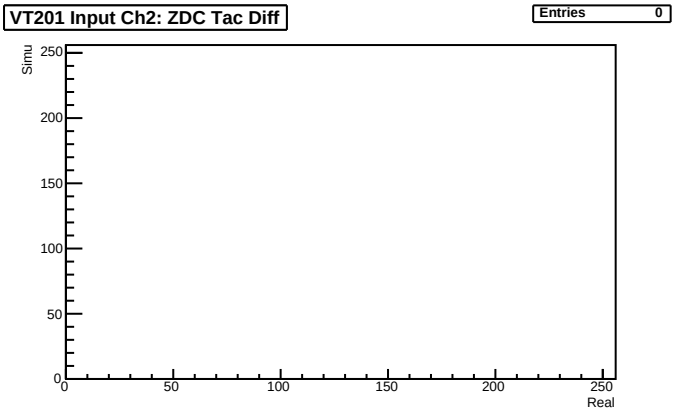

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 7761022
3.807e+06
3.80873e+06
73515
71781
East
West

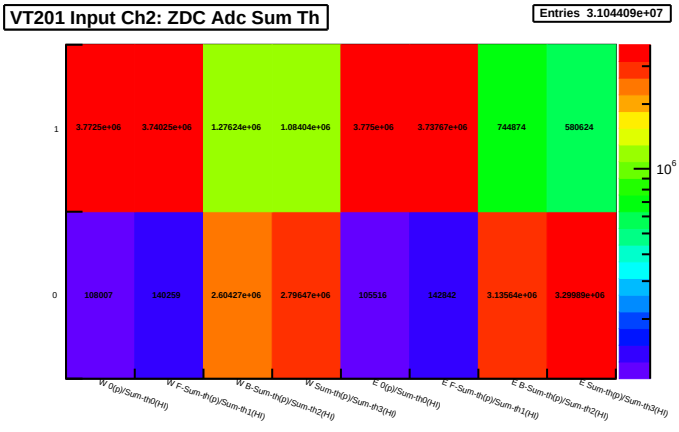

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

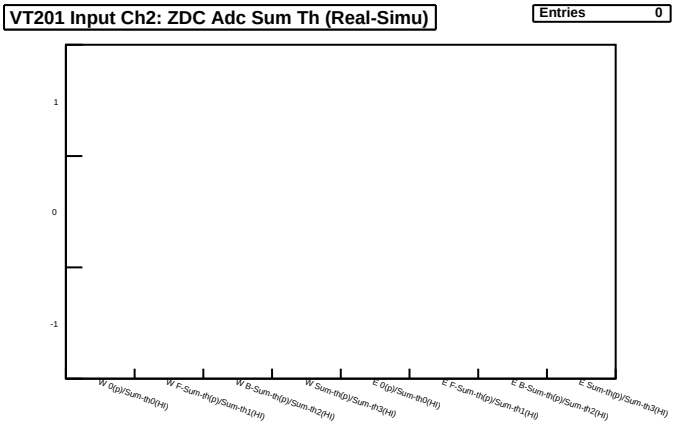

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 7761022
3.88051e+06
3.88051e+06
East
West

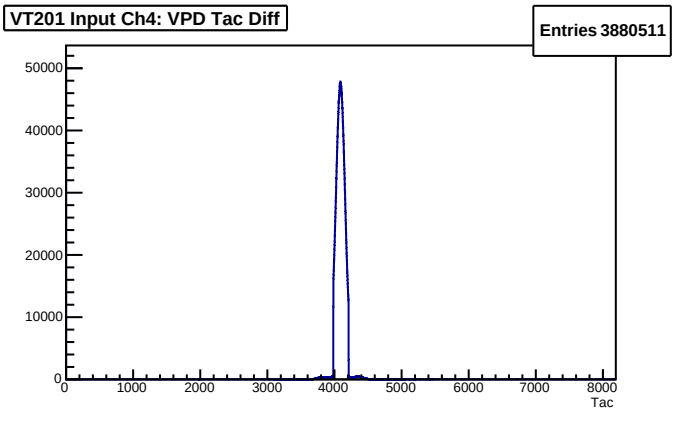

VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

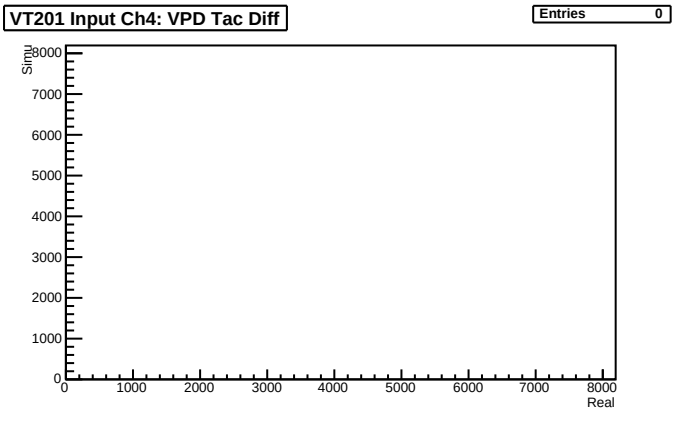

VT201 Input Ch2: ZDC Tac Diff
Entries 3880511
Tac

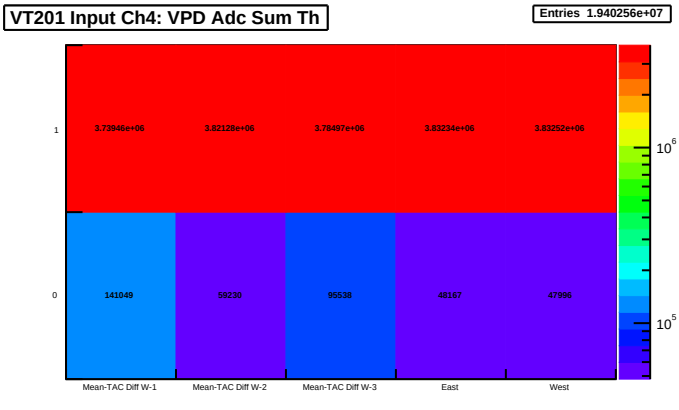

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

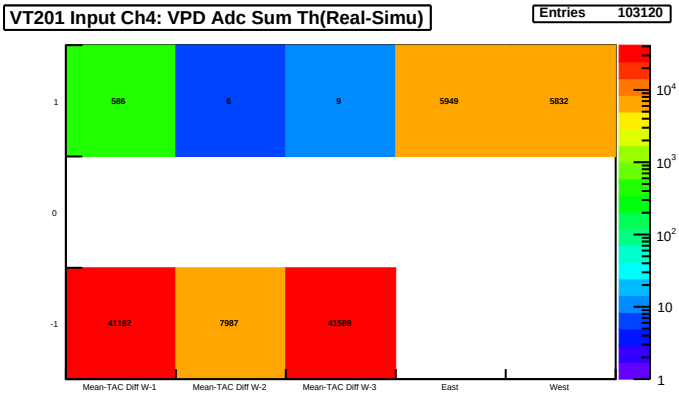

VT201 Input Ch2: ZDC Adc Sum Th
Entries 3.104409e+07
3.7725e+06
3.74025e+06
1.27624e+06
1.08404e+06
3.775e+06
3.73767e+06
744874
580624
108007
140259
2.60427e+06
2.79647e+06
105516
142842
3.13564e+06
3.29989e+06
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

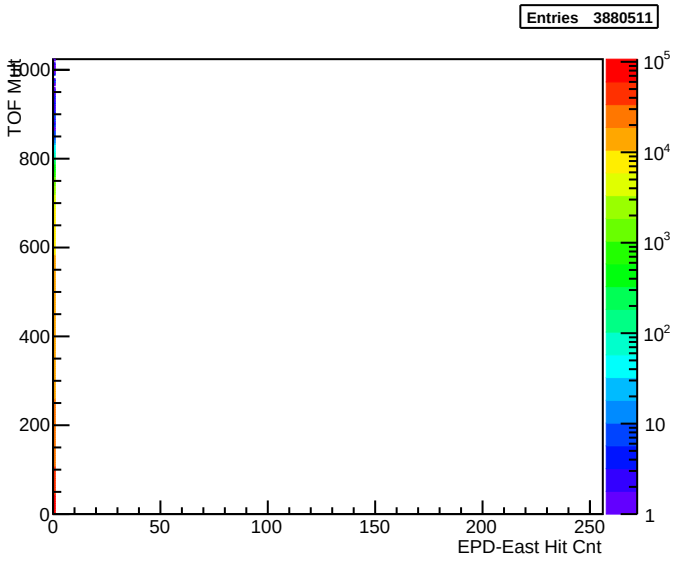

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

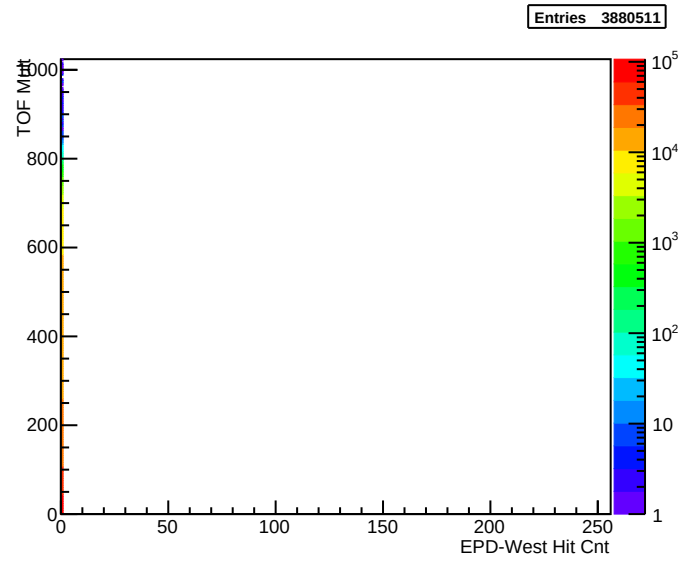

VT201 Input Ch4: VPD Tac Diff
Entries 3880511
Tac

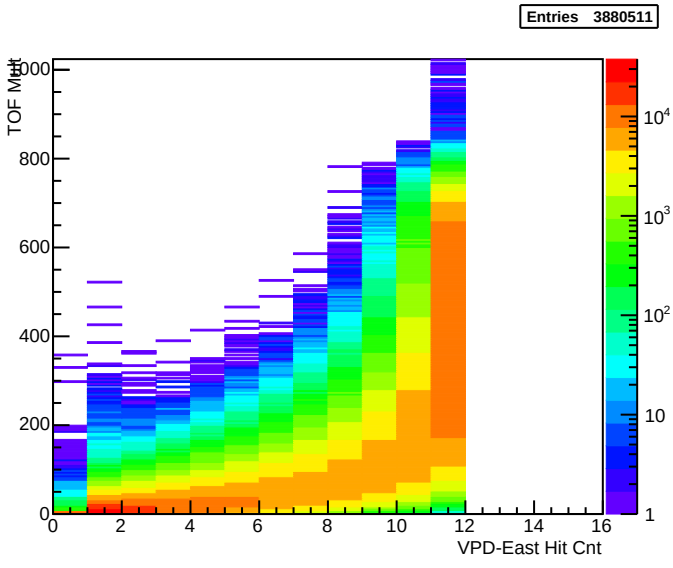

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

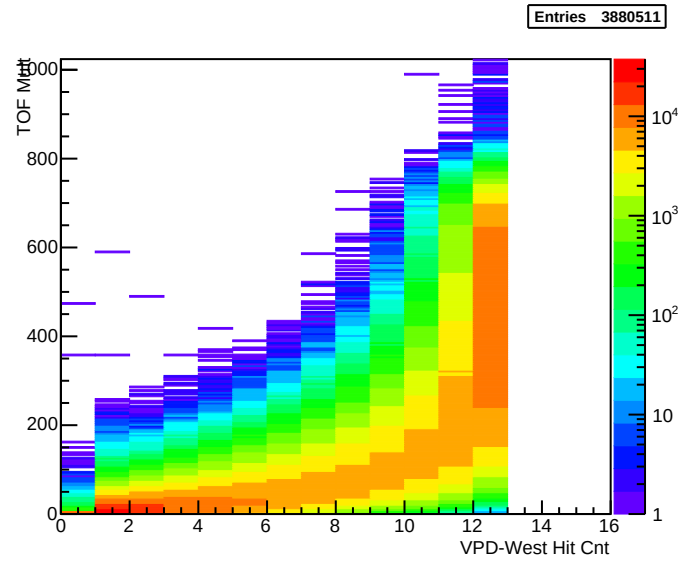

VT201 Input Ch4: VPD Adc Sum Th
Entries 1.940256e+07
3.73946e+06
3.82128e+06
3.78497e+06
3.83234e+06
3.83252e+06
141049
59230
95538
48167
47996
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

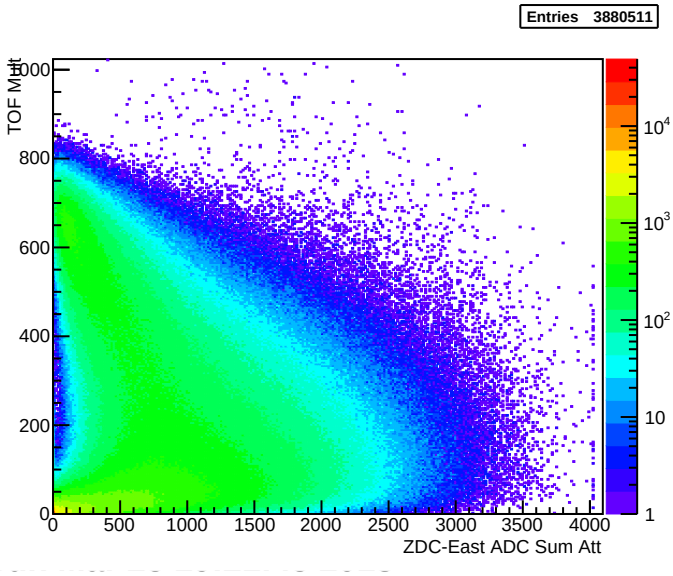

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 103120
586
6
9
5949
5832
41162
7987
41589
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

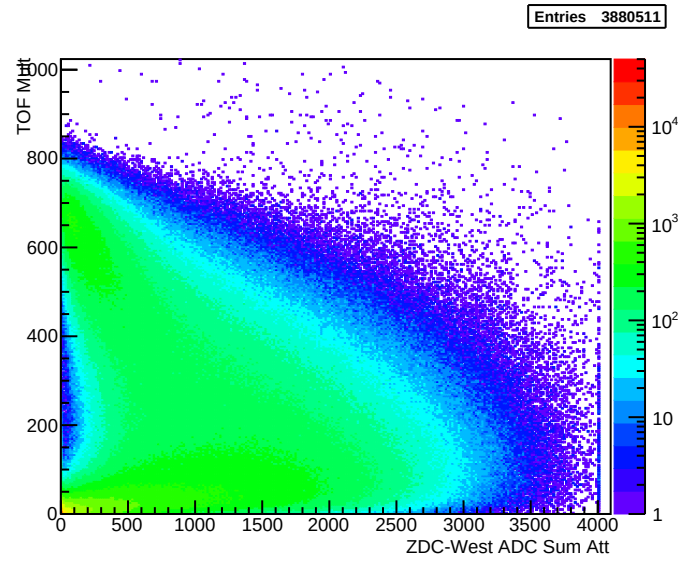

Entries 3880511
TOF Mult
EPD-East Hit Cnt

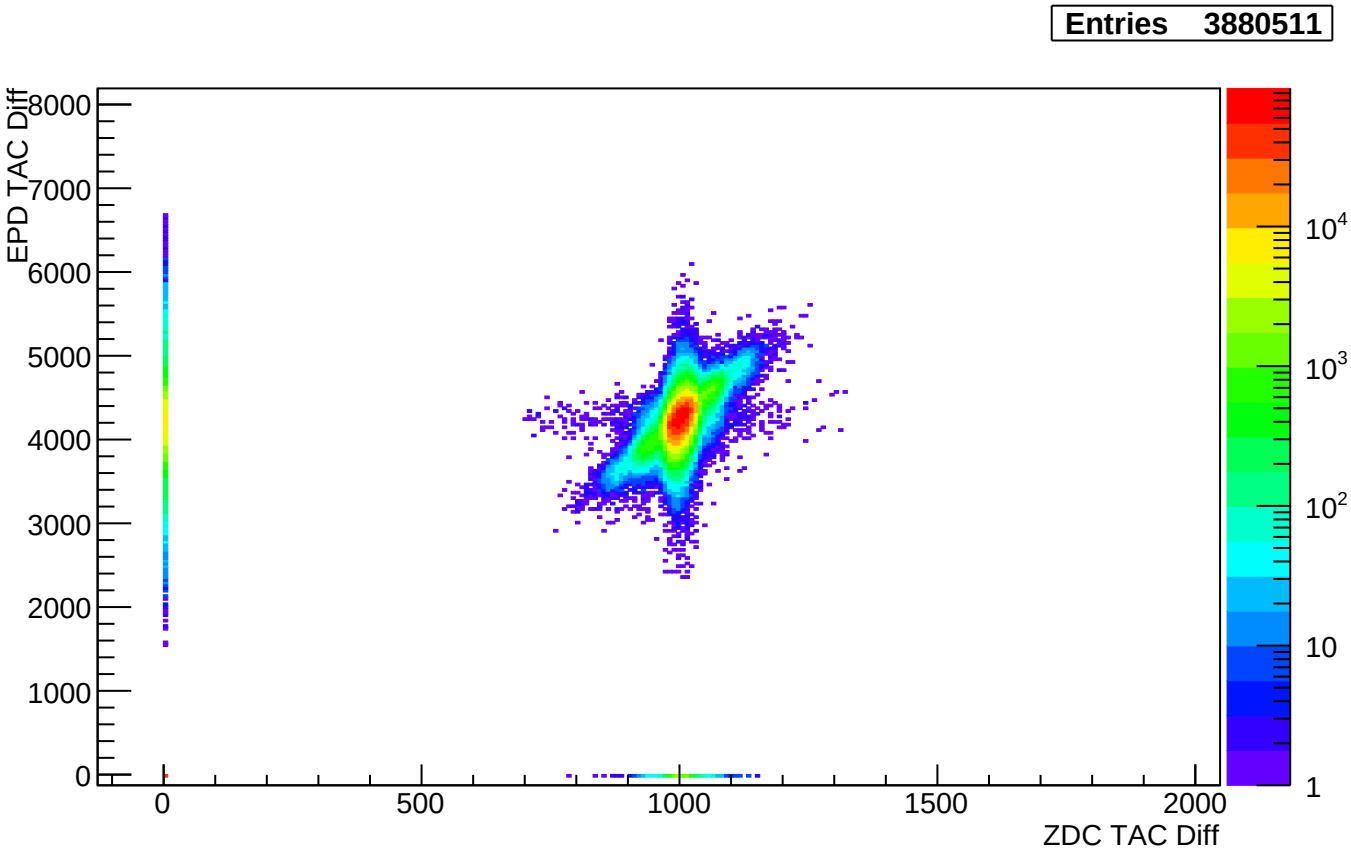

Entries 3880511
TOF Mult
EPD-West Hit Cnt

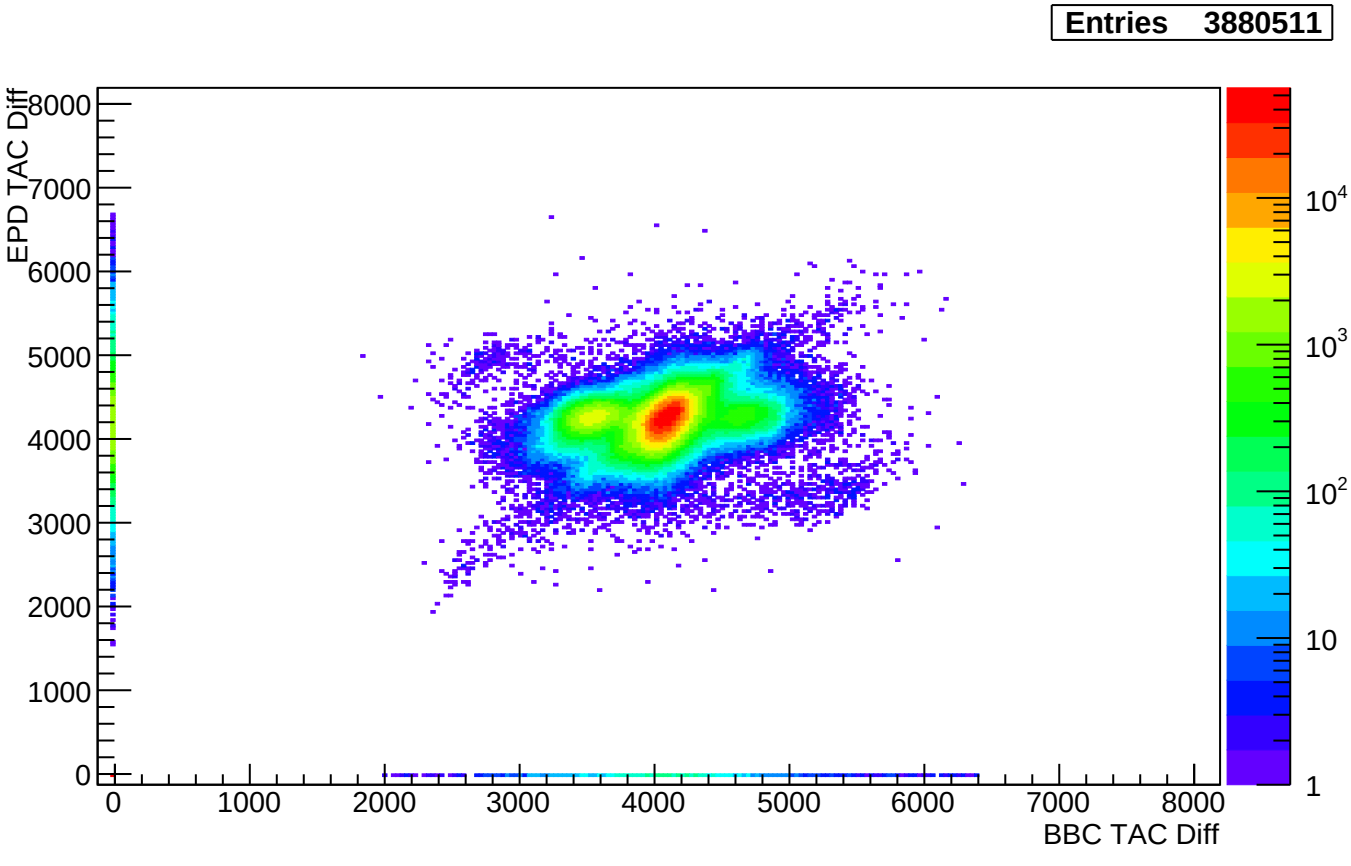

Entries 3880511
TOF Mult
VPD-East Hit Cnt

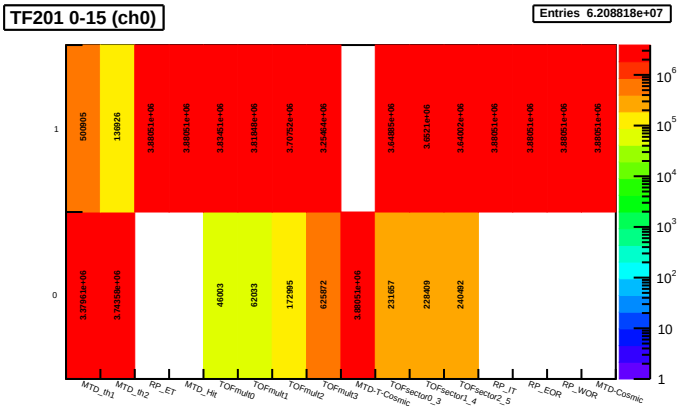

Entries 3880511
TOF Mult
VPD-West Hit Cnt

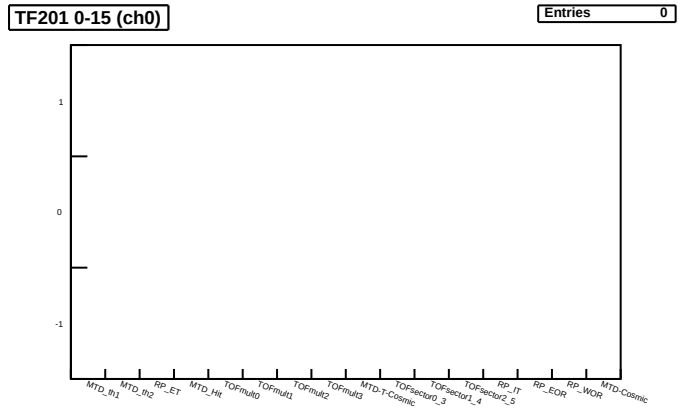

Entries 3880511
TOF Mult
ZDC-East ADC Sum Att

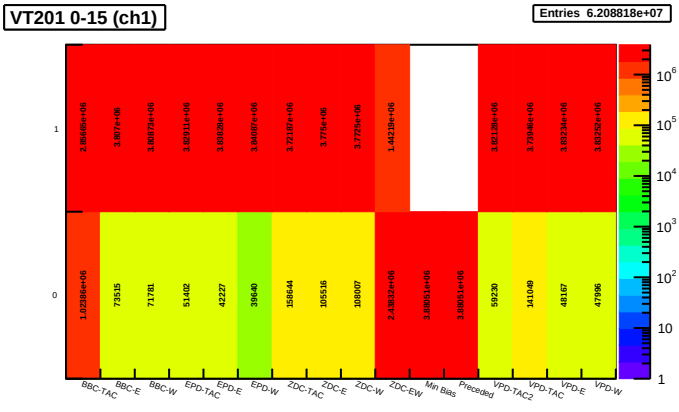

Entries 3880511
TOF Mult
ZDC-West ADC Sum Att

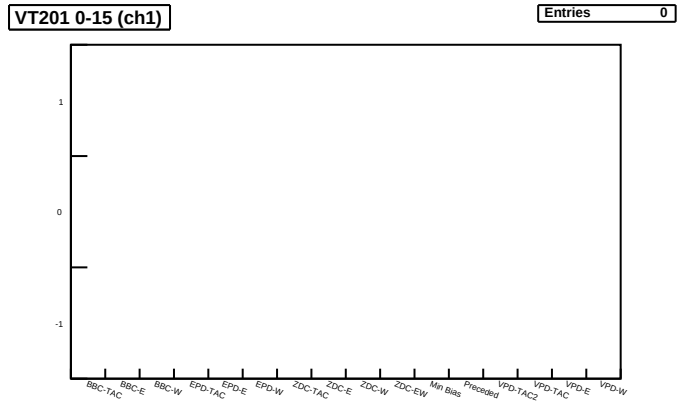

Entries 3880511
EPD TAC Diff
ZDC TAC Diff
0
500
1000
1500
2000
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2
10^3
10^4

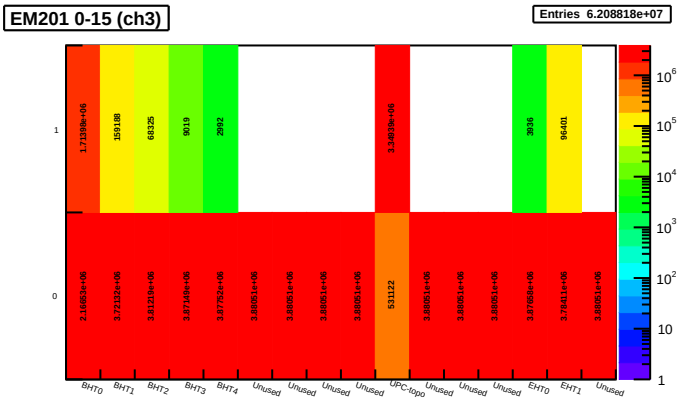

Entries 3880511
EPD TAC Diff
BBC TAC Diff
0
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2
10^3
10^4

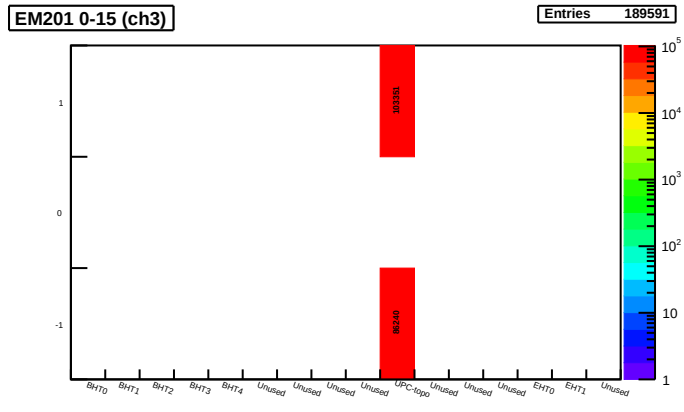

TF201 0-15 (ch0)
Entries 6.208818e+07
MTD_tfl1
MTD_tfl2
RP_-ET
MTD_Hit
TOFmult0
TOFmult1
TOFmult2
TOFmult3
MTD-T-Cosmic
TOFsector0_3
TOFsector1_4
TOFsector2_5
RP_IT
RP_EOR
RP_WOR
MTD-Cosmic

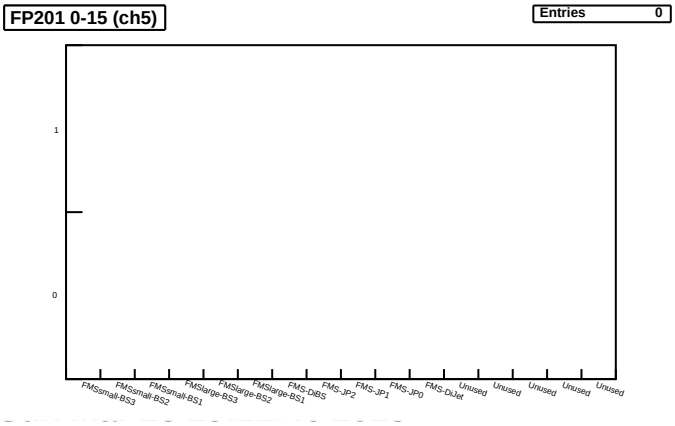

TF201 0-15 (ch0)
Entries 0

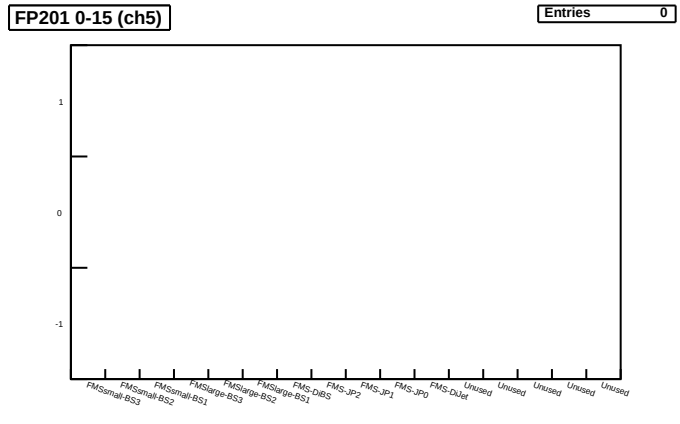

VT201 0-15 (ch1)
Entries 6.208818e+07
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-EW
Min Bias
Preceded
VPD-TAC2
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 0


EM201 0-15 (ch3)
Entries 6.208818e+07
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1


EM201 0-15 (ch3)
Entries 189591
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused


FP201 0-15 (ch5)
Entries 0