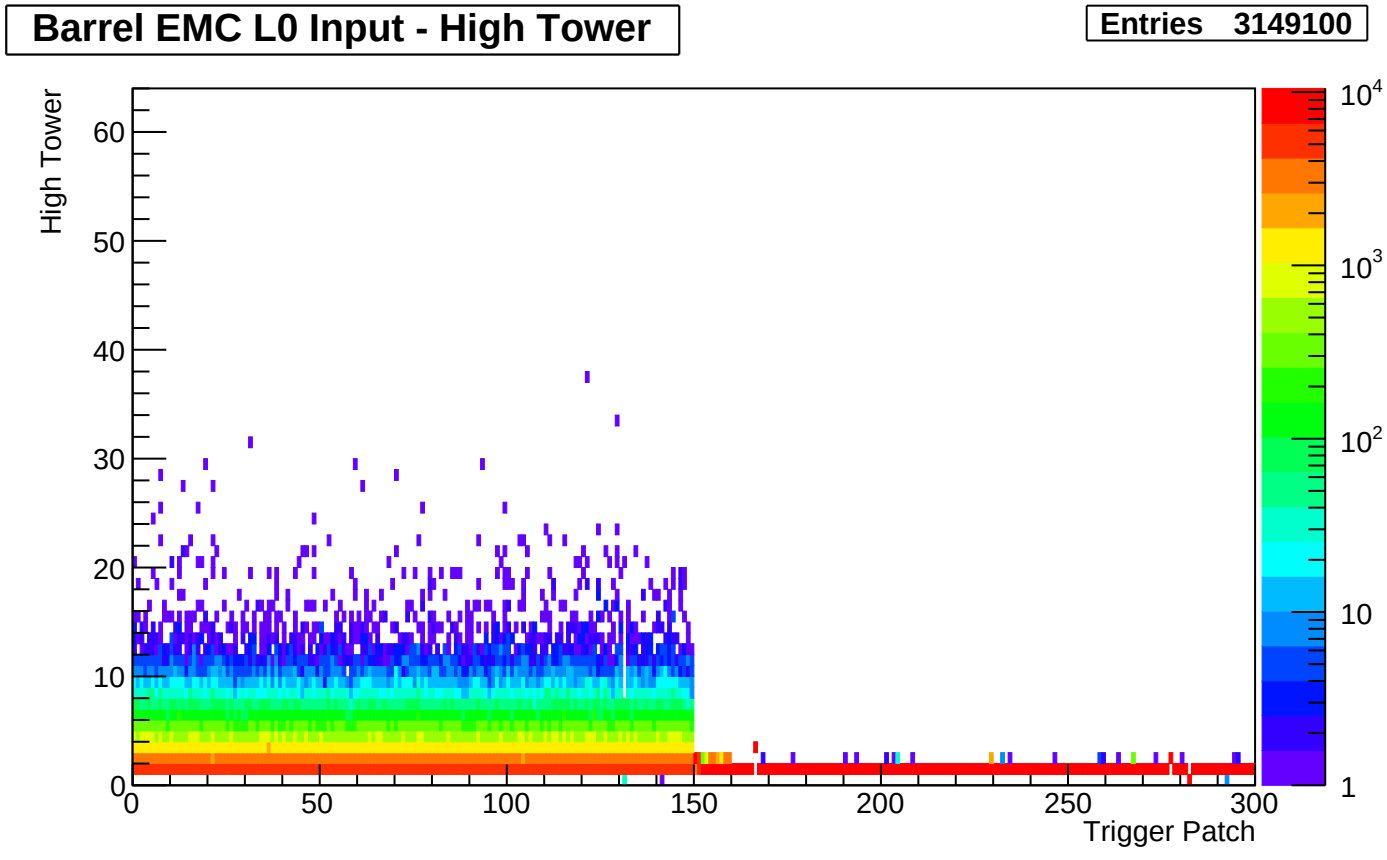
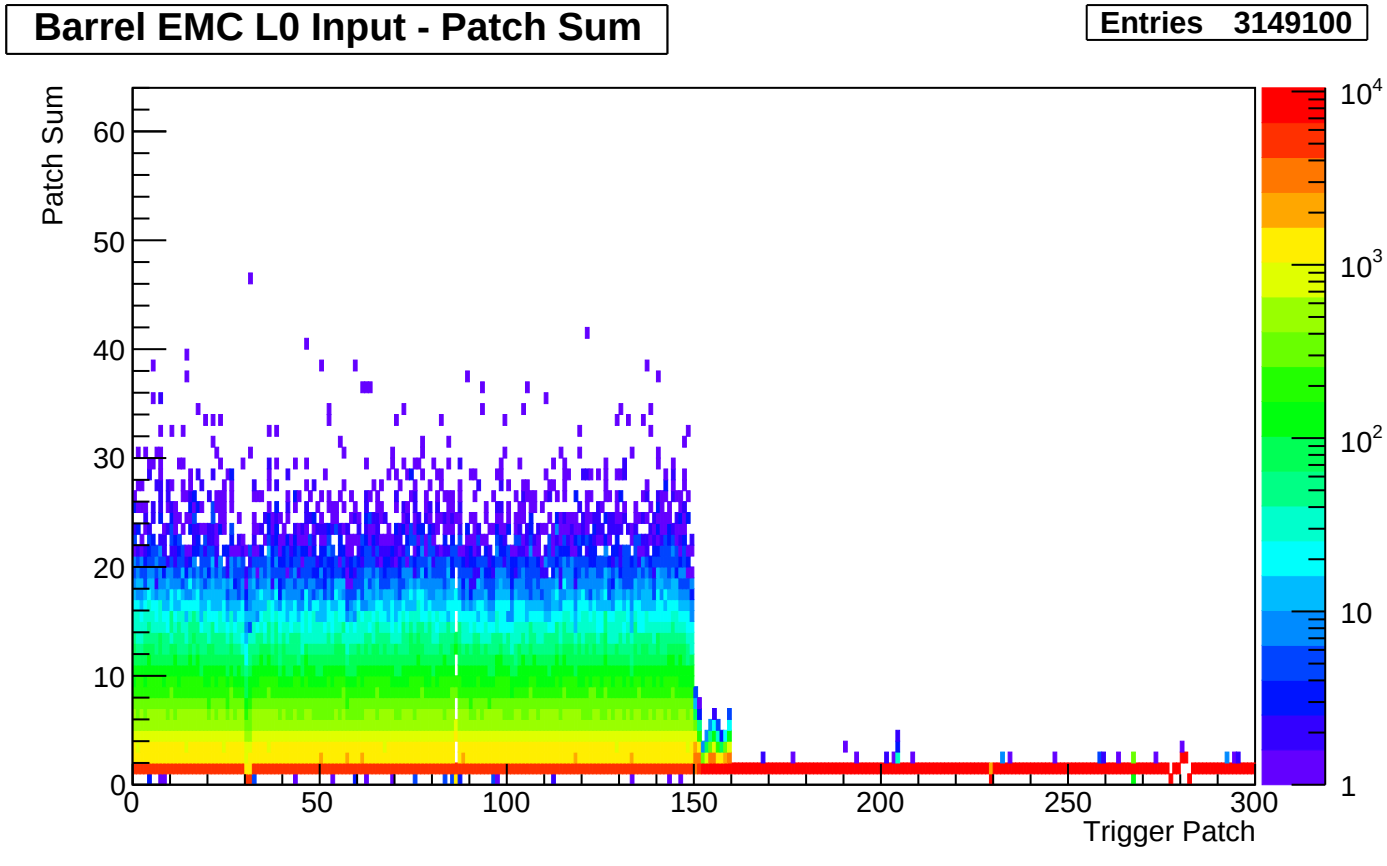
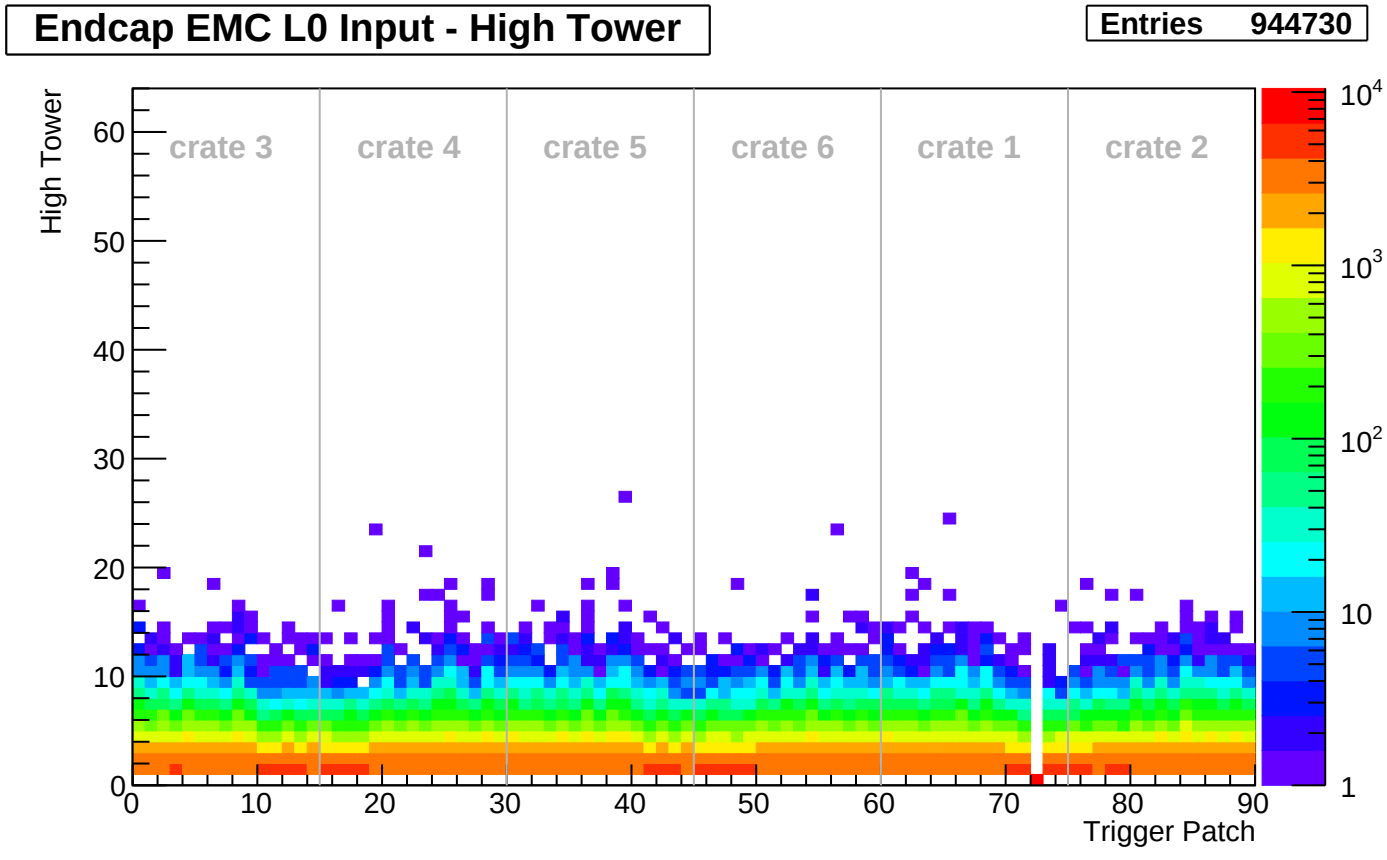
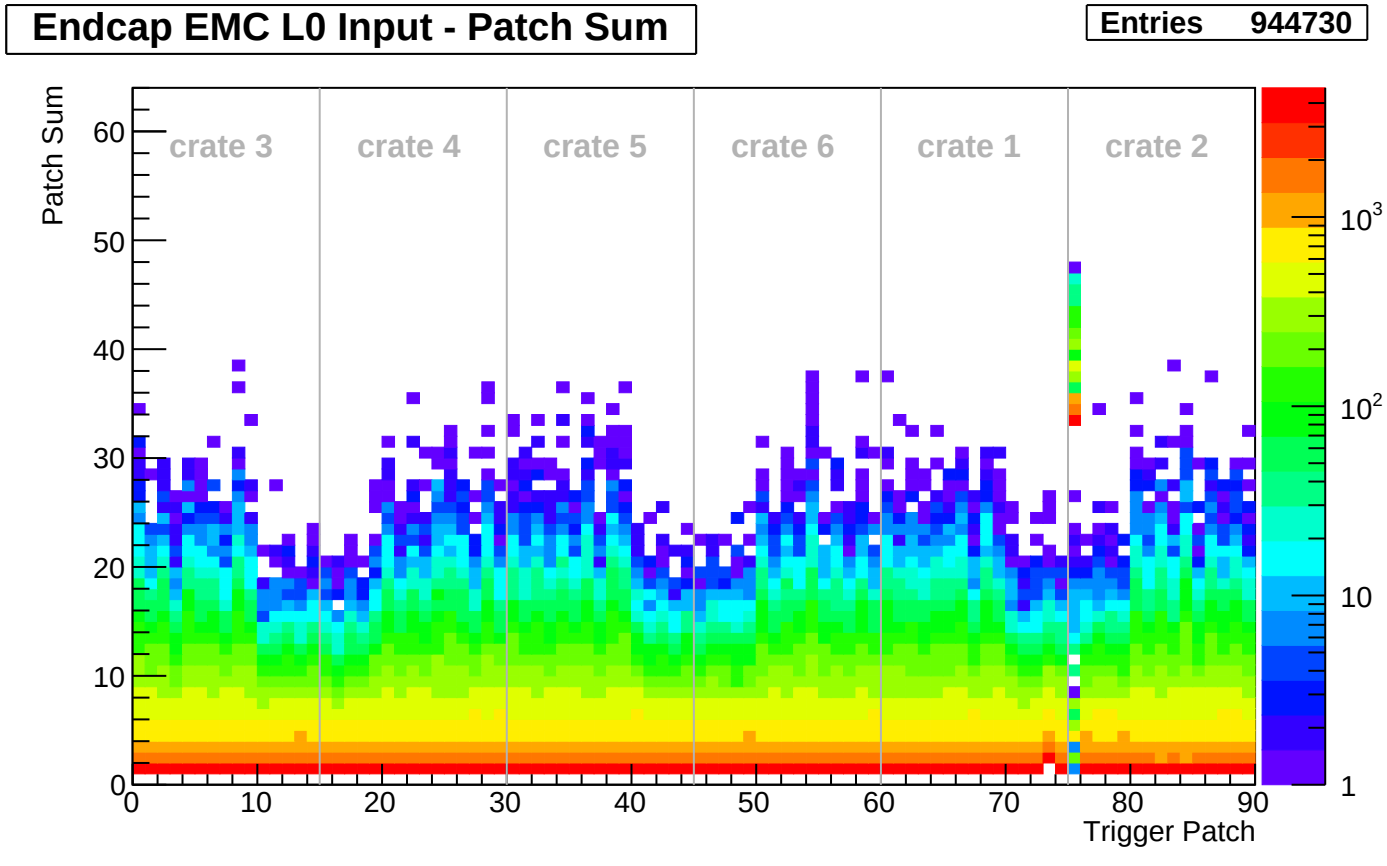
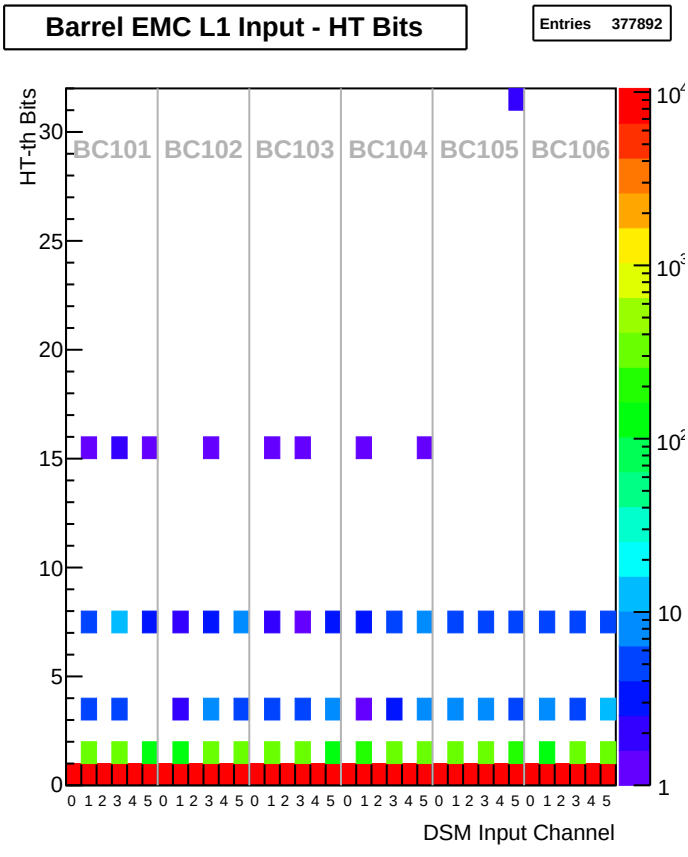
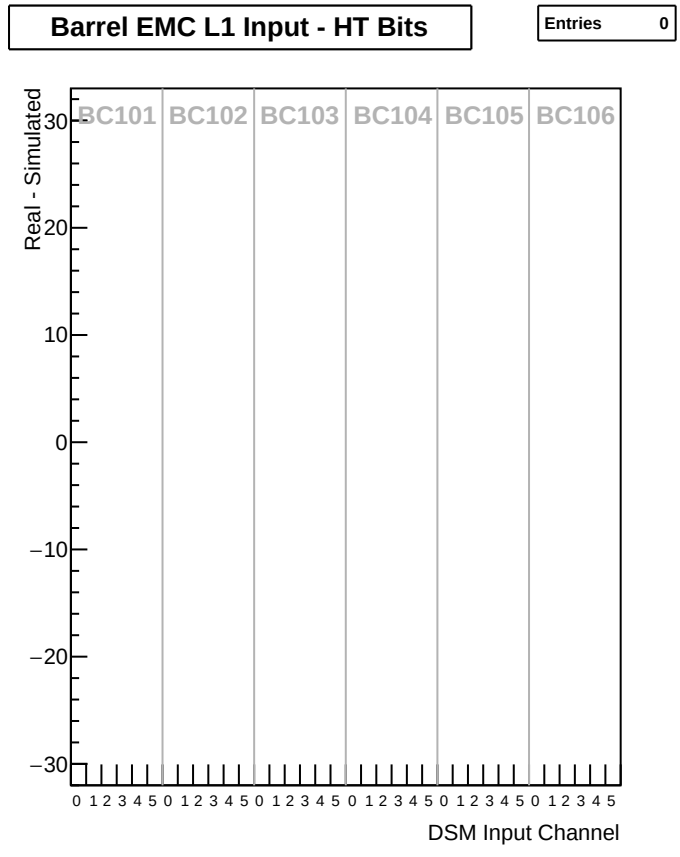

Barrel EMC L0 Input - High Tower
Entries 3149100
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

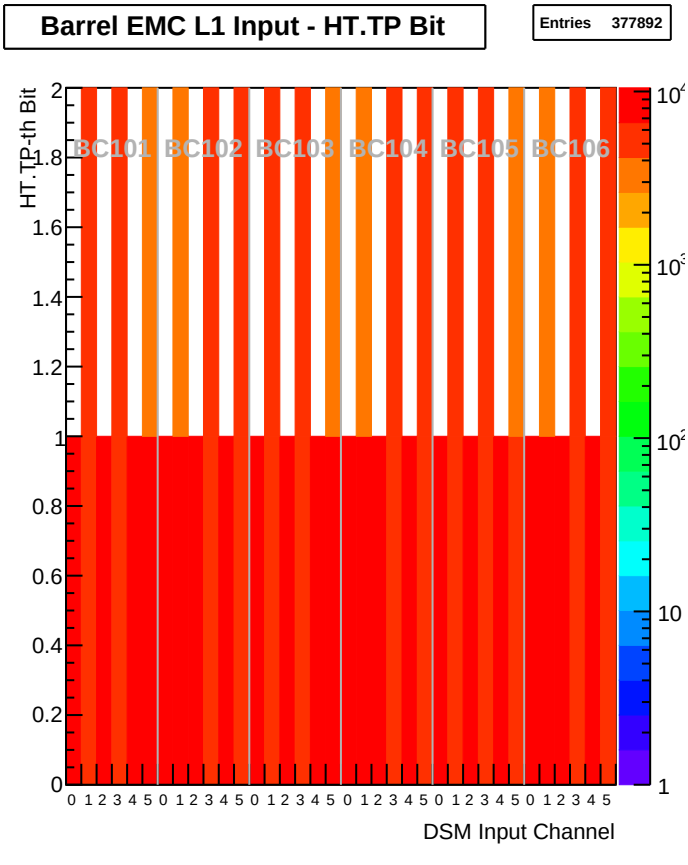

Barrel EMC L0 Input - Patch Sum
Entries 3149100
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

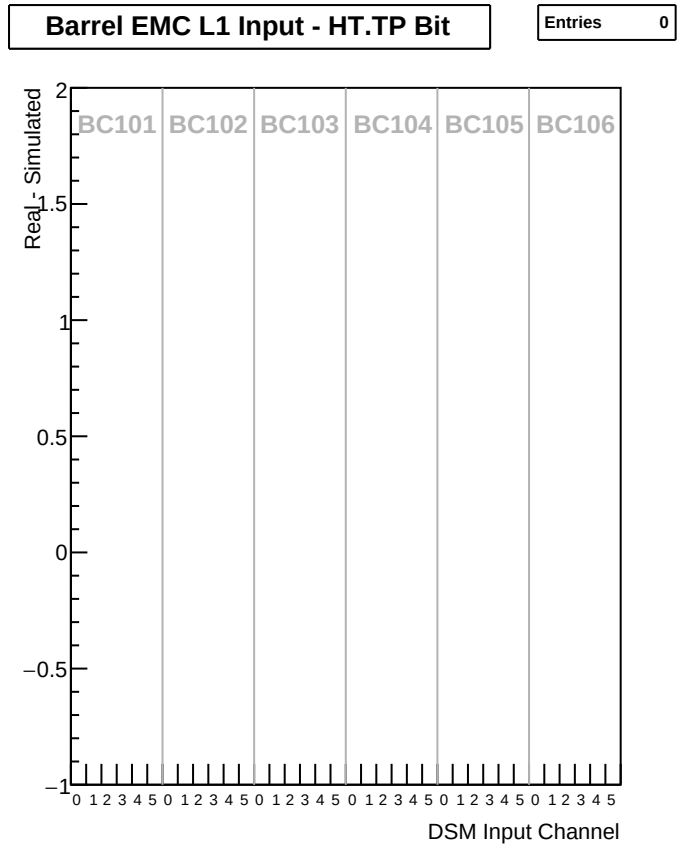

Endcap EMC L0 Input - High Tower
Entries 944730
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

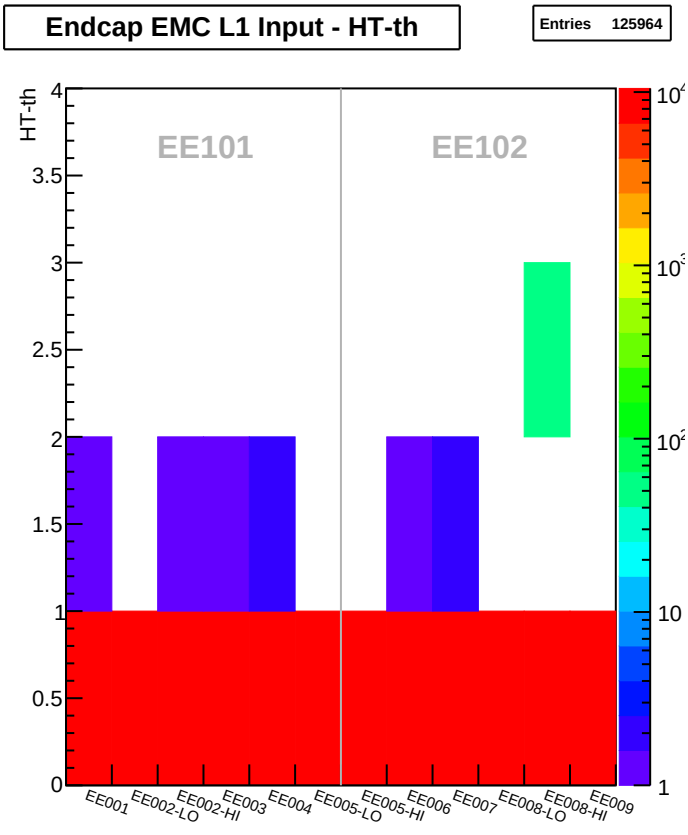

Endcap EMC L0 Input - Patch Sum
Entries 944730
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

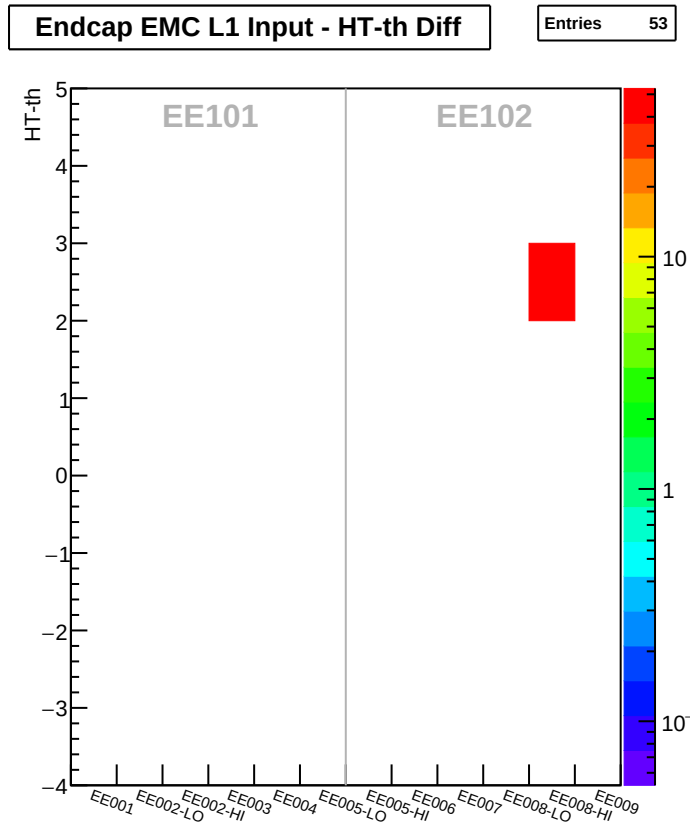

Barrel EMC L1 Input - HT Bits
Entries 377892
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

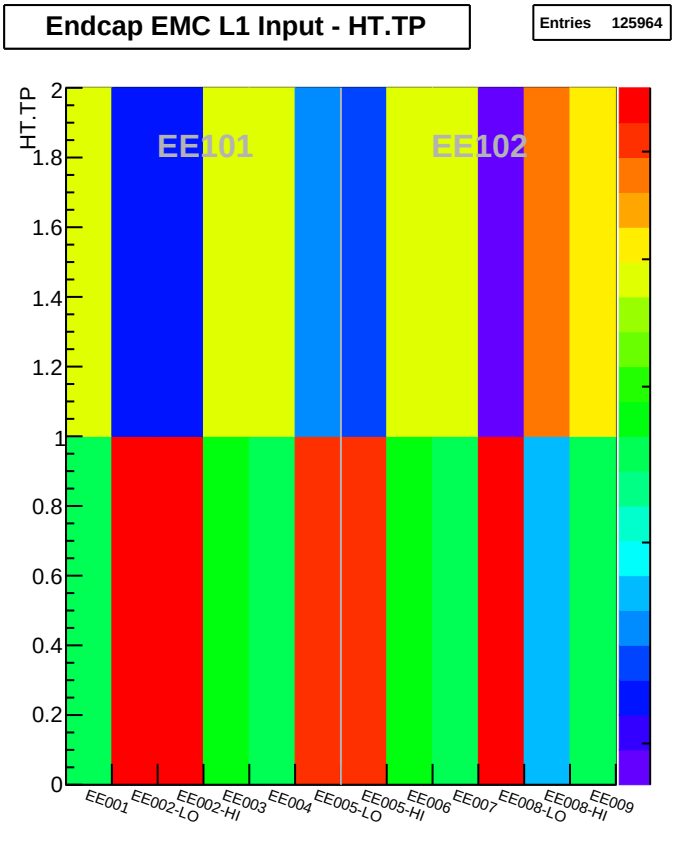

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

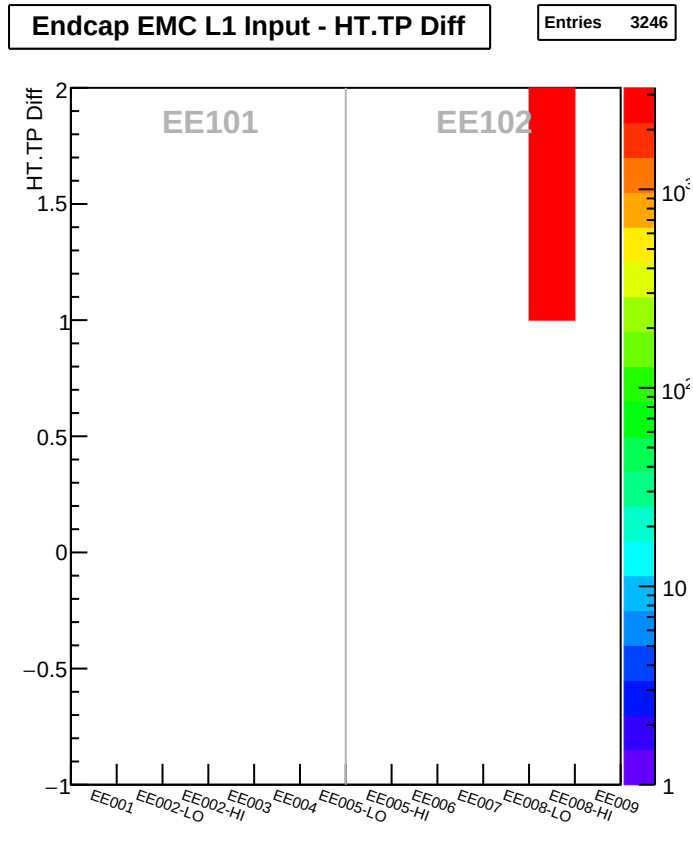

Barrel EMC L1 Input - HT.TP Bit
Entries 377892
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

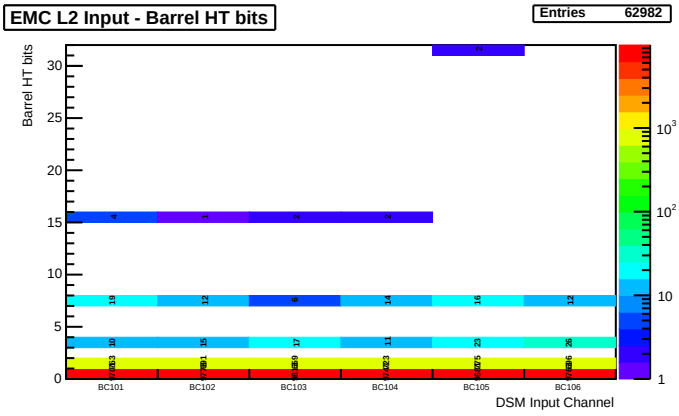

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

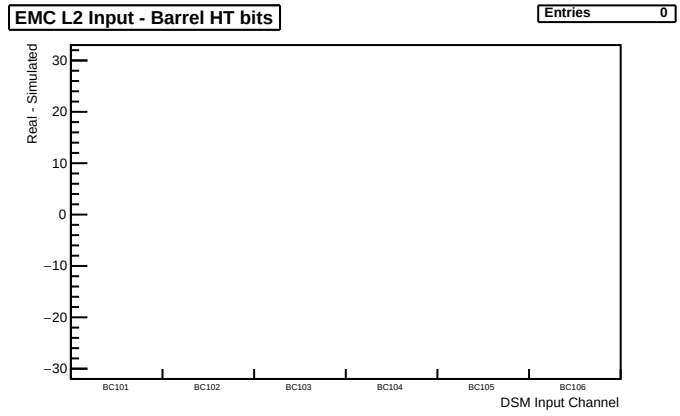

Endcap EMC L1 Input - HT-th
Entries 125964
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

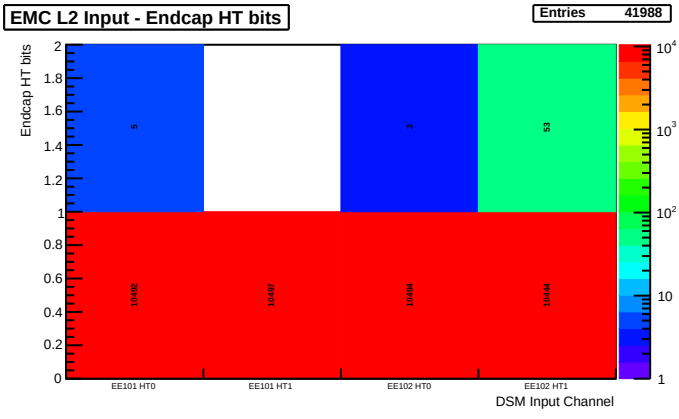

Endcap EMC L1 Input - HT-th Diff
Entries 53
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

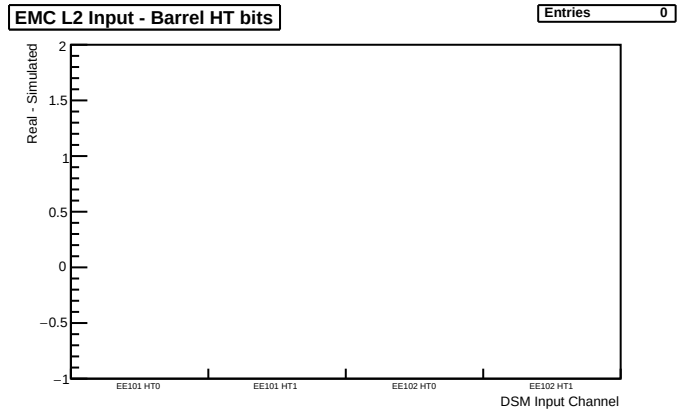

Endcap EMC L1 Input - HT.TP
Entries 125964
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

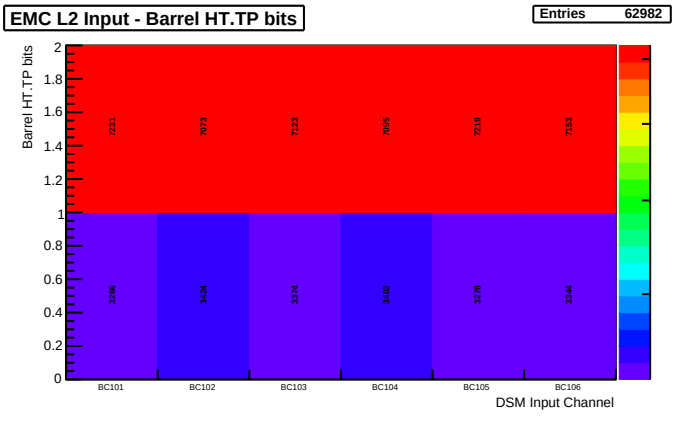

Endcap EMC L1 Input - HT.TP Diff
Entries 3246
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

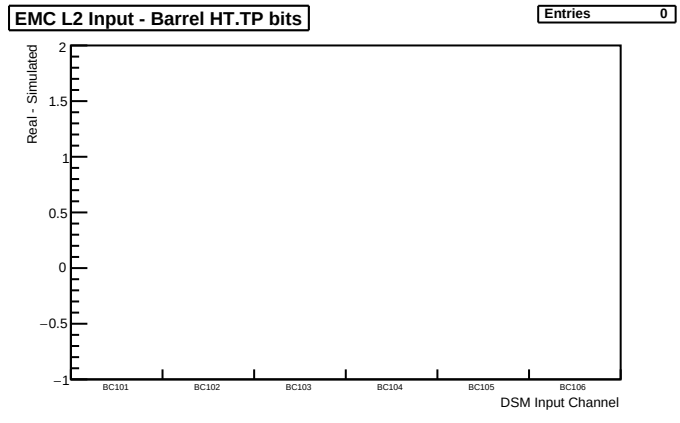

EMC L2 Input - Barrel HT bits
Entries 62982
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

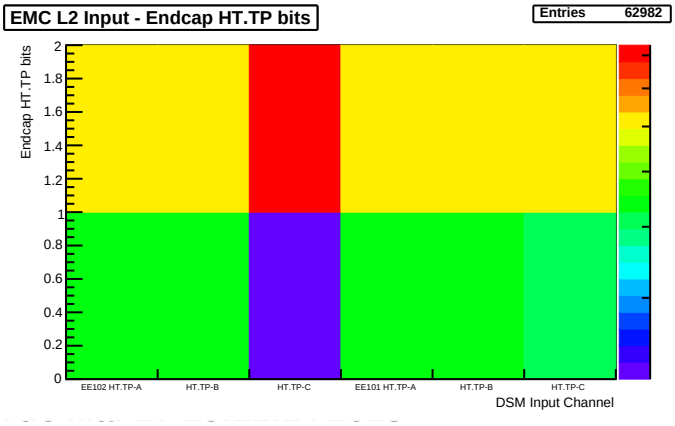

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

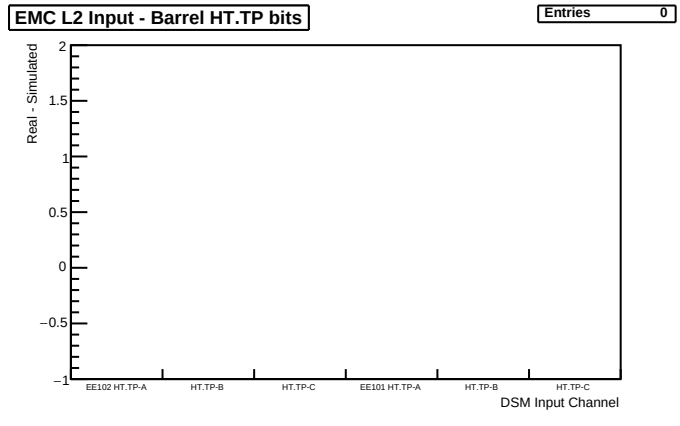

EMC L2 Input - Endcap HT bits
Entries 41988
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

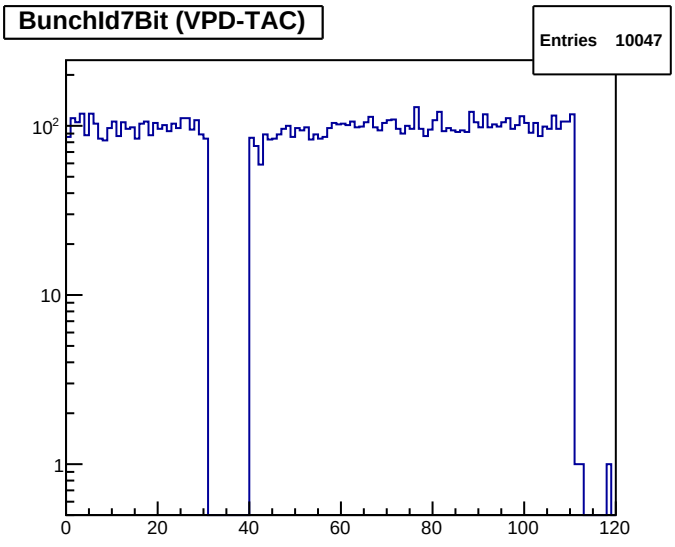

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

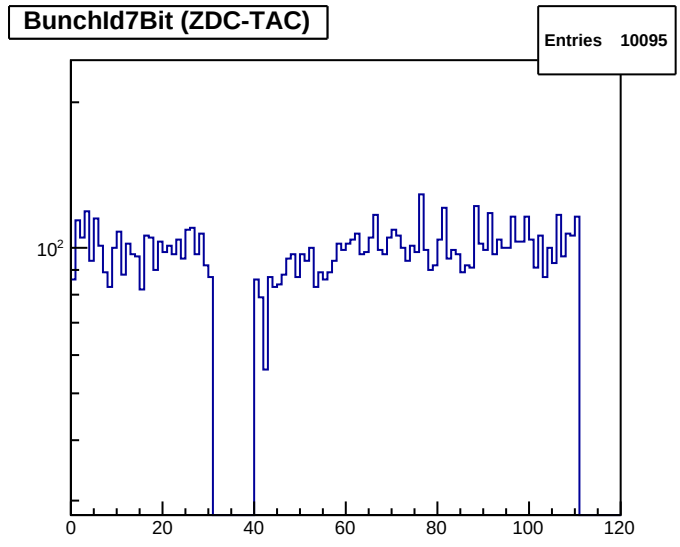

EMC L2 Input - Barrel HT.TP bits
Entries 62982
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

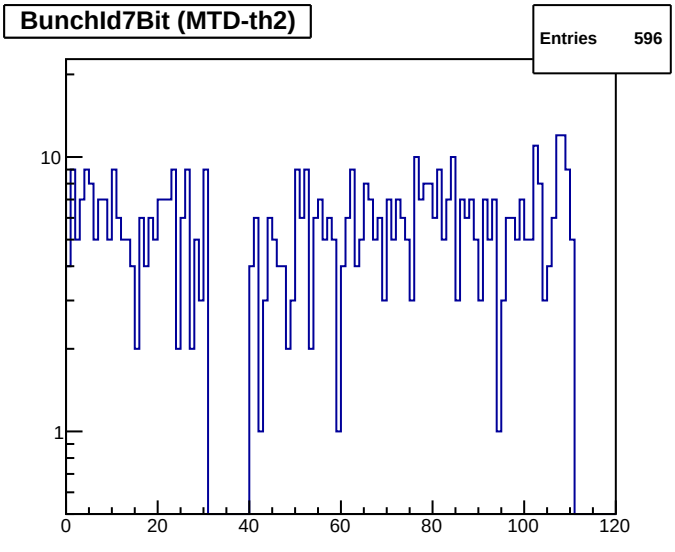

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

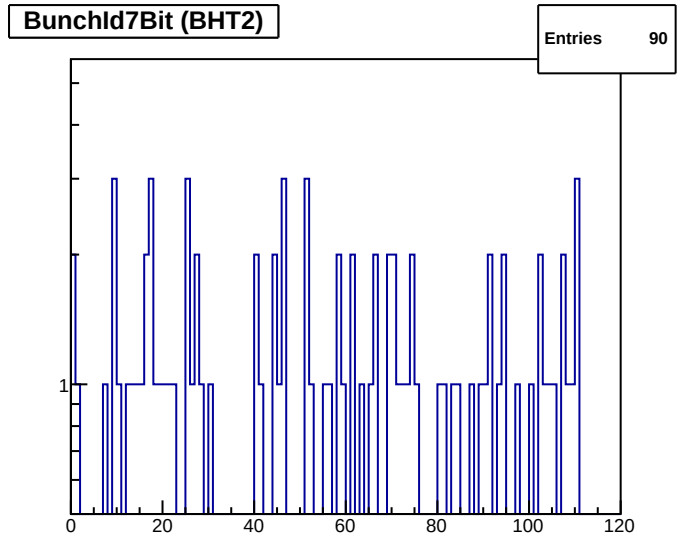

EMC L2 Input - Endcap HT.TP bits
Entries 62982
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

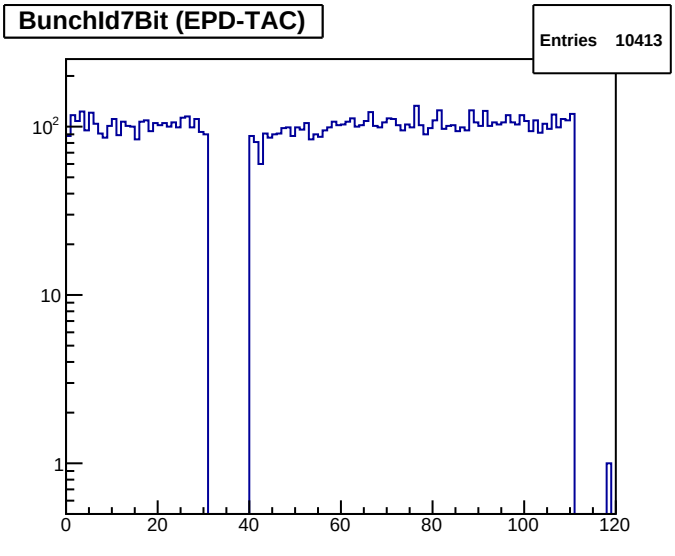

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

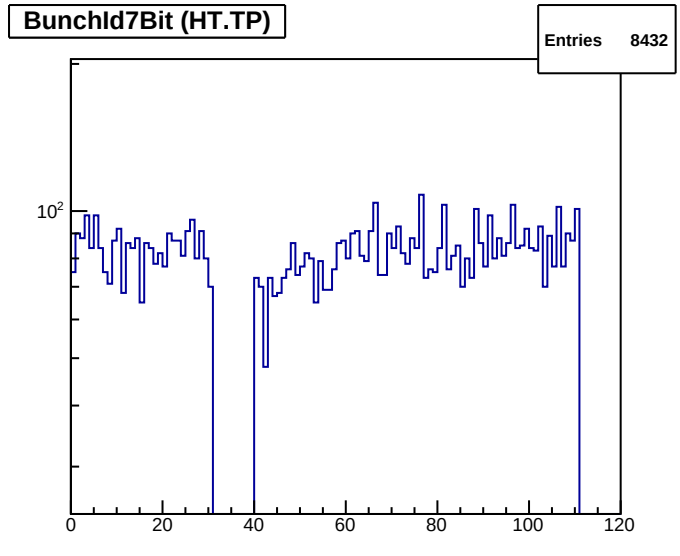

BunchId7Bit (VPD-TAC)
Entries 10047
10^2
10
1
0
20
40
60
80
100
120

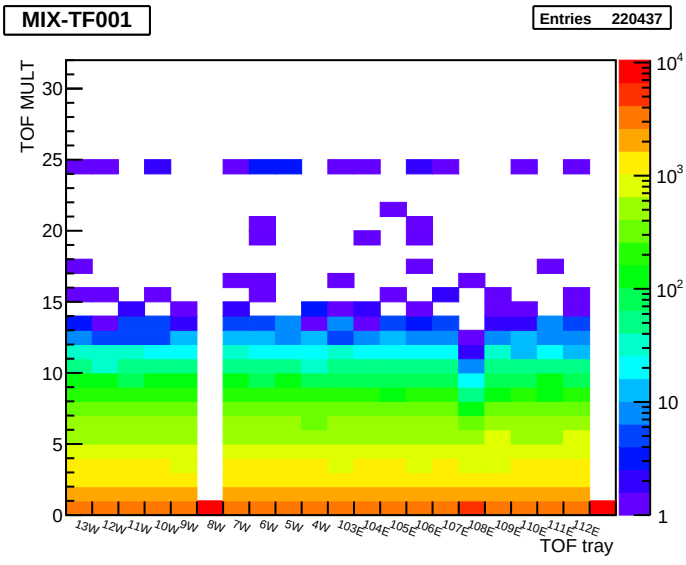

BunchId7Bit (ZDC-TAC)
Entries 10095
10^2
0
20
40
60
80
100
120

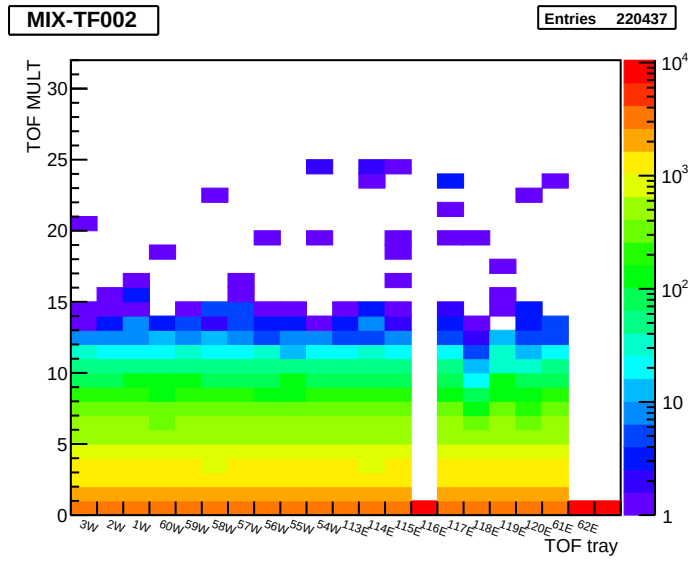

BunchId7Bit (MTD-th2)
Entries 596
10
1
0
20
40
60
80
100
120

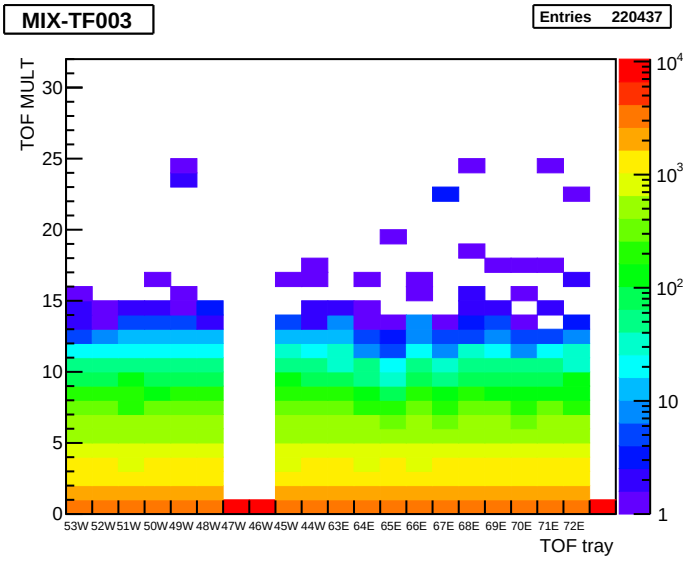

BunchId7Bit (BHT2)
Entries 90
1
0
20
40
60
80
100
120

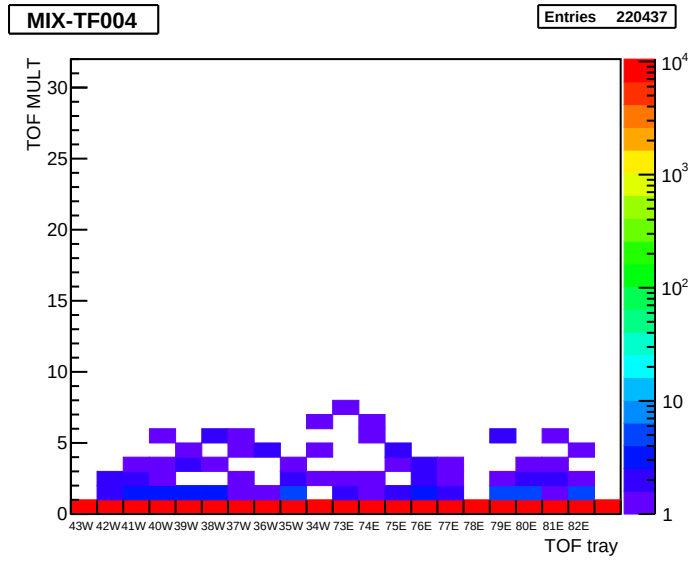

BunchId7Bit (EPD-TAC)
Entries 10413
10^2
10
1
0
20
40
60
80
100
120

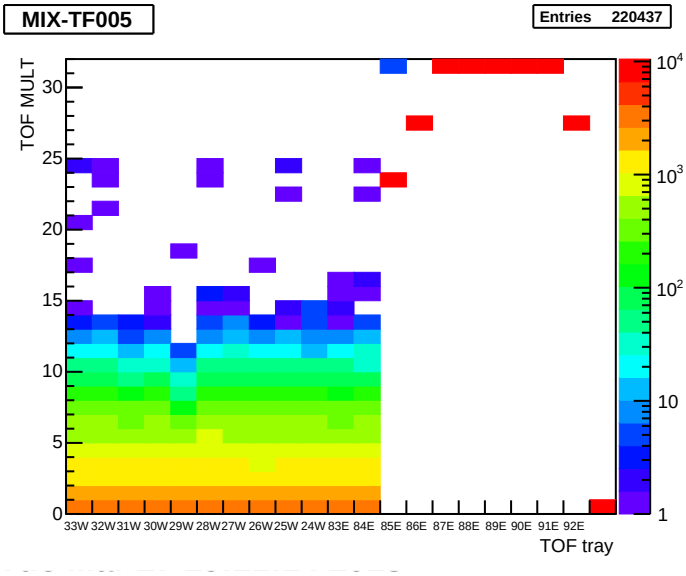

BunchId7Bit (HT.TP)
Entries 8432
10^2
0
20
40
60
80
100
120

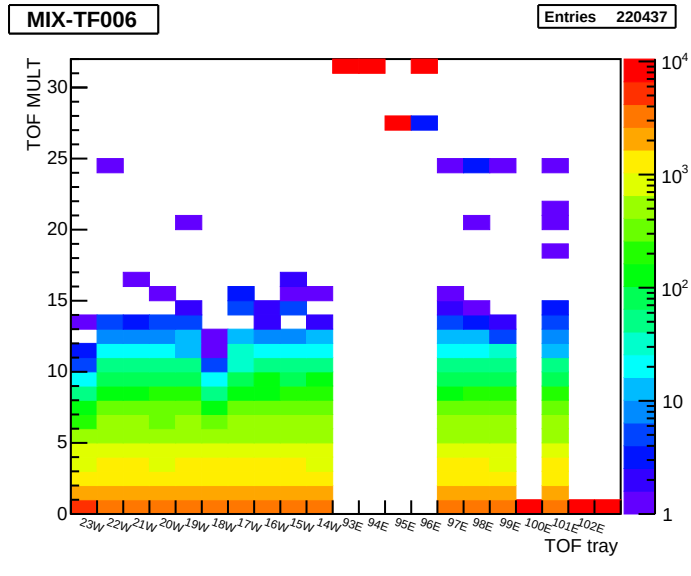

MIX-TF001
Entries 220437
TOF MULT
TOF tray

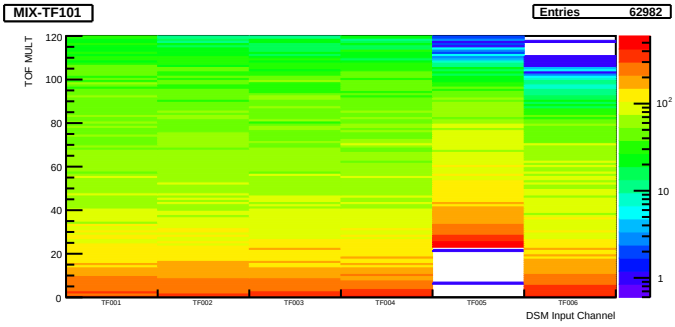

MIX-TF002
Entries 220437
TOF MULT
TOF tray

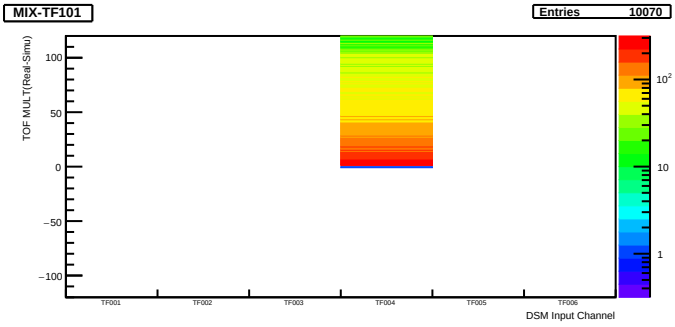

MIX-TF003
Entries 220437
TOF MULT
TOF tray

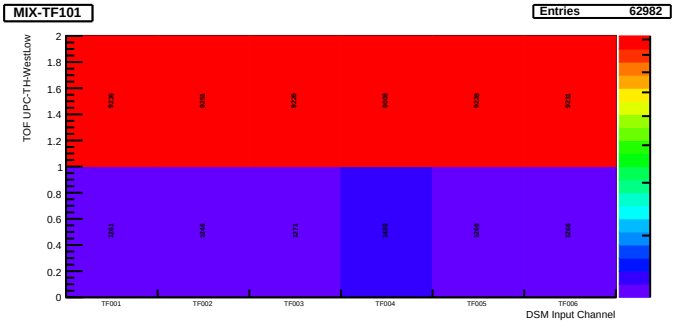

MIX-TF004
Entries 220437
TOF MULT
TOF tray

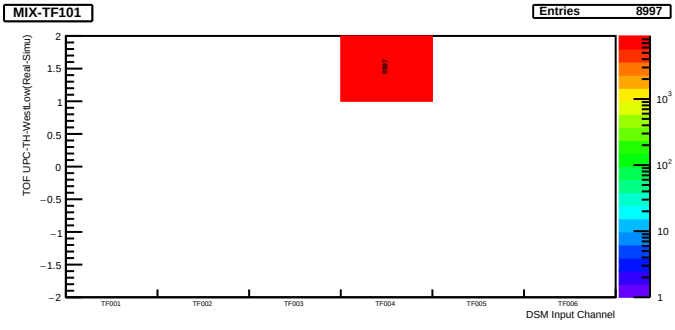

MIX-TF005
Entries 220437
TOF MULT
TOF tray

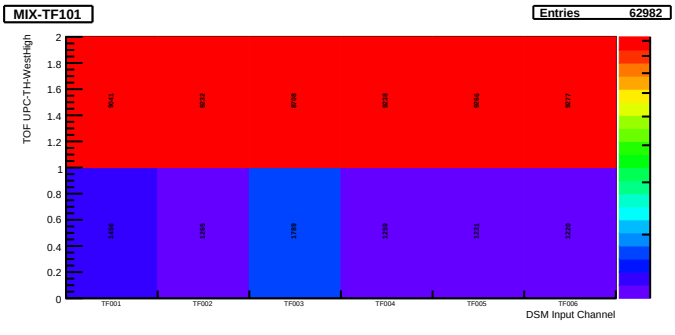

MIX-TF006
Entries 220437
TOF MULT
TOF tray

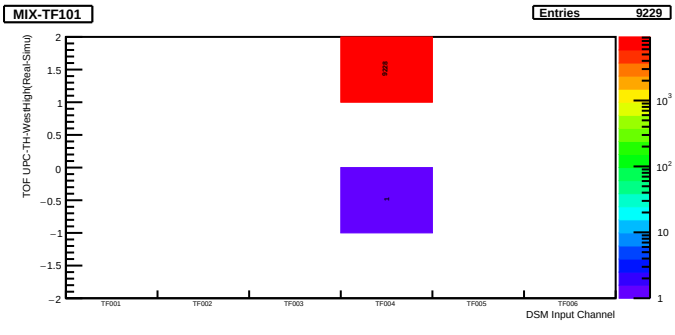

MIX-TF101
Entries 62982
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

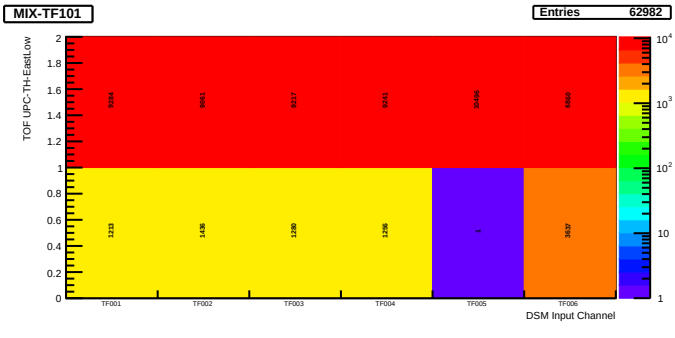

MIX-TF101
Entries 10070
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

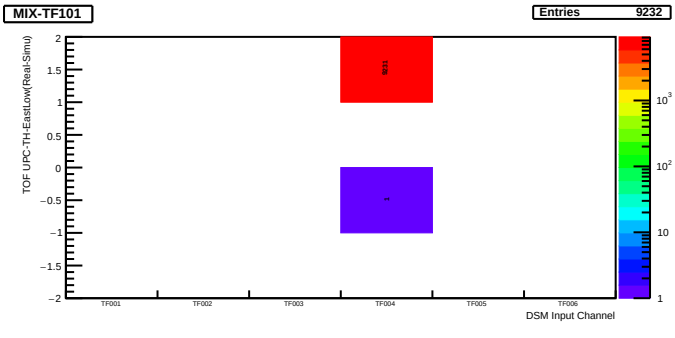

MIX-TF101
Entries 62982
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

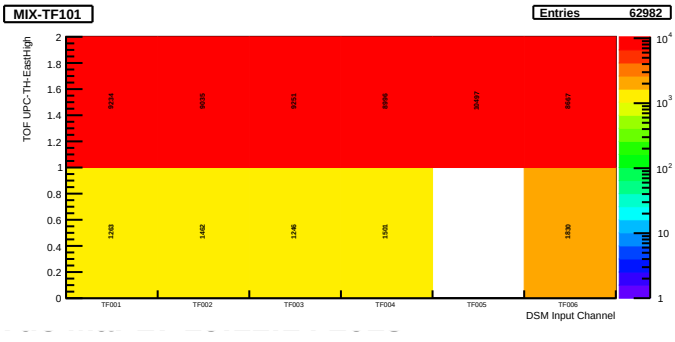

MIX-TF101
Entries 8997
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

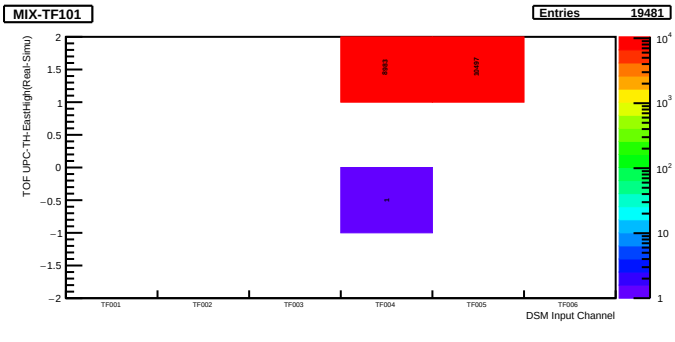

MIX-TF101
Entries 62982
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

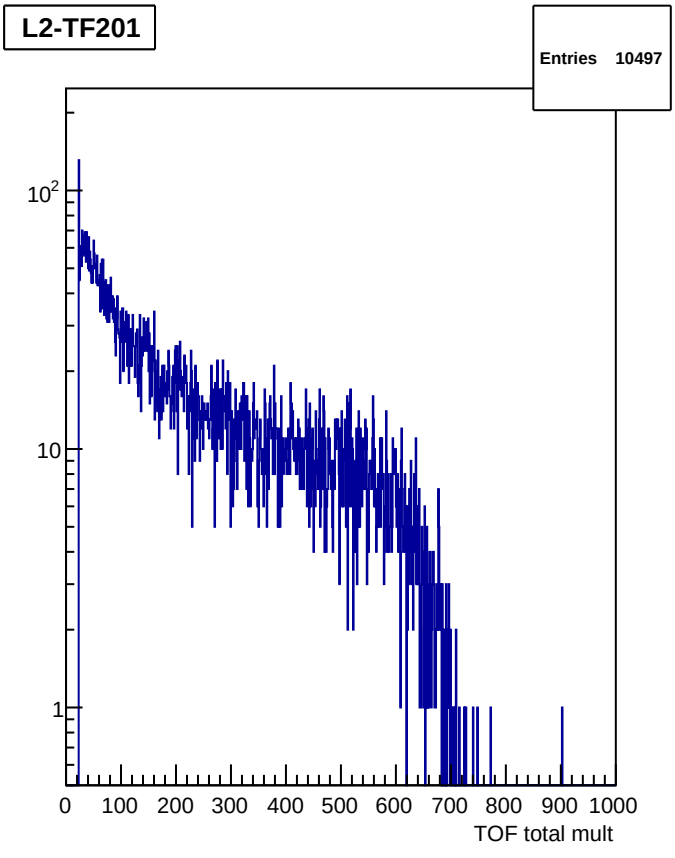

MIX-TF101
Entries 9229
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

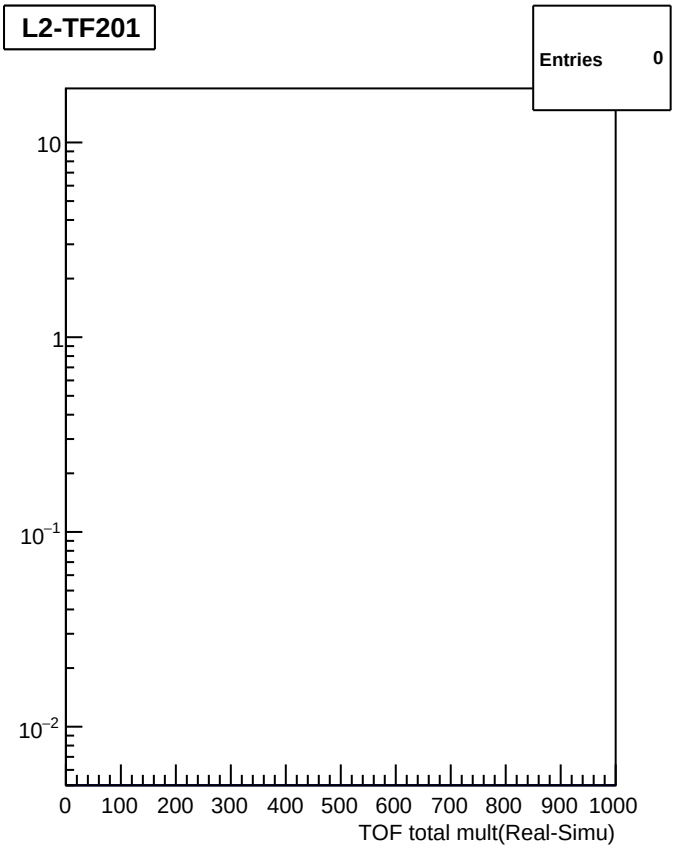

MIX-TF101
Entries 62982
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

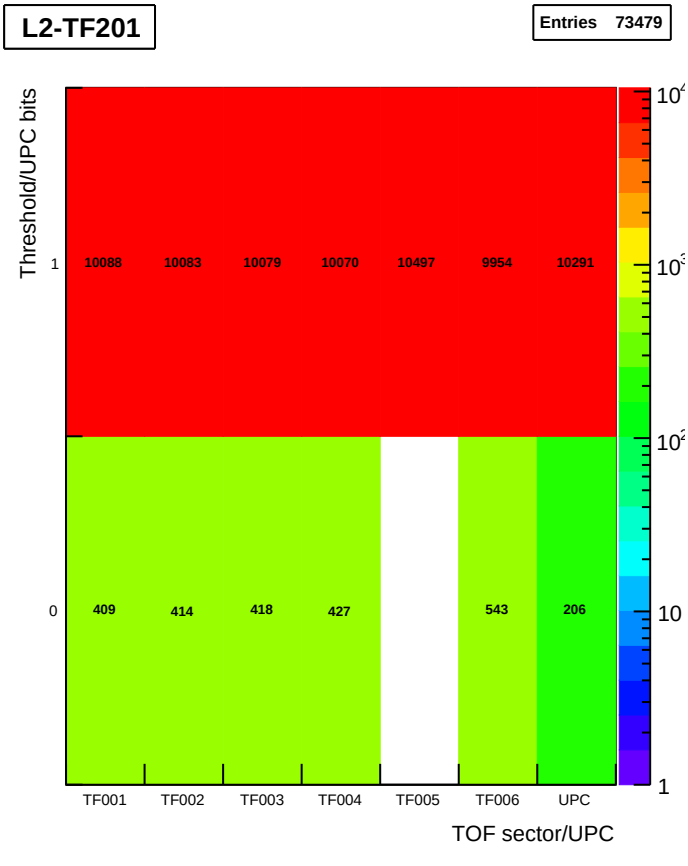

MIX-TF101
Entries 9232
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

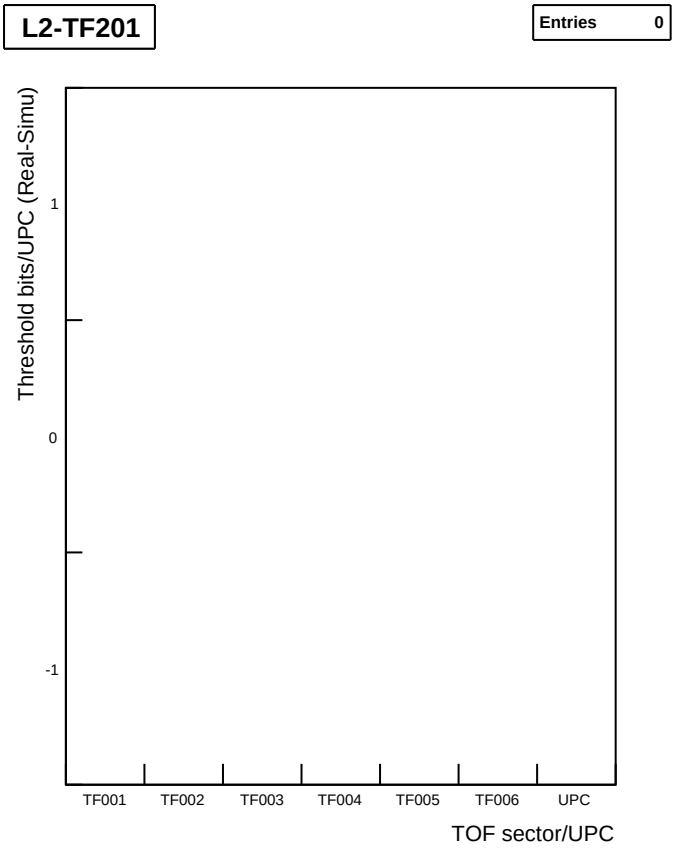

MIX-TF101
Entries 62982
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

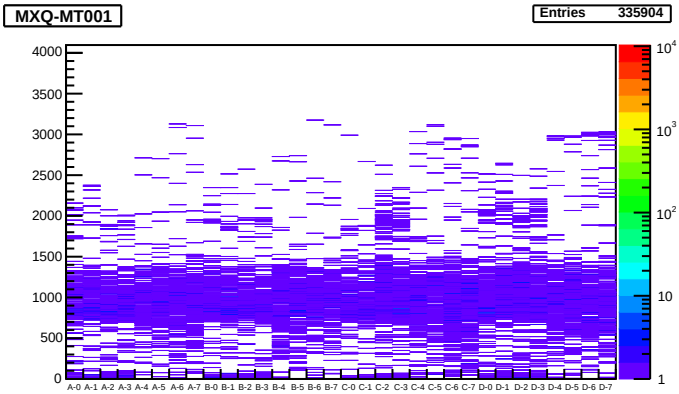

MIX-TF101
Entries 19481
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

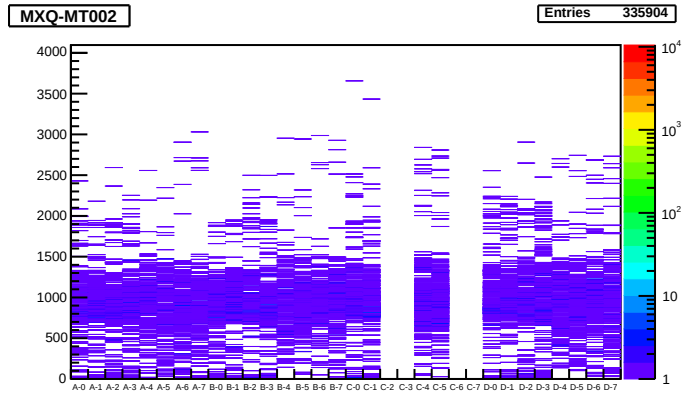

L2-TF201
Entries 10497
TOF total mult

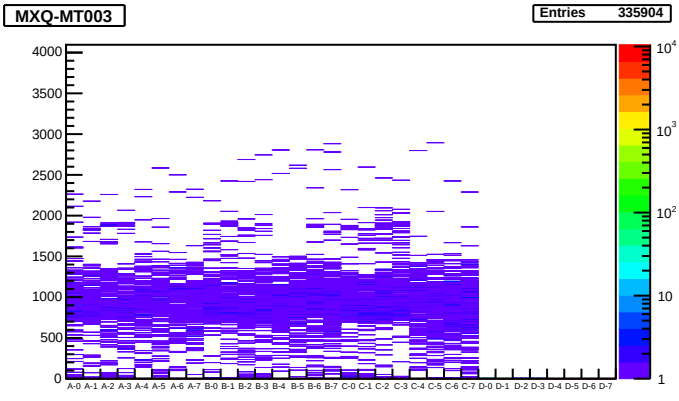

L2-TF201
Entries 0
TOF total mult(Real-Simu)

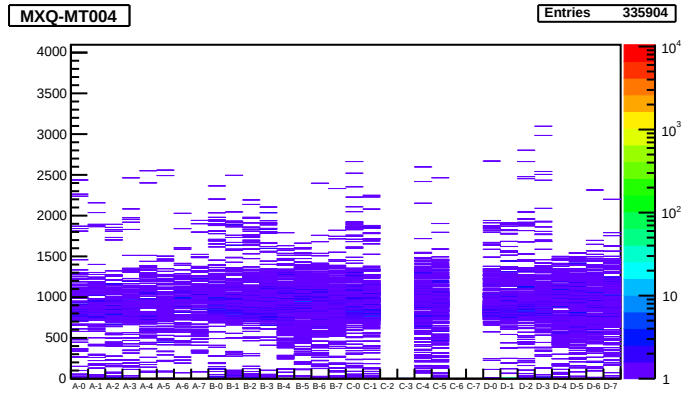

L2-TF201
Entries 73479
Threshold/UPC bits
10088
10083
10079
10070
10497
9954
10291
409
414
418
427
543
206
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

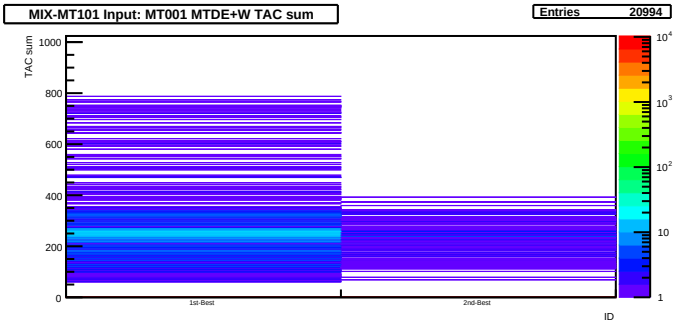

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

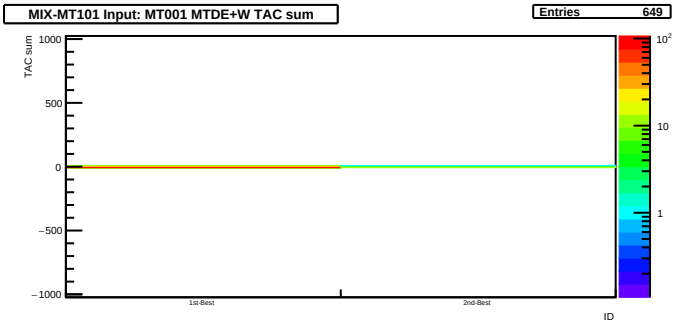

MXQ-MT001
Entries 335904
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

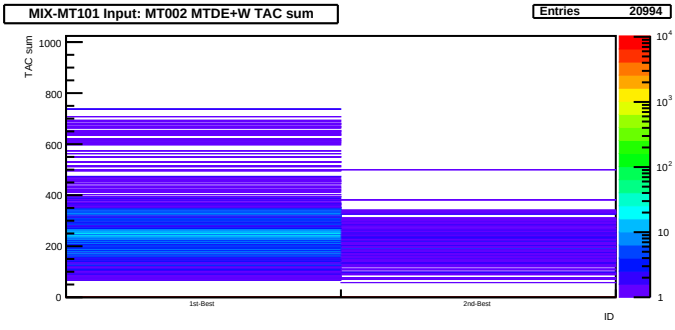

MXQ-MT002
Entries 335904
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

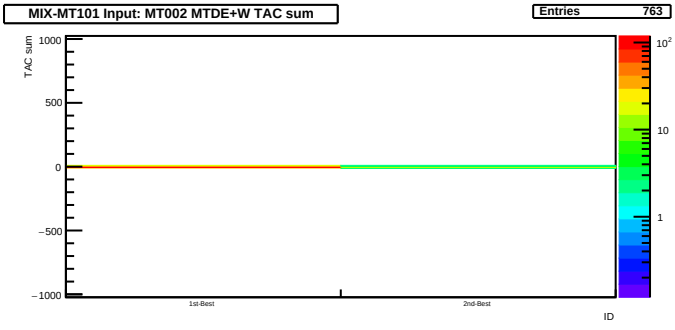

MXQ-MT003
Entries 335904
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

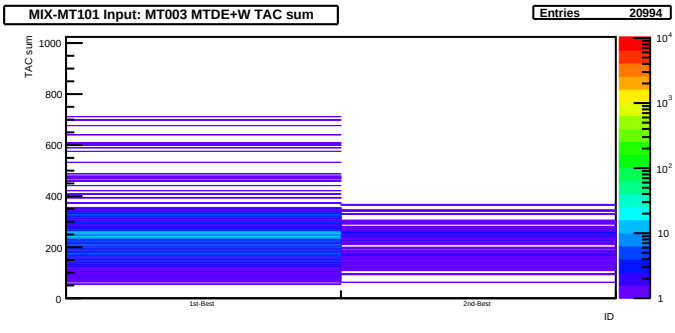

MXQ-MT004
Entries 335904
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

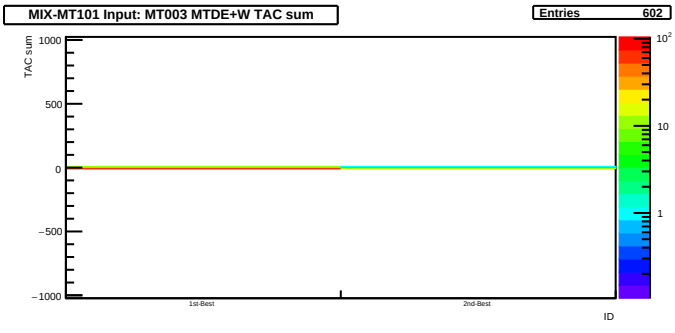

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 20994
TAC sum
1st-Best
2nd-Best
ID

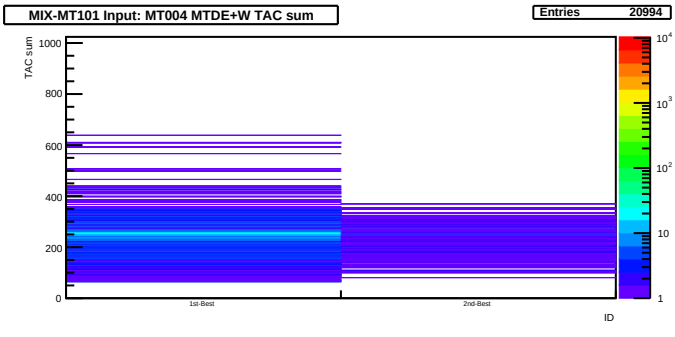

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 649
TAC sum
1st-Best
2nd-Best
ID

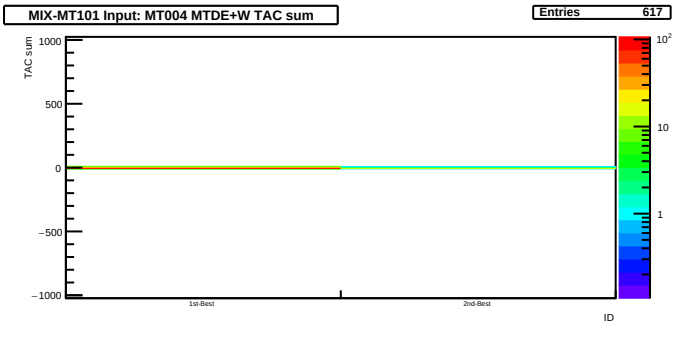

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 20994
TAC sum
1st-Best
2nd-Best
ID

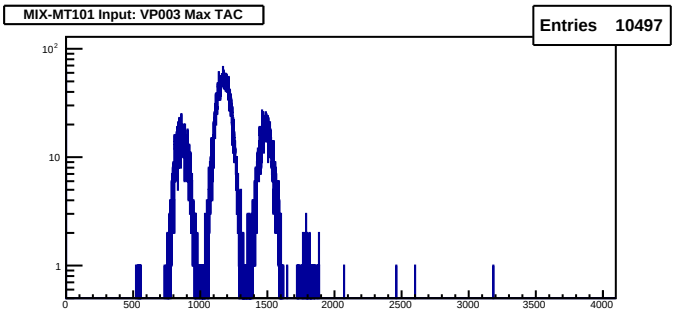

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 763
TAC sum
1st-Best
2nd-Best
ID

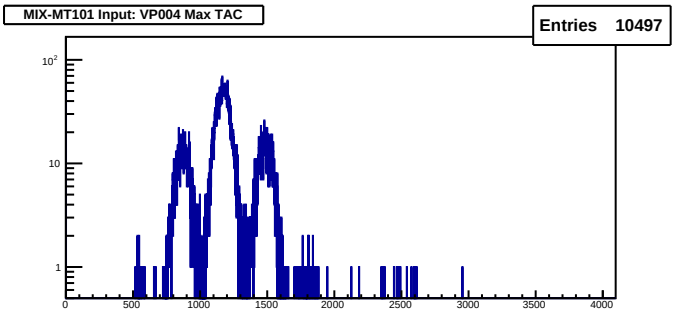

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 20994
TAC sum
1st-Best
2nd-Best
ID

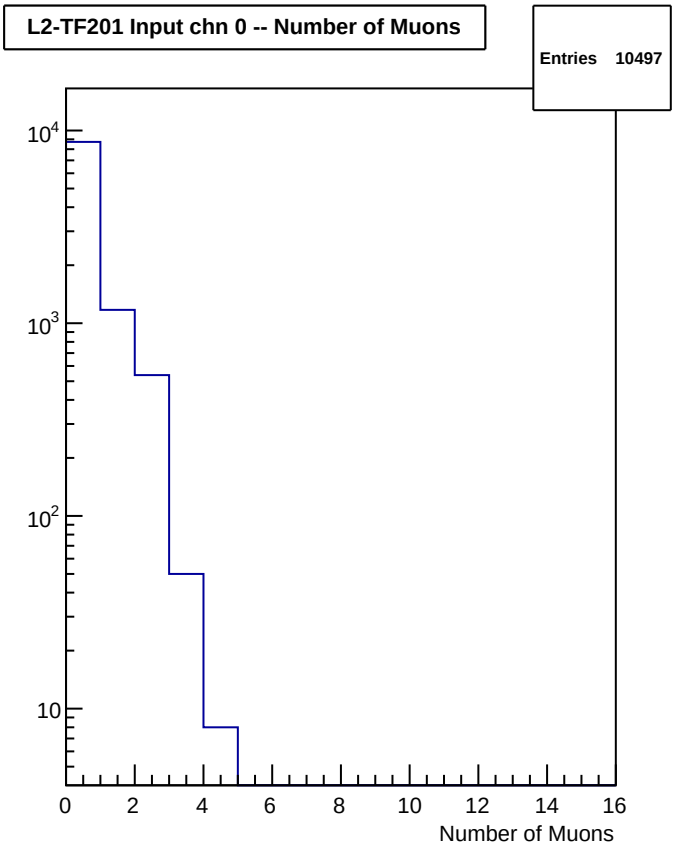

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 602
TAC sum
1st-Best
2nd-Best
ID

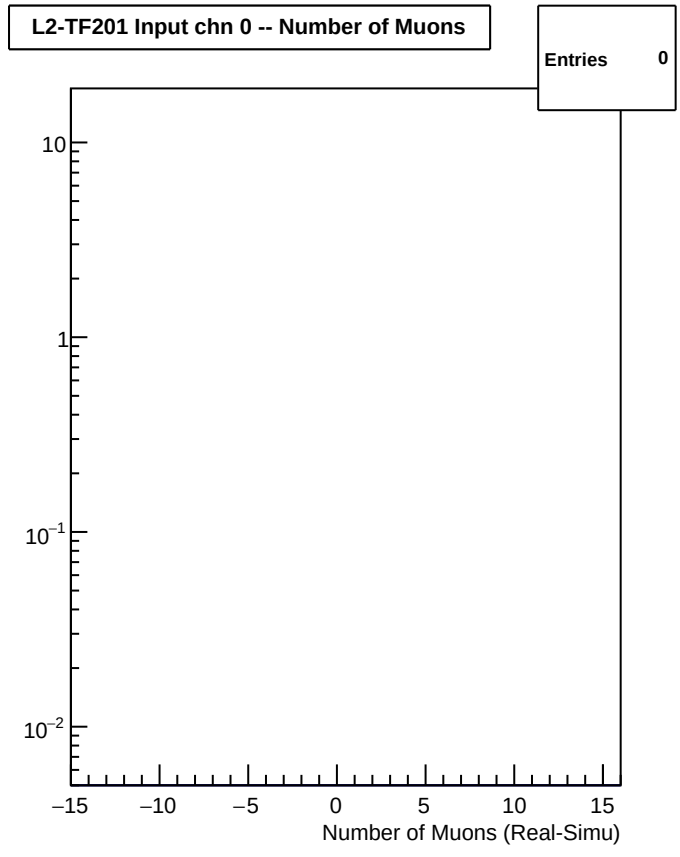

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 20994
TAC sum
1st-Best
2nd-Best
ID

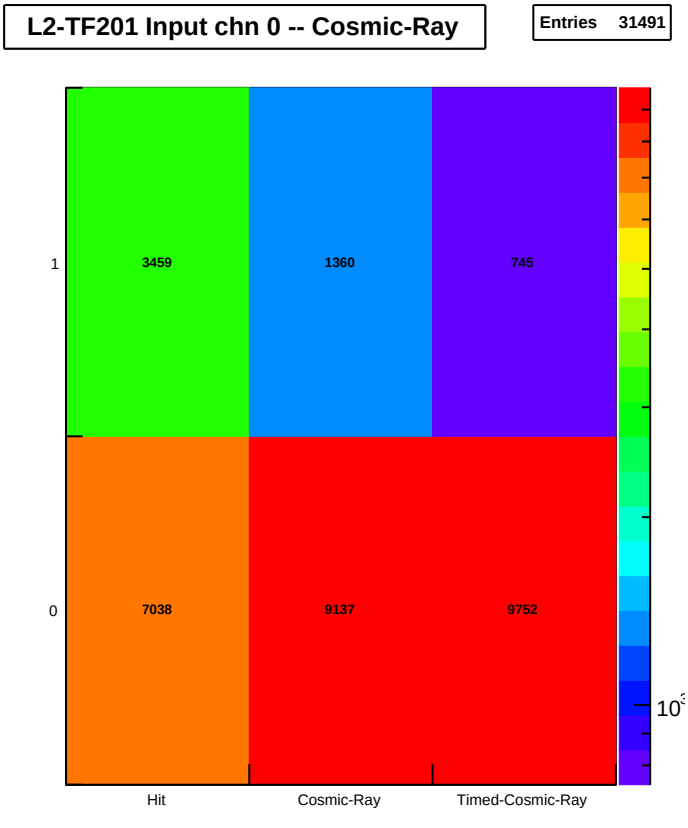

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 617
TAC sum
1st-Best
2nd-Best
ID

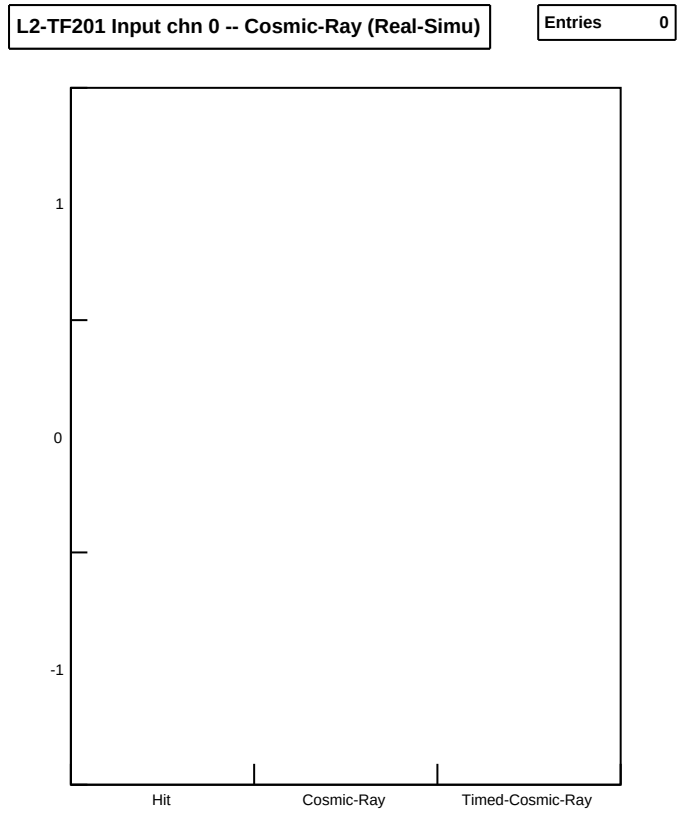

MIX-MT101 Input: VP003 Max TAC
Entries 10497

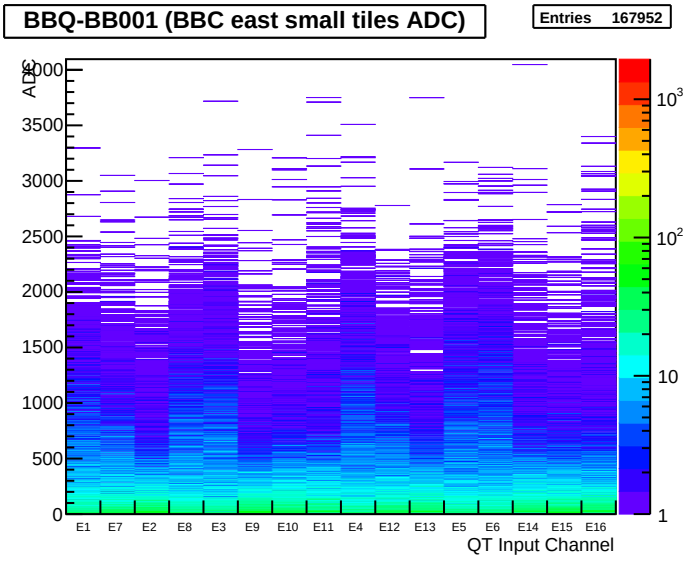

MIX-MT101 Input: VP004 Max TAC
Entries 10497

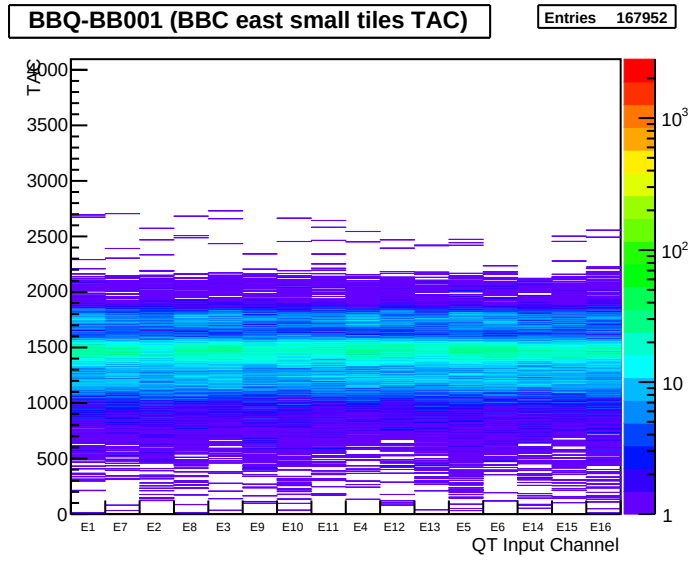

L2-TF201 Input chn 0 -- Number of Muons
Entries 10497
Number of Muons

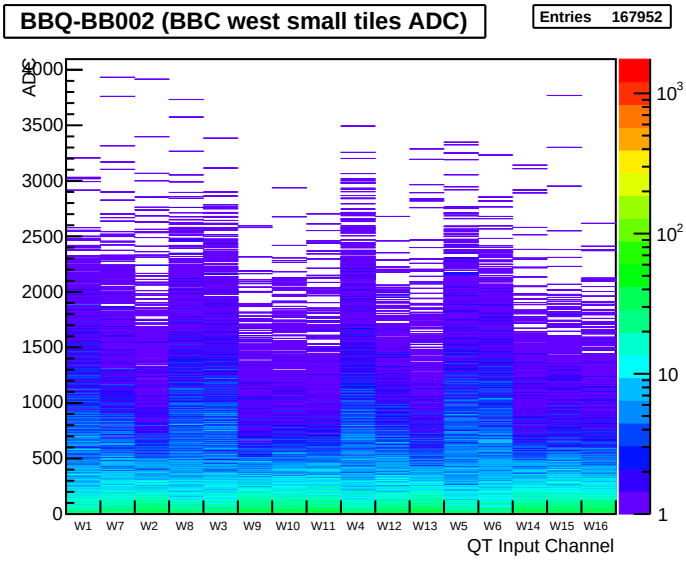

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

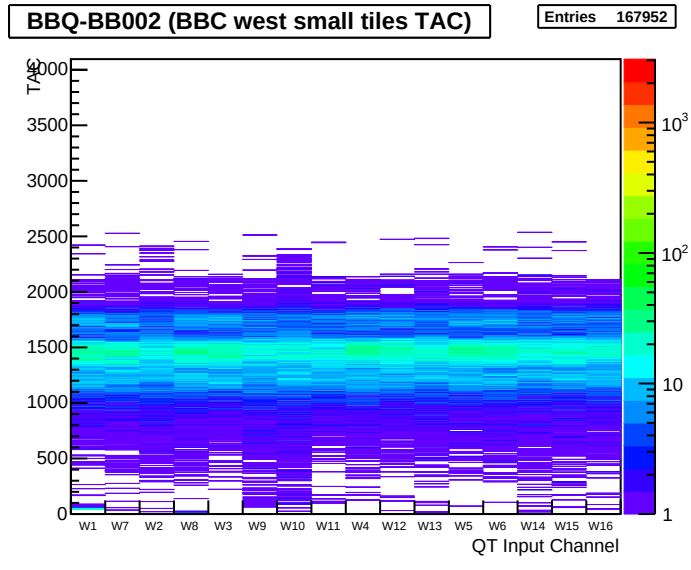

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 31491
3459
1360
745
7038
9137
9752
Hit
Cosmic-Ray
Timed-Cosmic-Ray

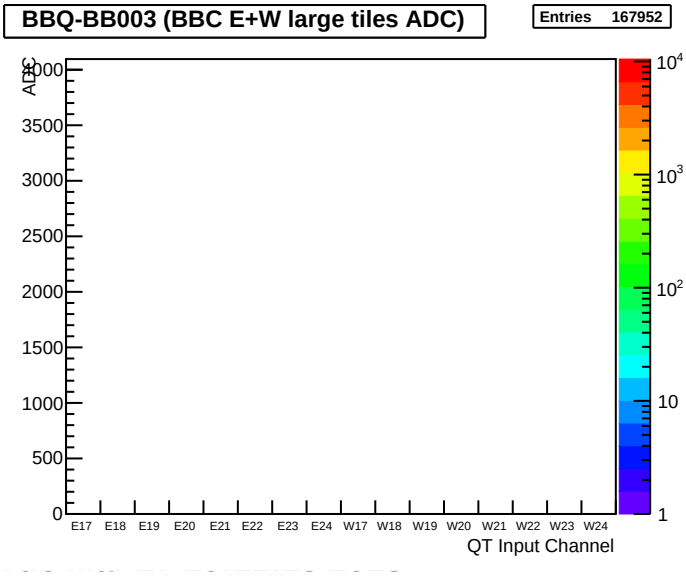

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

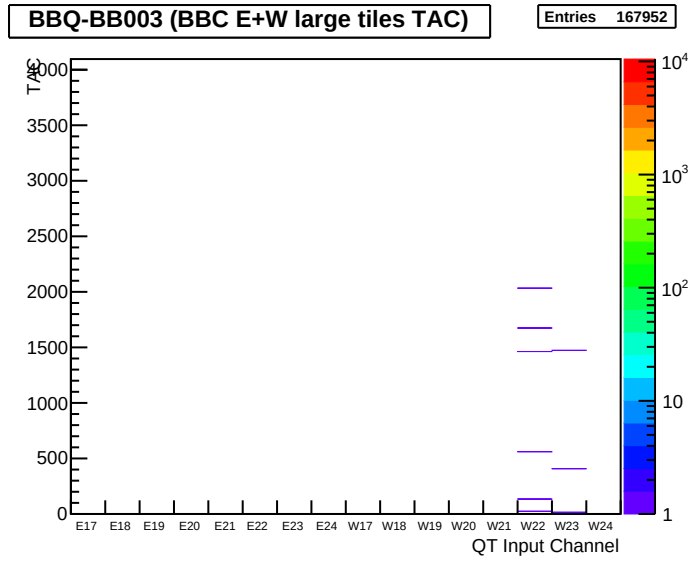

BBQ-BB001 (BBC east small tiles ADC)
Entries 167952
ADC
QT Input Channel

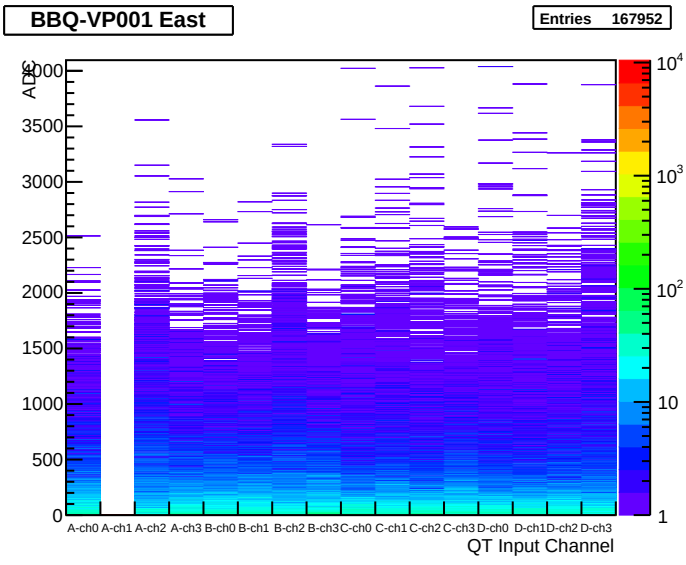

BBQ-BB001 (BBC east small tiles TAC)
Entries 167952
TAC
QT Input Channel

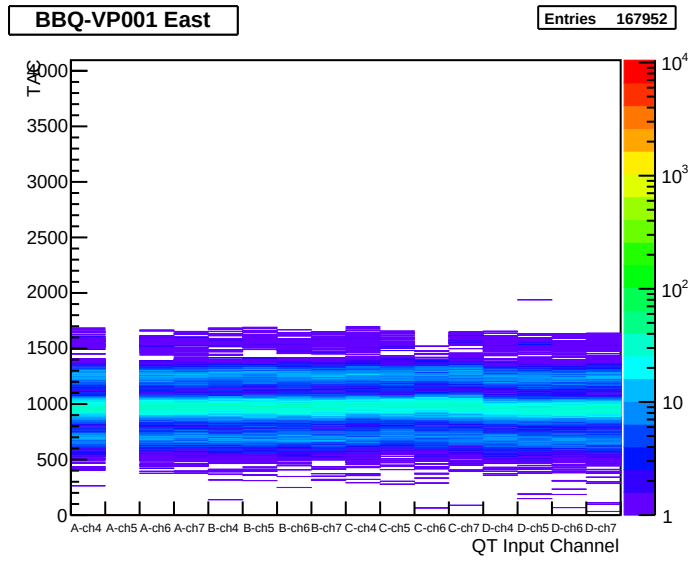

BBQ-BB002 (BBC west small tiles ADC)
Entries 167952
ADC
QT Input Channel

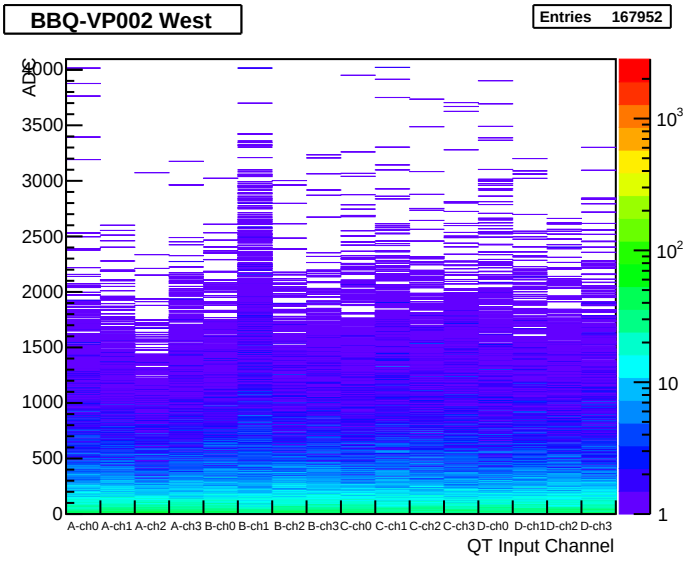

BBQ-BB002 (BBC west small tiles TAC)
Entries 167952
TAC
QT Input Channel

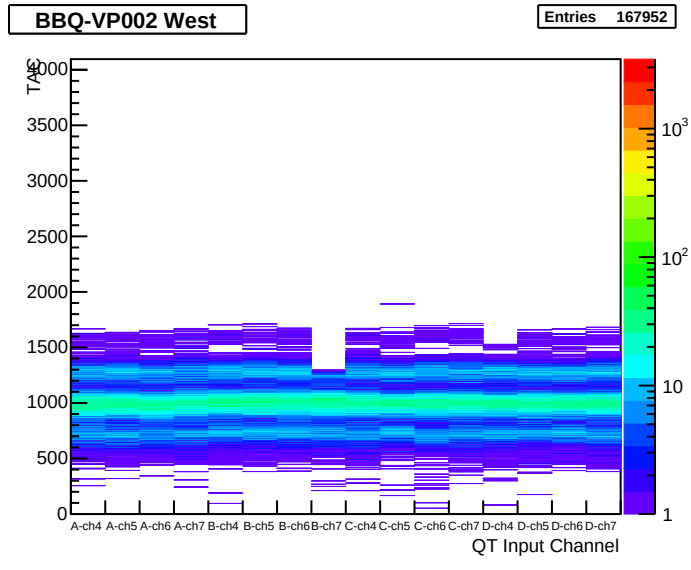

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 167952
ADC
QT Input Channel

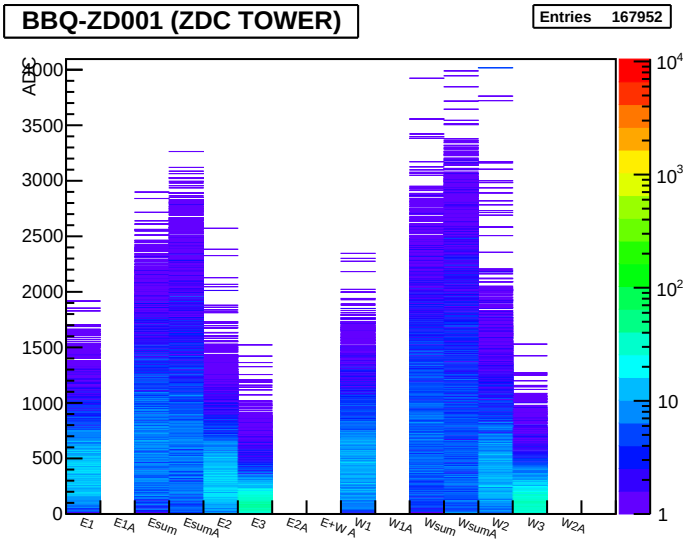

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 167952
TAC
QT Input Channel

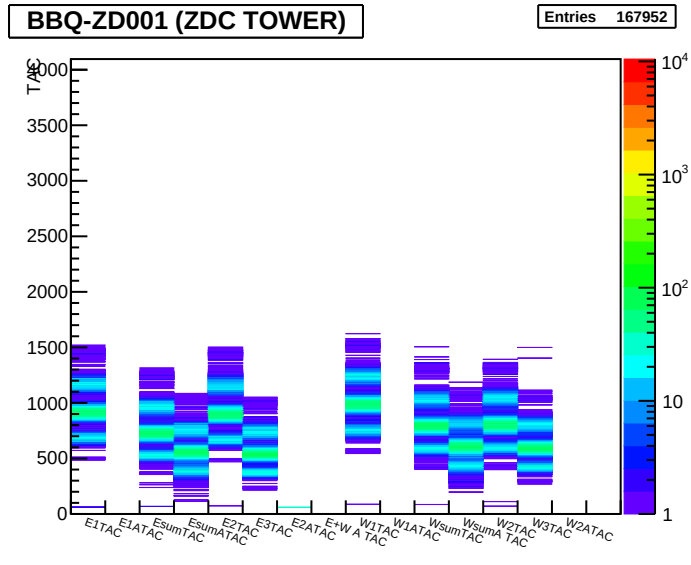

BBQ-VP001 East
Entries 167952
ADC
QT Input Channel

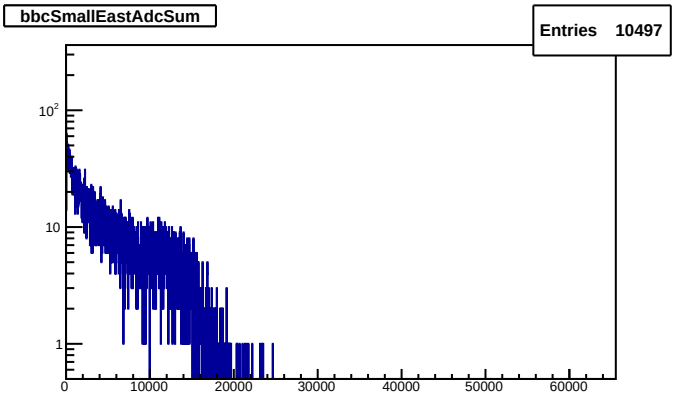

BBQ-VP001 East
Entries 167952
TAC
QT Input Channel

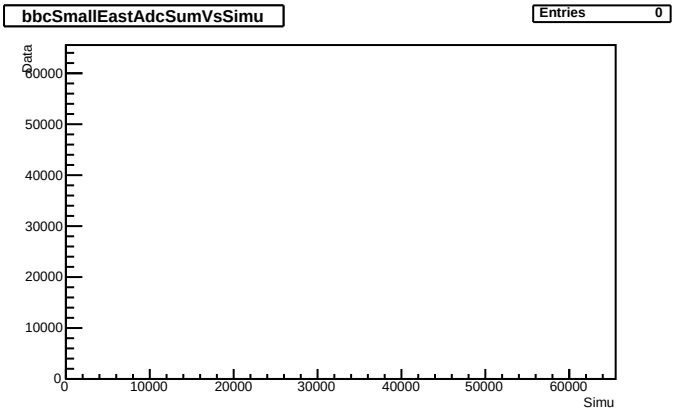

BBQ-VP002 West
Entries 167952
ADC
QT Input Channel

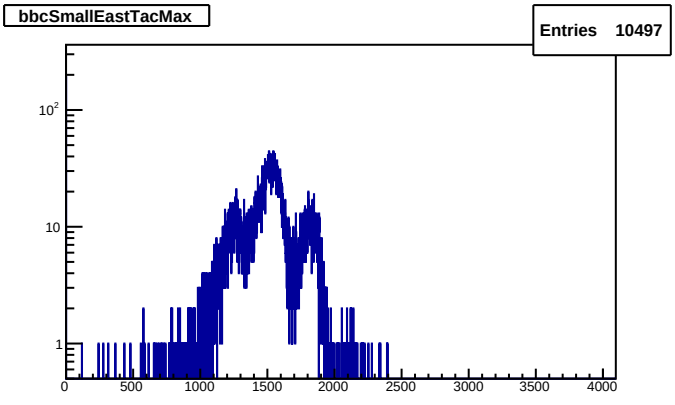

BBQ-VP002 West
Entries 167952
TAC
QT Input Channel

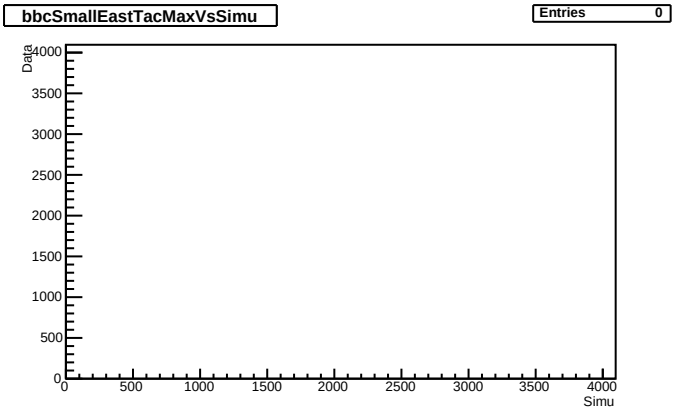

BBQ-ZD001 (ZDC TOWER)
Entries 167952
ADC

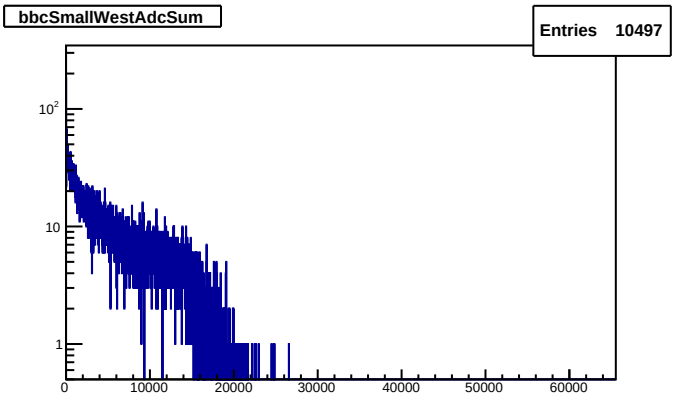

BBQ-ZD001 (ZDC TOWER)
Entries 167952
TAC

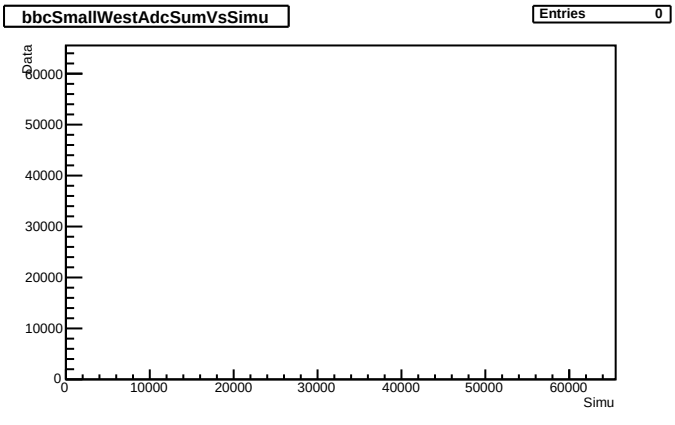

bbcSmallEastAdcSum
Entries 10497

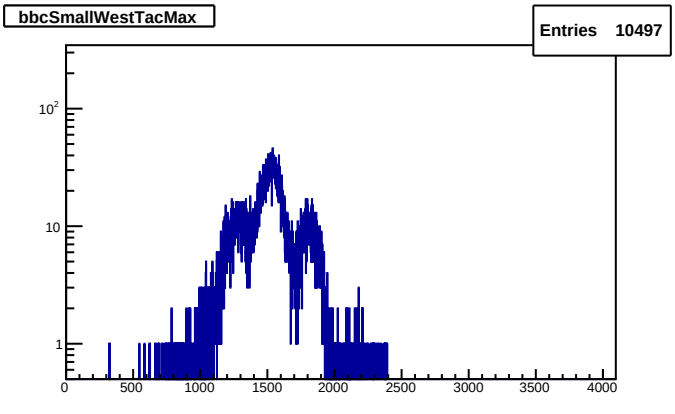

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

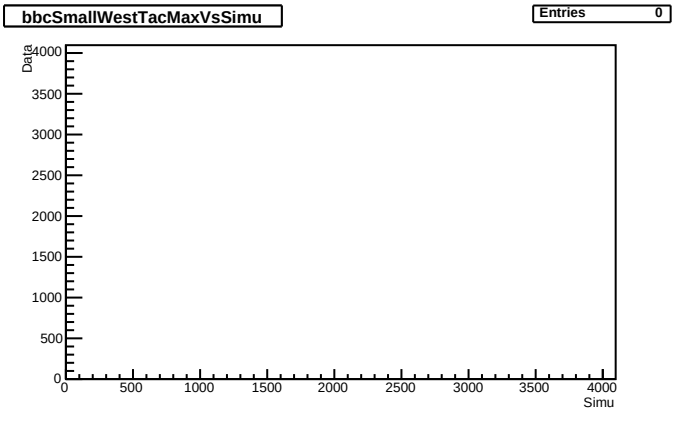

bbcSmallEastTacMax
Entries 10497

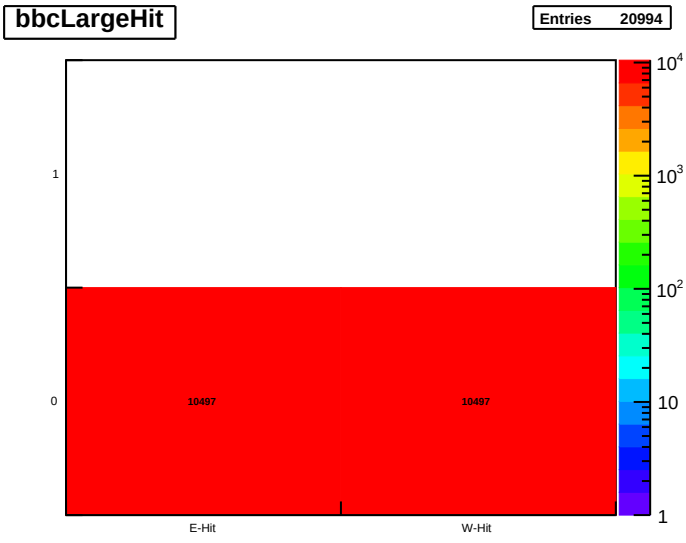

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

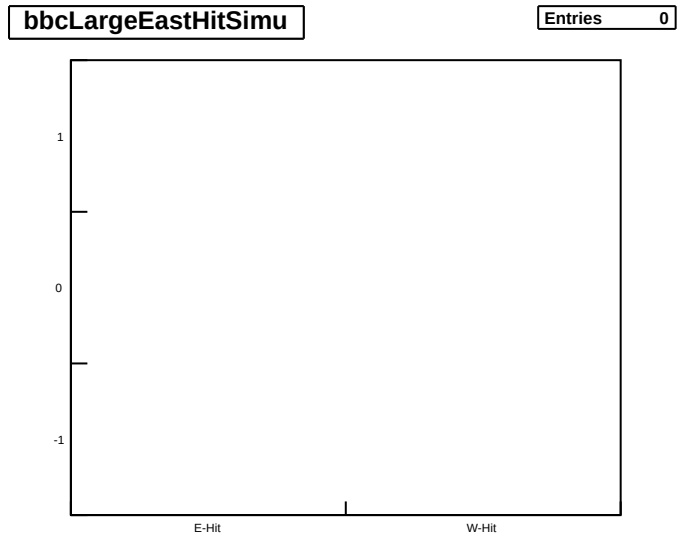

bbcSmallWestAdcSum
Entries 10497

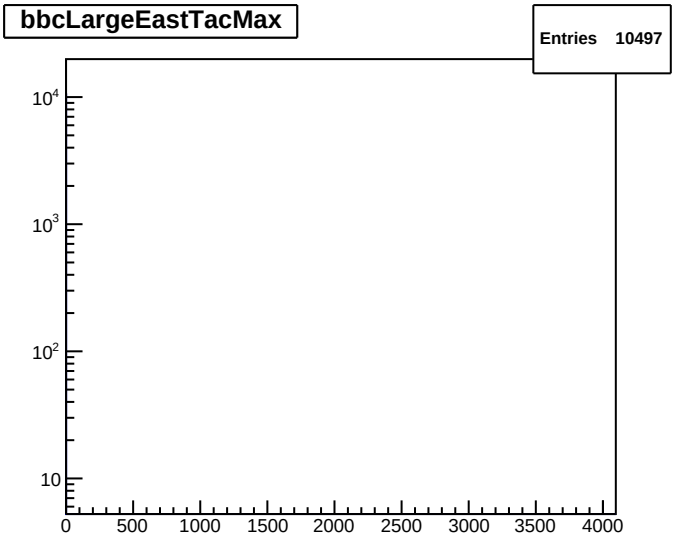

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

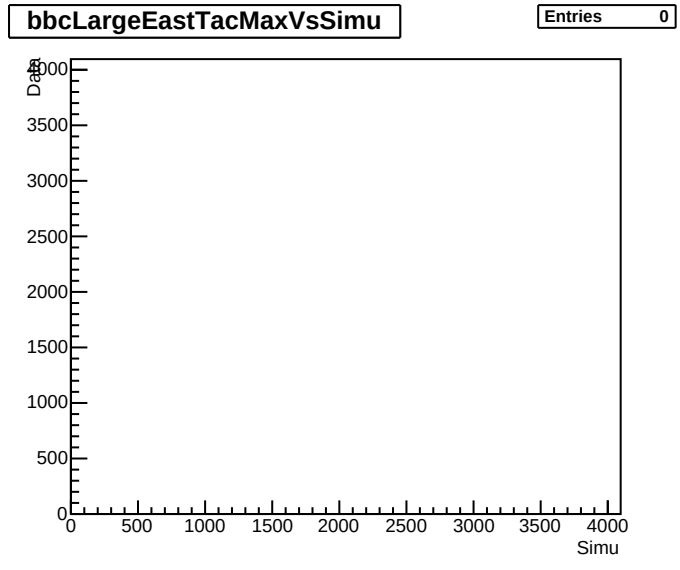

bbcSmallWestTacMax
Entries 10497

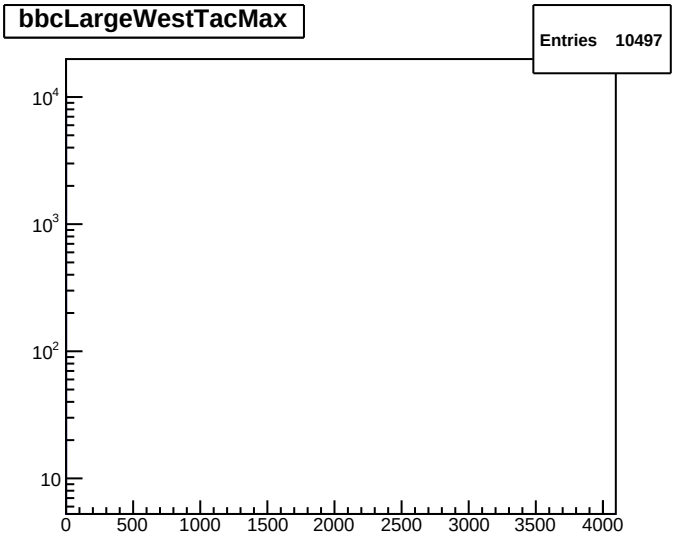

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

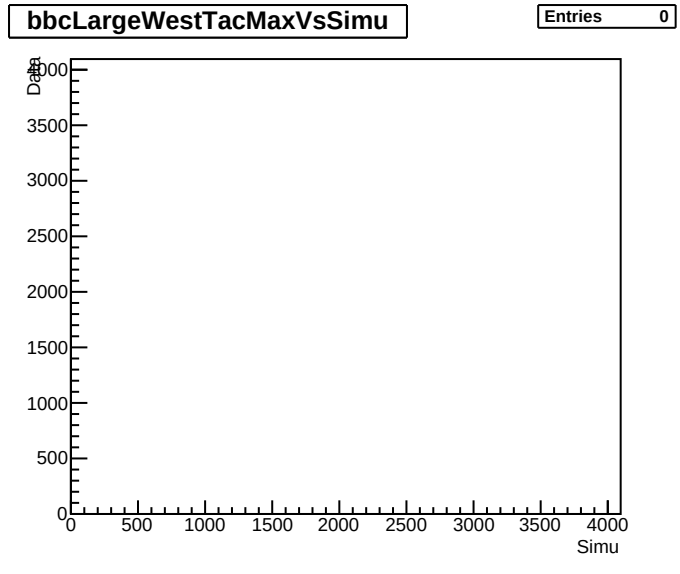

bbcLargeHit
Entries 20994
10497
10497
E-Hit
W-Hit

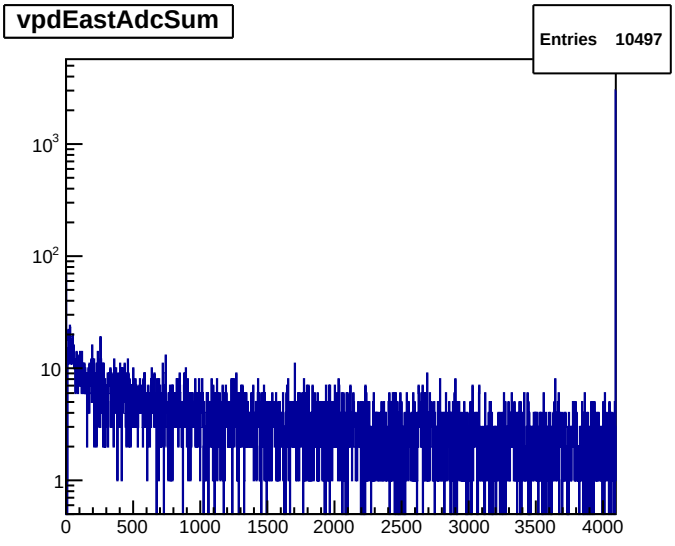

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

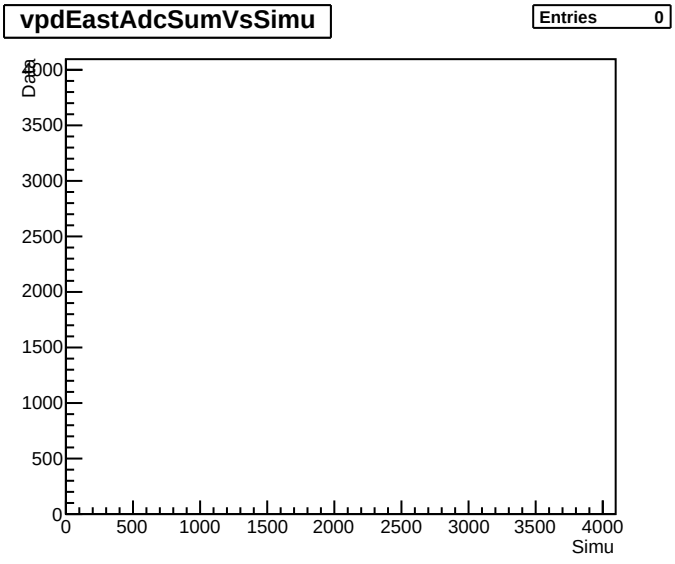

bbcLargeEastTacMax
Entries 10497

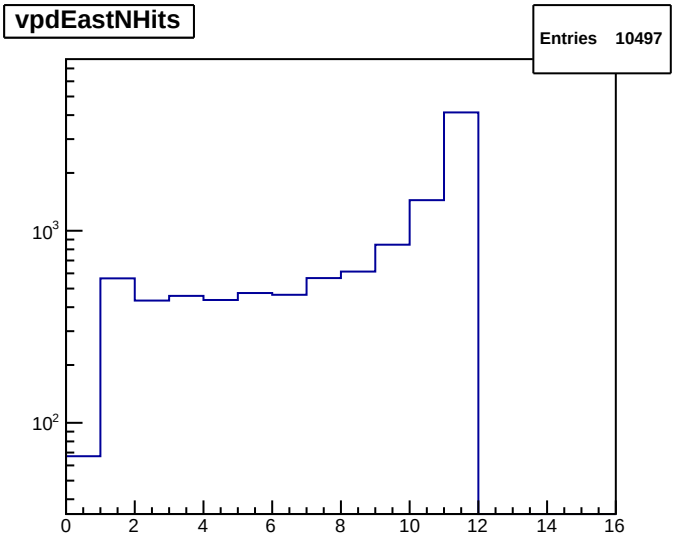

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

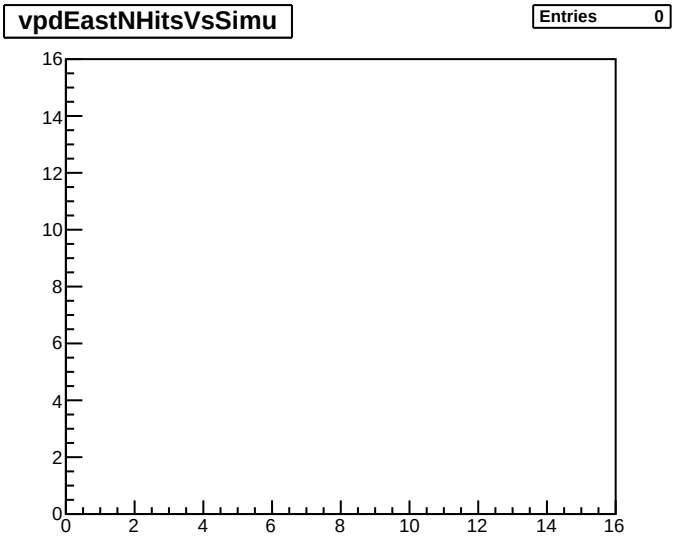

bbcLargeWestTacMax
Entries 10497

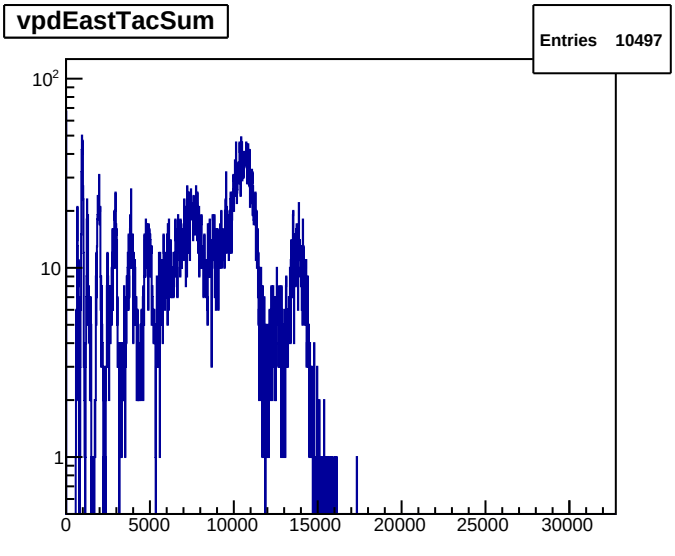

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

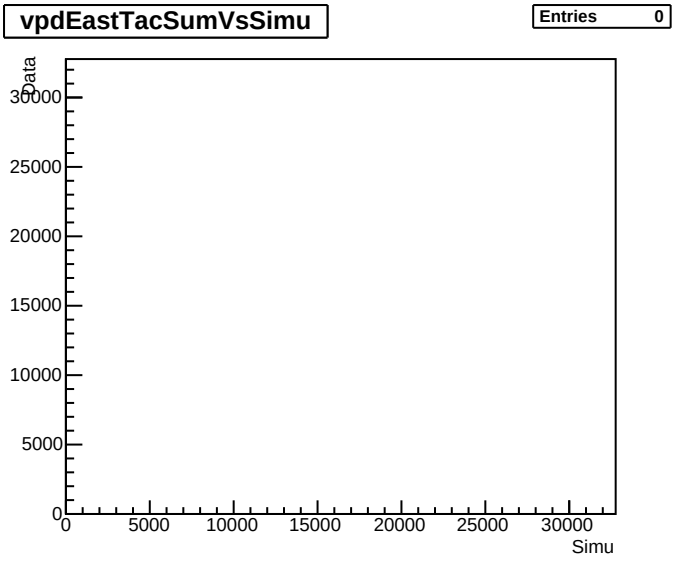

vpdEastAdcSum
Entries 10497

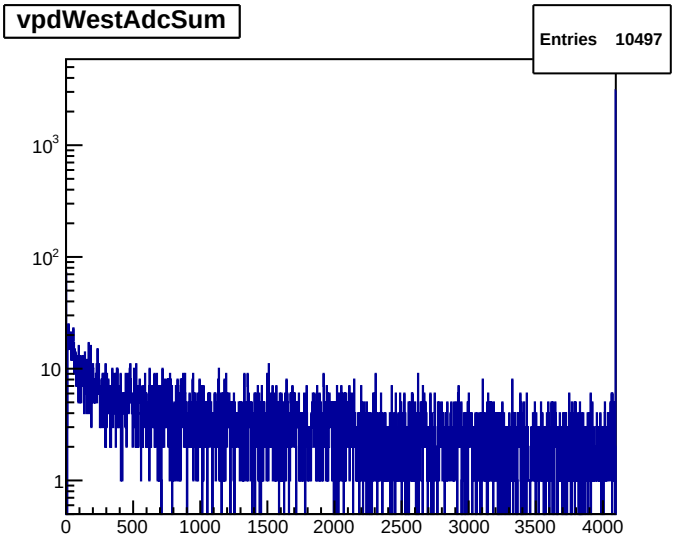

vpdEastAdcSumVsSimu
Entries 0
Data
Simu

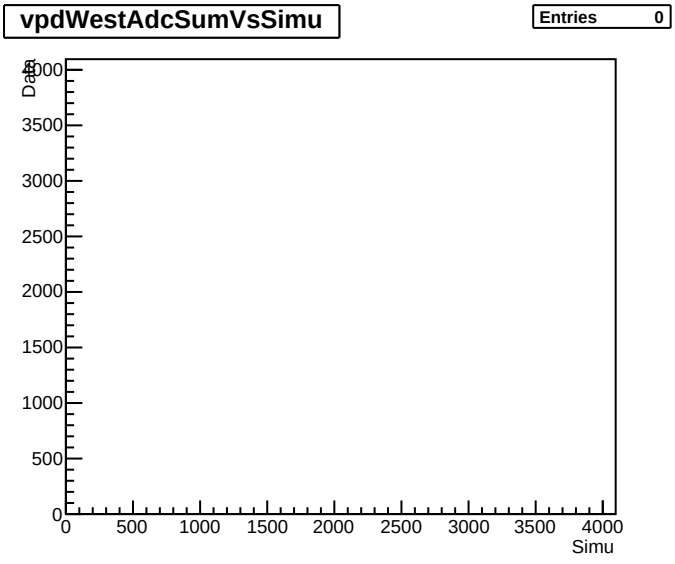

vpdEastNHits
Entries 10497

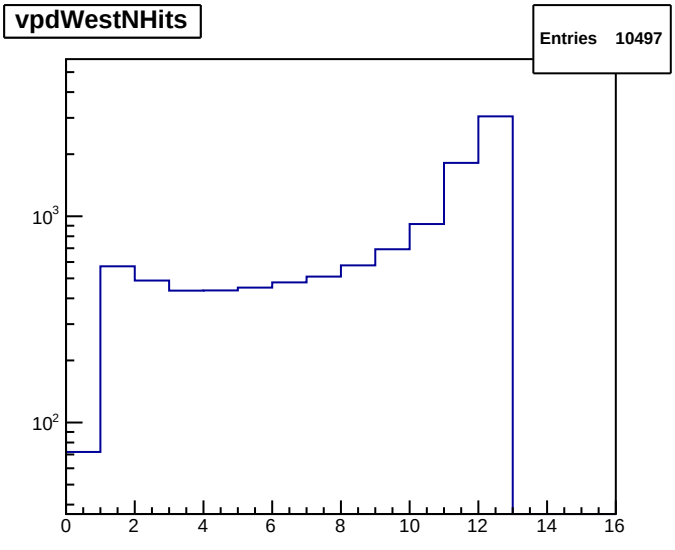

vpdEastNHitsVsSimu
Entries 0

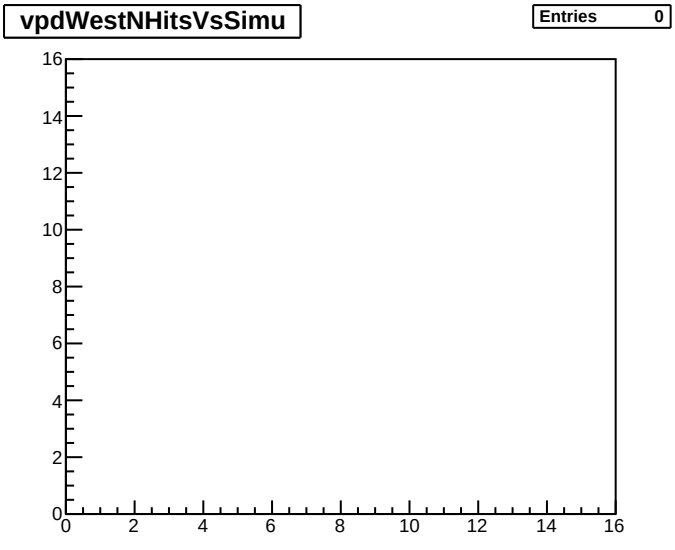

vpdEastTacSum
Entries 10497

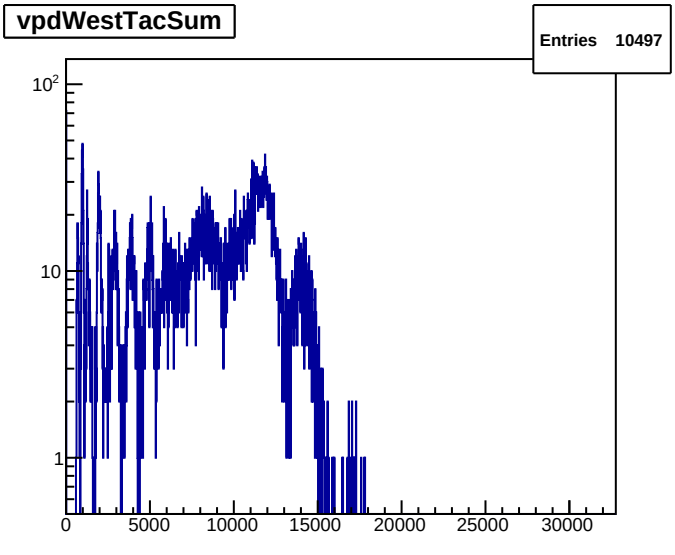

vpdEastTacSumVsSimu
Entries 0
Data
Simu

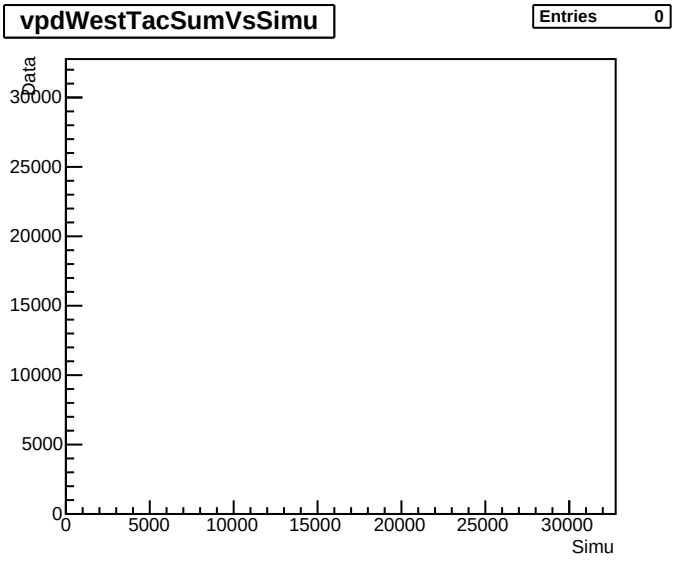

vpdWestAdcSum
Entries 10497
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

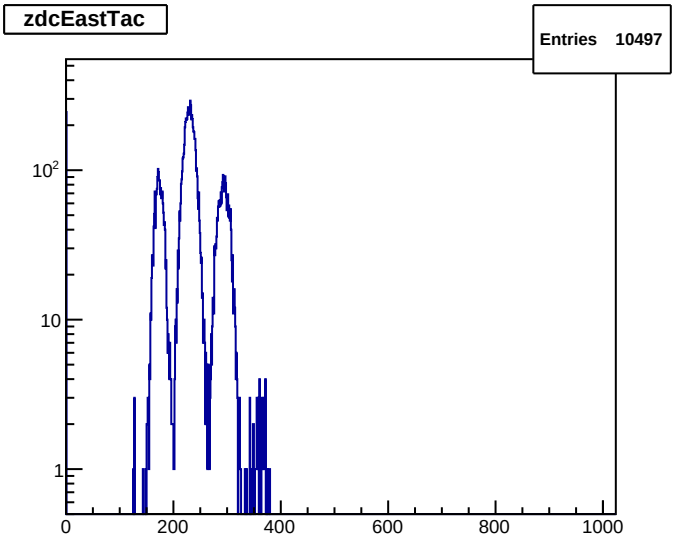

vpdWestAdcSumVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

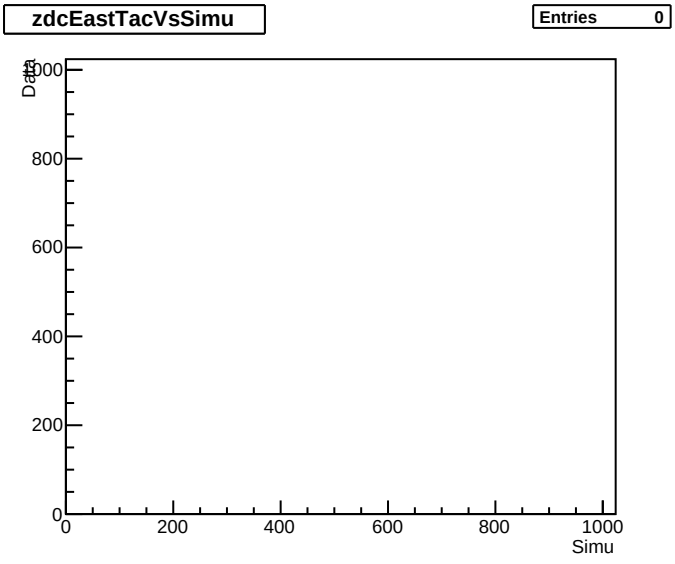

vpdWestNHits
Entries 10497
10^3
10^2
0 2 4 6 8 10 12 14 16

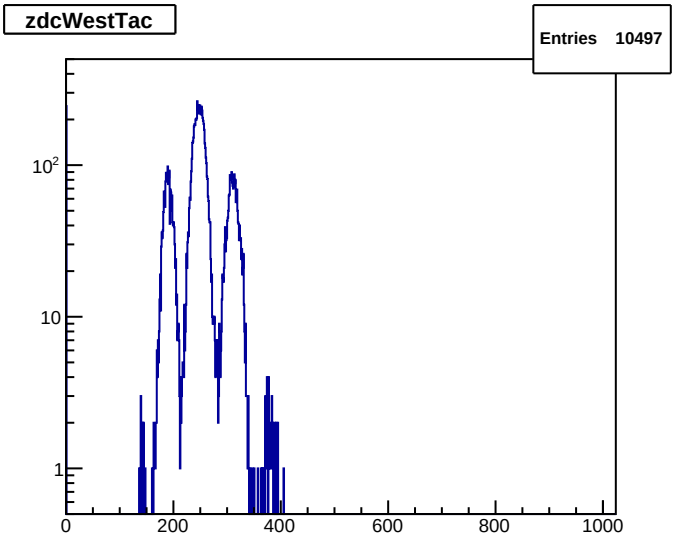

vpdWestNHitsVsSimu
Entries 0
16
14
12
10
8
6
4
2
0
0 2 4 6 8 10 12 14 16

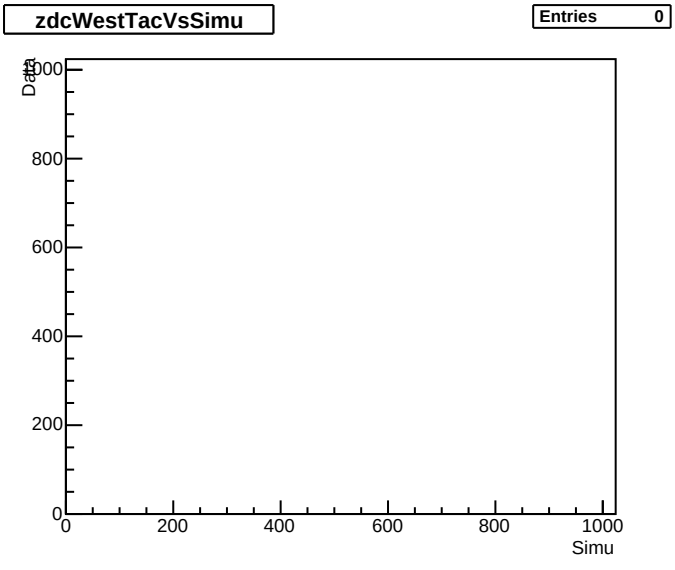

vpdWestTacSum
Entries 10497
10^2
10
1
0 5000 10000 15000 20000 25000 30000

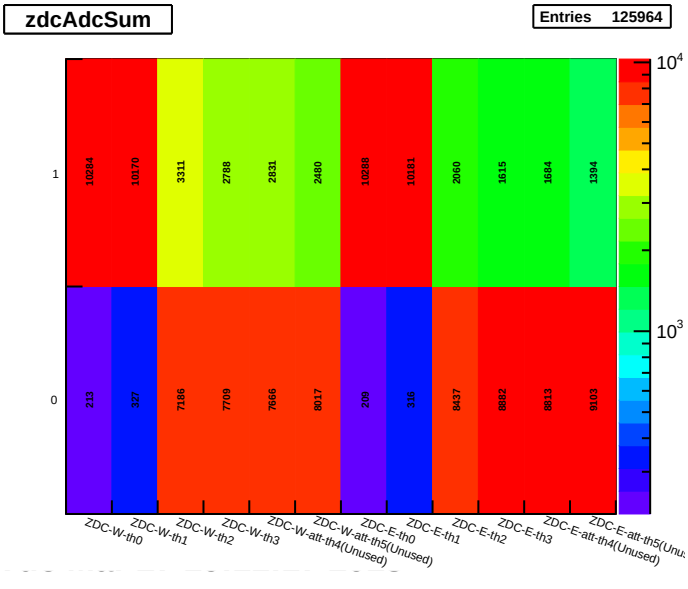

vpdWestTacSumVsSimu
Entries 0
Data
30000
25000
20000
15000
10000
5000
0
0 5000 10000 15000 20000 25000 30000
Simu

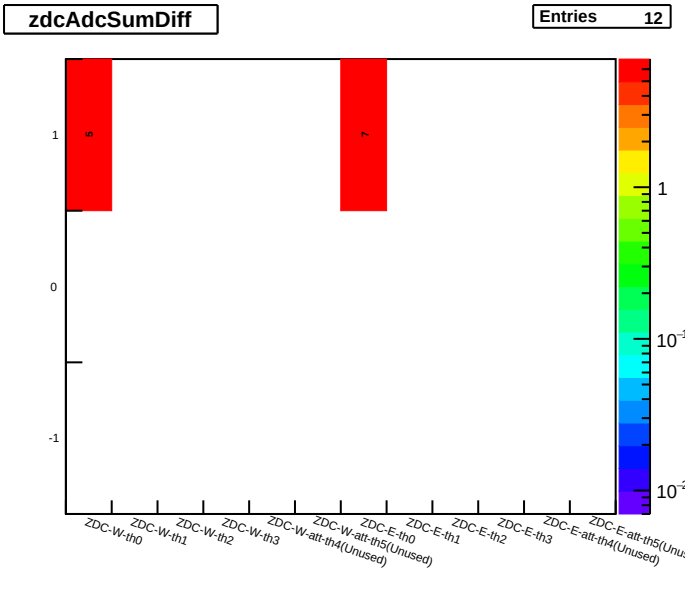

zdcEastTac
Entries 10497
10^2
10
1
0
200
400
600
800
1000

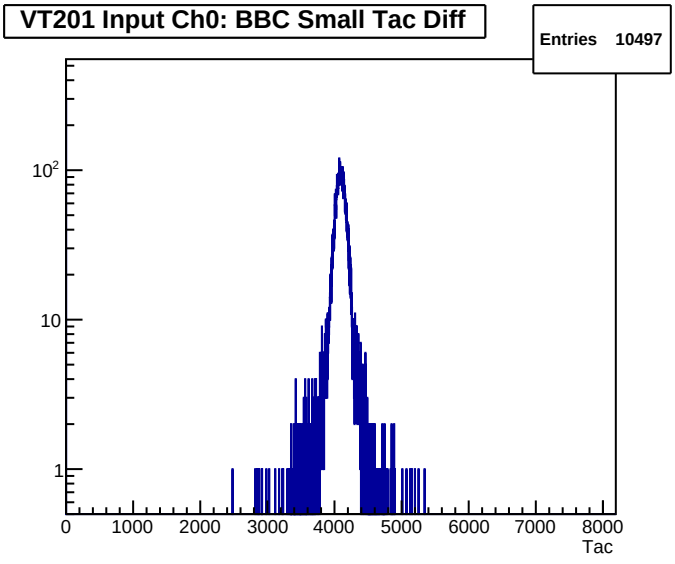

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

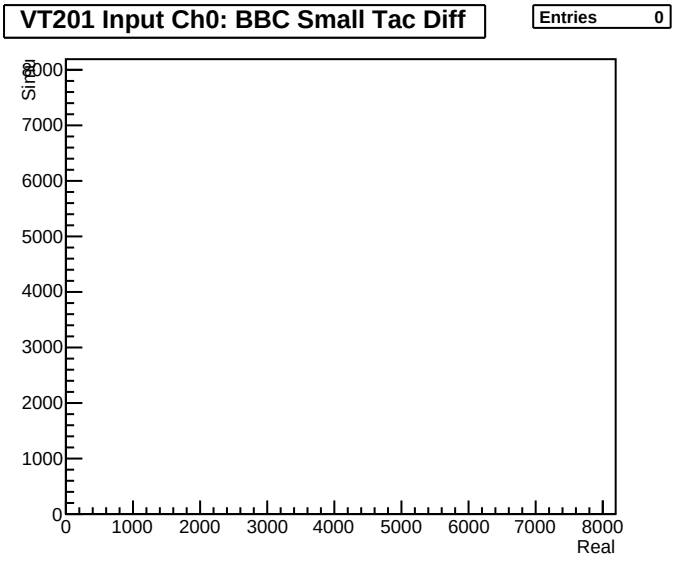

zdcWestTac
Entries 10497
10^2
10
1
0
200
400
600
800
1000

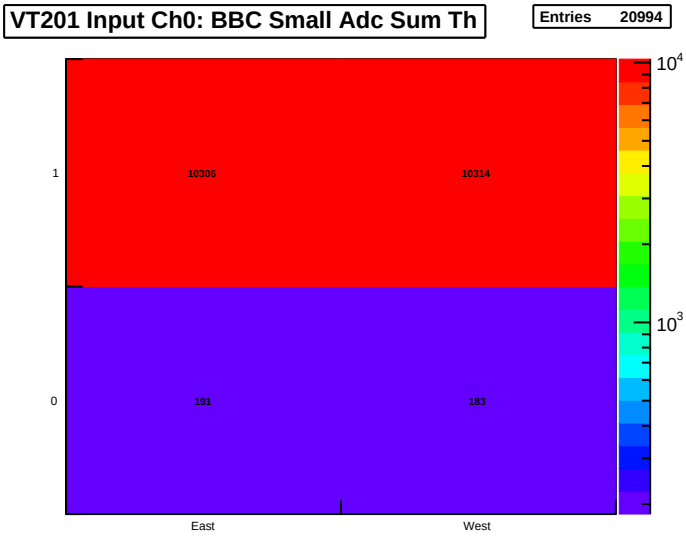

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

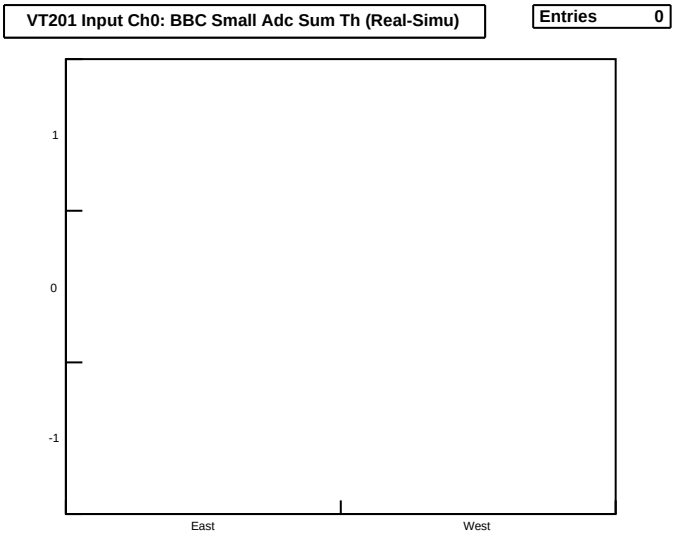

zdcAdcSum
Entries 125964
1
0
10284
10170
3311
2788
2831
2480
10288
10181
2060
1615
1684
1394
213
327
7186
7709
7666
8017
209
316
8437
8882
8813
9103
10^4
10^3
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

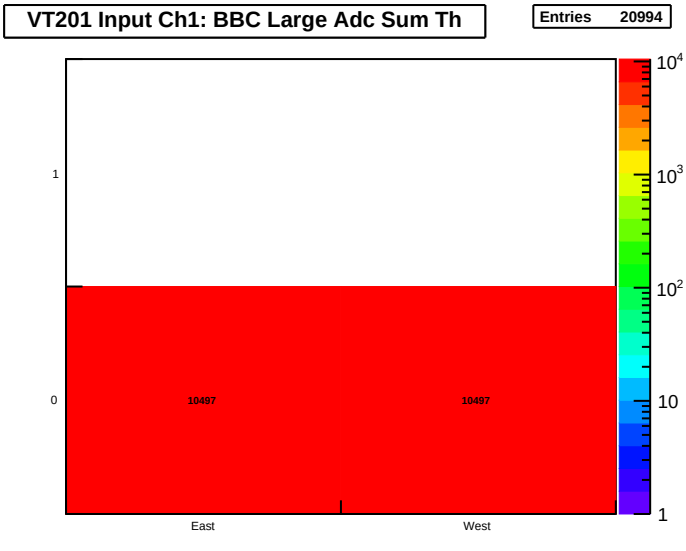

zdcAdcSumDiff
Entries 12
1
0
-1
5
7
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

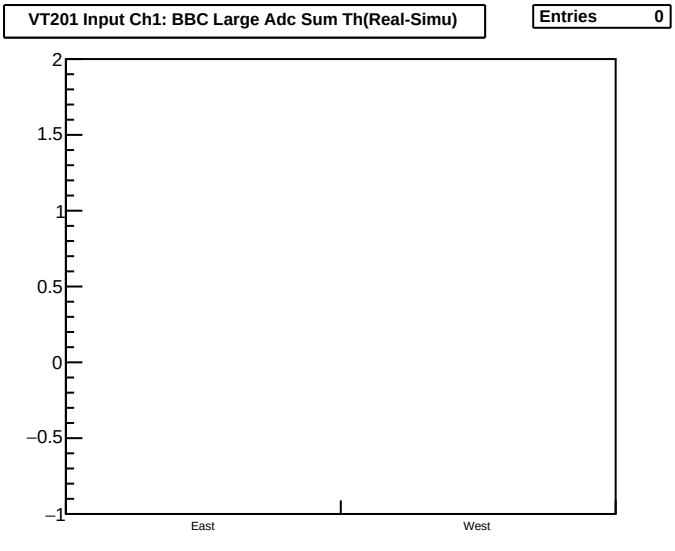

VT201 Input Ch0: BBC Small Tac Diff
Entries 10497
Tac

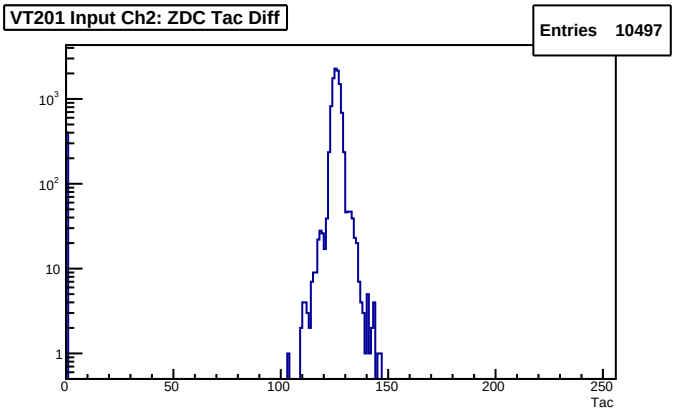

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

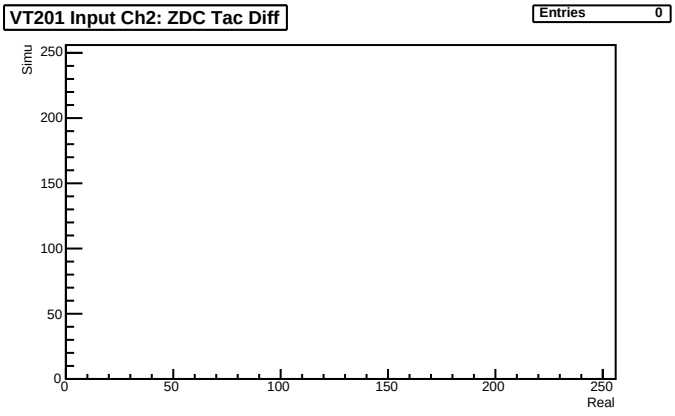

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 20994
10306
10314
191
183
East
West

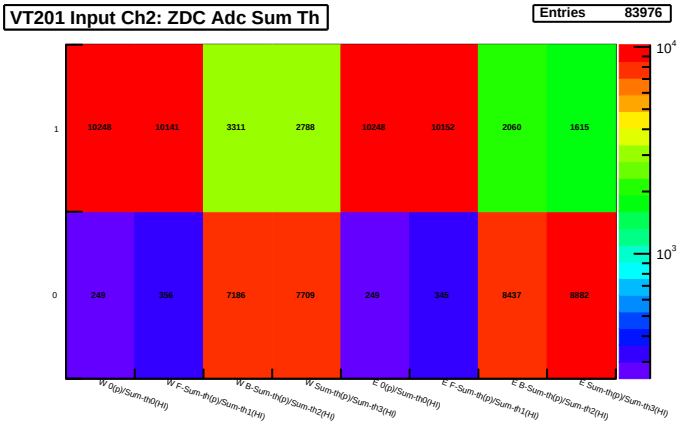

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

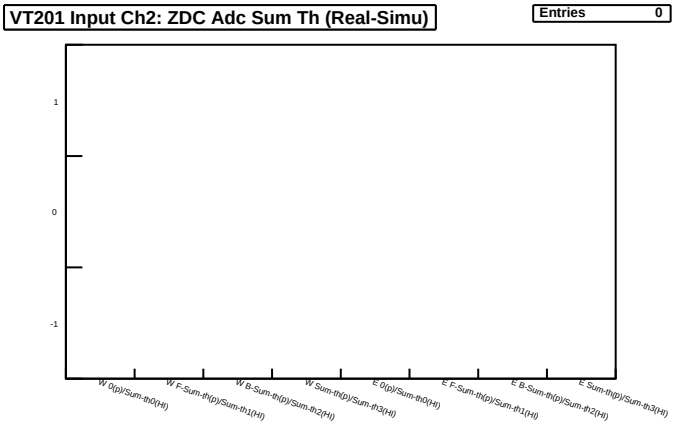

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 20994
10497
10497
East
West

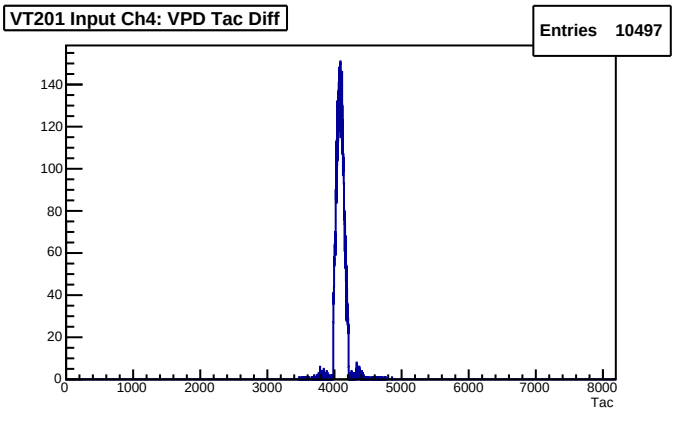

VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

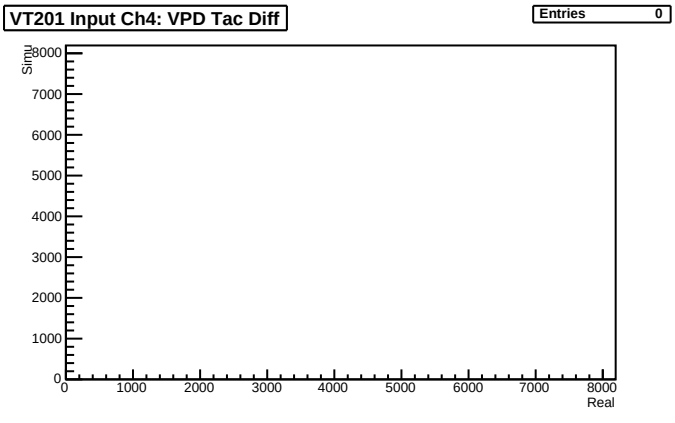

VT201 Input Ch2: ZDC Tac Diff
Entries 10497
Tac

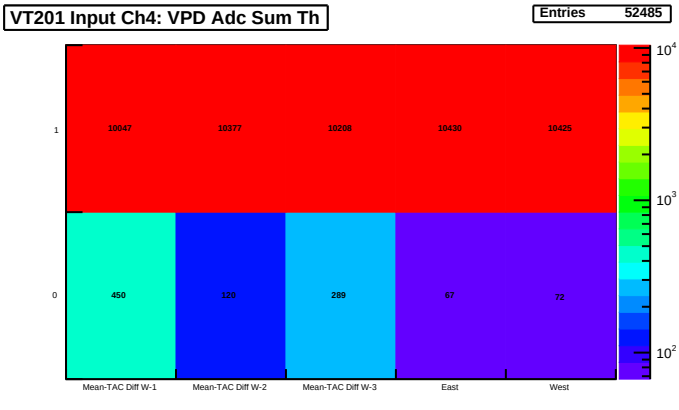

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

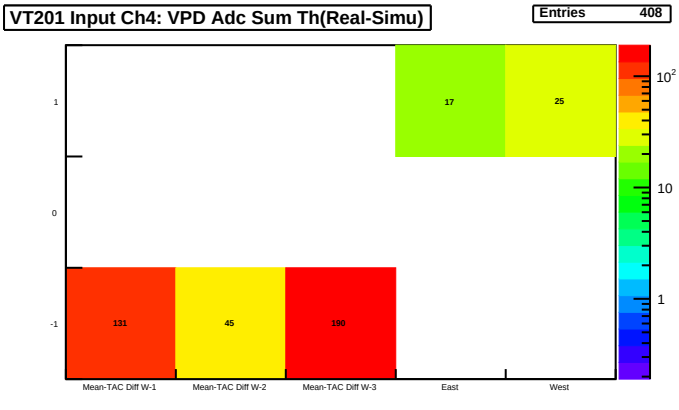

VT201 Input Ch2: ZDC Adc Sum Th
Entries 83976
10248 10141 3311 2788 10248 10152 2060 1615
249 356 7186 7709 249 345 8437 8882
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

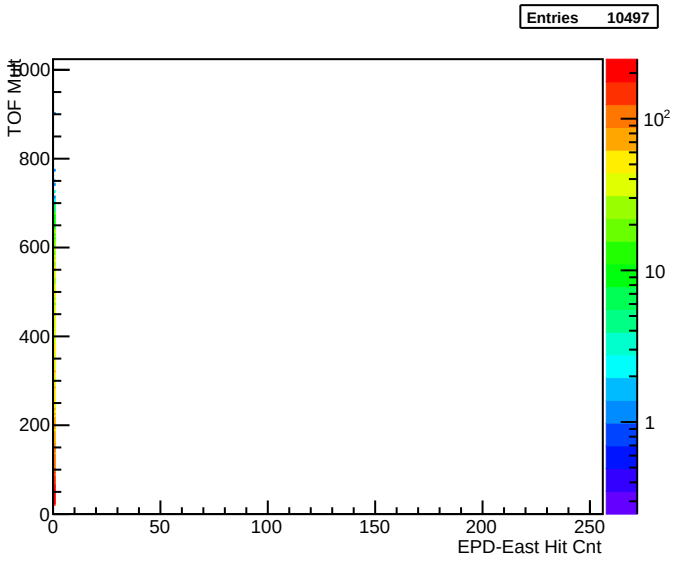

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

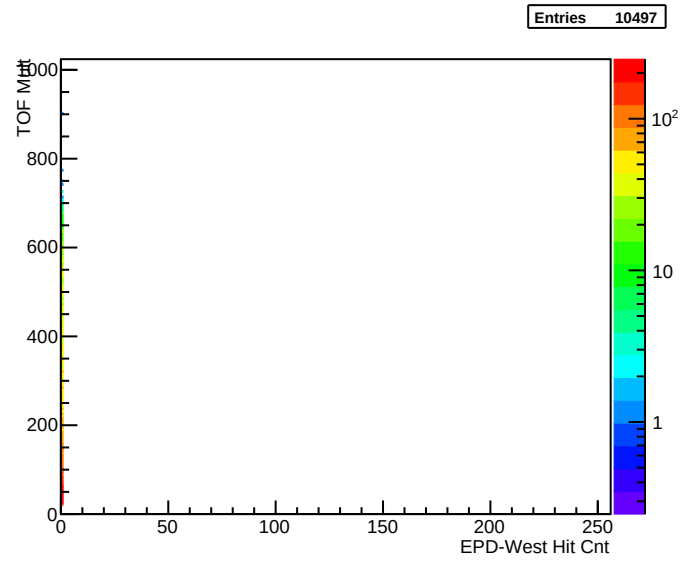

VT201 Input Ch4: VPD Tac Diff
Entries 10497
Tac

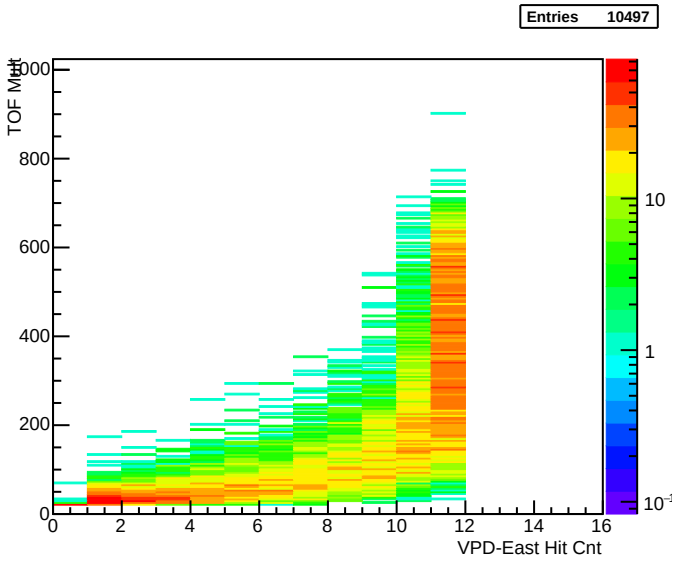

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

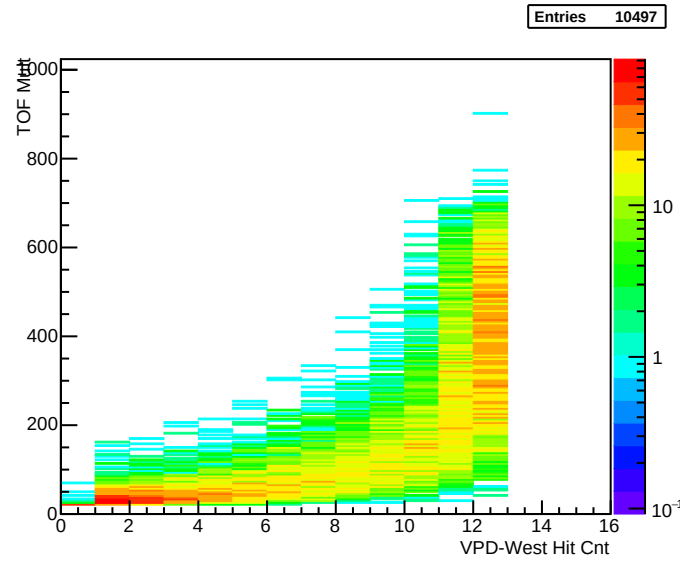

VT201 Input Ch4: VPD Adc Sum Th
Entries 52485
10047 10377 10208 10430 10425
450 120 289 67 72
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

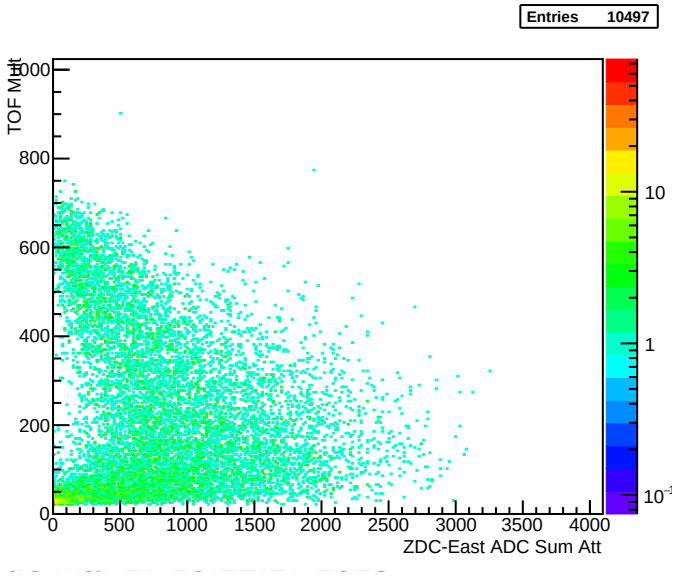

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 408
17 25
131 45 190
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

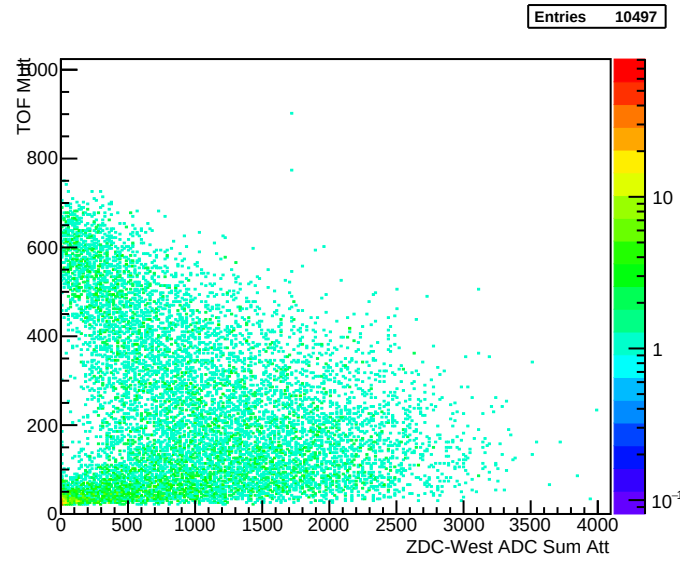

Entries 10497
TOF Mult
EPD-East Hit Cnt

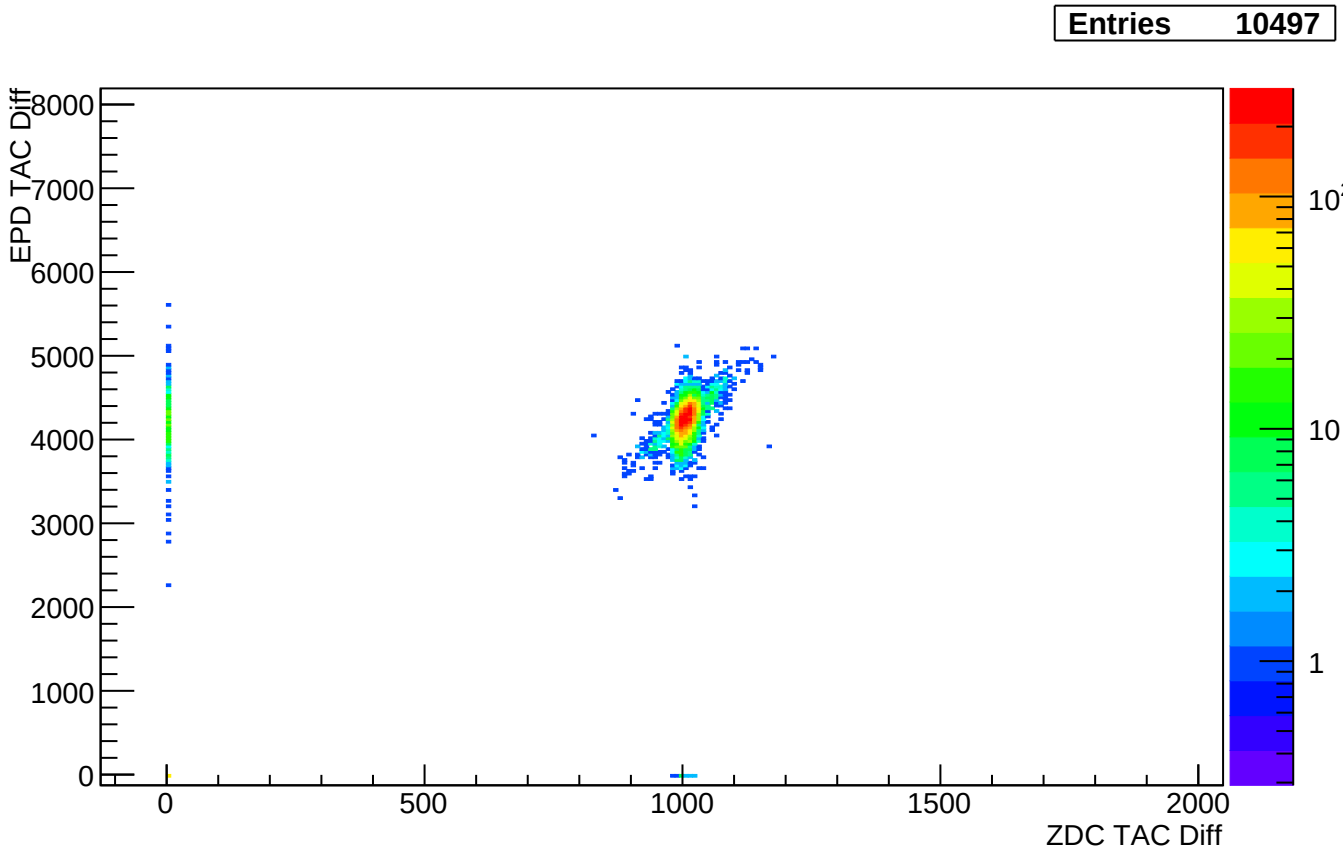

Entries 10497
TOF Mult
EPD-West Hit Cnt

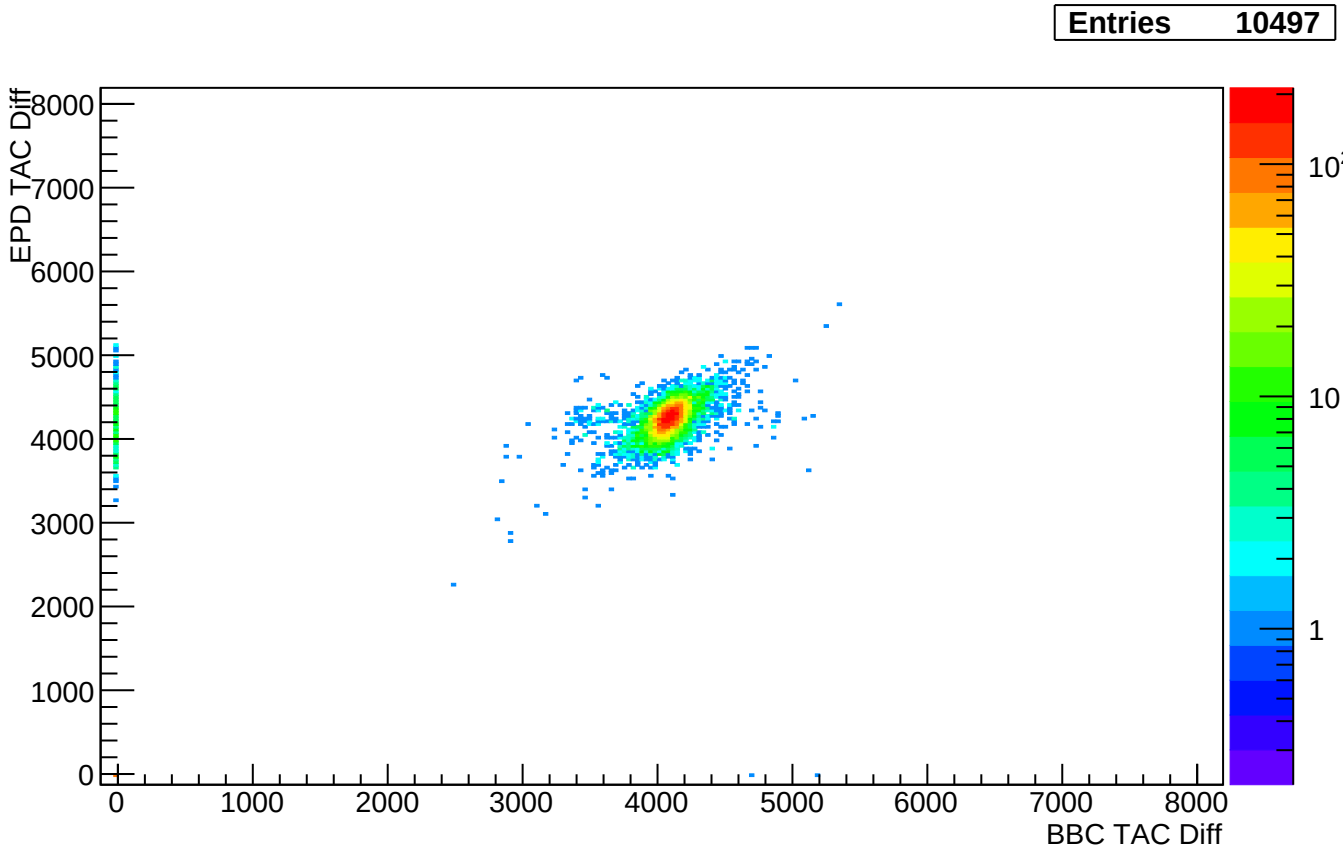

Entries 10497
TOF Mult
VPD-East Hit Cnt

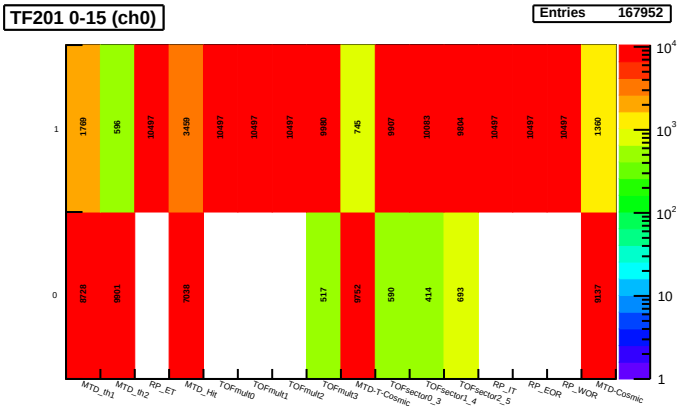

Entries 10497
TOF Mult
VPD-West Hit Cnt

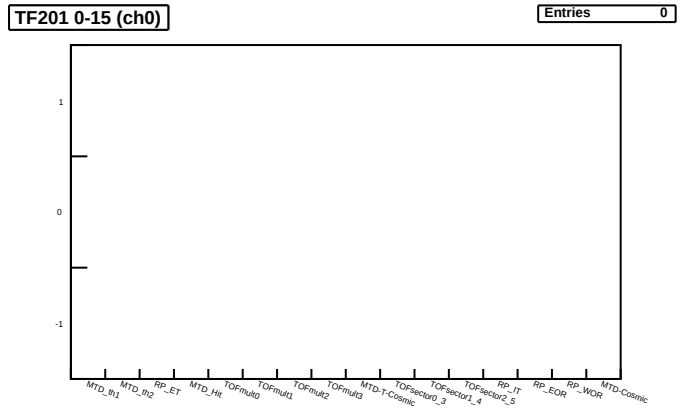

Entries 10497
TOF Mult
ZDC-East ADC Sum Att

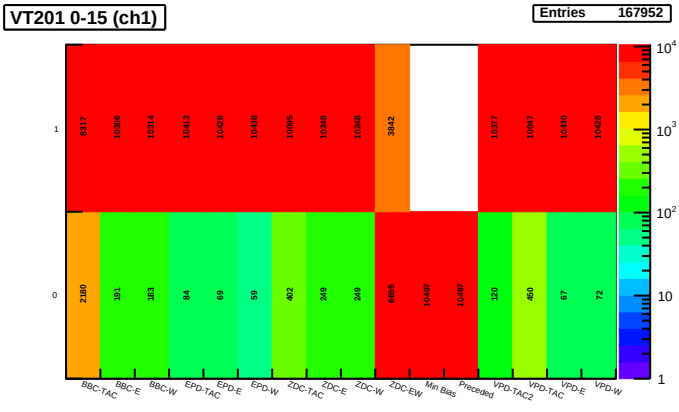

Entries 10497
TOF Mult
ZDC-West ADC Sum Att

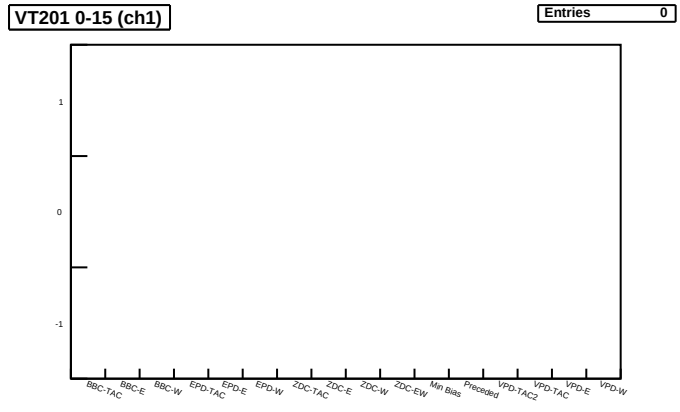

Entries 10497
EPD TAC Diff
ZDC TAC Diff
0
1000
2000
3000
4000
5000
6000
7000
8000
0
500
1000
1500
2000
1
10
10^2

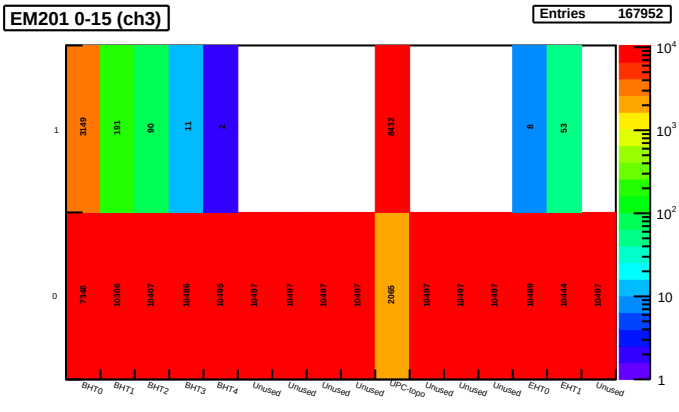

Entries 10497
EPD TAC Diff
BBC TAC Diff
0
1000
2000
3000
4000
5000
6000
7000
8000
0
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2

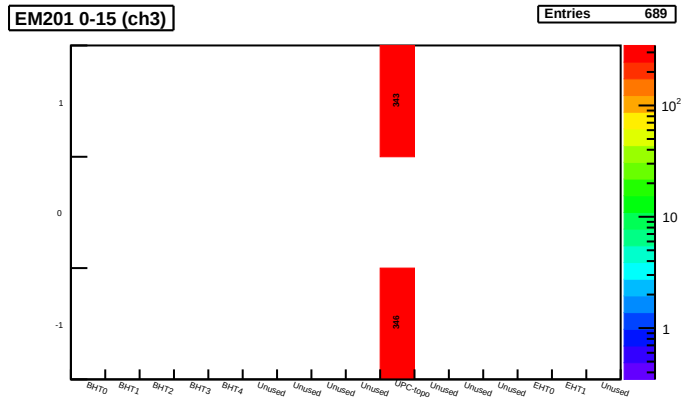

TF201 0-15 (ch0)
Entries 167952
1769 596 10497 3459 10497 10497 10497 9980 745 9907 10083 9604 10497 10497 10497 1360
8728 9901 7038 517 9752 590 414 693 9137
MTD_th1 MTD_th2 RP_ET MTD_Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

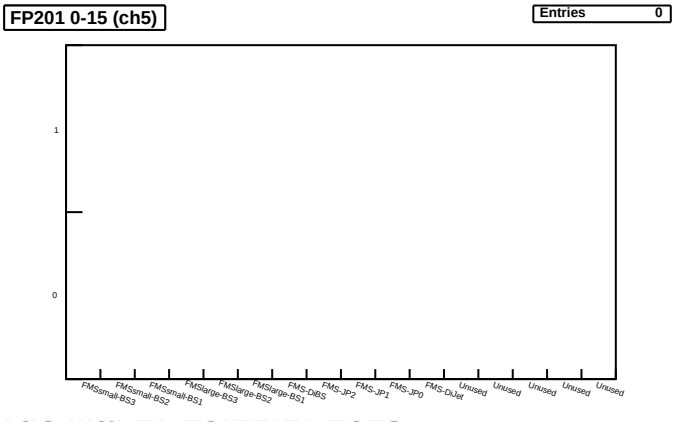

TF201 0-15 (ch0)
Entries 0
MTD_th1 MTD_th2 RP_ET MTD_Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

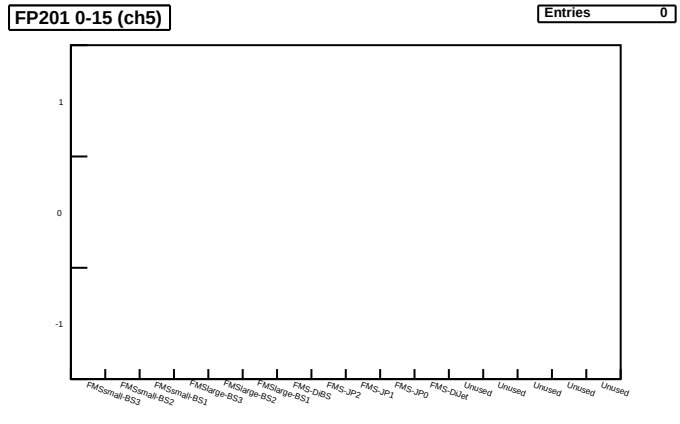

VT201 0-15 (ch1)
Entries 167952
8317 10306 10314 10413 10428 10438 10095 10248 10248 3842 10377 10047 10430 10425
2180 191 183 84 69 59 402 249 249 6655 10497 10497 120 460 67 72
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 167952
3149 191 90 11 2 8432 8 53
7348 10306 10497 10486 10495 10497 10497 10497 10497 2065 10497 10497 10497 10489 10444 10497
BH0 BH1 BH2 BH3 BH4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EH0 EH1 Unused


EM201 0-15 (ch3)
Entries 689
343
346
BH0 BH1 BH2 BH3 BH4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EH0 EH1 Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused