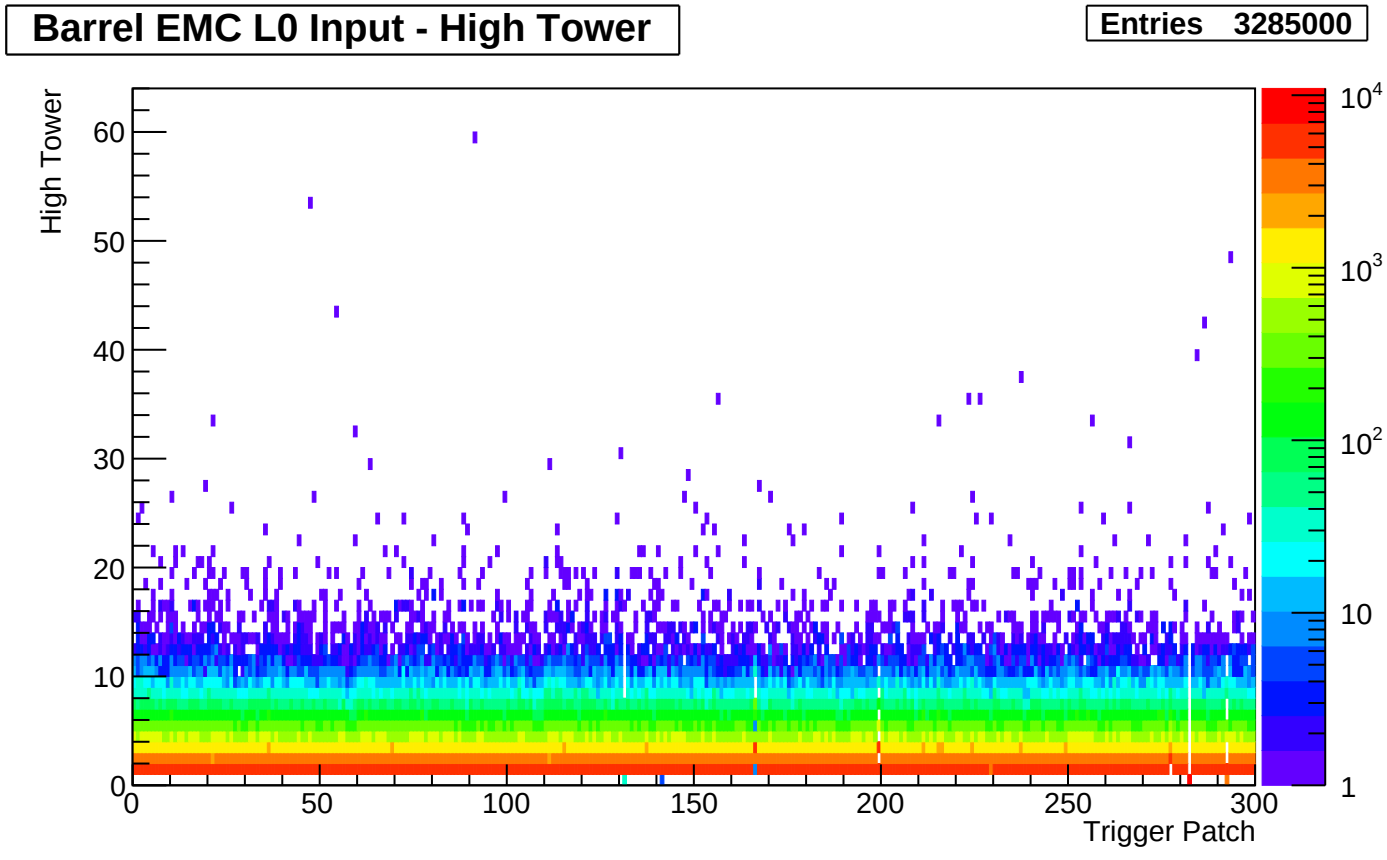
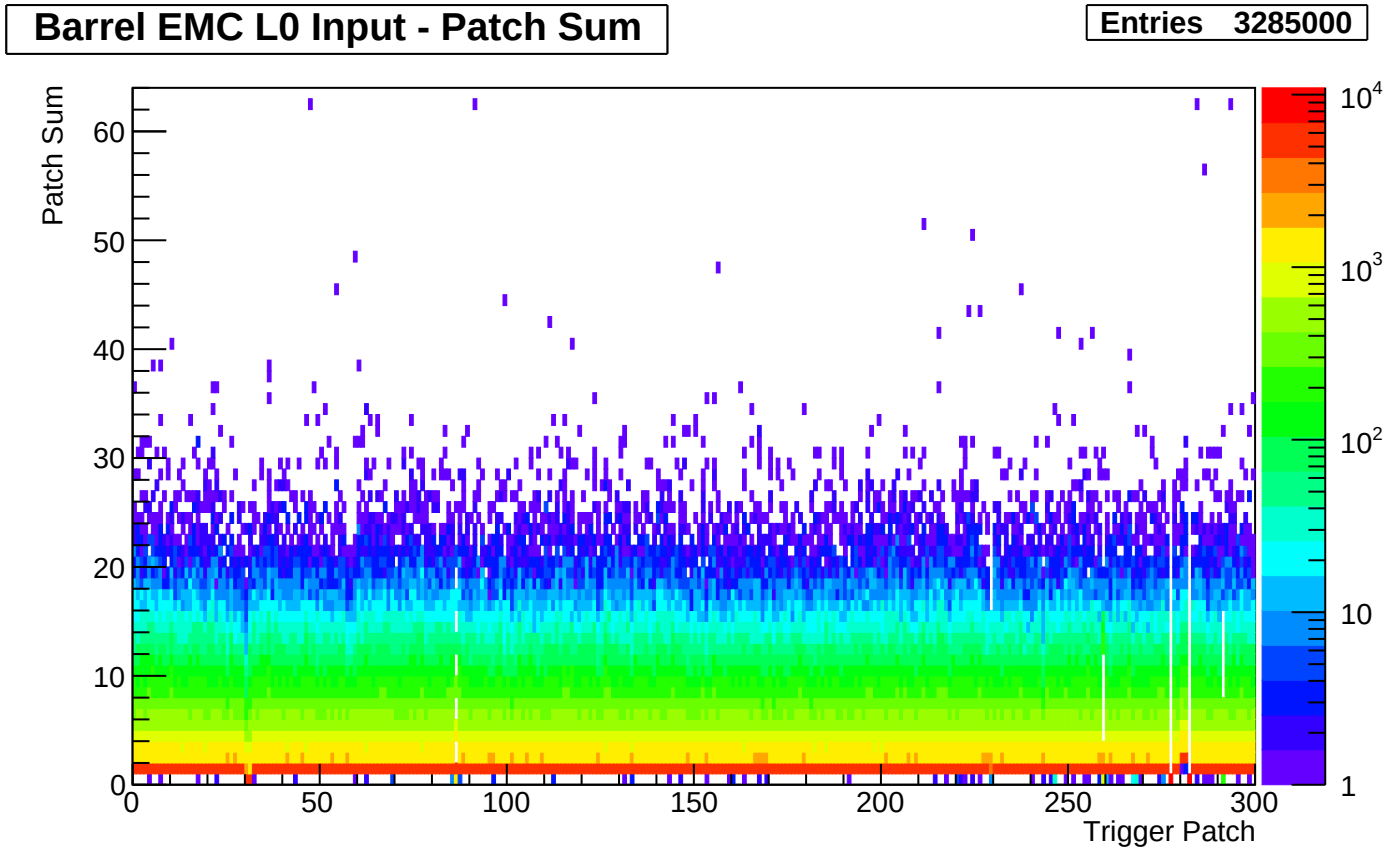
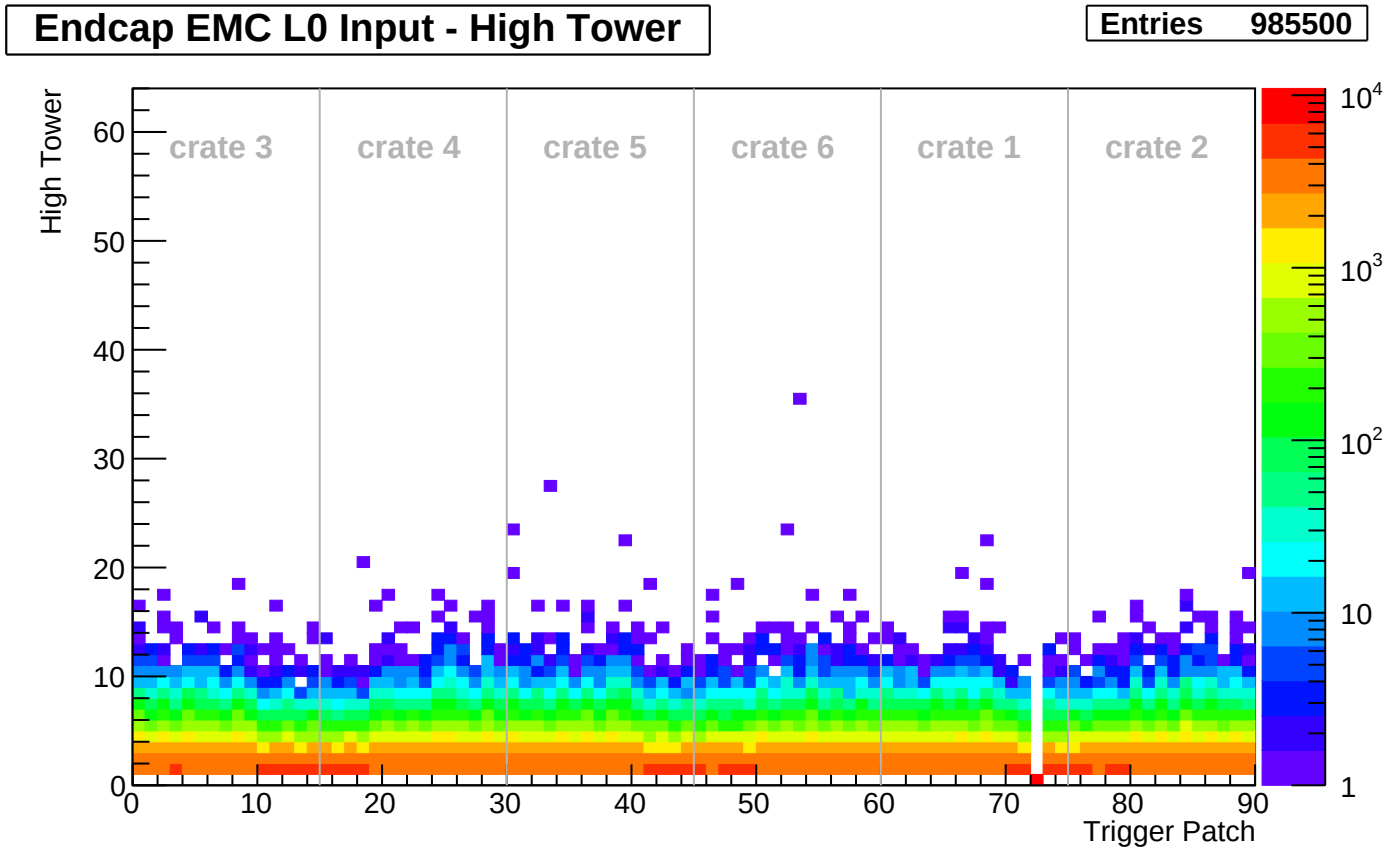
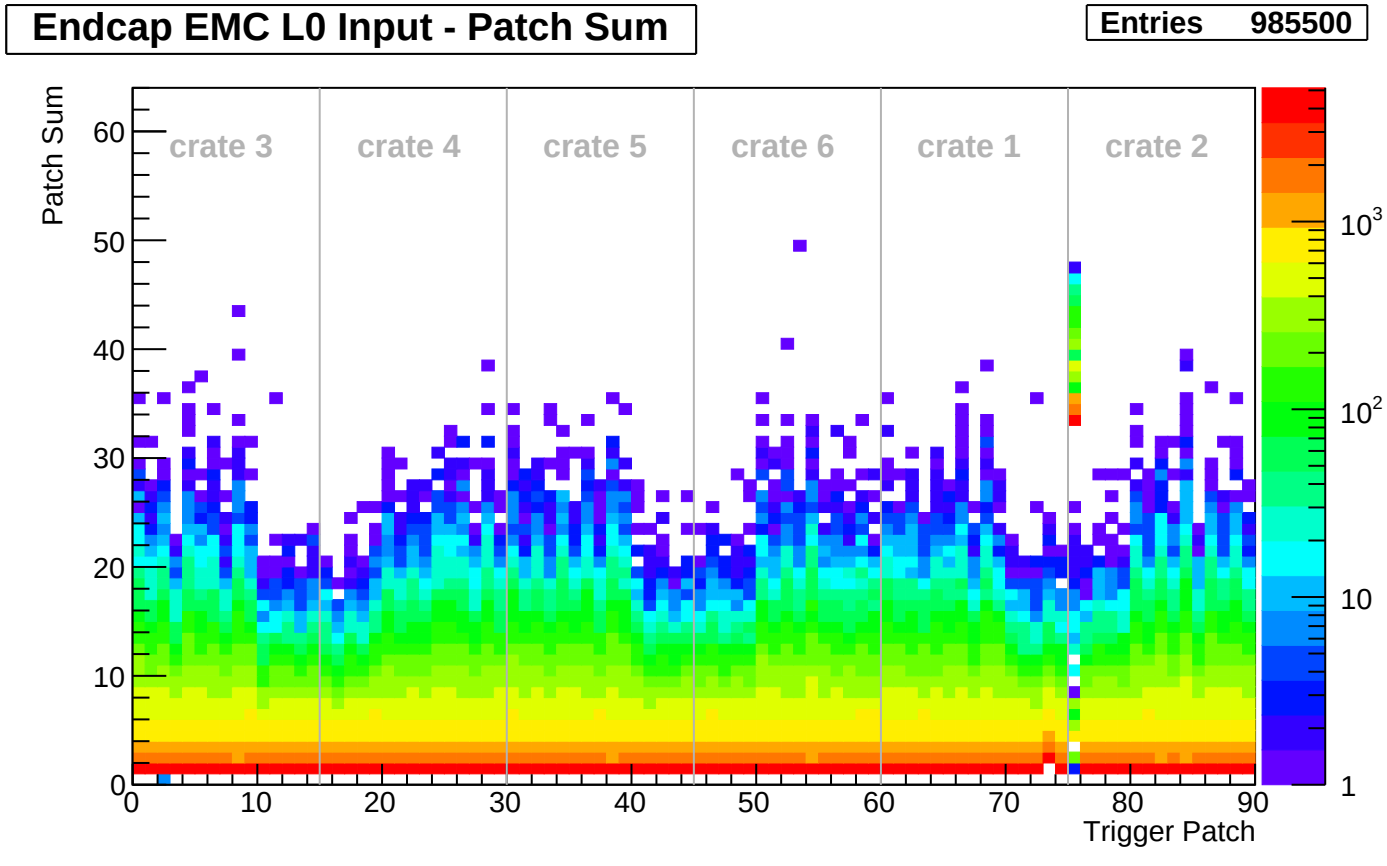
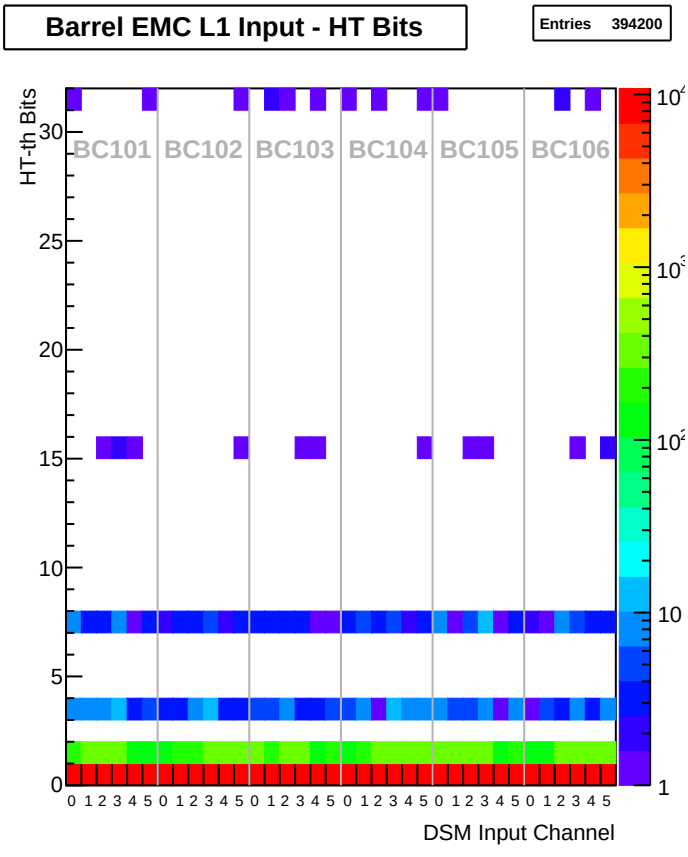
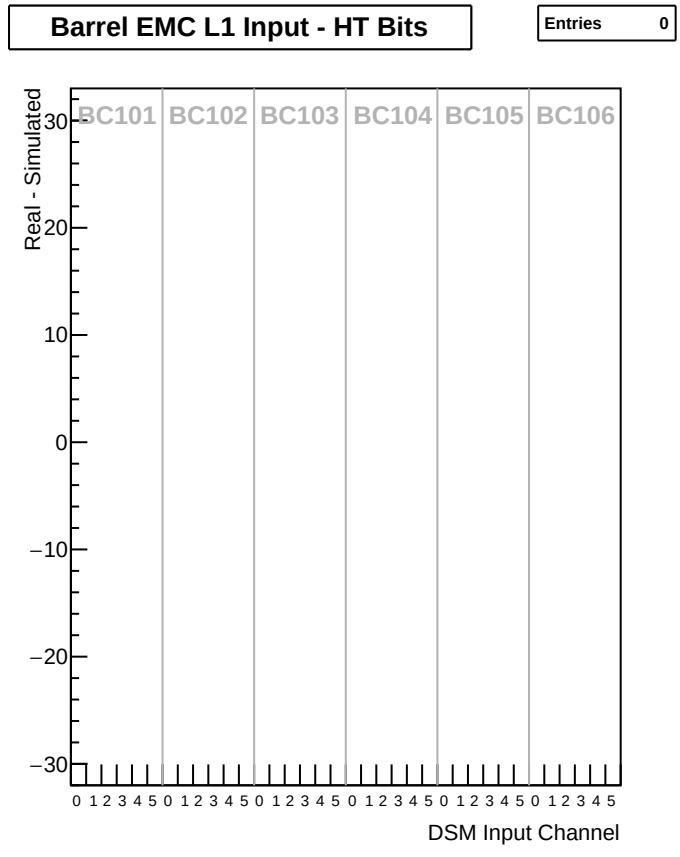

Barrel EMC L0 Input - High Tower
Entries 3285000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

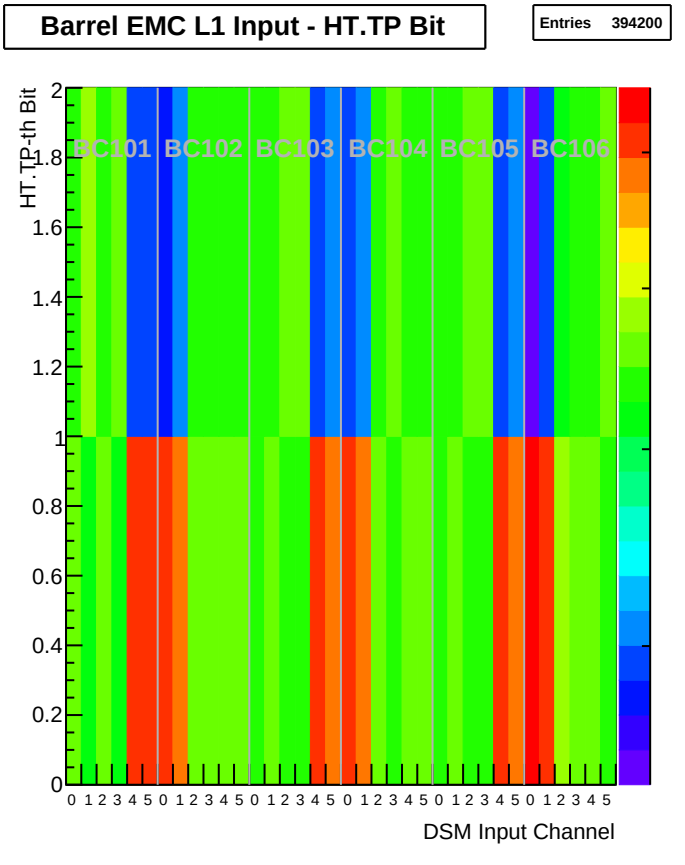

Barrel EMC L0 Input - Patch Sum
Entries 3285000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

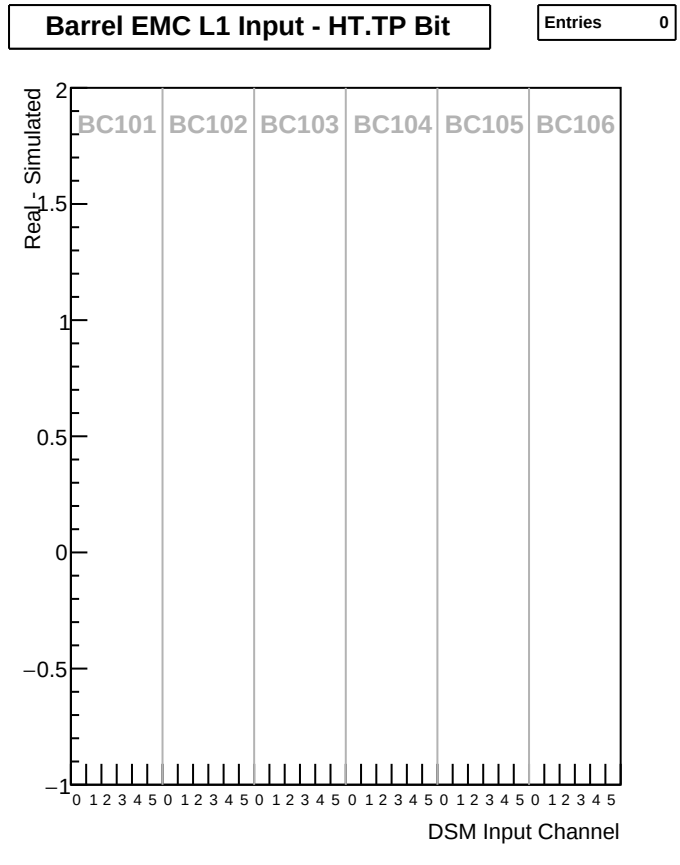

Endcap EMC L0 Input - High Tower
Entries 985500
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

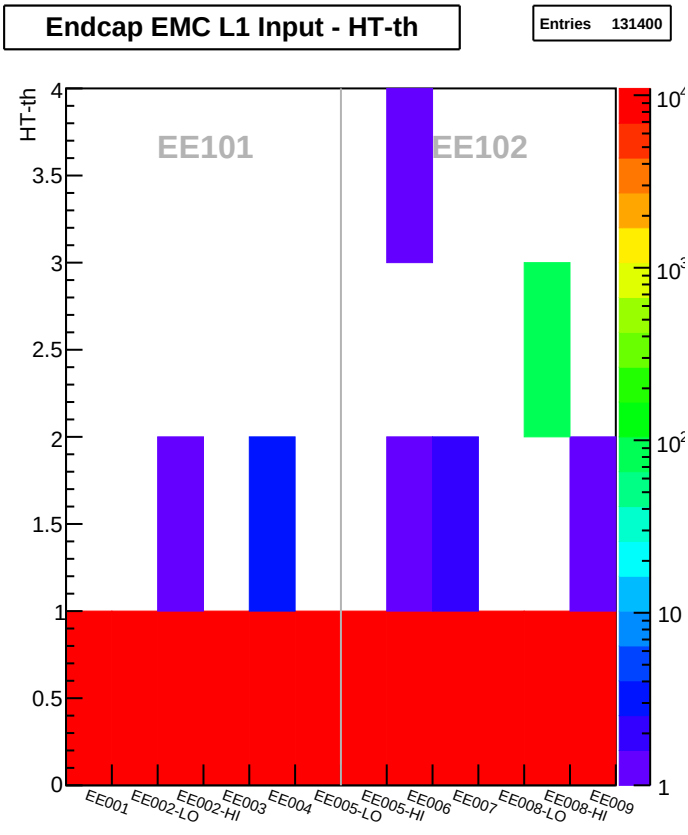

Endcap EMC L0 Input - Patch Sum
Entries 985500
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

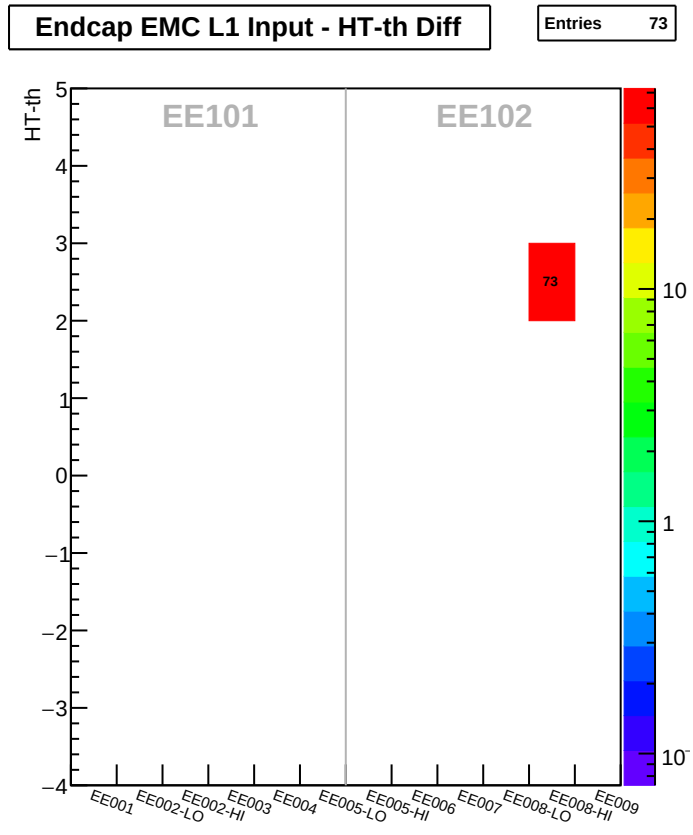

Barrel EMC L1 Input - HT Bits
Entries 394200
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

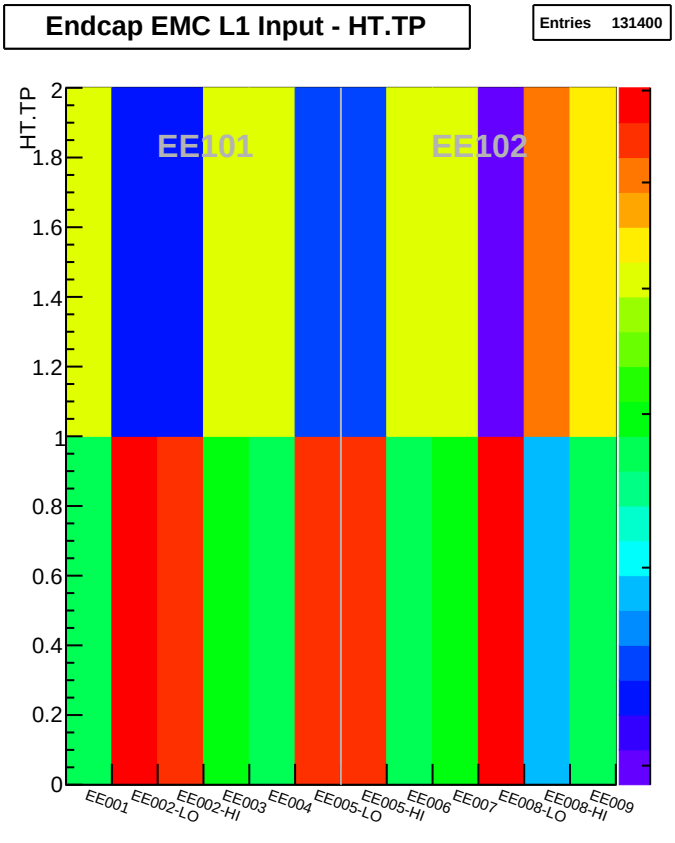

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

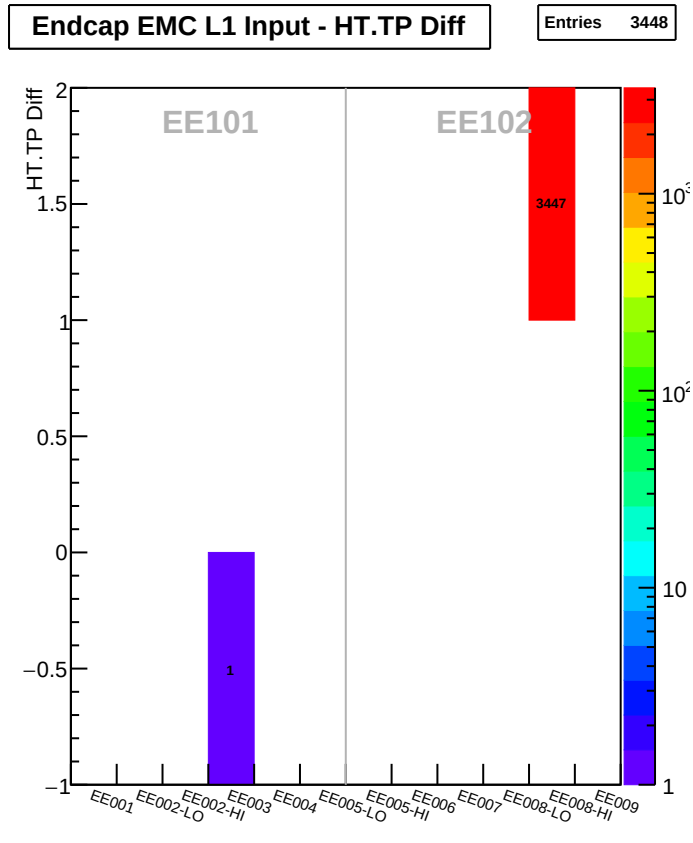

Barrel EMC L1 Input - HT.TP Bit
Entries 394200
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

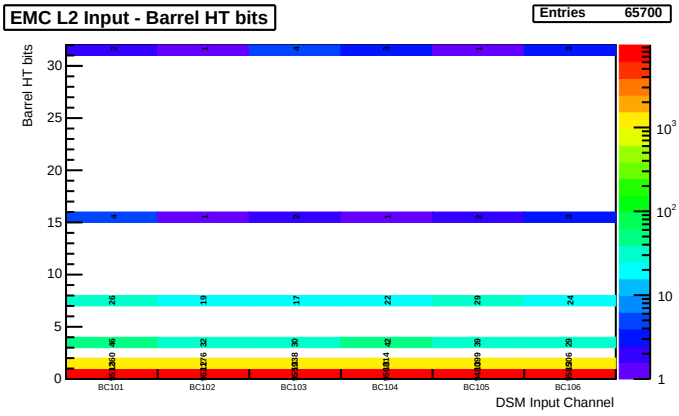

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

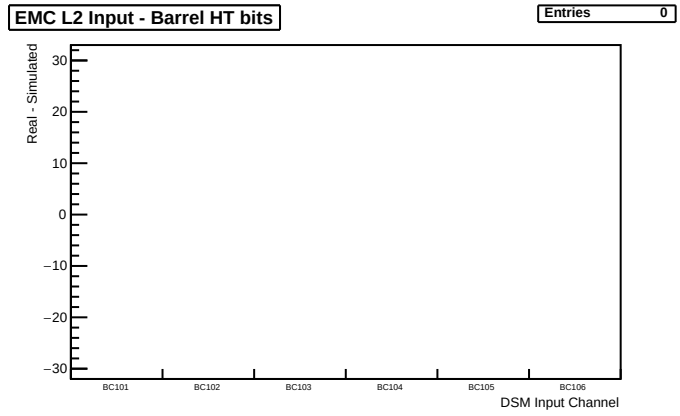

Endcap EMC L1 Input - HT-th
Entries 131400
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

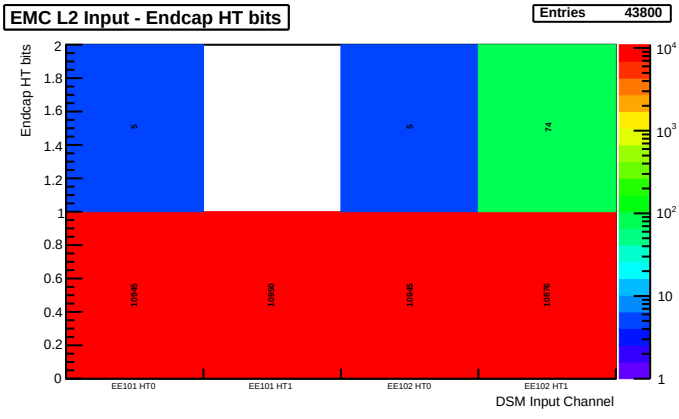

Endcap EMC L1 Input - HT-th Diff
Entries 73
HT-th
EE101
EE102
73
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

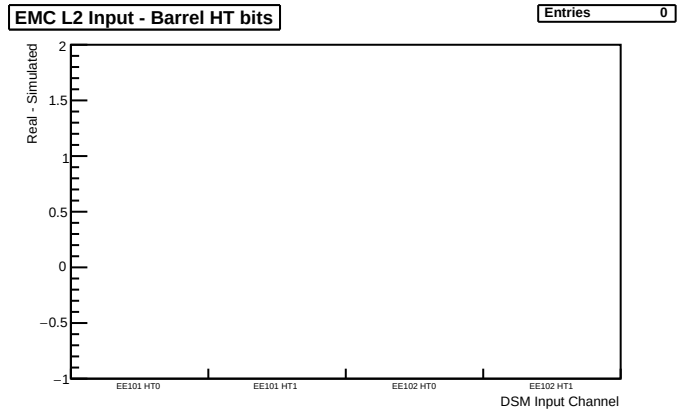

Endcap EMC L1 Input - HT.TP
Entries 131400
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

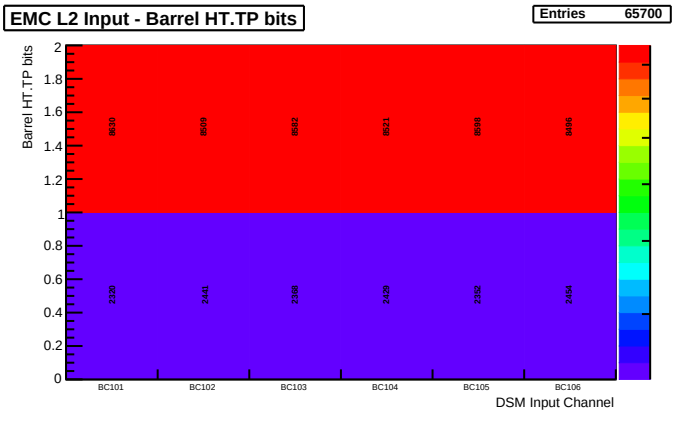

Endcap EMC L1 Input - HT.TP Diff
Entries 3448
HT.TP Diff
EE101
EE102
3447
1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

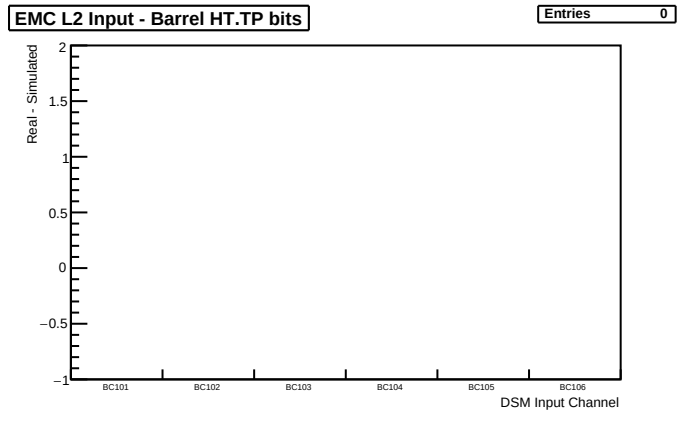

EMC L2 Input - Barrel HT bits
Entries 65700
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

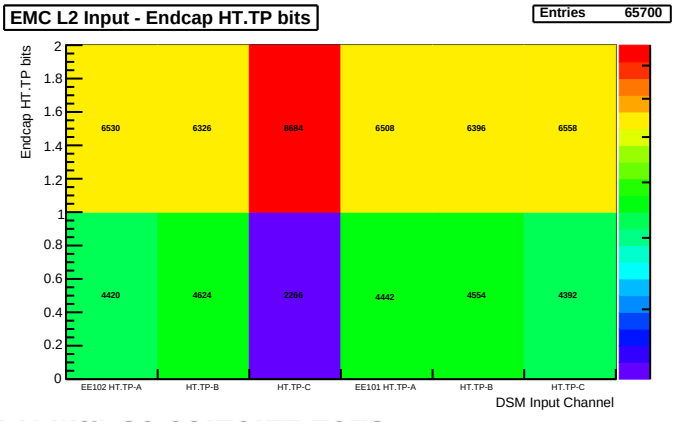

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

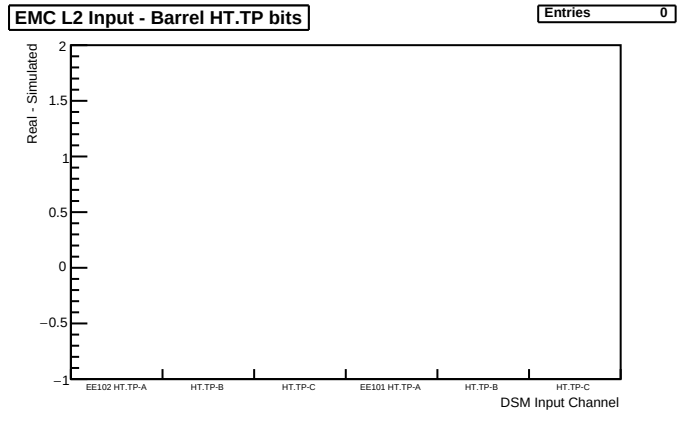

EMC L2 Input - Endcap HT bits
Entries 43800
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

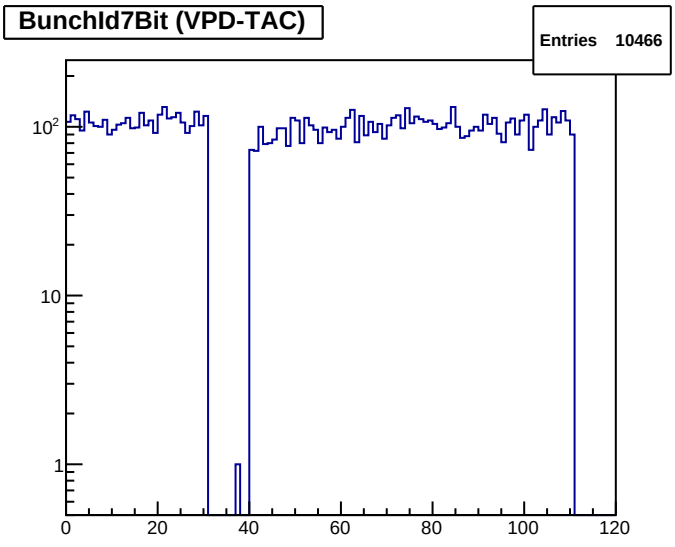

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

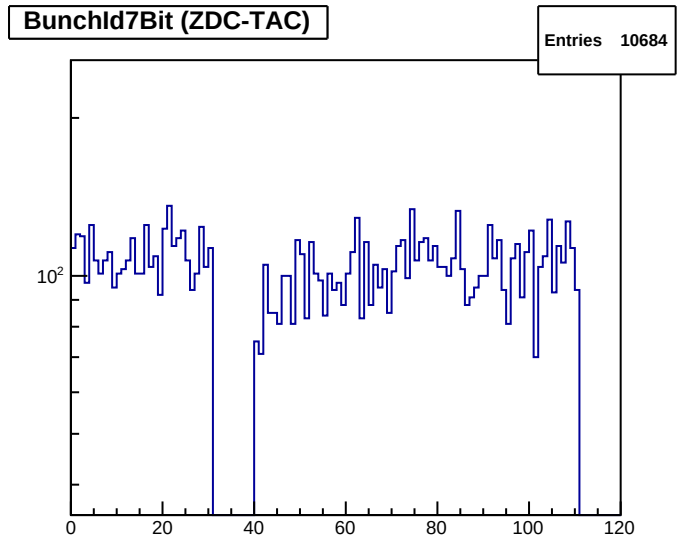

EMC L2 Input - Barrel HT.TP bits
Entries 65700
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

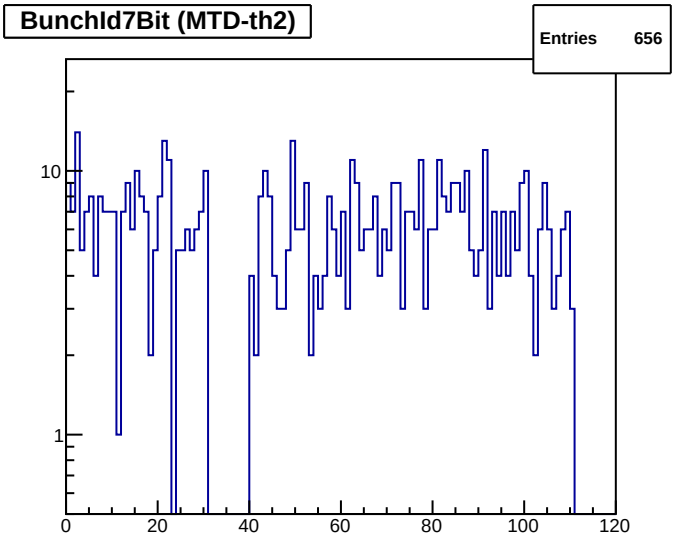

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

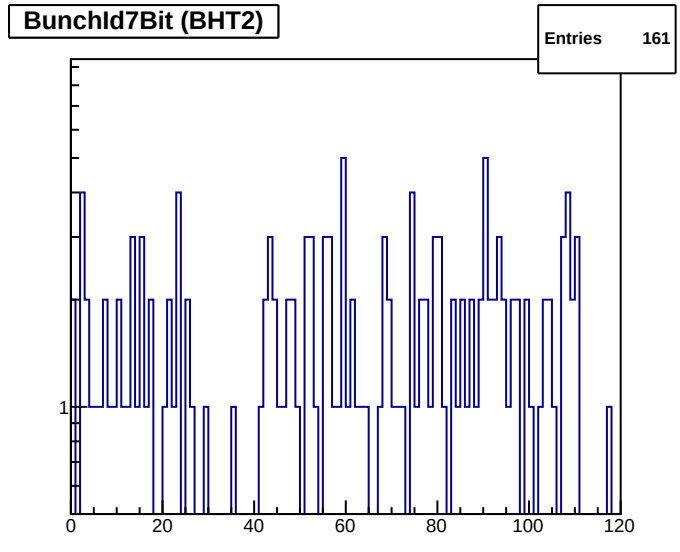

EMC L2 Input - Endcap HT.TP bits
Entries 65700
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

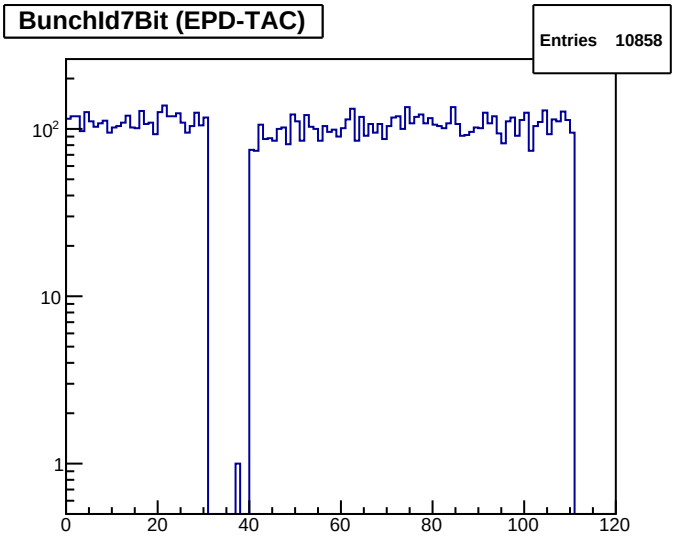

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

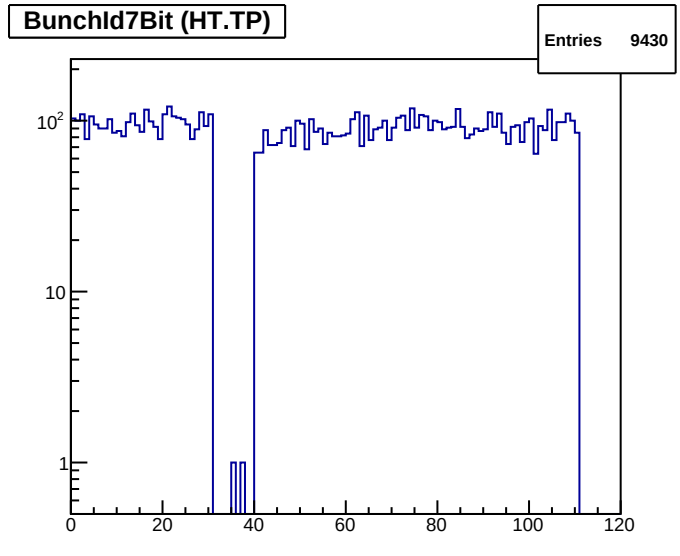

BunchId7Bit (VPD-TAC)
Entries 10466
10^2
10
1
0
20
40
60
80
100
120

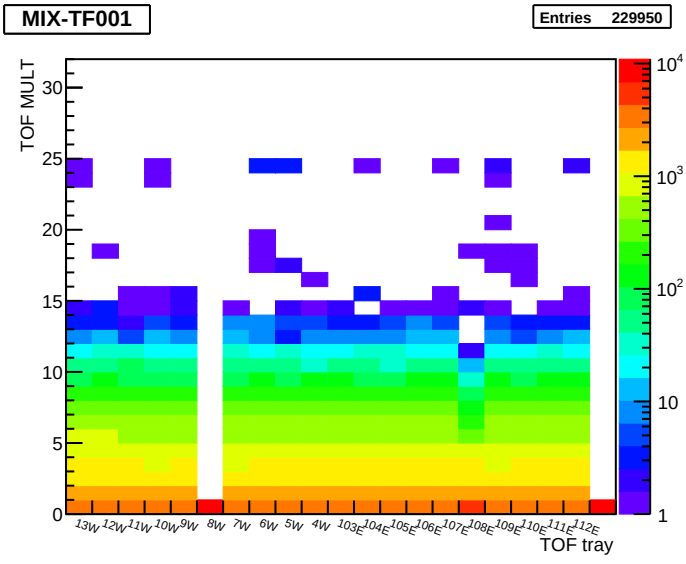

BunchId7Bit (ZDC-TAC)
Entries 10684
10^2
0
20
40
60
80
100
120

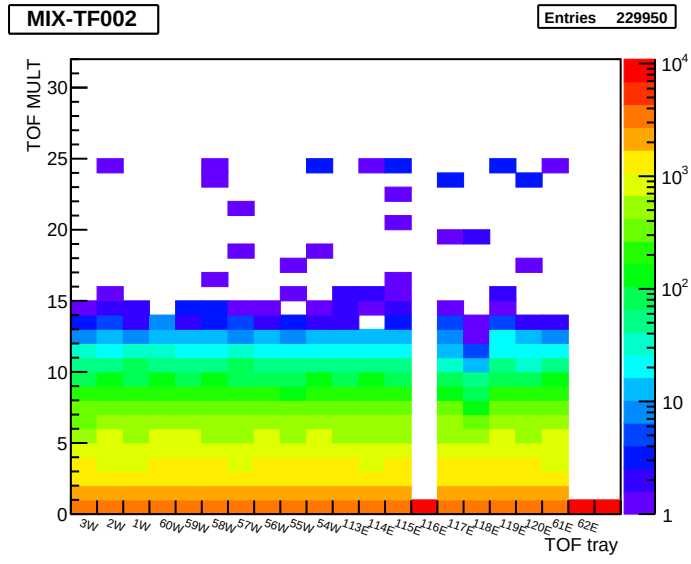

BunchId7Bit (MTD-th2)
Entries 656
10
1
0
20
40
60
80
100
120

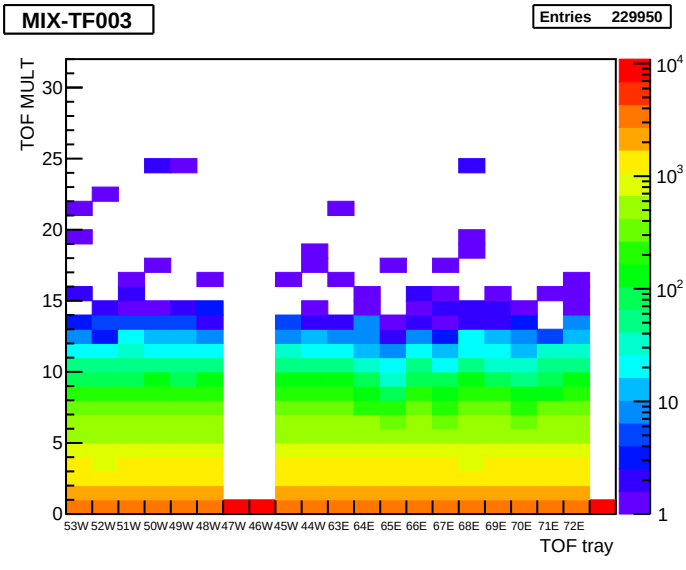

BunchId7Bit (BHT2)
Entries 161
1
0
20
40
60
80
100
120

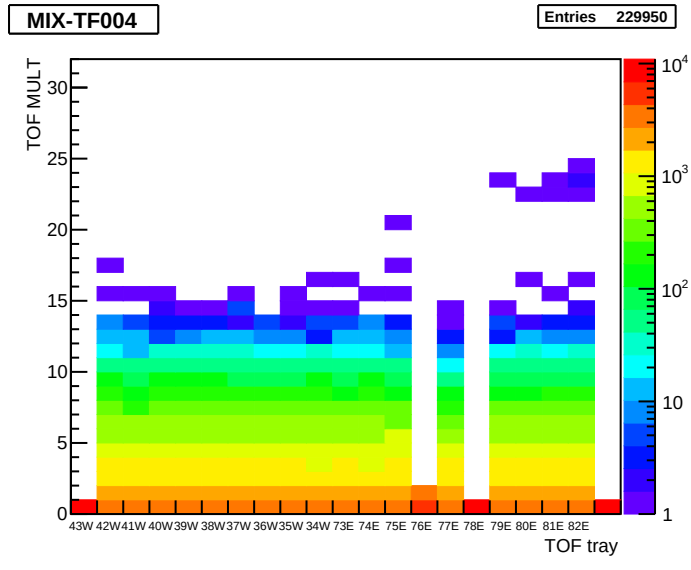

BunchId7Bit (EPD-TAC)
Entries 10858
10^2
10
1
0
20
40
60
80
100
120

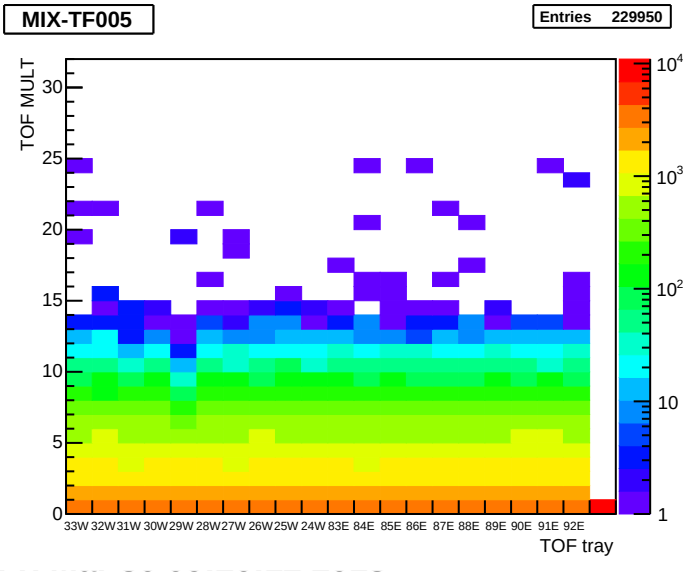

BunchId7Bit (HT.TP)
Entries 9430
10^2
10
1
0
20
40
60
80
100
120

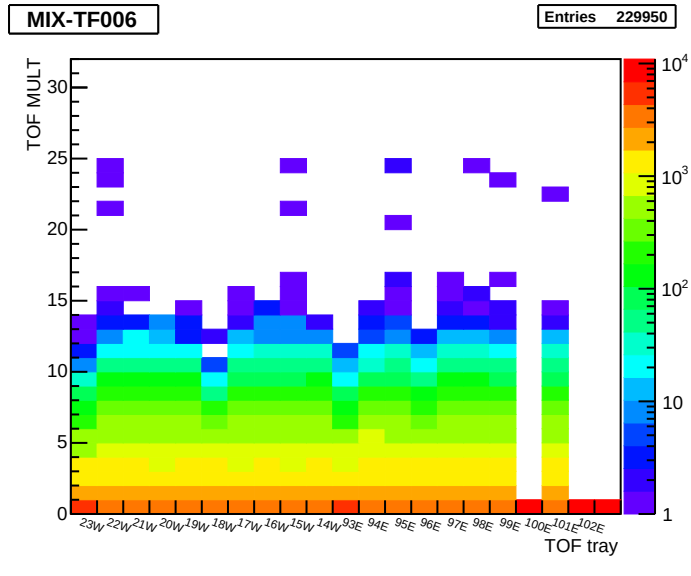

MIX-TF001
Entries 229950
TOF MULT
TOF tray

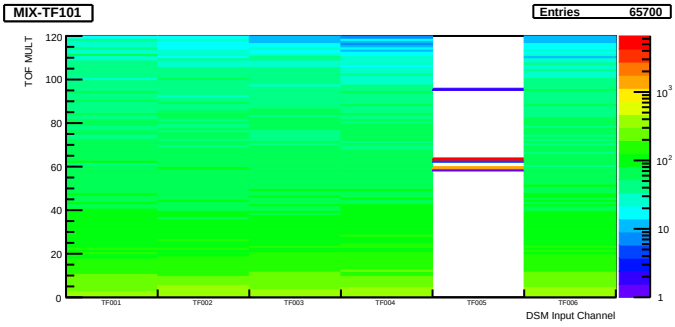

MIX-TF002
Entries 229950
TOF MULT
TOF tray

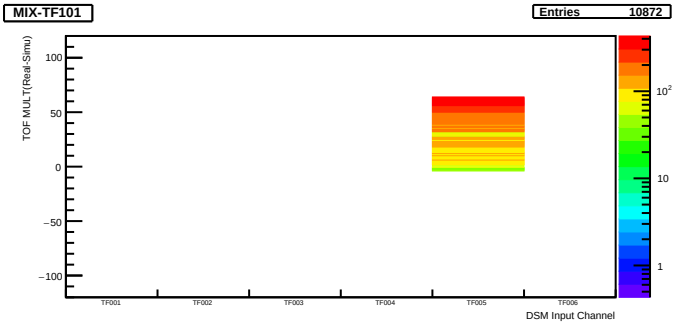

MIX-TF003
Entries 229950
TOF MULT
TOF tray

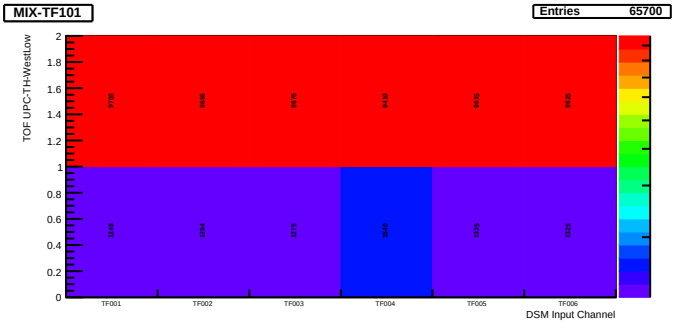

MIX-TF004
Entries 229950
TOF MULT
TOF tray

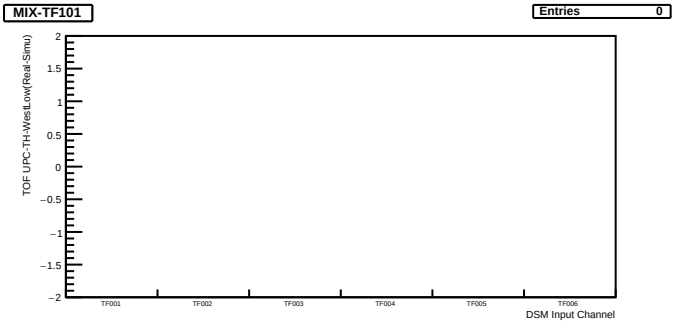

MIX-TF005
Entries 229950
TOF MULT
TOF tray

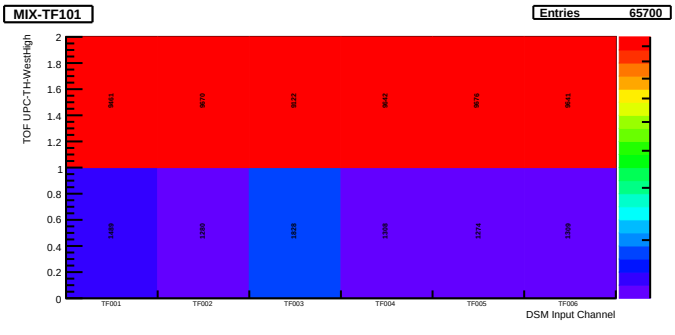

MIX-TF006
Entries 229950
TOF MULT
TOF tray

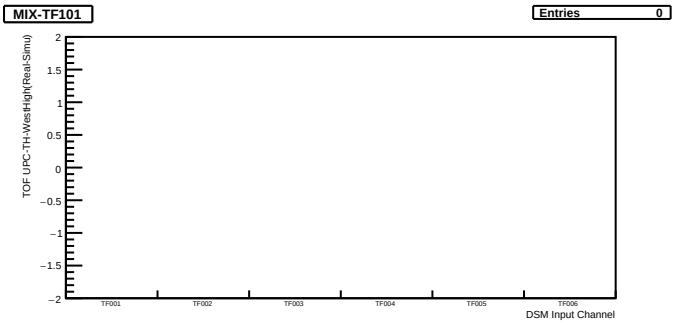

MIX-TF101
Entries 65700
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

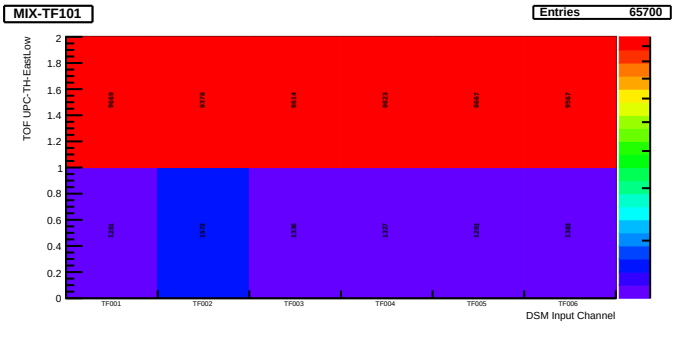

MIX-TF101
Entries 10872
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

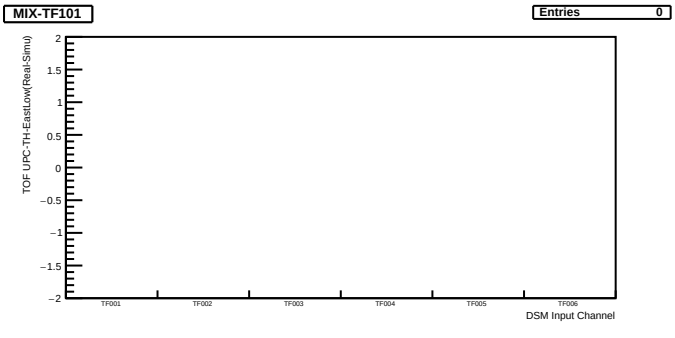

MIX-TF101
Entries 65700
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

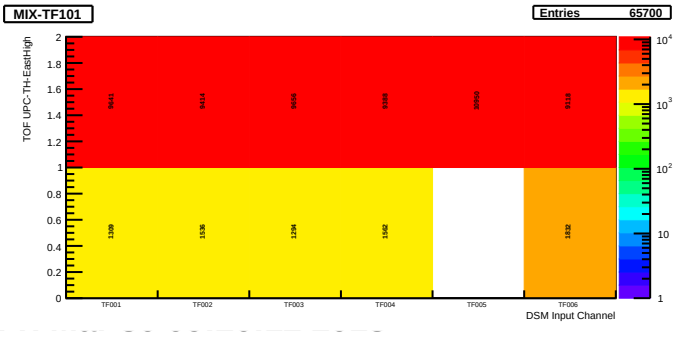

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

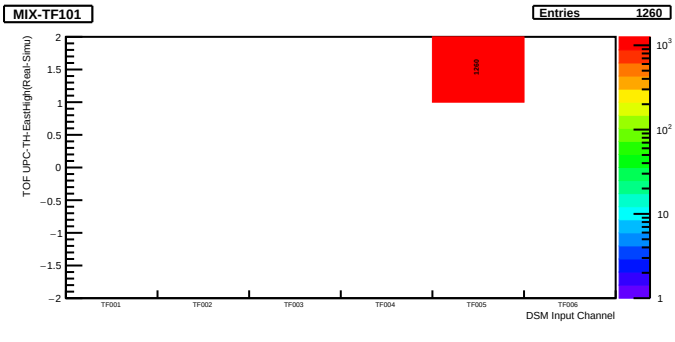

MIX-TF101
Entries 65700
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

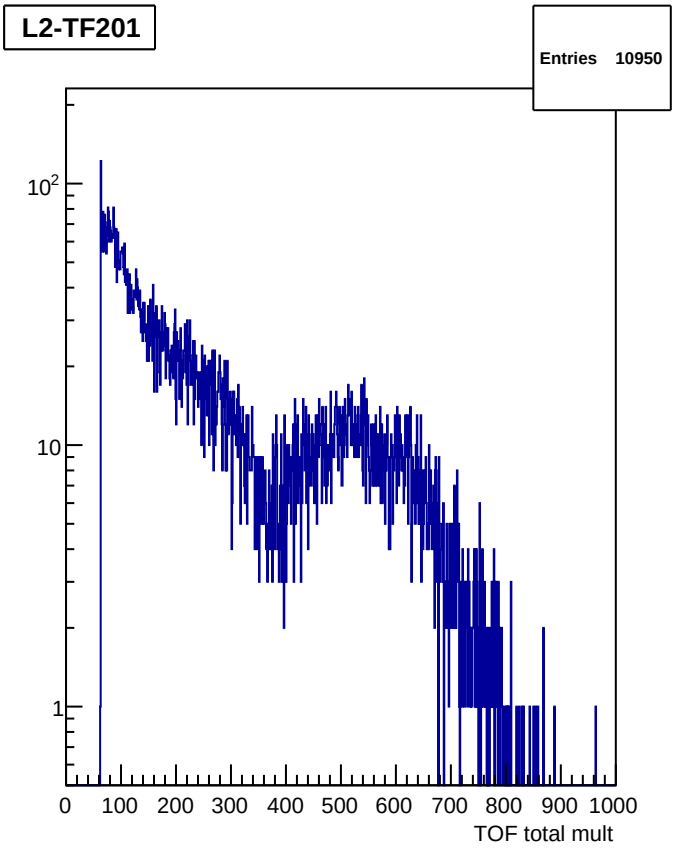

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

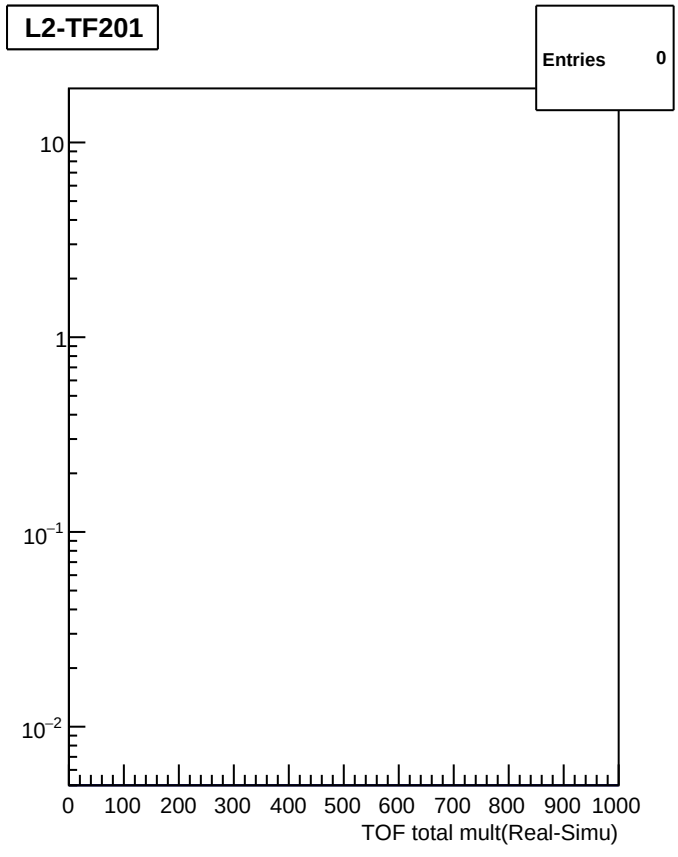

MIX-TF101
Entries 65700
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

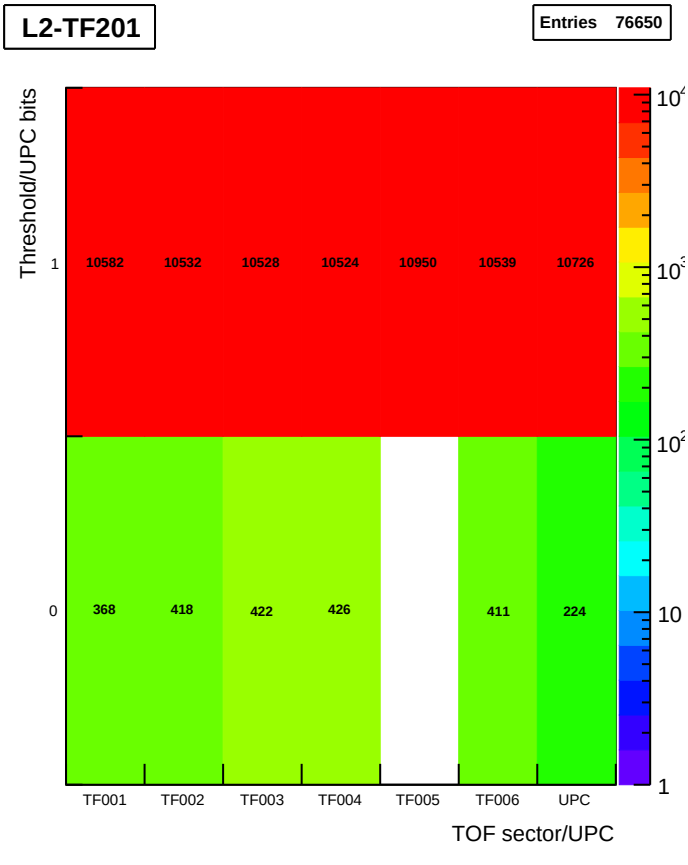

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

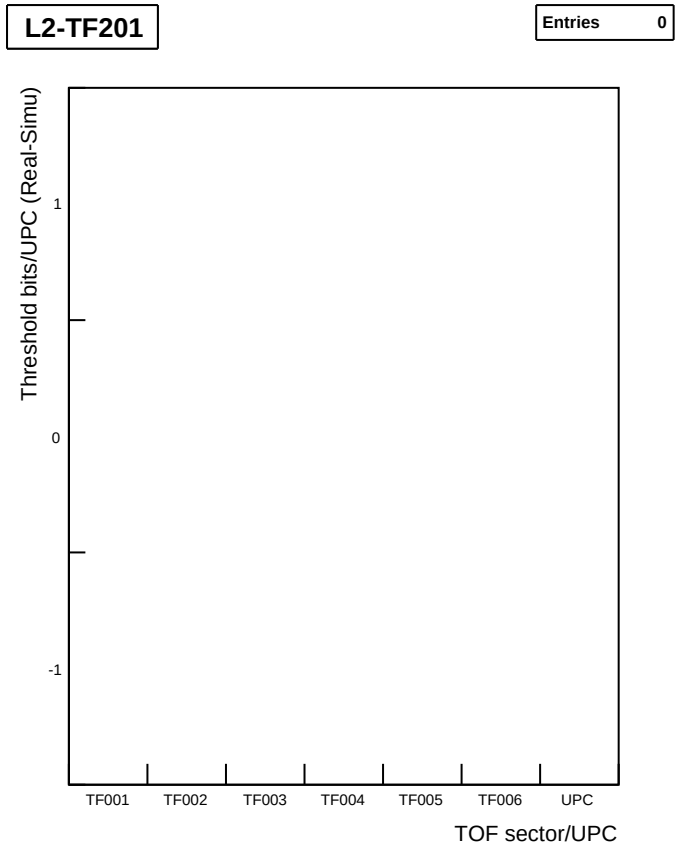

MIX-TF101
Entries 65700
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

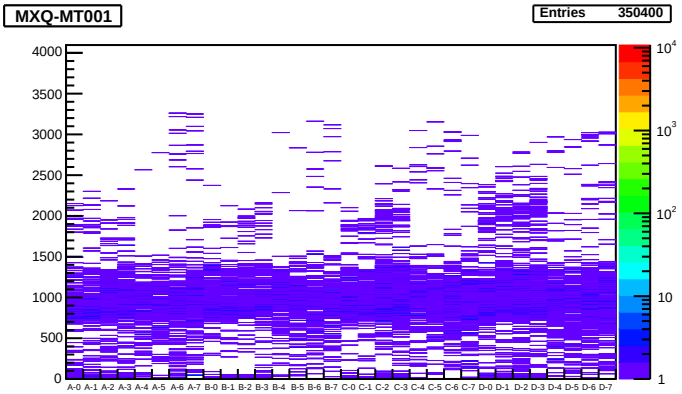

MIX-TF101
Entries 1260
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

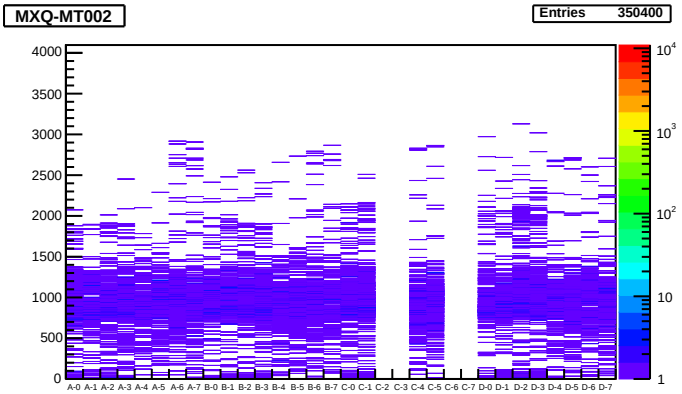

L2-TF201
Entries 10950
10^2
10
1
0 100 200 300 400 500 600 700 800 900 1000
TOF total mult

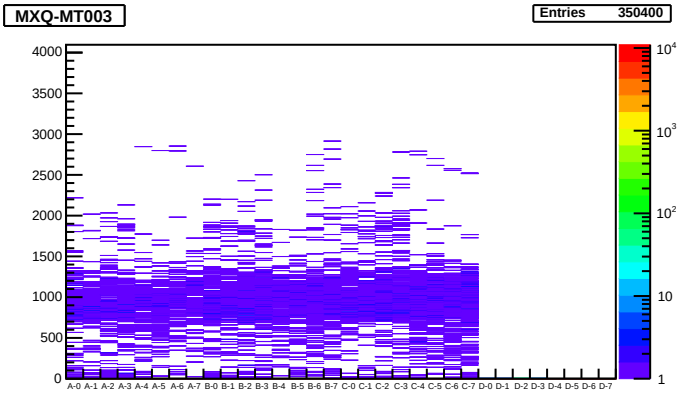

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
0 100 200 300 400 500 600 700 800 900 1000
TOF total mult(Real-Simu)

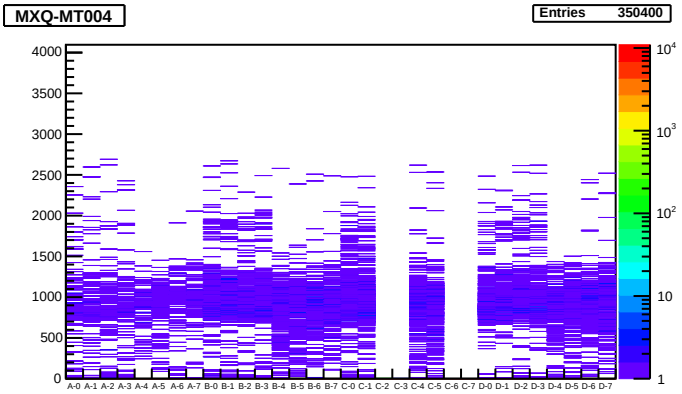

L2-TF201
Entries 76650
Threshold/UPC bits
1
0
10582 10532 10528 10524 10950 10539 10726
368 418 422 426 411 224
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC
10
1

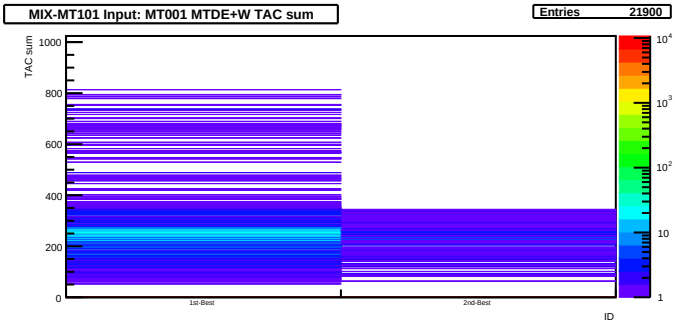

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

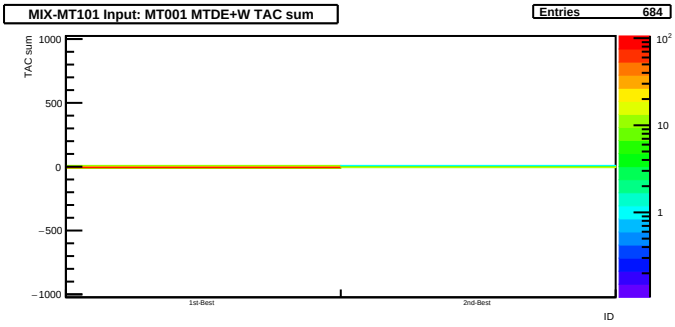

MXQ-MT001
Entries 350400

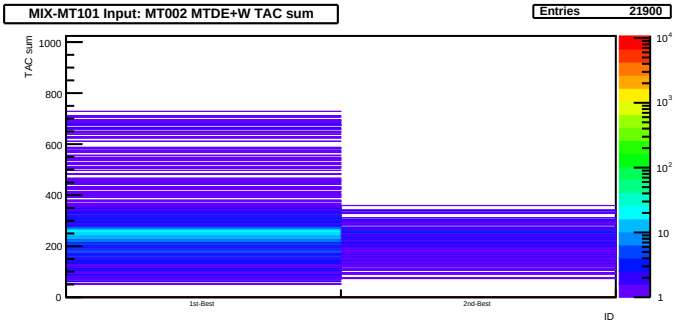

MXQ-MT002
Entries 350400

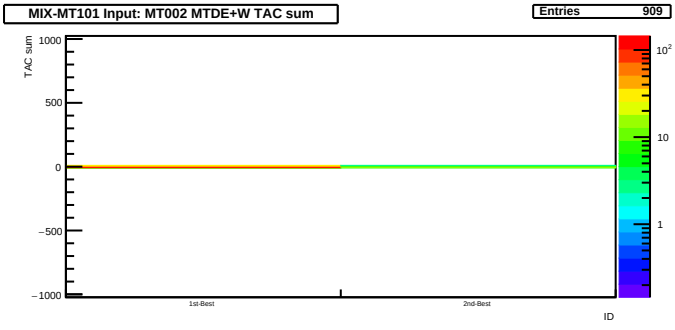

MXQ-MT003
Entries 350400

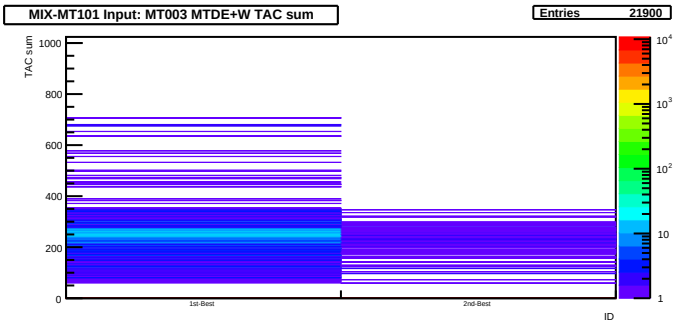

MXQ-MT004
Entries 350400

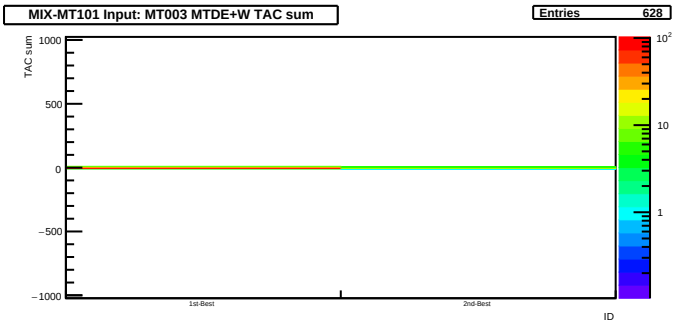

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 21900
TAC sum
1st-Best
2nd-Best
ID

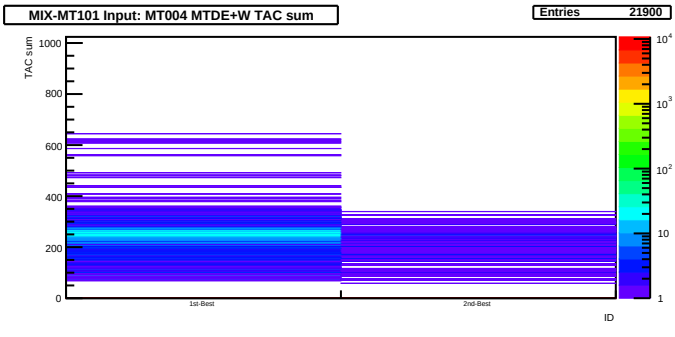

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 684
TAC sum
1st-Best
2nd-Best
ID

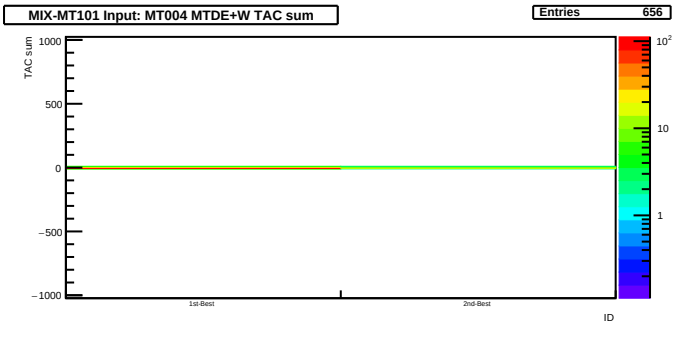

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 21900
TAC sum
1st-Best
2nd-Best
ID

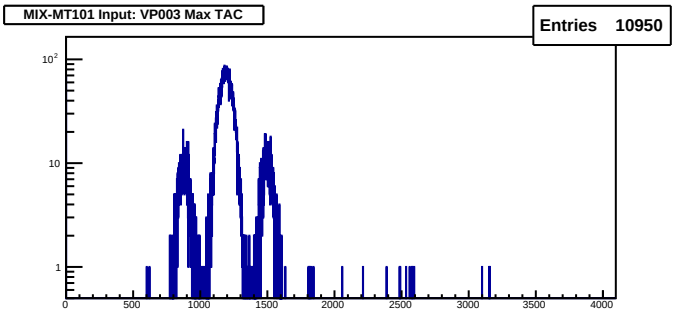

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 909
TAC sum
1st-Best
2nd-Best
ID

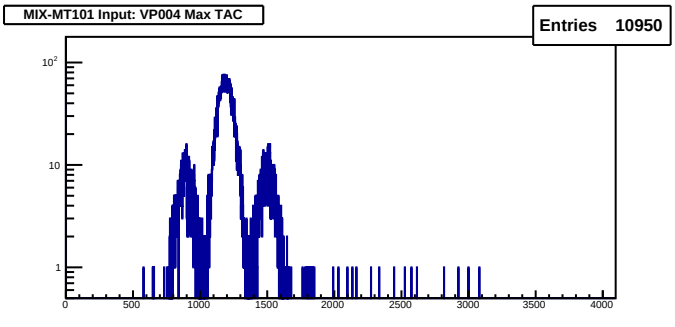

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 21900
TAC sum
1st-Best
2nd-Best
ID

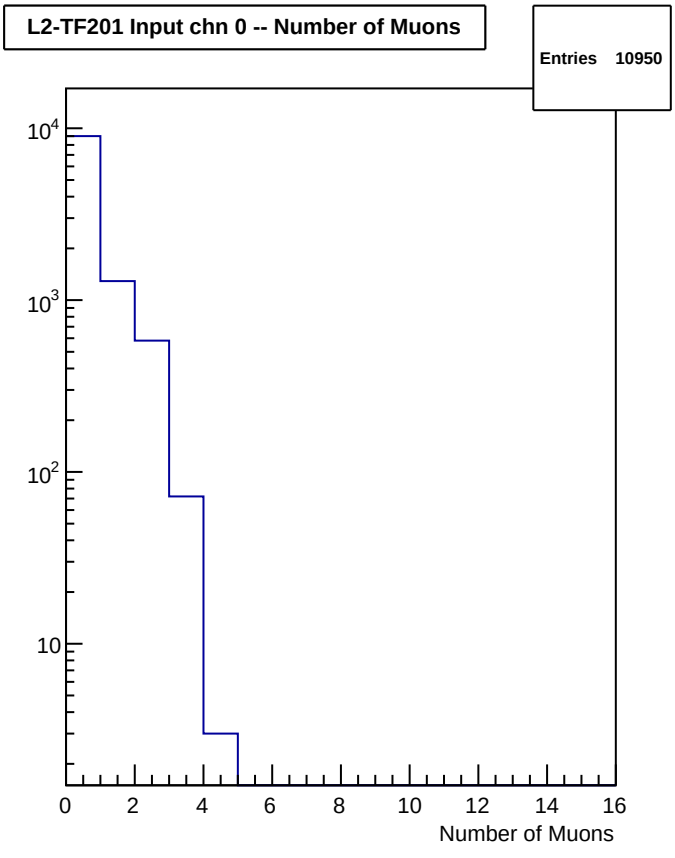

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 628
TAC sum
1st-Best
2nd-Best
ID

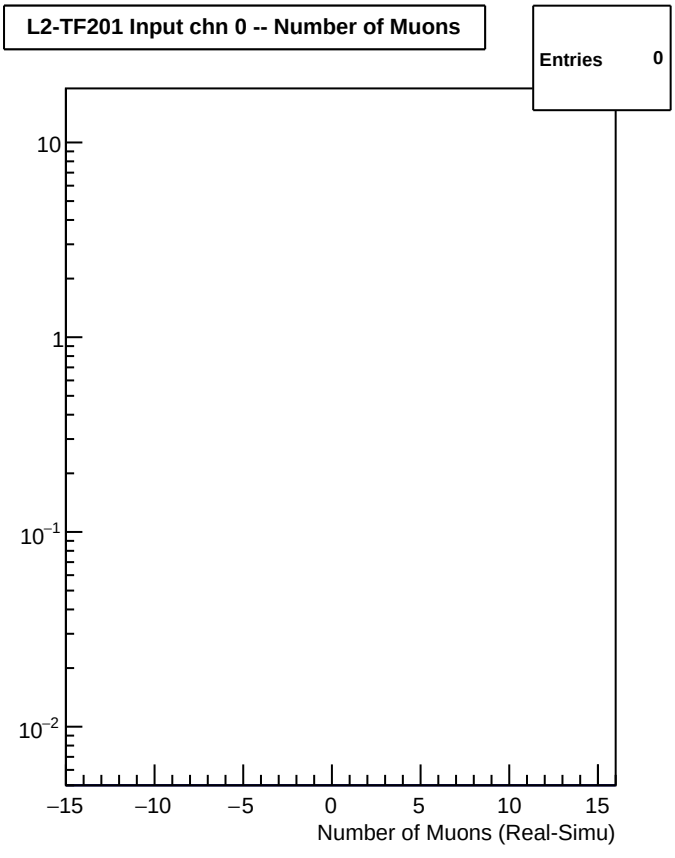

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 21900
TAC sum
1st-Best
2nd-Best
ID

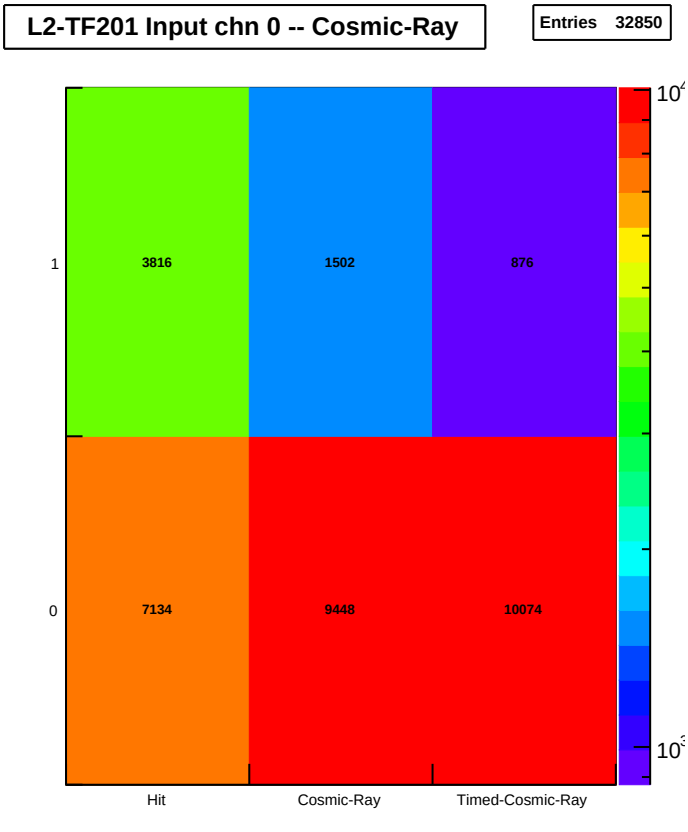

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 656
TAC sum
1st-Best
2nd-Best
ID

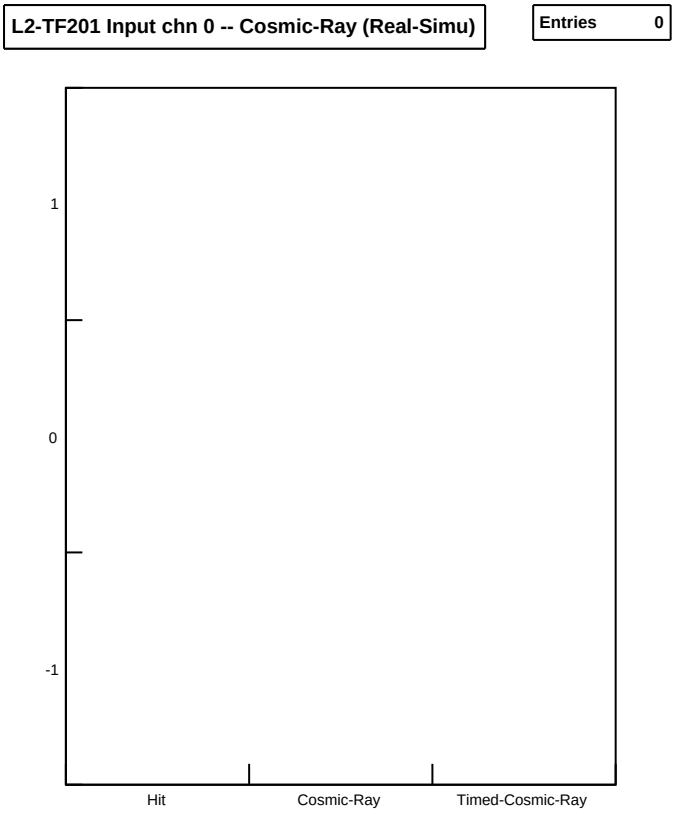

MIX-MT101 Input: VP003 Max TAC
Entries 10950

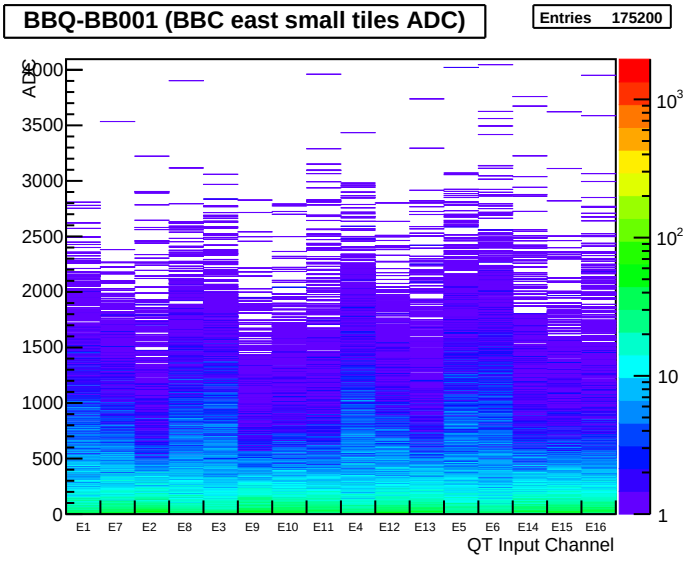

MIX-MT101 Input: VP004 Max TAC
Entries 10950

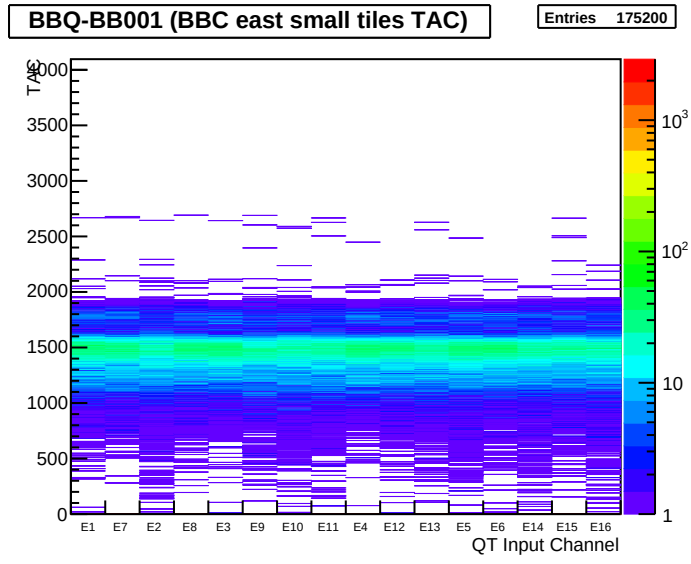

L2-TF201 Input chn 0 -- Number of Muons
Entries 10950
Number of Muons

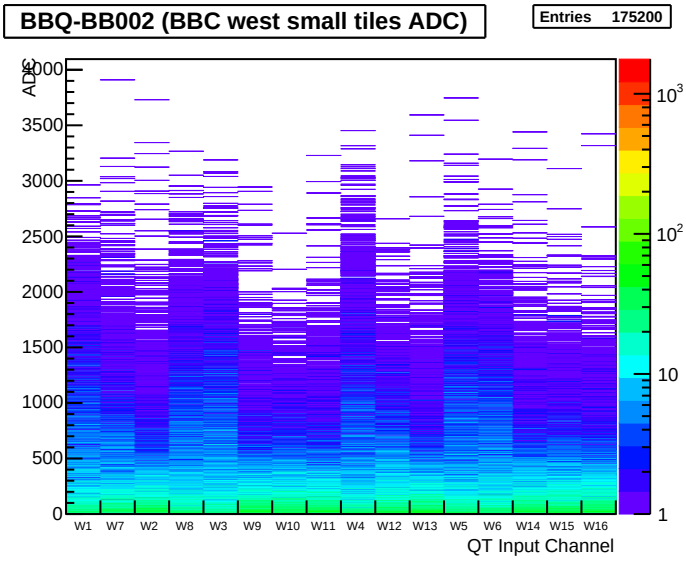

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

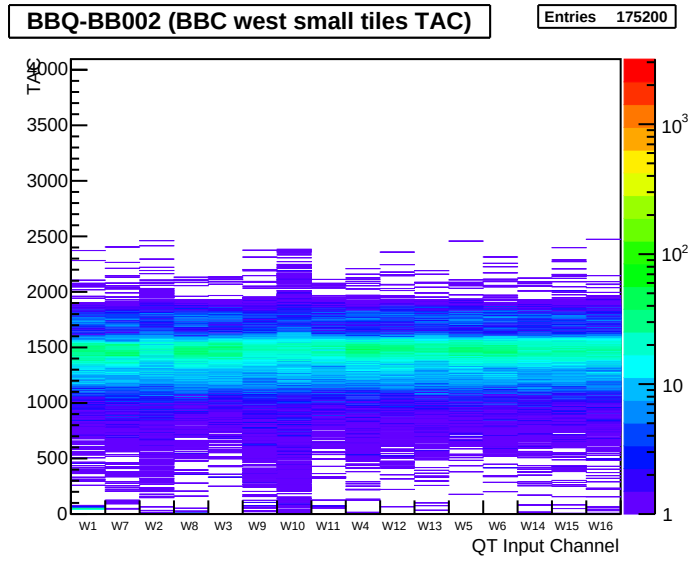

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 32850
3816
1502
876
7134
9448
10074
Hit
Cosmic-Ray
Timed-Cosmic-Ray

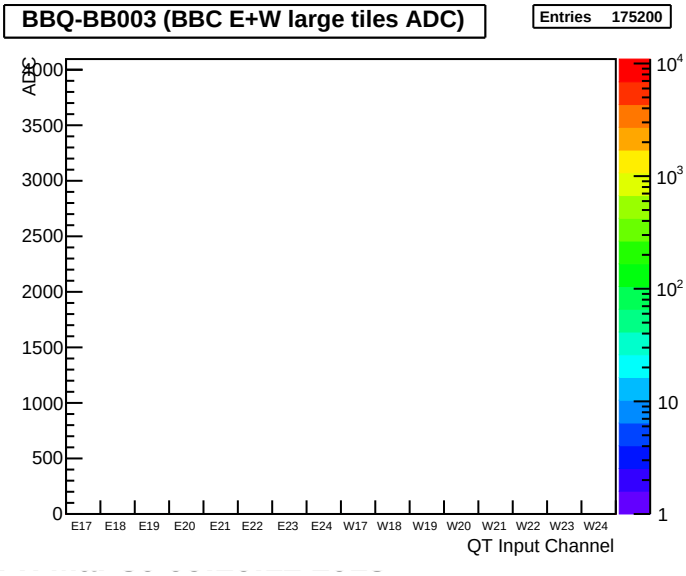

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

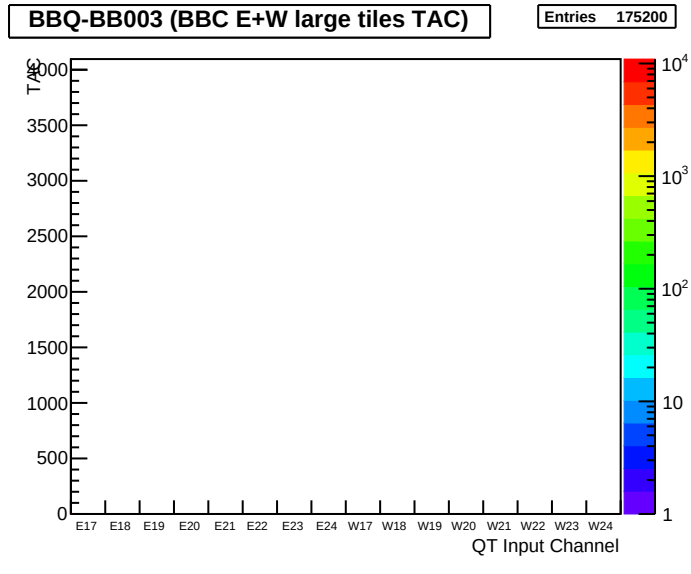

BBQ-BB001 (BBC east small tiles ADC)
Entries 175200
ADC
QT Input Channel

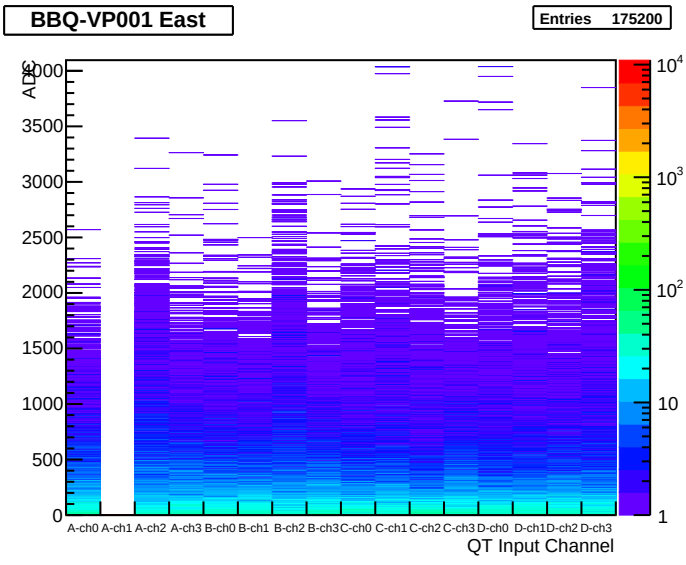

BBQ-BB001 (BBC east small tiles TAC)
Entries 175200
TAC
QT Input Channel

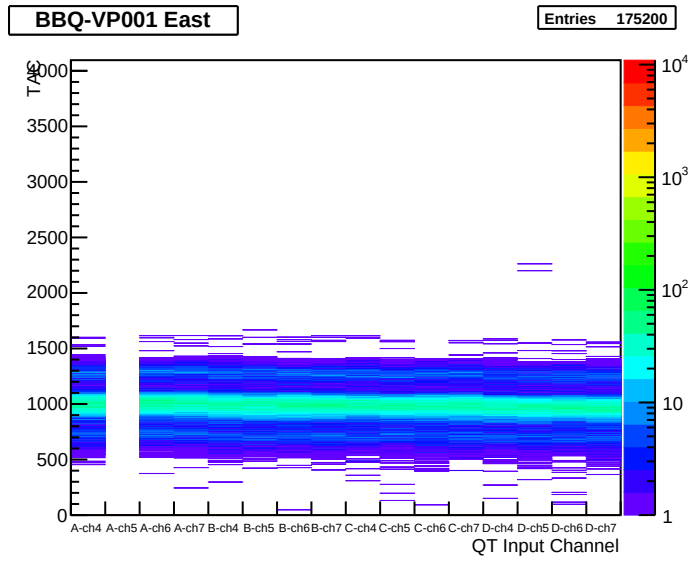

BBQ-BB002 (BBC west small tiles ADC)
Entries 175200
ADC
QT Input Channel

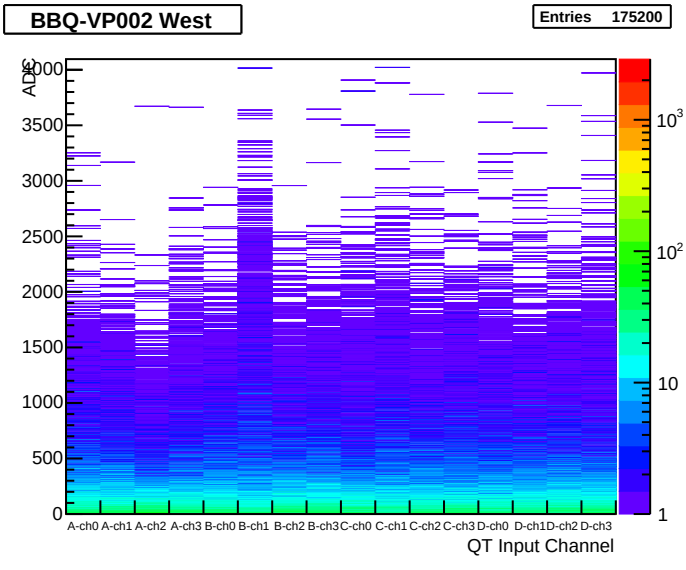

BBQ-BB002 (BBC west small tiles TAC)
Entries 175200
TAC
QT Input Channel

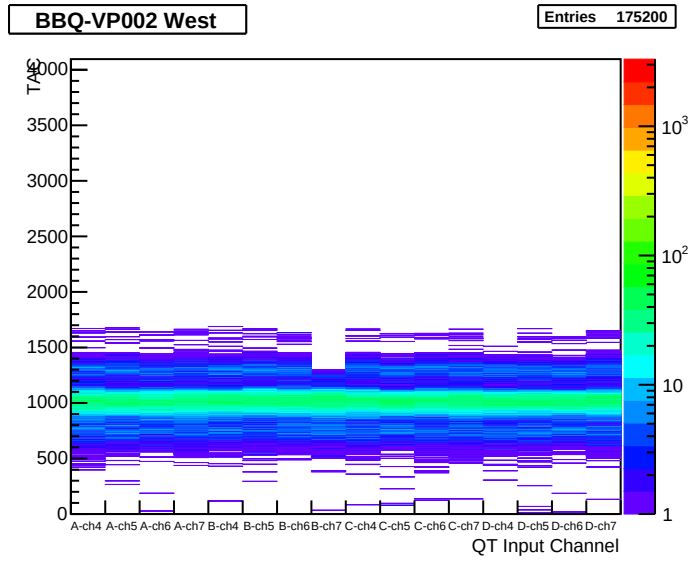

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 175200
ADC
QT Input Channel

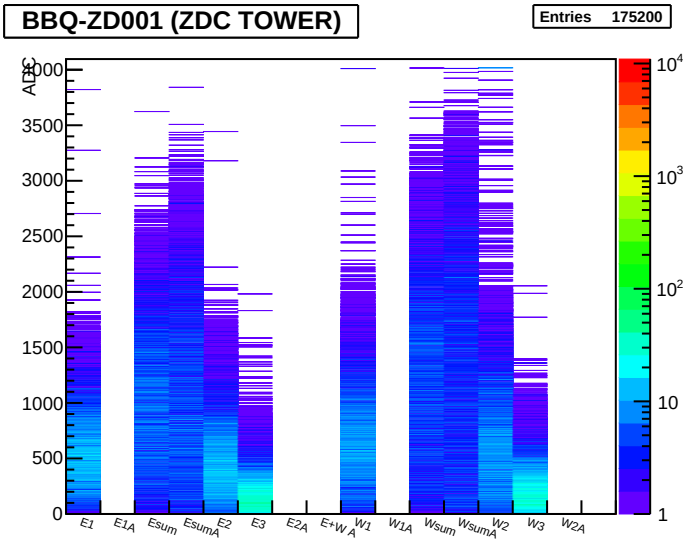

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 175200
TAC
QT Input Channel

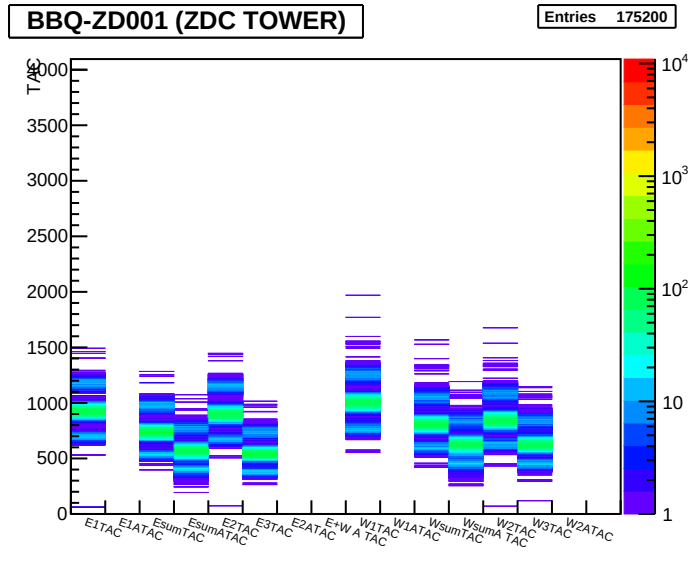

BBQ-VP001 East
Entries 175200
ADC
QT Input Channel

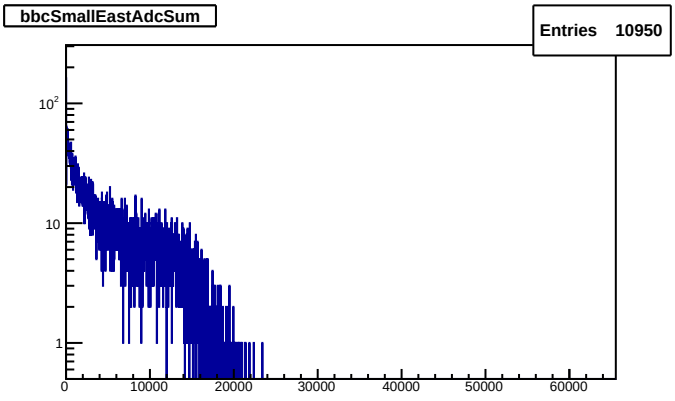

BBQ-VP001 East
Entries 175200
TAC
QT Input Channel

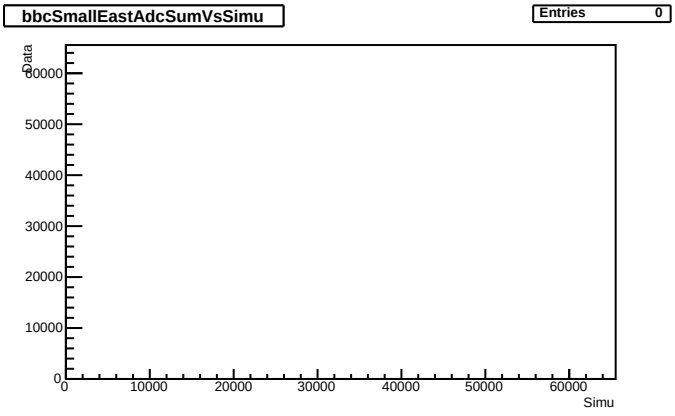

BBQ-VP002 West
Entries 175200
ADC
QT Input Channel

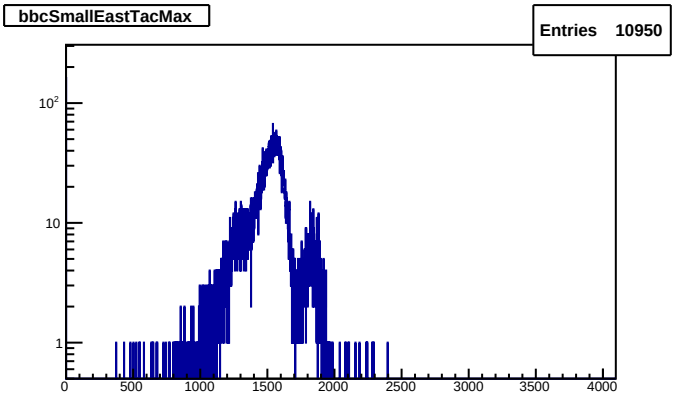

BBQ-VP002 West
Entries 175200
TAC
QT Input Channel

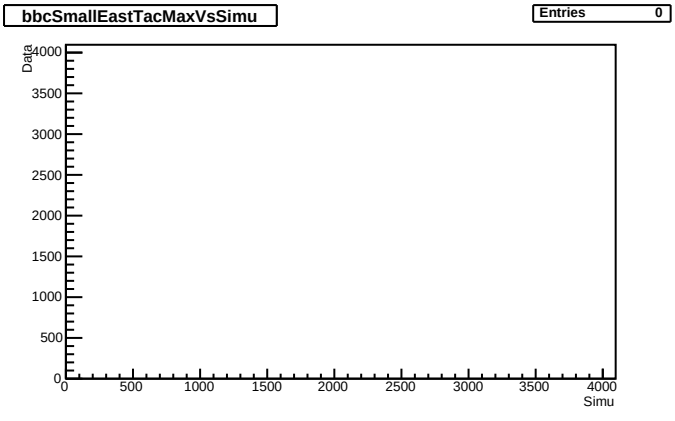

BBQ-ZD001 (ZDC TOWER)
Entries 175200
ADC

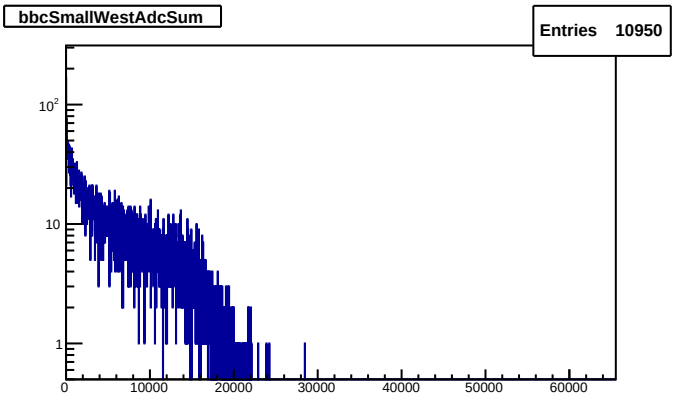

BBQ-ZD001 (ZDC TOWER)
Entries 175200
TAC

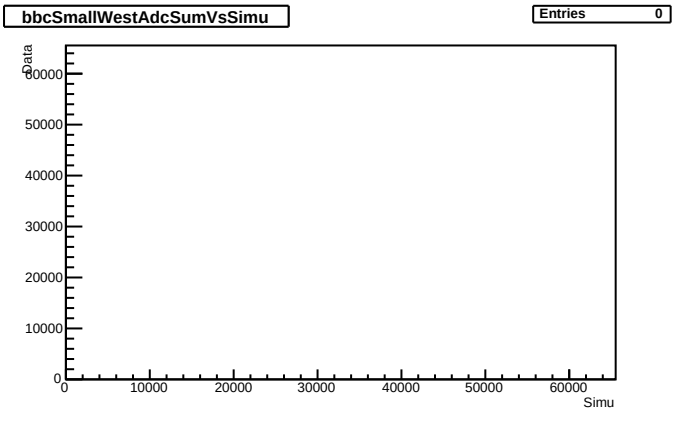

bbcSmallEastAdcSum
Entries 10950

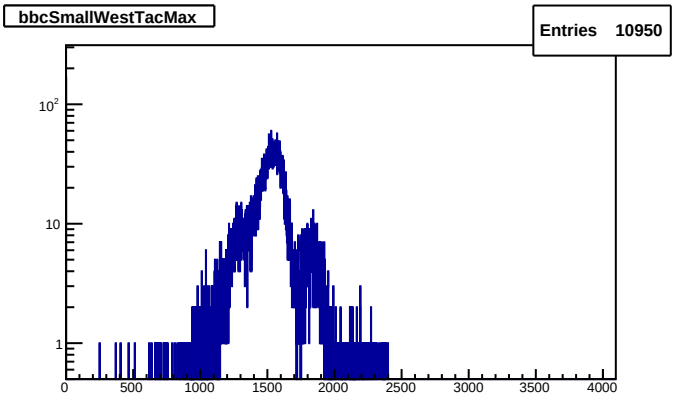

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

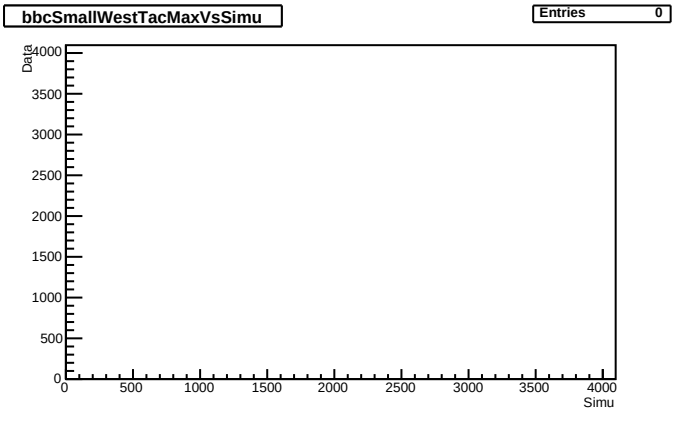

bbcSmallEastTacMax
Entries 10950

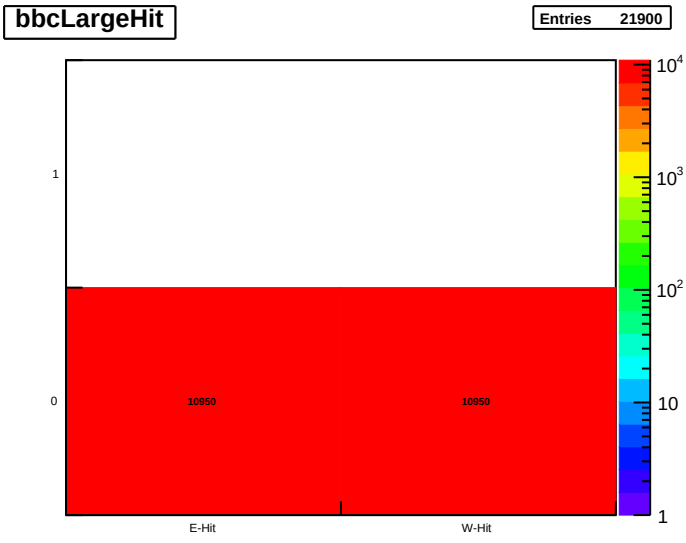

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

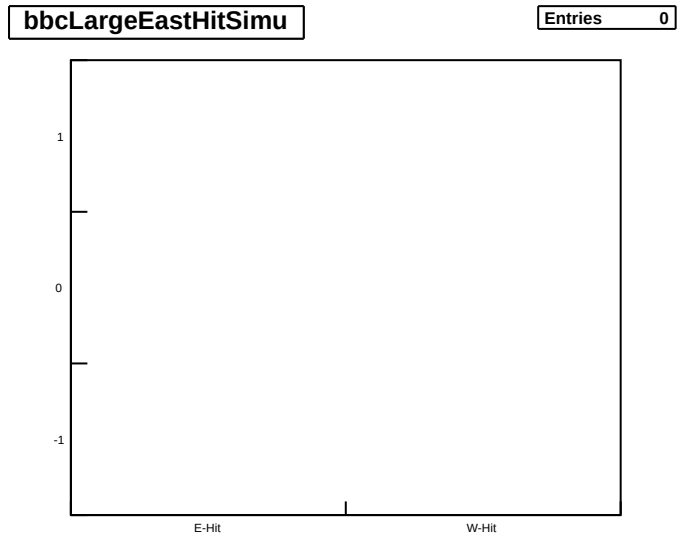

bbcSmallWestAdcSum
Entries 10950

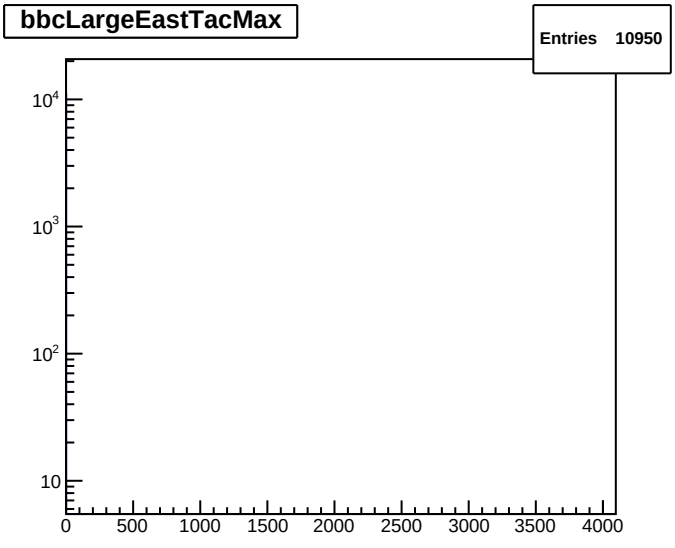

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

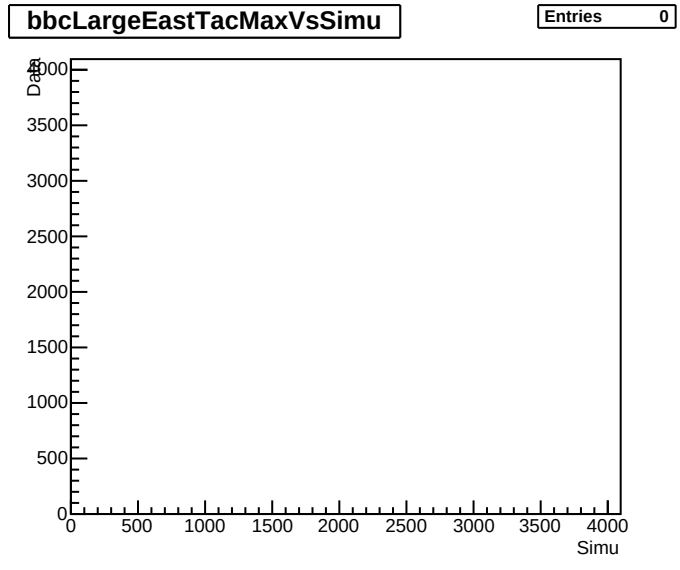

bbcSmallWestTacMax
Entries 10950

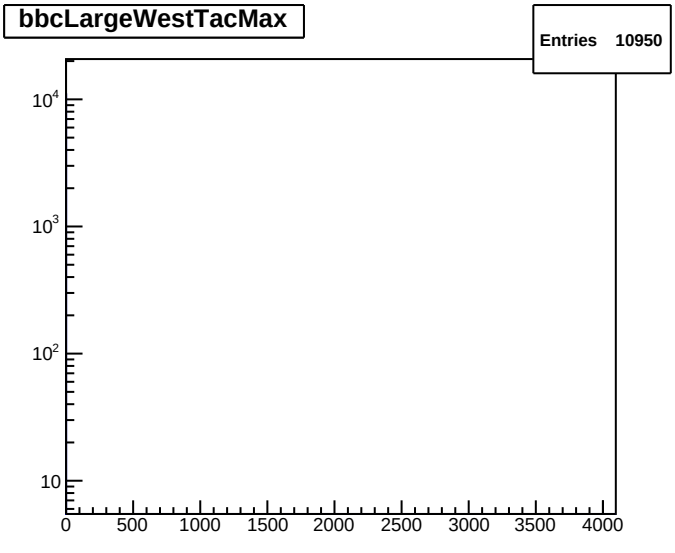

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

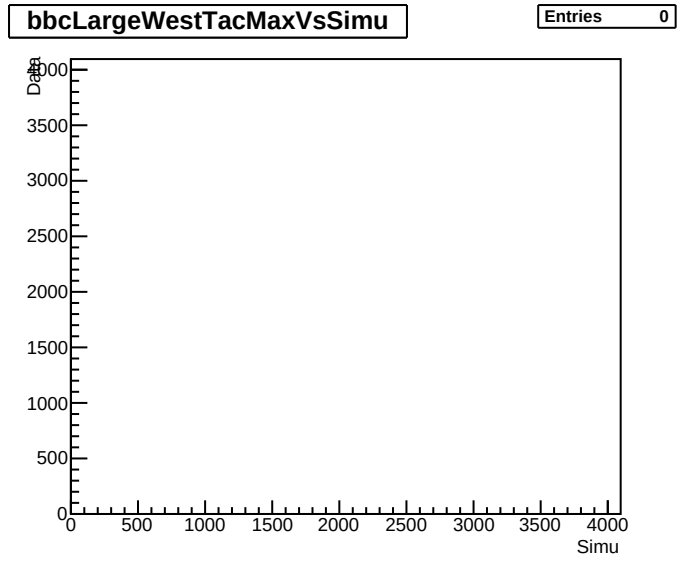

bbcLargeHit
Entries 21900
10950
10950
E-Hit
W-Hit

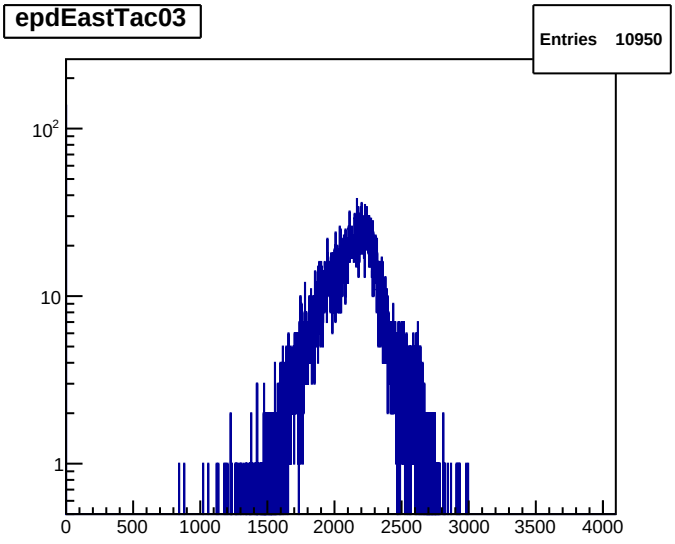

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

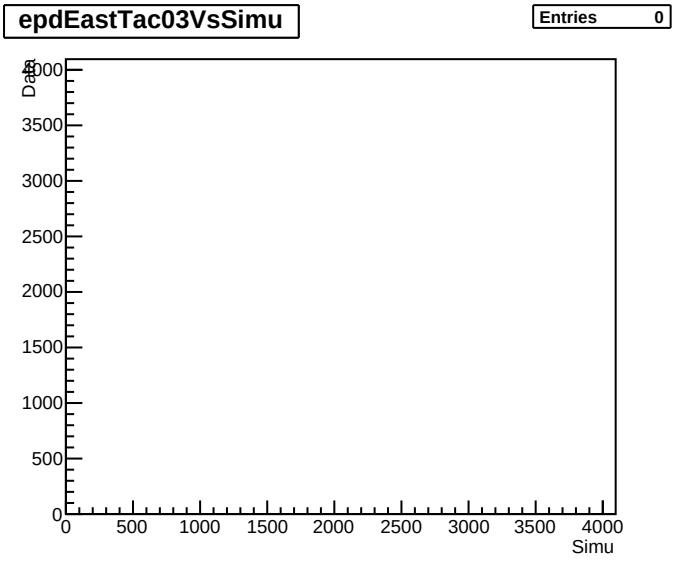

bbcLargeEastTacMax
Entries 10950

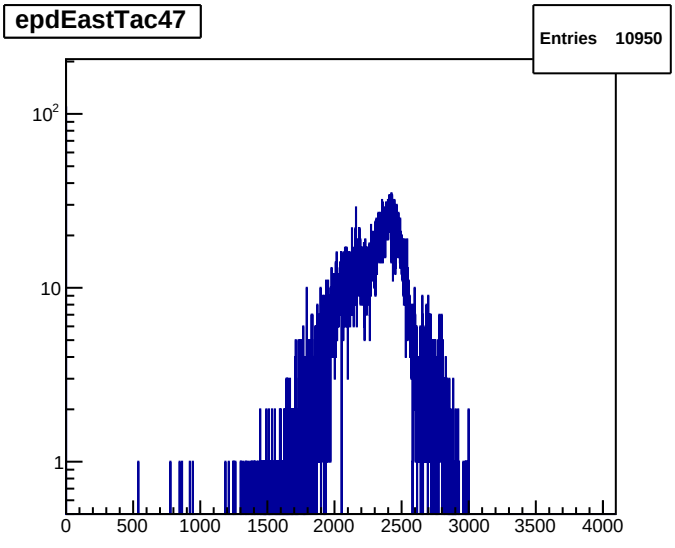

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

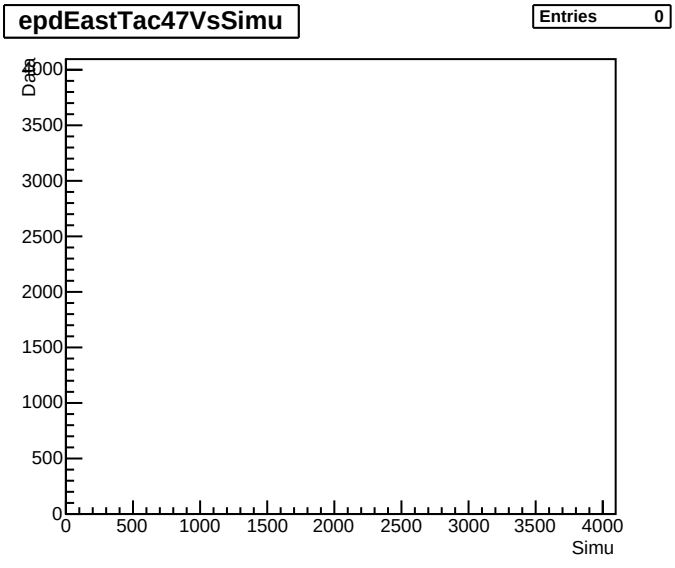

bbcLargeWestTacMax
Entries 10950

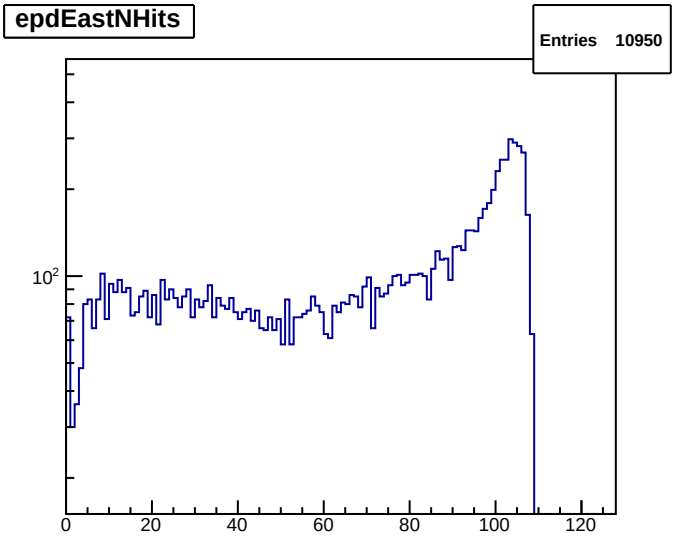

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

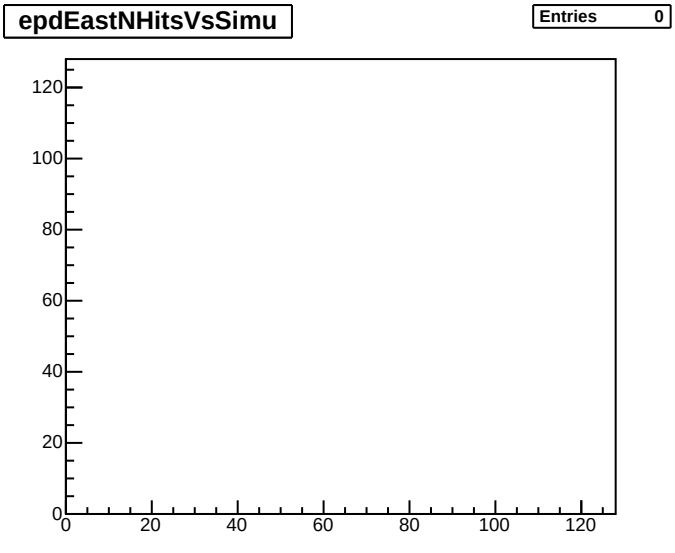

epdEastTac03
Entries 10950
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

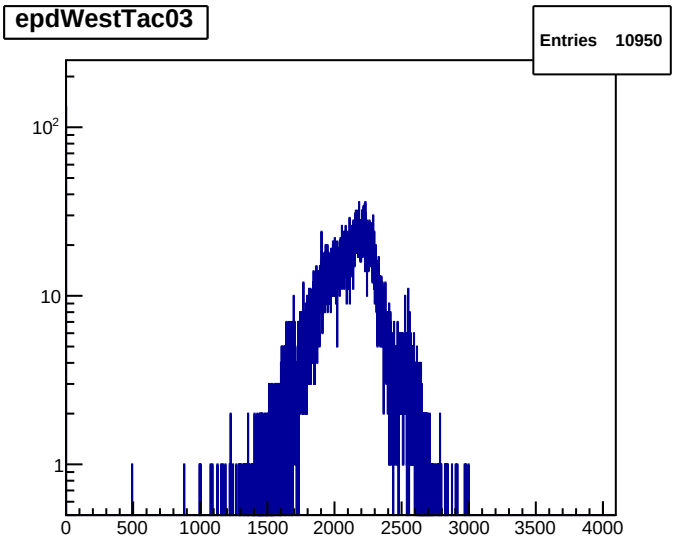

epdEastTac03VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

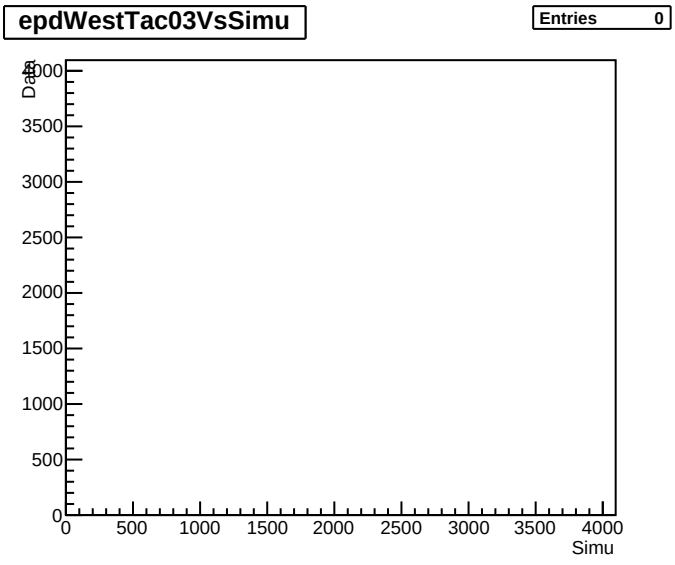

epdEastTac47
Entries 10950
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

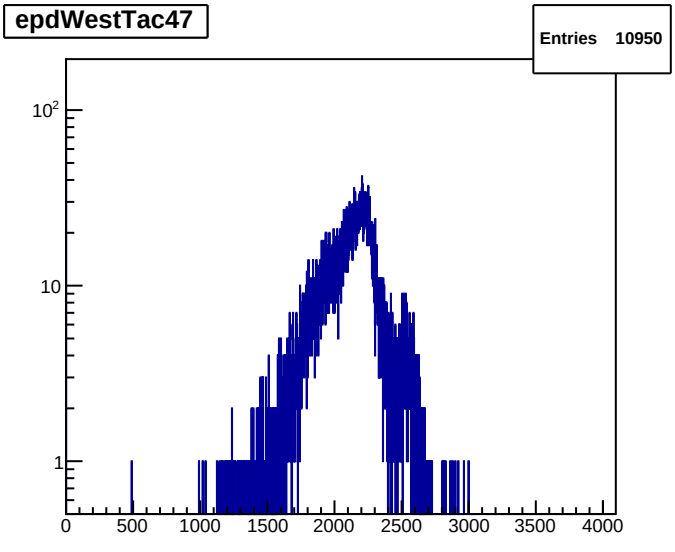

epdEastTac47VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

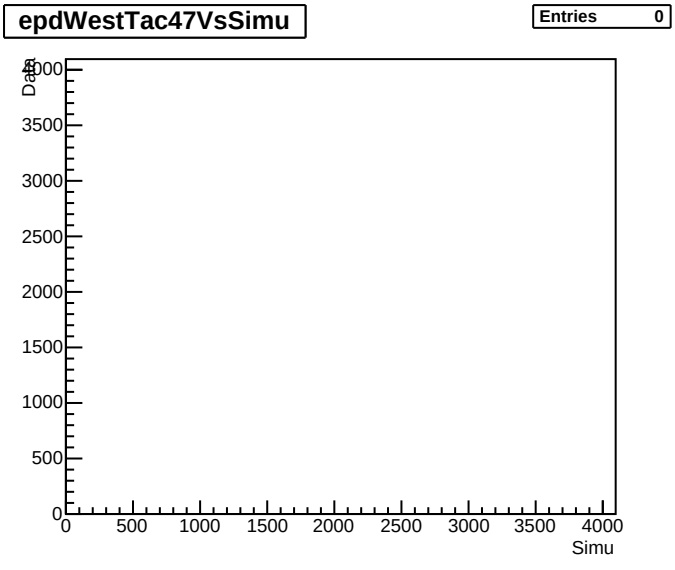

epdEastNHits
Entries 10950
10^2
0 20 40 60 80 100 120

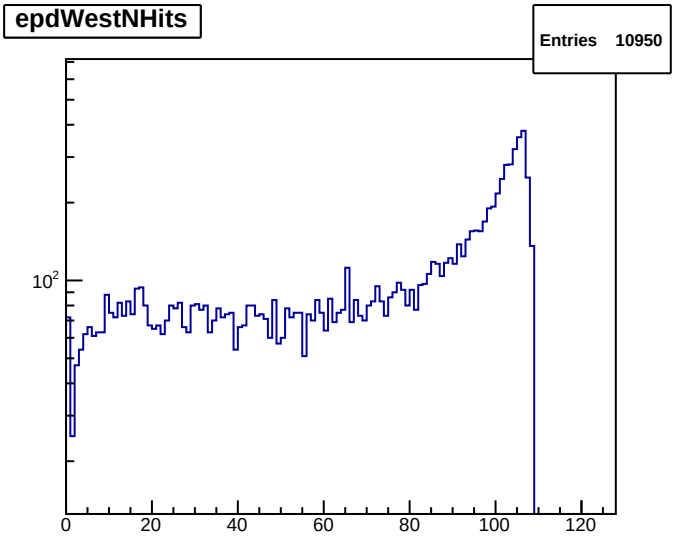

epdEastNHitsVsSimu
Entries 0
120 100 80 60 40 20 0
0 20 40 60 80 100 120

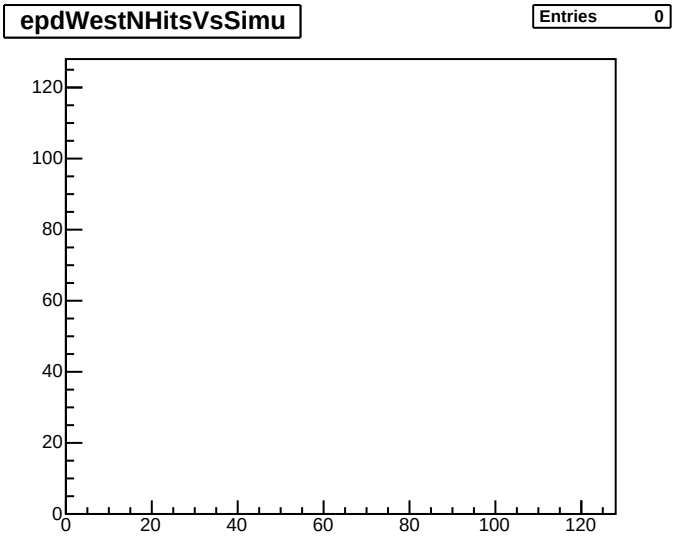

epdWestTac03
Entries 10950
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

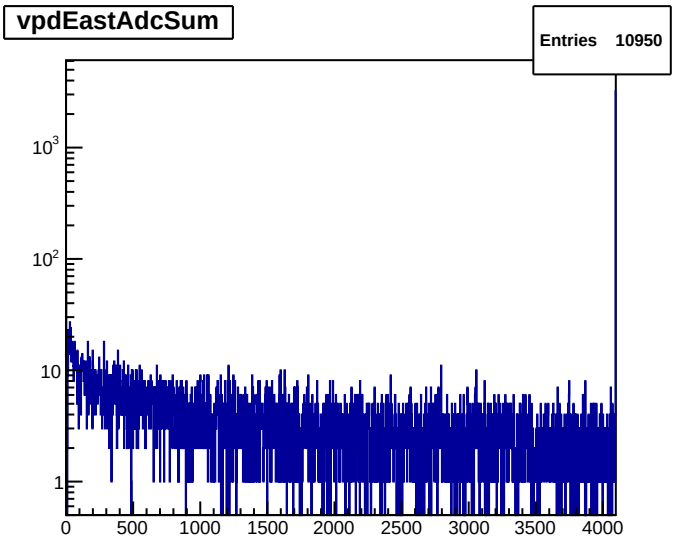

epdWestTac03VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

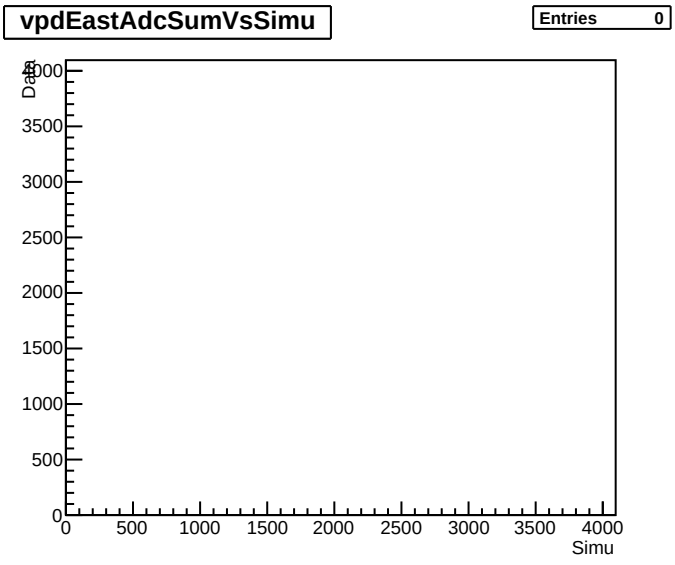

epdWestTac47
Entries 10950
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

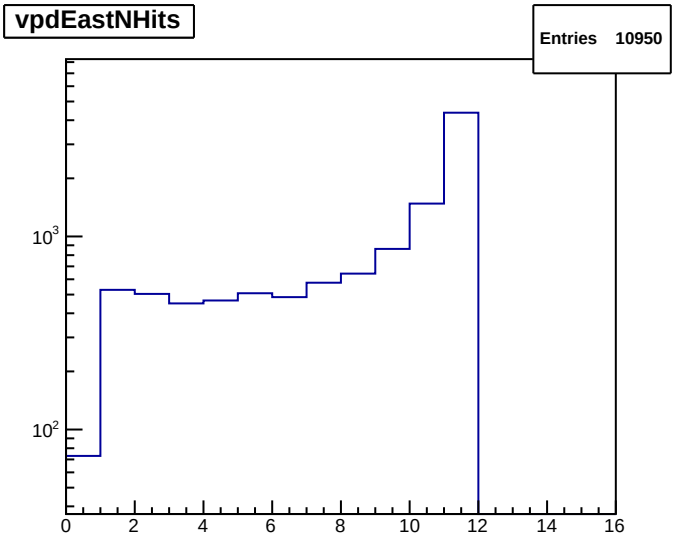

epdWestTac47VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

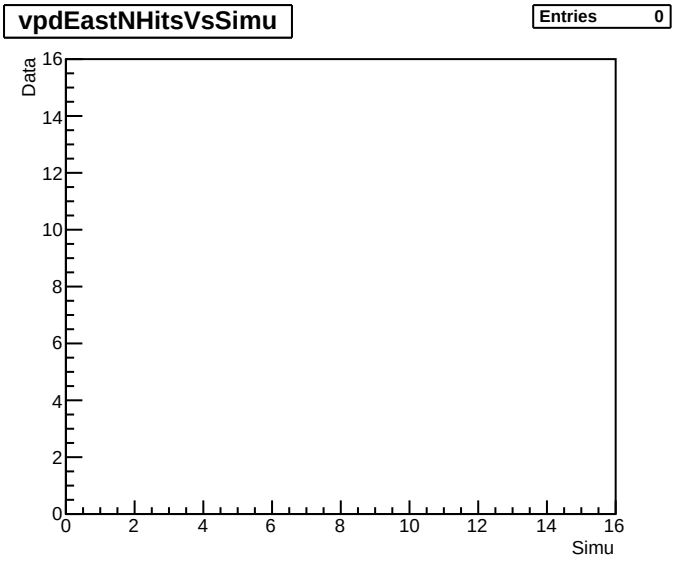

epdWestNHits
Entries 10950
10^2
0 20 40 60 80 100 120

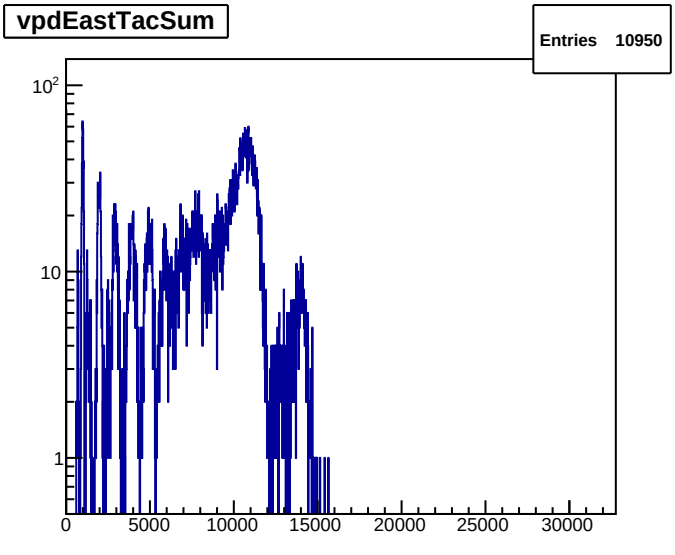

epdWestNHitsVsSimu
Entries 0
120
100
80
60
40
20
0
0 20 40 60 80 100 120

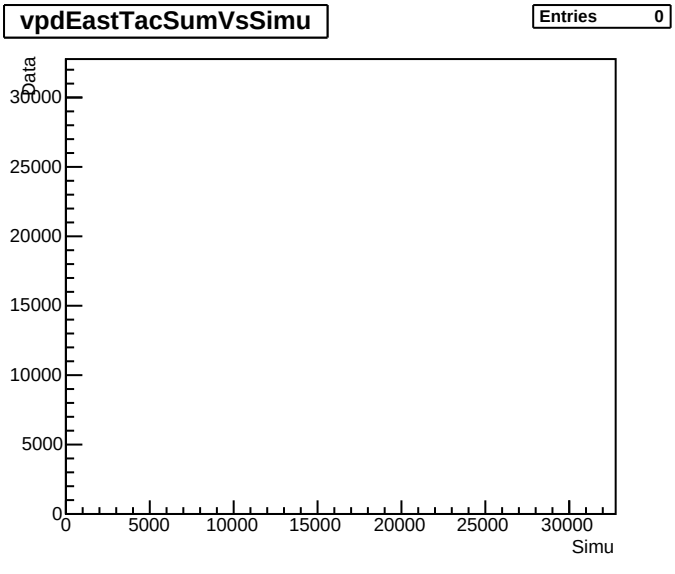

vpdEastAdcSum
Entries 10950
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

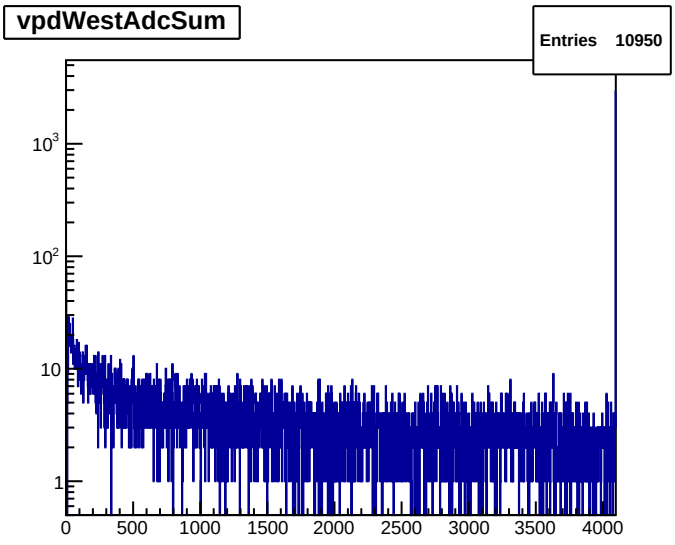

vpdEastAdcSumVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

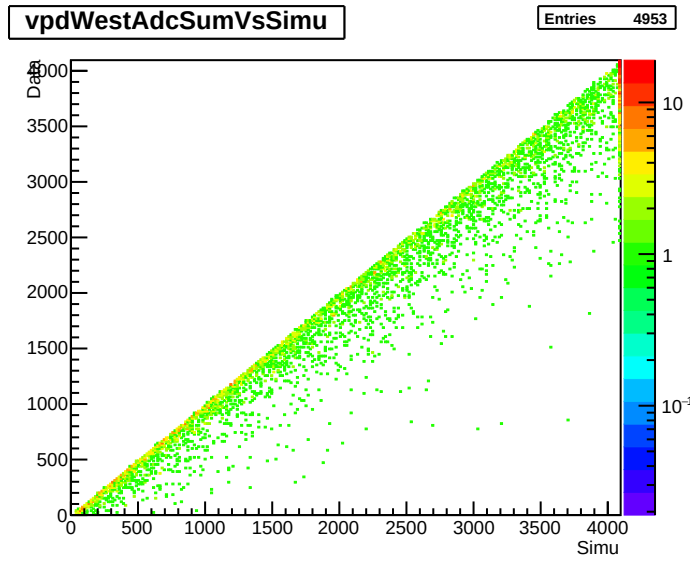

vpdEastNHits
Entries 10950
10^3
10^2
0 2 4 6 8 10 12 14 16

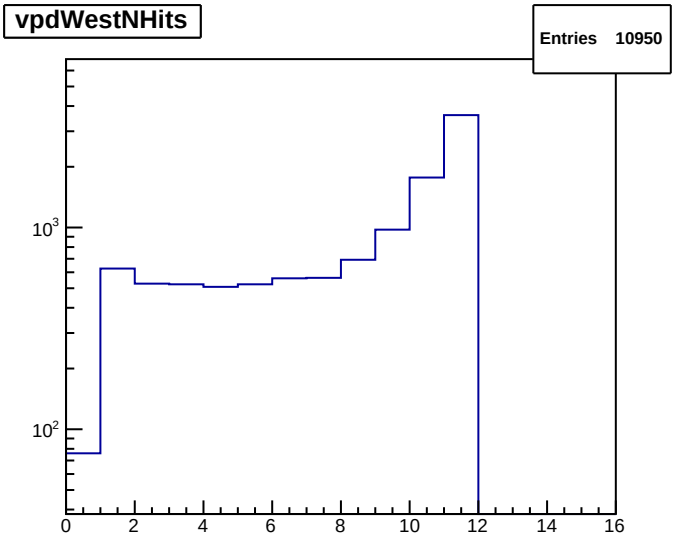

vpdEastNHitsVsSimu
Entries 0
Data
16
14
12
10
8
6
4
2
0
0 2 4 6 8 10 12 14 16
Simu

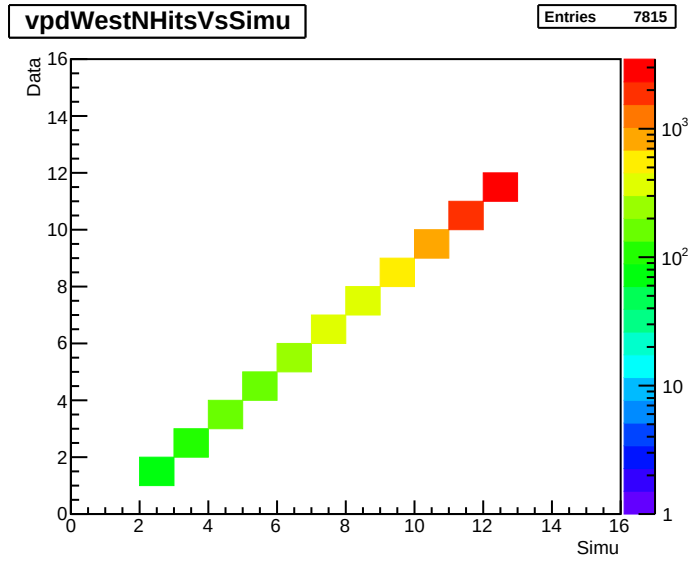

vpdEastTacSum
Entries 10950
10^2
10
1
0 5000 10000 15000 20000 25000 30000

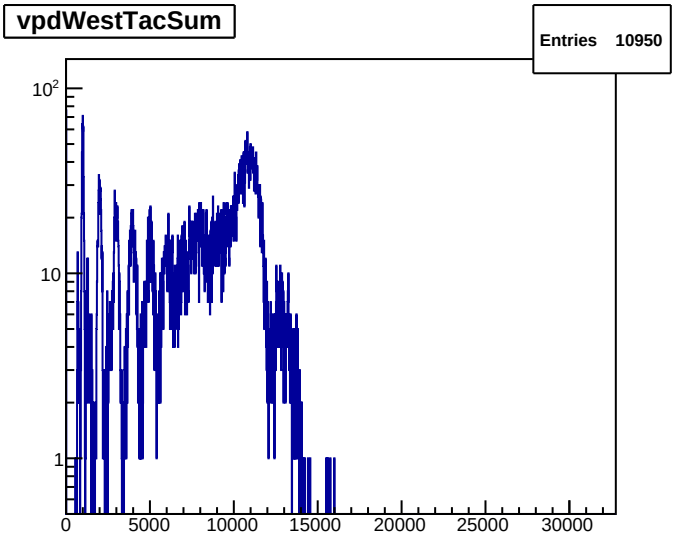

vpdEastTacSumVsSimu
Entries 0
Data
30000
25000
20000
15000
10000
5000
0
0 5000 10000 15000 20000 25000 30000
Simu

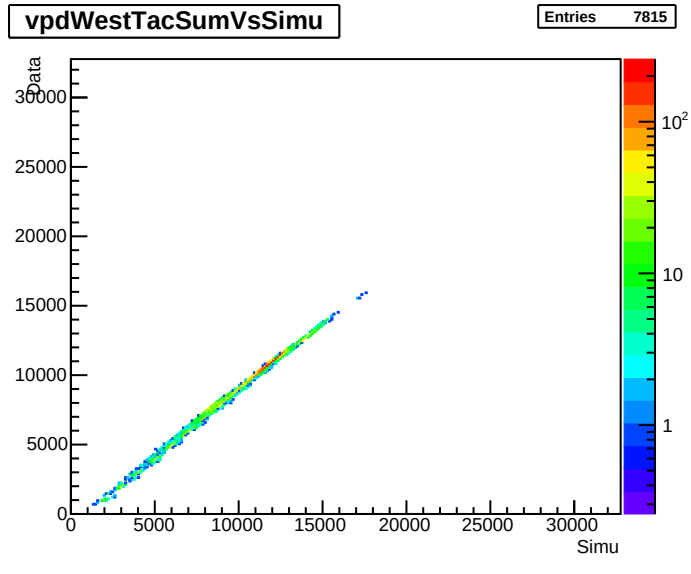

vpdWestAdcSum
Entries 10950

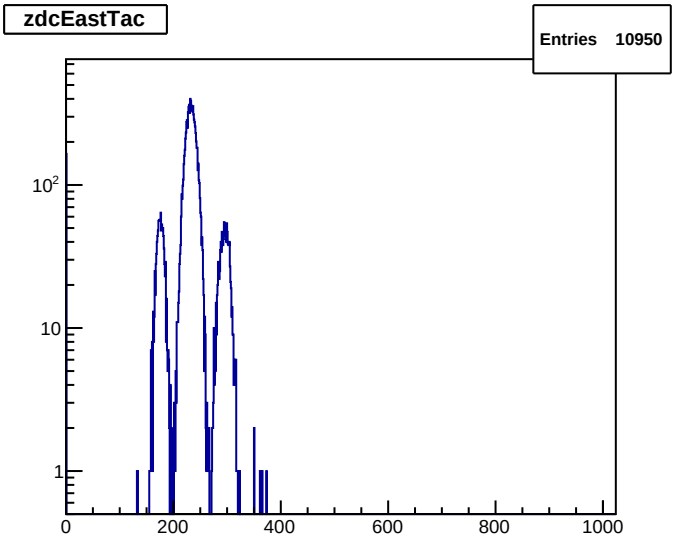

vpdWestAdcSumVsSimu
Entries 4953
Data
Simu

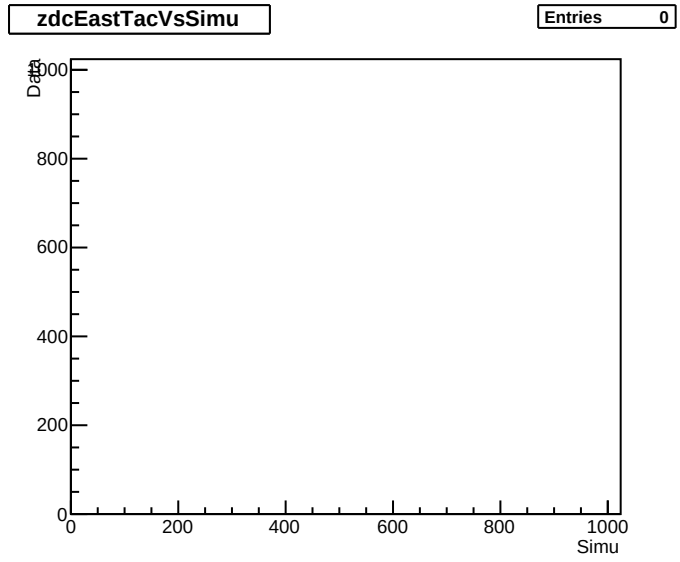

vpdWestNHits
Entries 10950

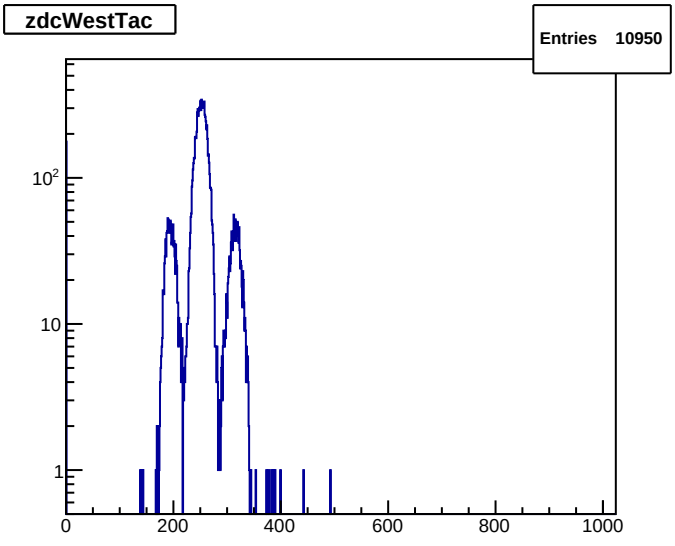

vpdWestNHitsVsSimu
Entries 7815
Data
Simu

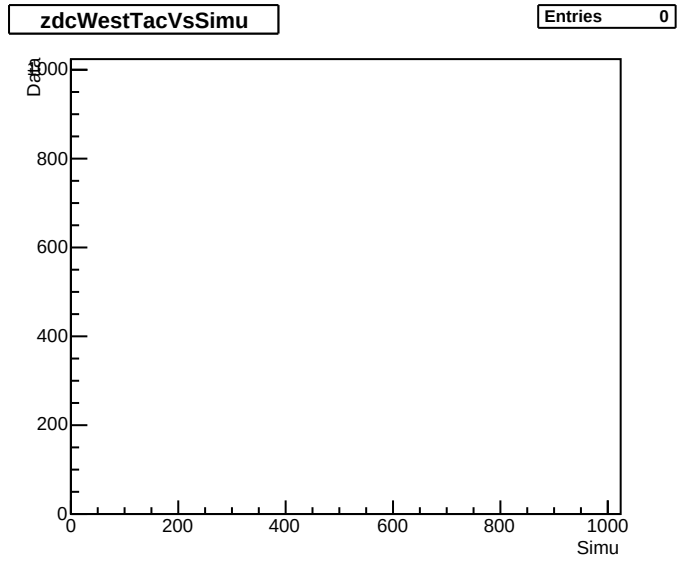

vpdWestTacSum
Entries 10950

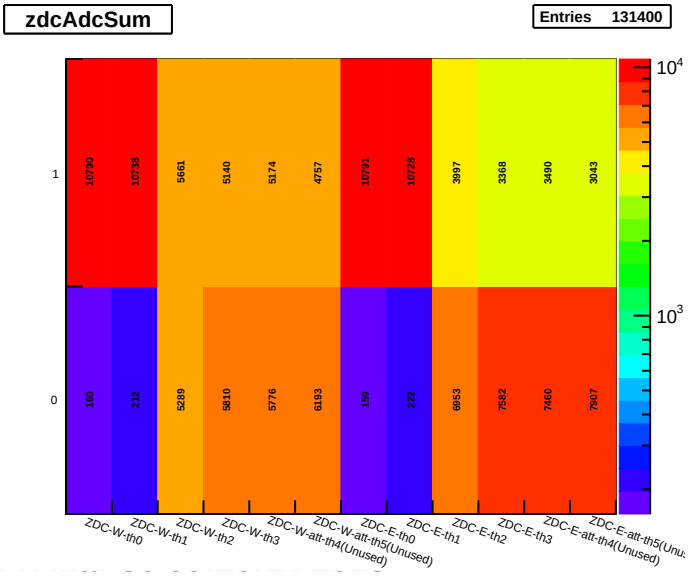

vpdWestTacSumVsSimu
Entries 7815
Data
Simu

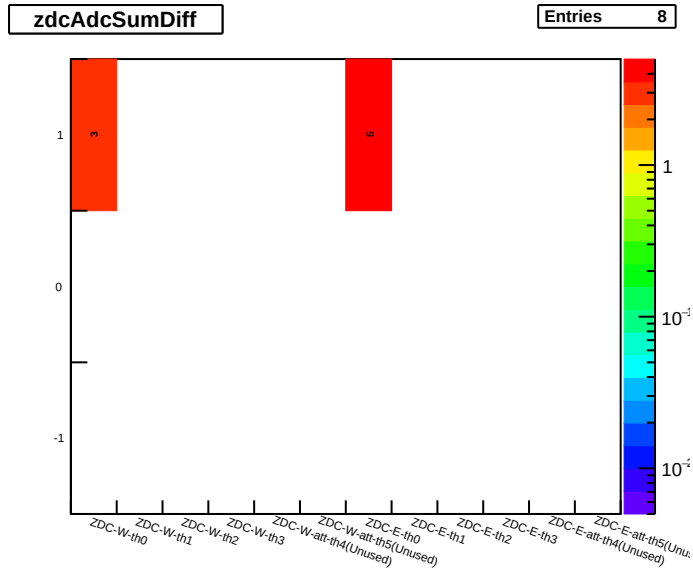

zdcEastTac
Entries 10950
10^2
10
1
0
200
400
600
800
1000

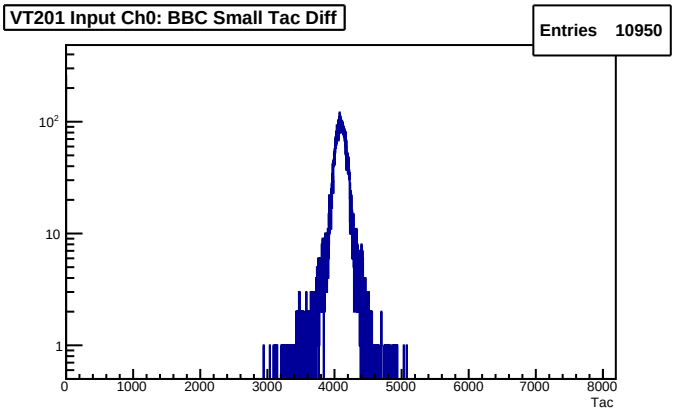

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

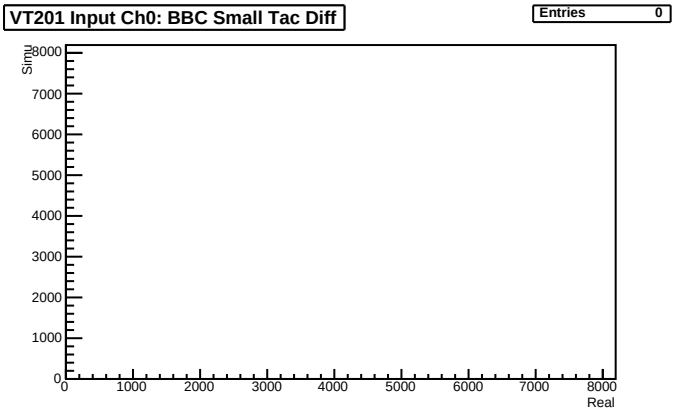

zdcWestTac
Entries 10950
10^2
10
1
0
200
400
600
800
1000

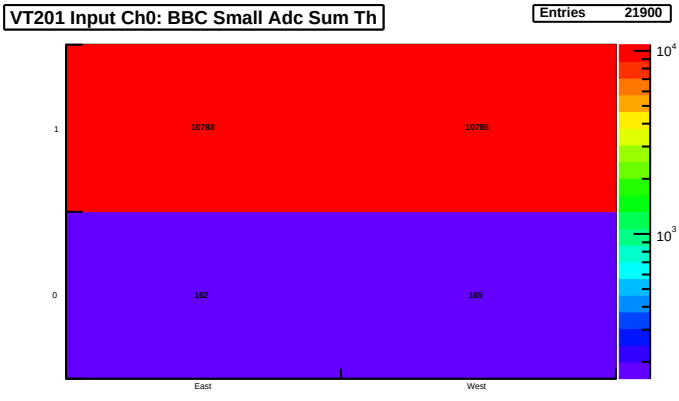

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

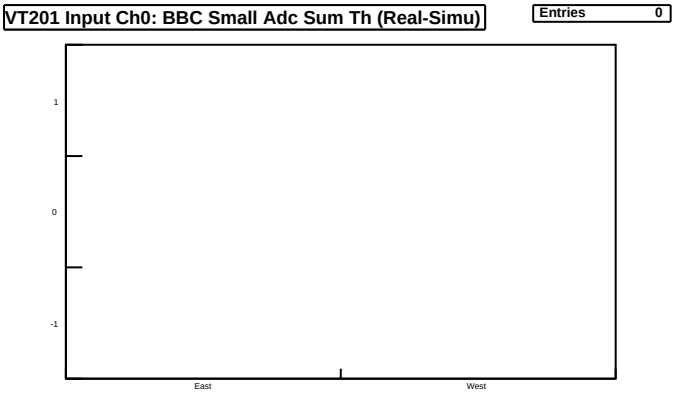

zdcAdcSum
Entries 131400
1
0
10790
10738
5661
5140
5174
4757
10791
10728
3997
3368
3490
3043
160
212
5289
5810
5776
6193
159
222
6953
7582
7460
7907
10^4
10^3
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

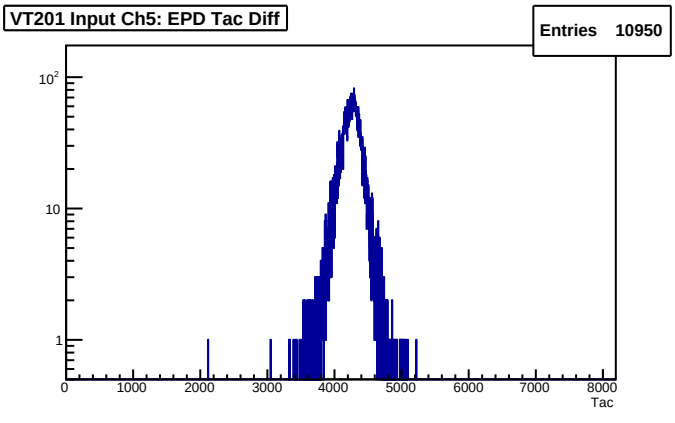

zdcAdcSumDiff
Entries 8
1
0
-1
3
5
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

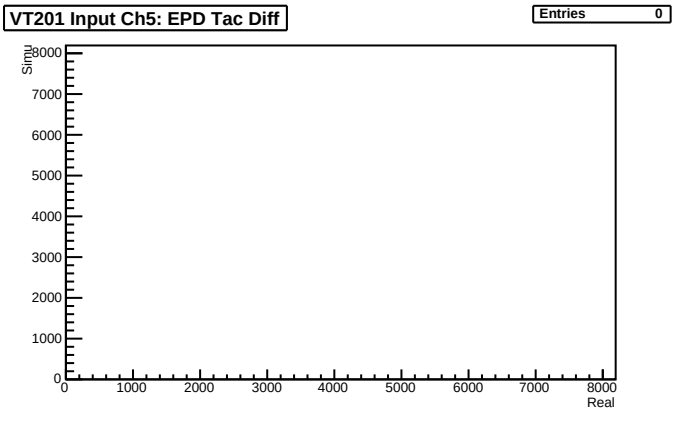

VT201 Input Ch0: BBC Small Tac Diff
Entries 10950
Tac

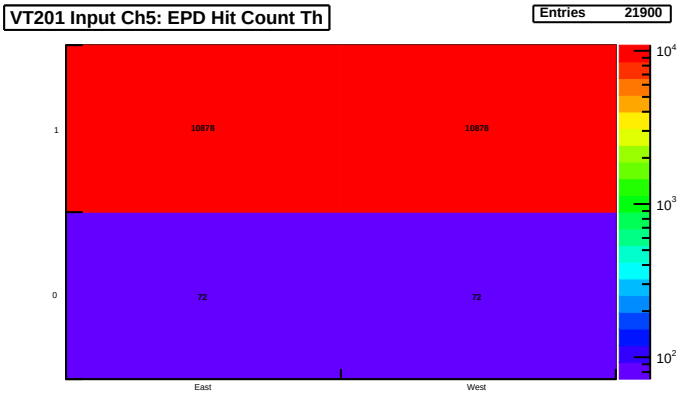

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

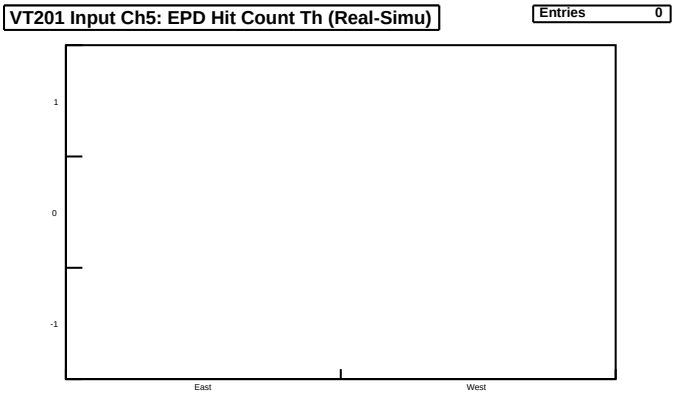

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 21900
10788
10785
162
165
East
West

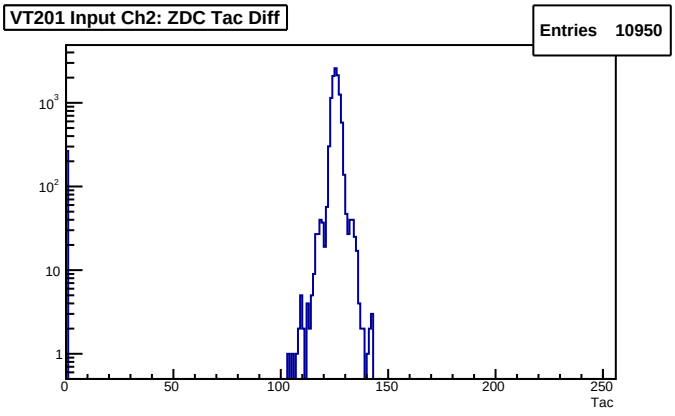

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

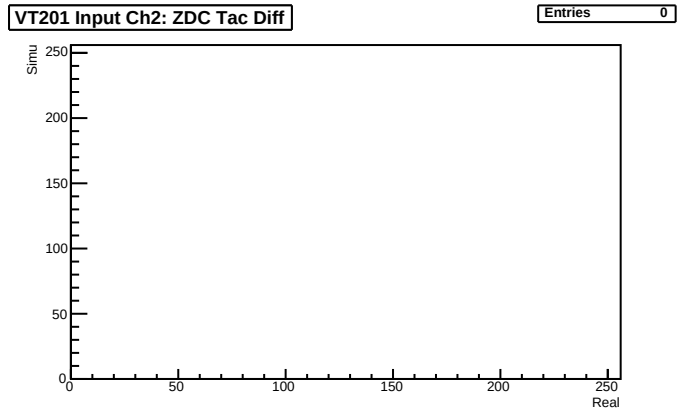

VT201 Input Ch5: EPD Tac Diff
Entries 10950
Tac

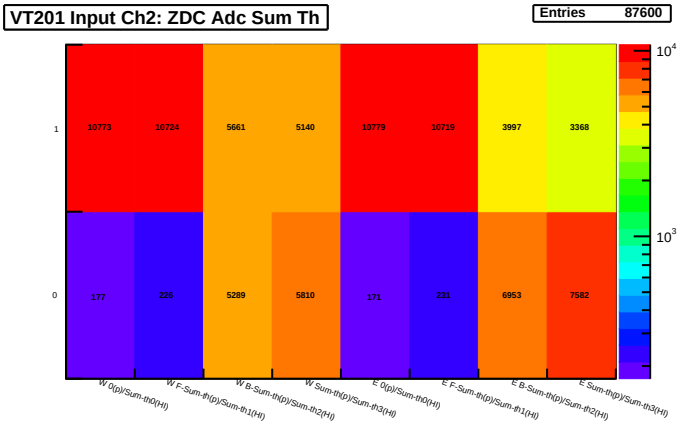

VT201 Input Ch5: EPD Tac Diff
Entries 0
Simu
Real

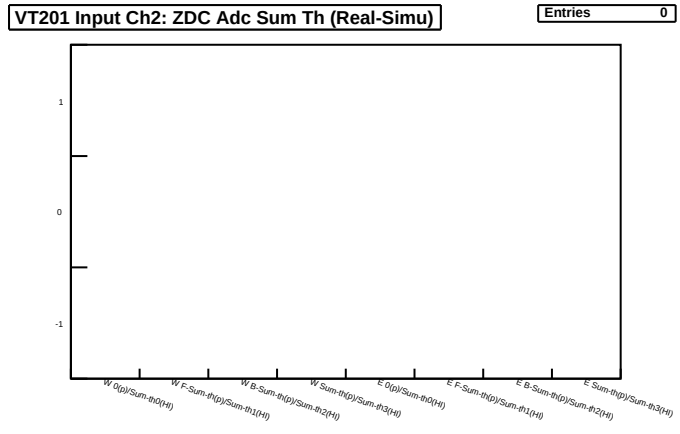

VT201 Input Ch5: EPD Hit Count Th
Entries 21900
10878
10878
72
72
East
West

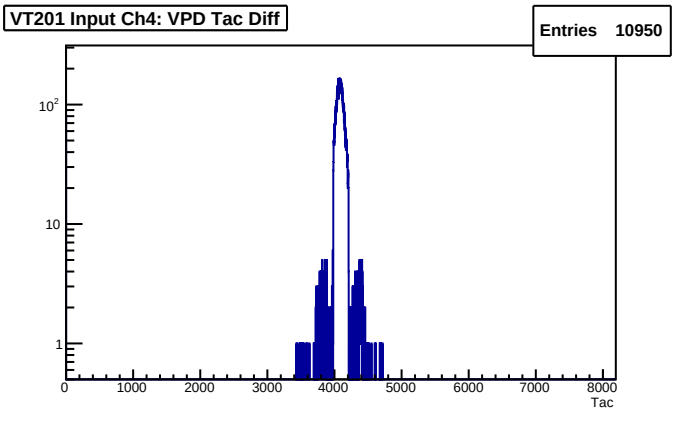

VT201 Input Ch5: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

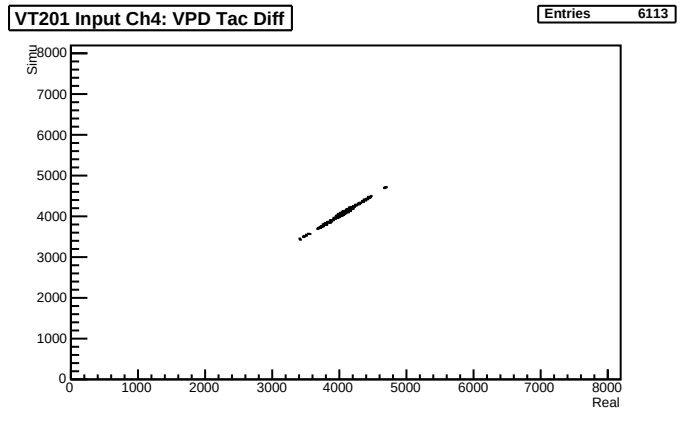

VT201 Input Ch2: ZDC Tac Diff
Entries 10950
Tac

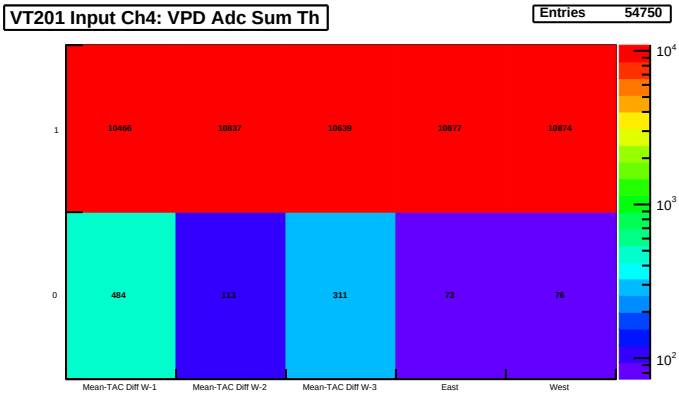

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

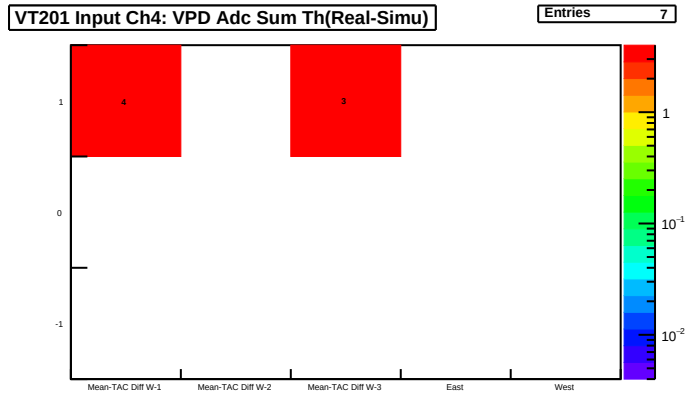

VT201 Input Ch2: ZDC Adc Sum Th
Entries 87600
10773
10724
5661
5140
10779
10719
3997
3368
177
226
5289
5810
171
231
6953
7582
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

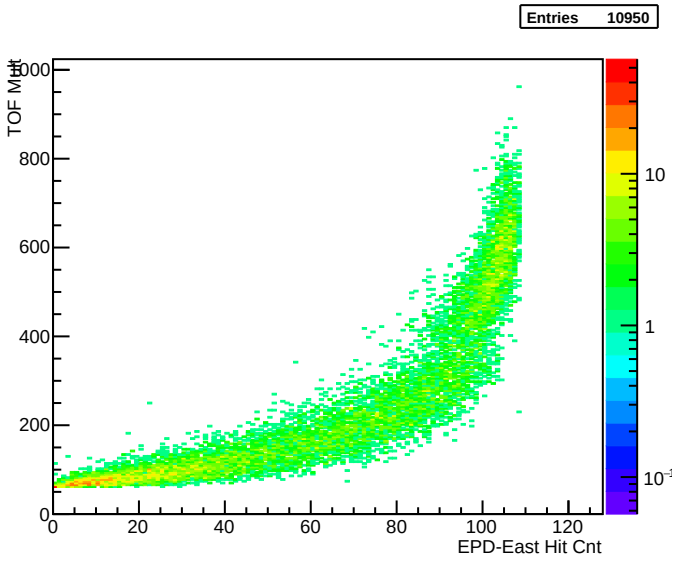

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

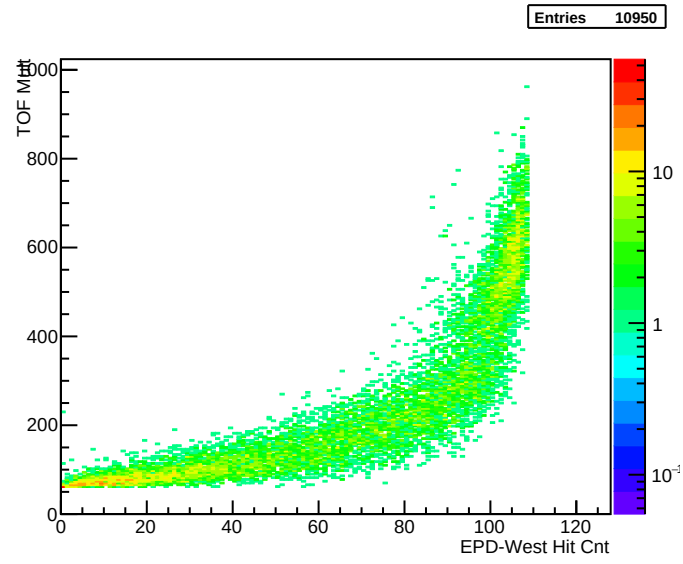

VT201 Input Ch4: VPD Tac Diff
Entries 10950
Tac

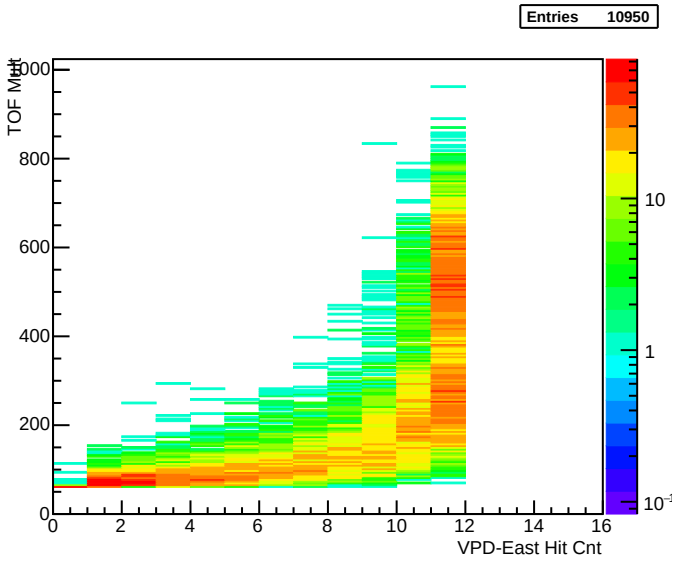

VT201 Input Ch4: VPD Tac Diff
Entries 6113
Simu
Real

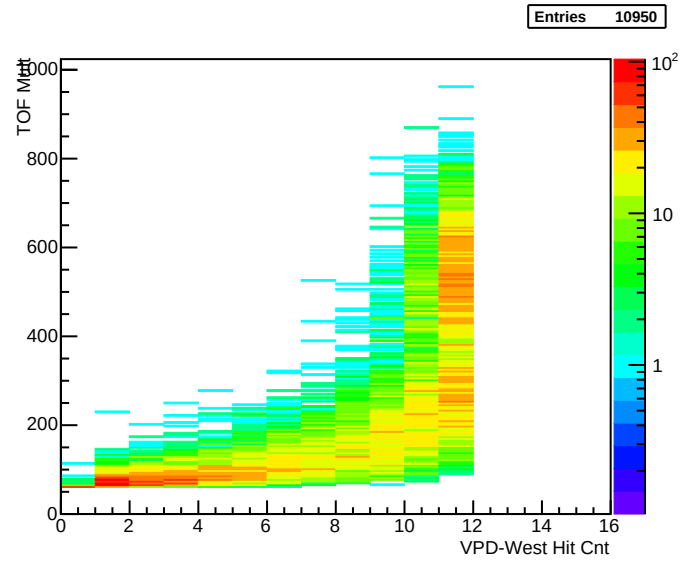

VT201 Input Ch4: VPD Adc Sum Th
Entries 54750
10466
10637
10639
10877
10874
484
113
311
73
76
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

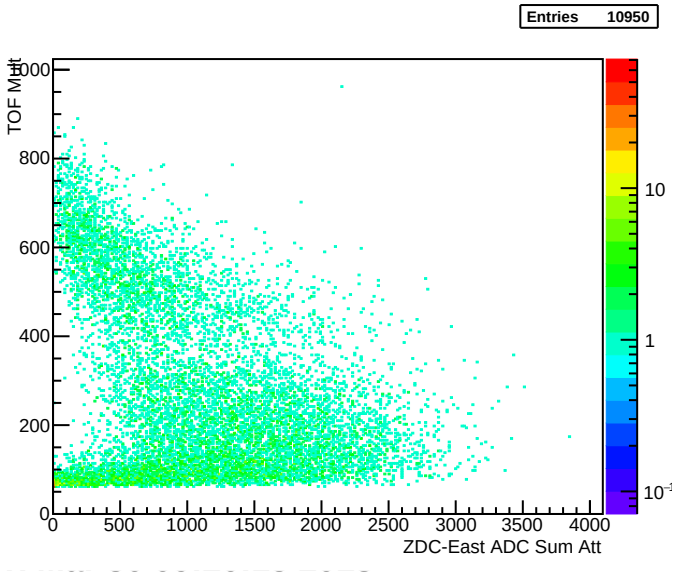

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 7
4
3
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

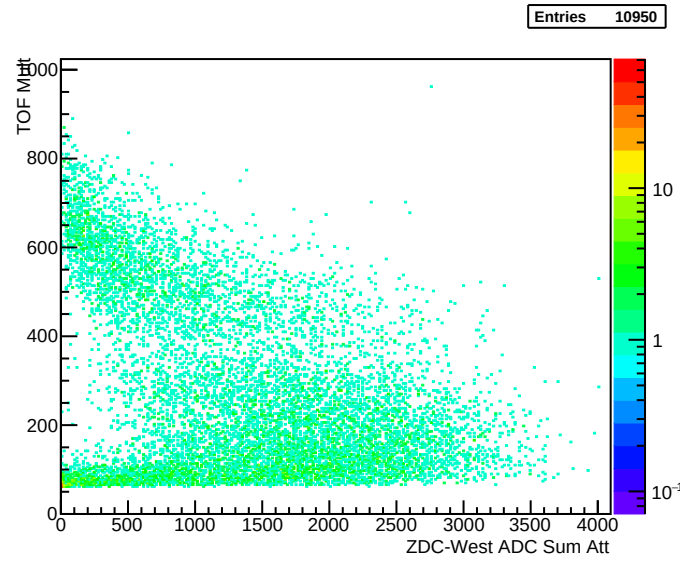

Entries 10950
TOF Mult
EPD-East Hit Cnt

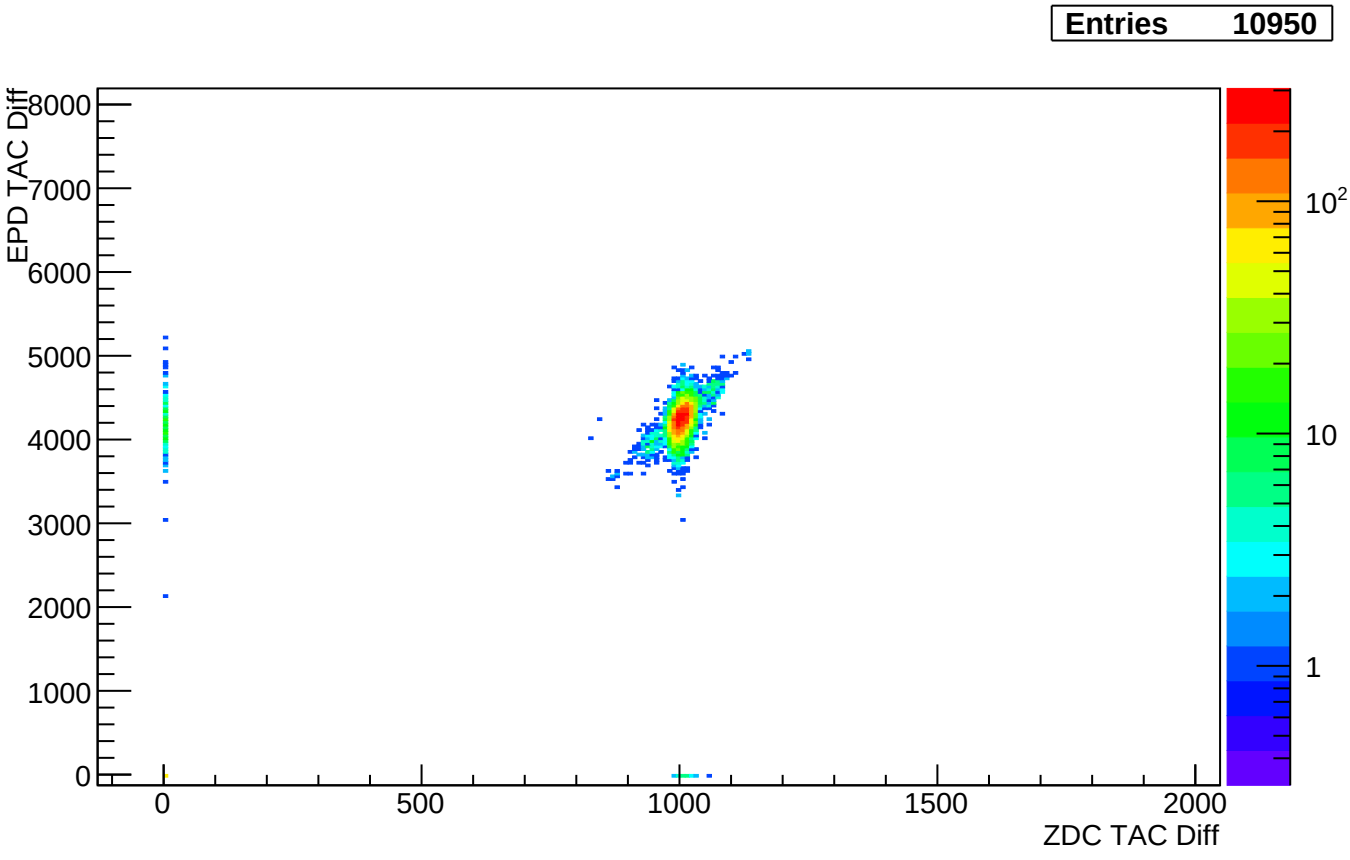

Entries 10950
TOF Mult
EPD-West Hit Cnt

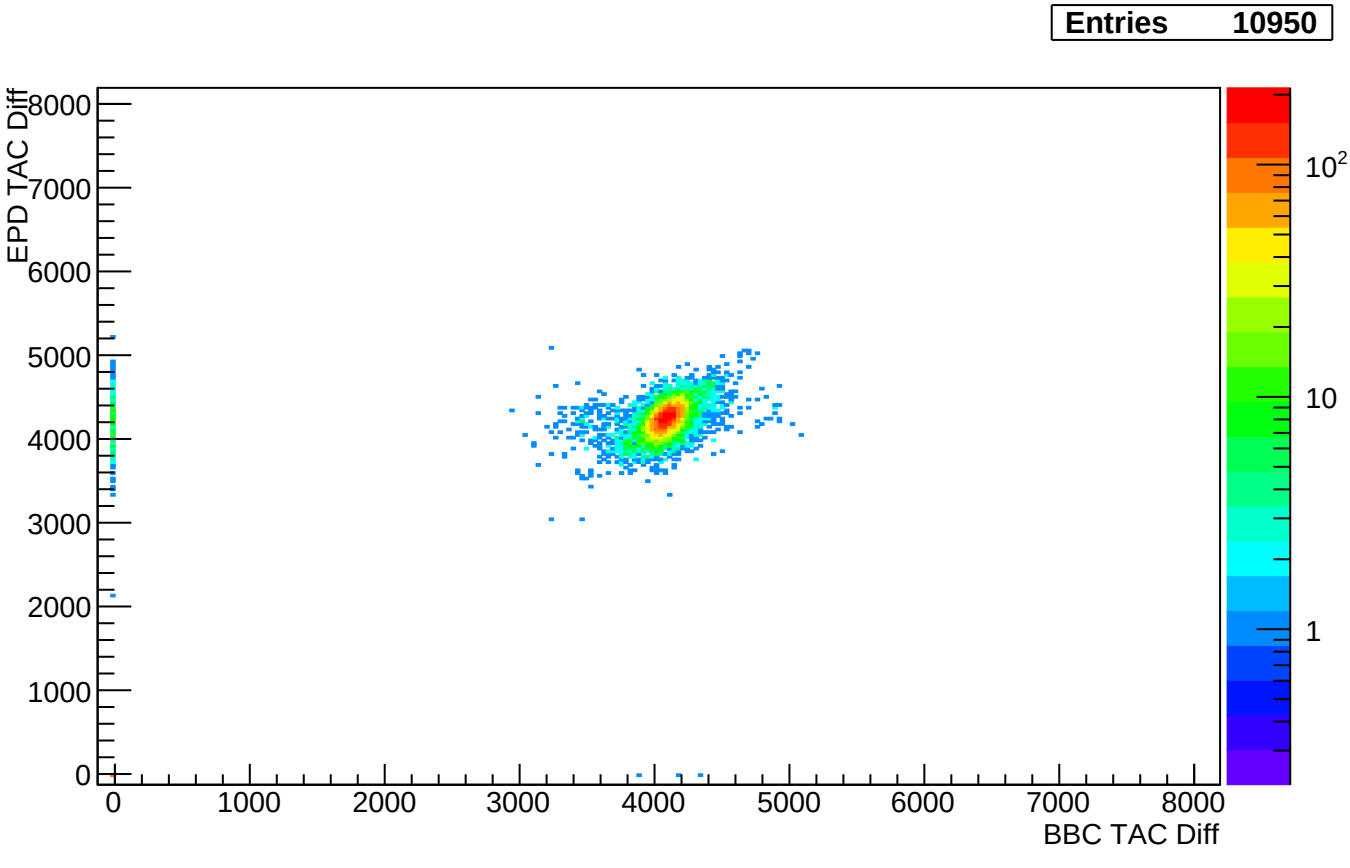

Entries 10950
TOF Mult
VPD-East Hit Cnt

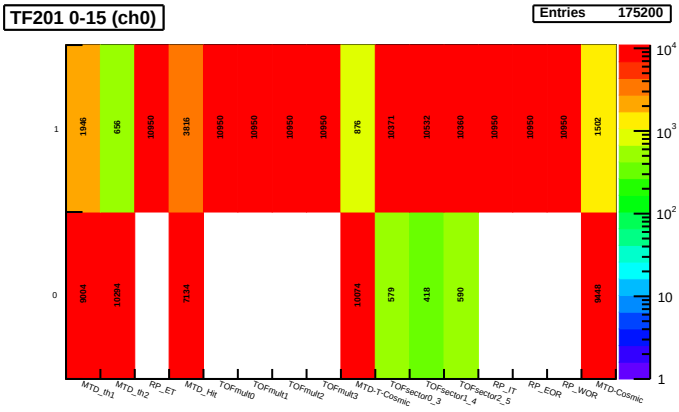

Entries 10950
TOF Mult
VPD-West Hit Cnt

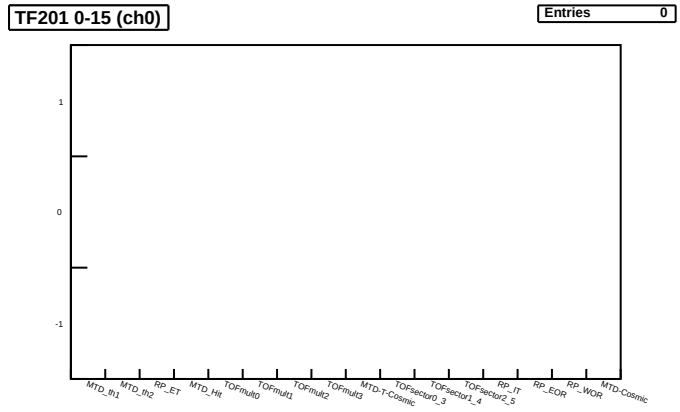

Entries 10950
TOF Mult
ZDC-East ADC Sum Att

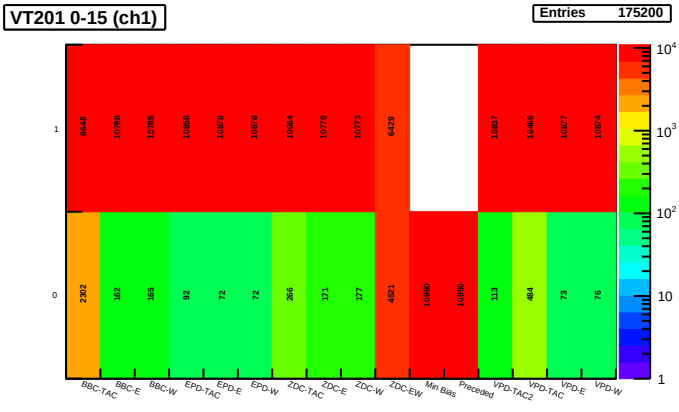

Entries 10950
TOF Mult
ZDC-West ADC Sum Att

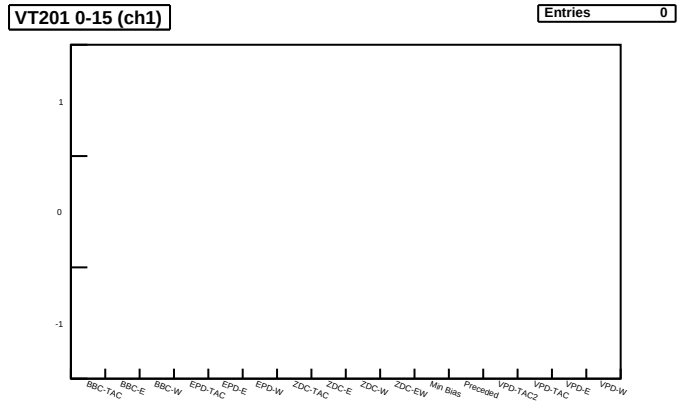

Entries 10950
EPD TAC Diff
ZDC TAC Diff
0
500
1000
1500
2000
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2

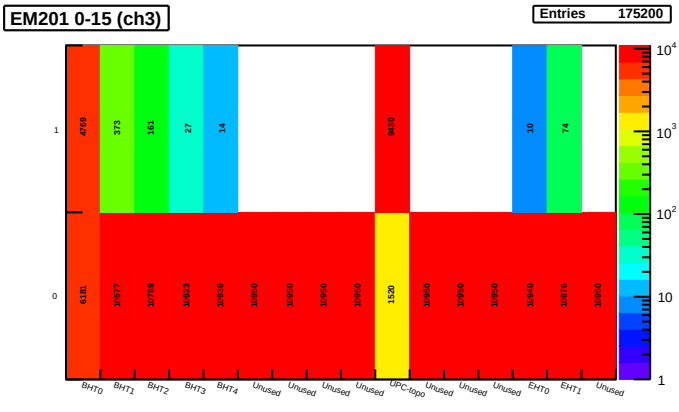

Entries 10950
EPD TAC Diff
BBC TAC Diff
0
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2

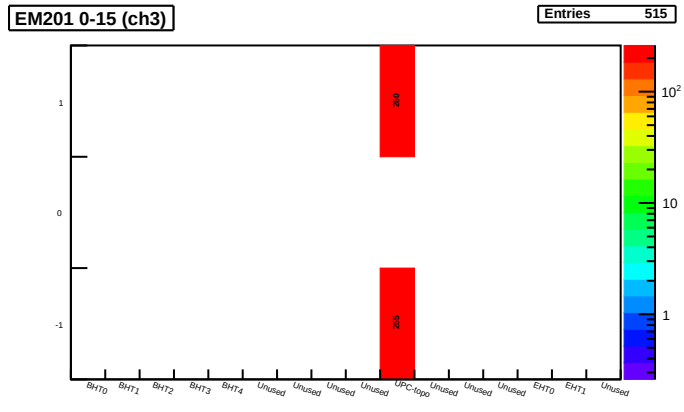

TF201 0-15 (ch0)
Entries 175200
1946 656 10950 3816 10950 10950 10950 10950 876 10371 10532 10360 10950 10950 10950 1502
9004 10394 7134 10074 579 418 590 9448
MTD_th1 MTD_th2 RP_ET MTD_Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

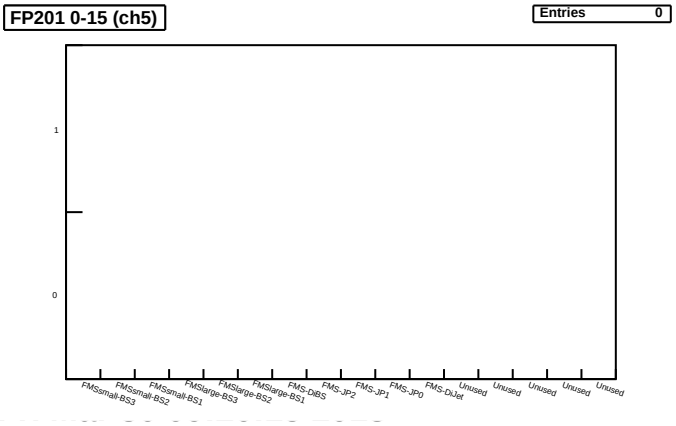

TF201 0-15 (ch0)
Entries 0
MTD_th1 MTD_th2 RP_ET MTD_Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

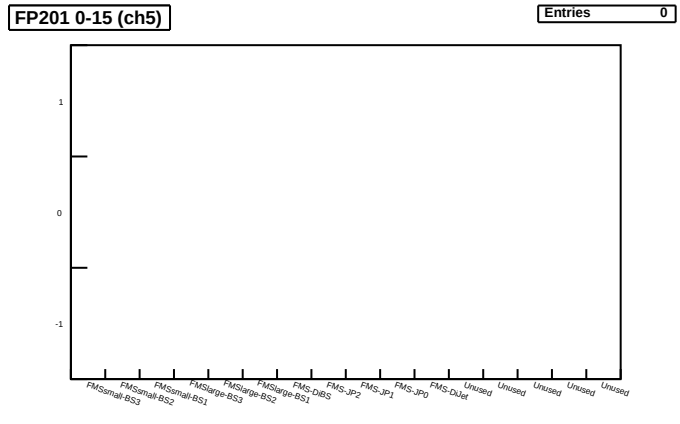

VT201 0-15 (ch1)
Entries 175200
8648 10788 10785 10868 10878 10878 10884 10779 10773 6429 10837 10466 10877 10874
2302 162 155 92 72 72 266 171 177 4521 10950 10950 113 484 73 76
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 175200
4769 373 161 27 14 9430 10 74
6181 10577 10789 10923 10936 10950 10950 10950 10950 1520 10950 10950 10950 10940 10876 10950
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused


EM201 0-15 (ch3)
Entries 515
260
255
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused