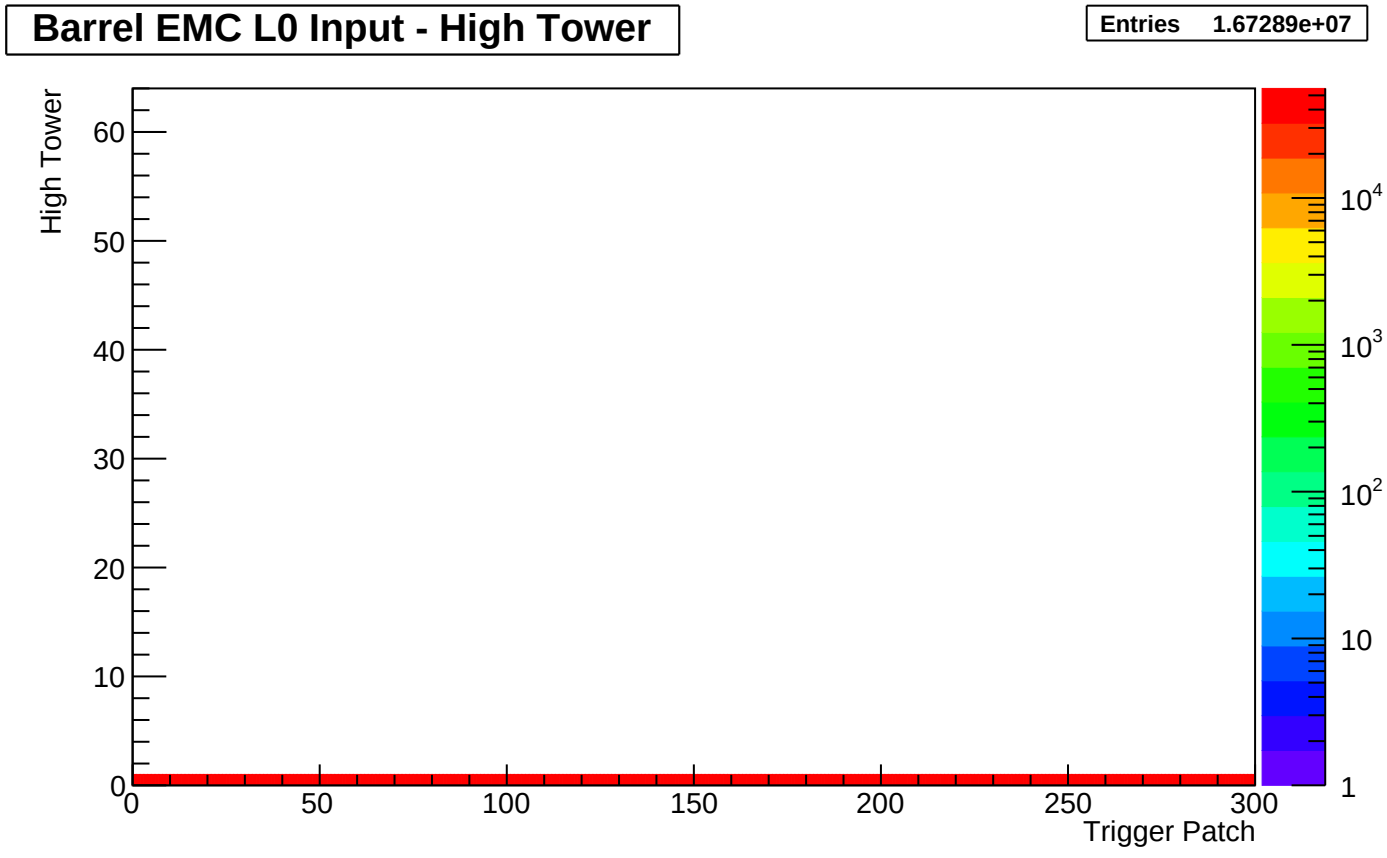
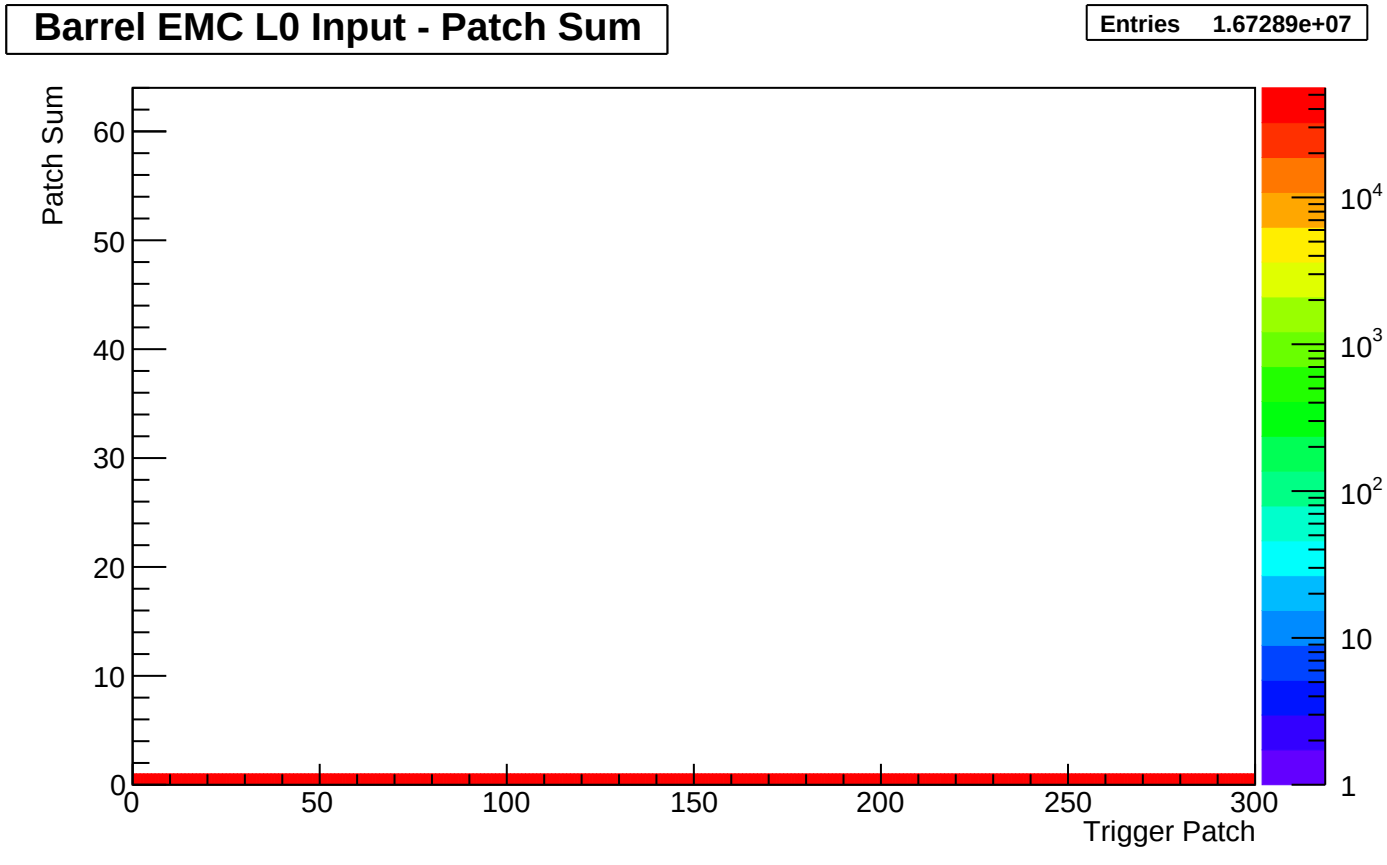
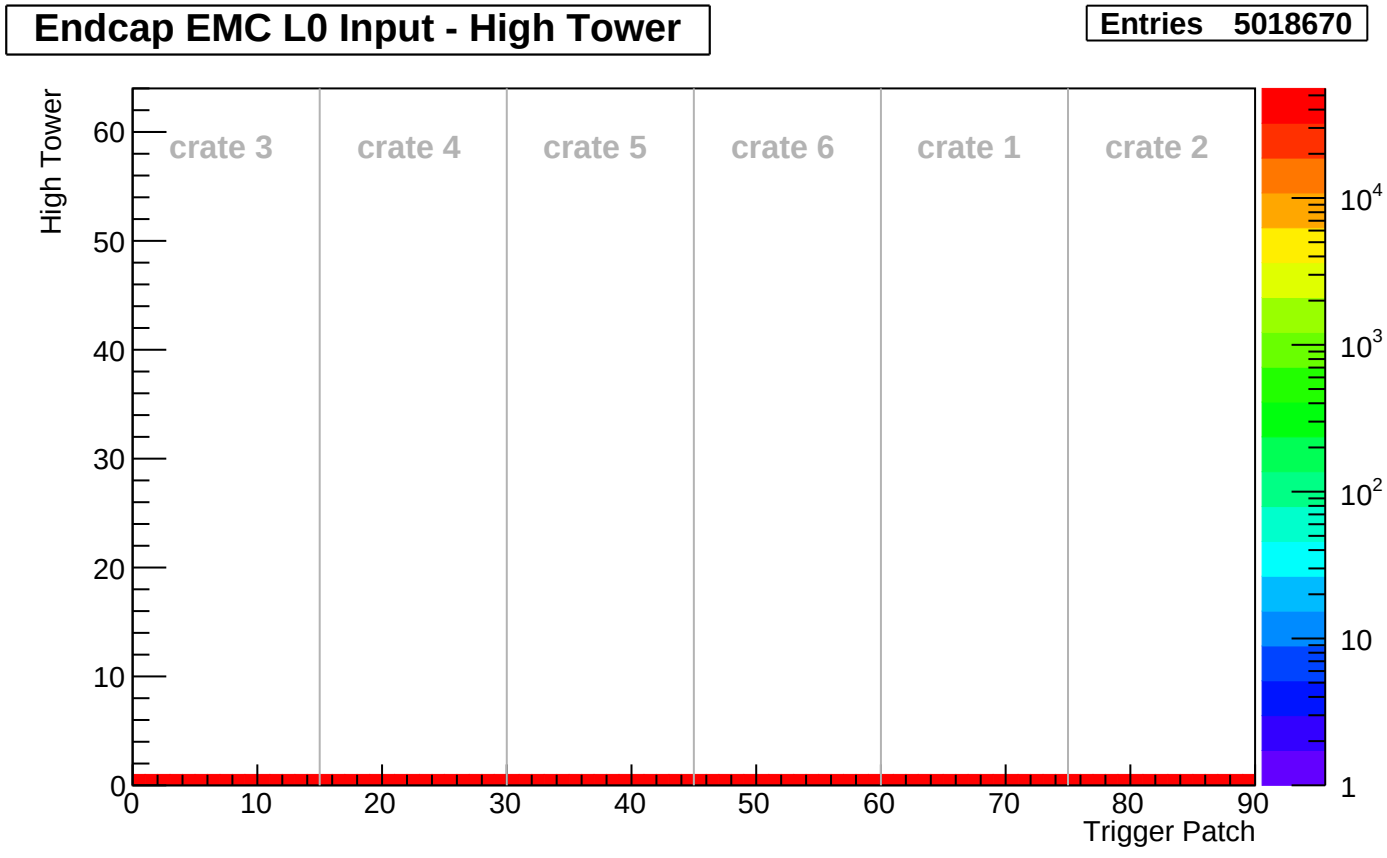
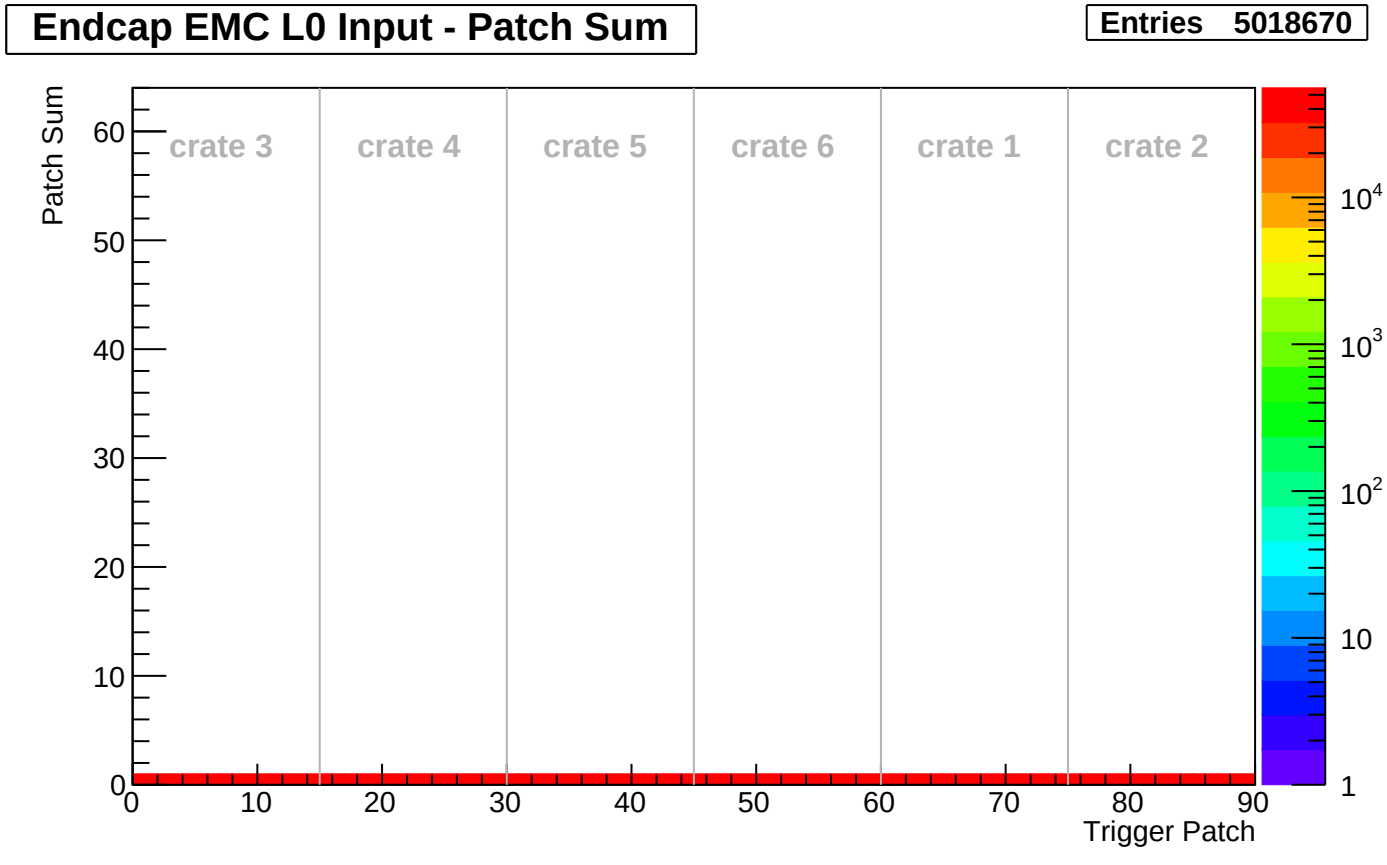
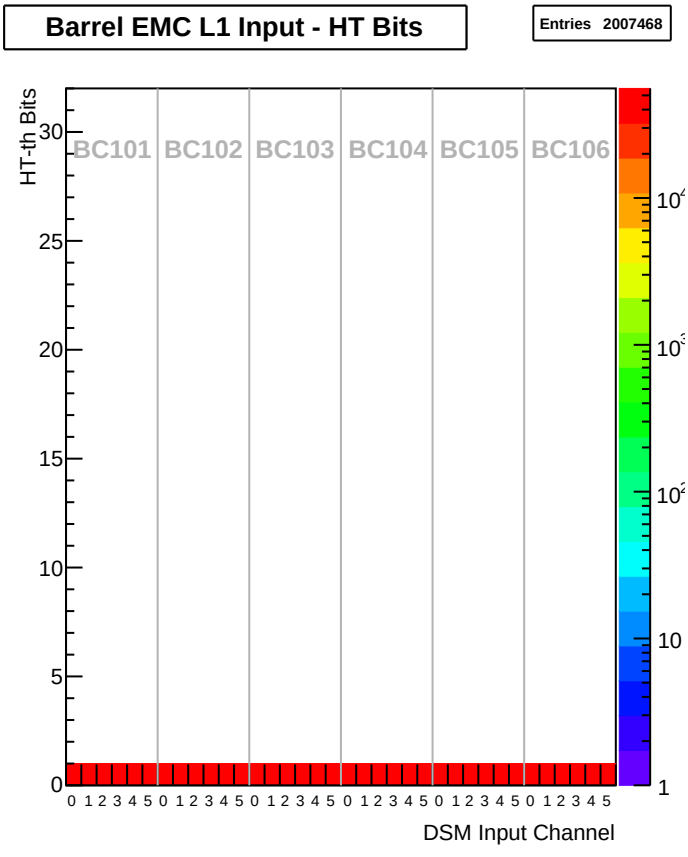
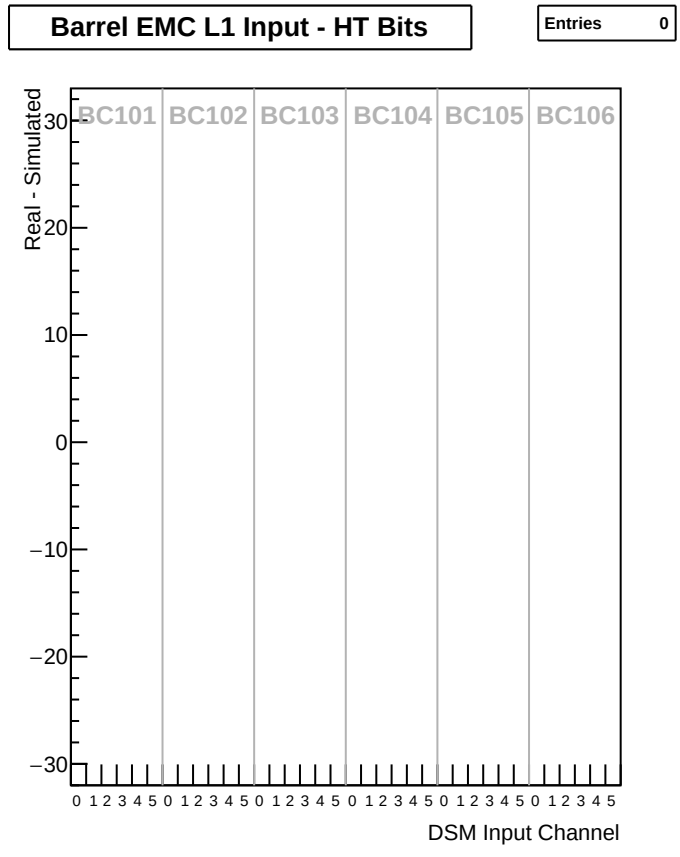

Barrel EMC L0 Input - High Tower
Entries 1.67289e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

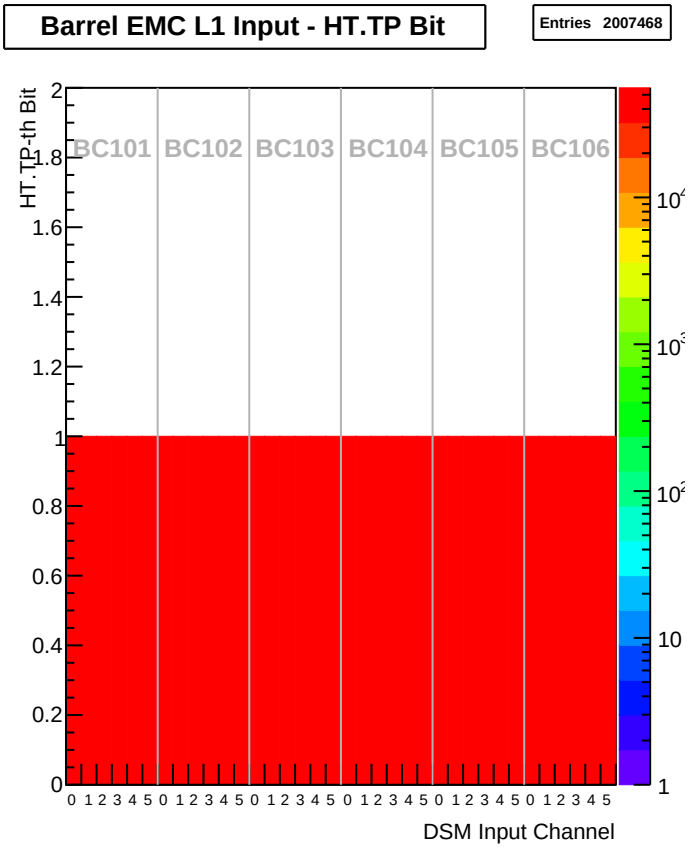

Barrel EMC L0 Input - Patch Sum
Entries 1.67289e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

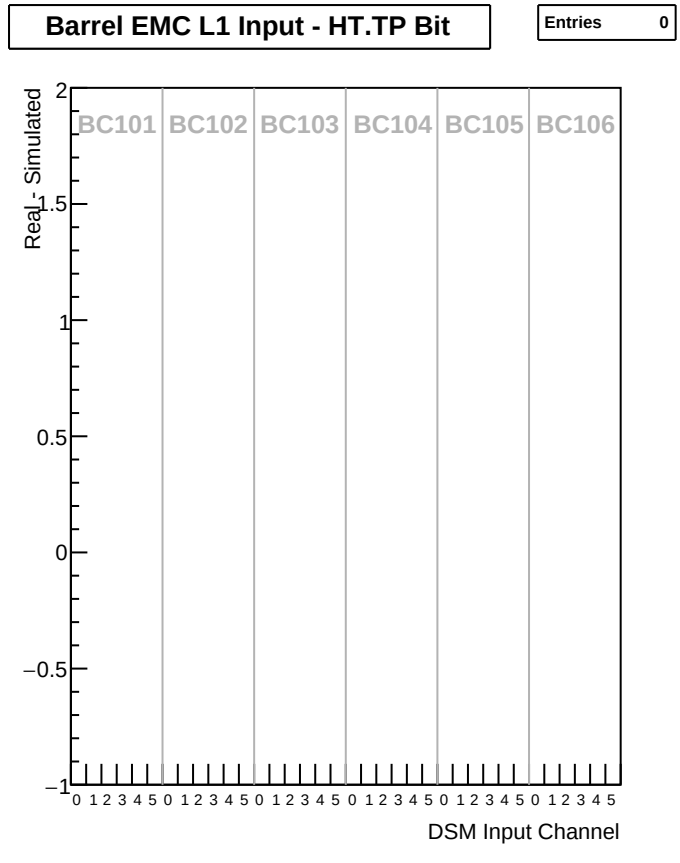

Endcap EMC L0 Input - High Tower
Entries 5018670
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

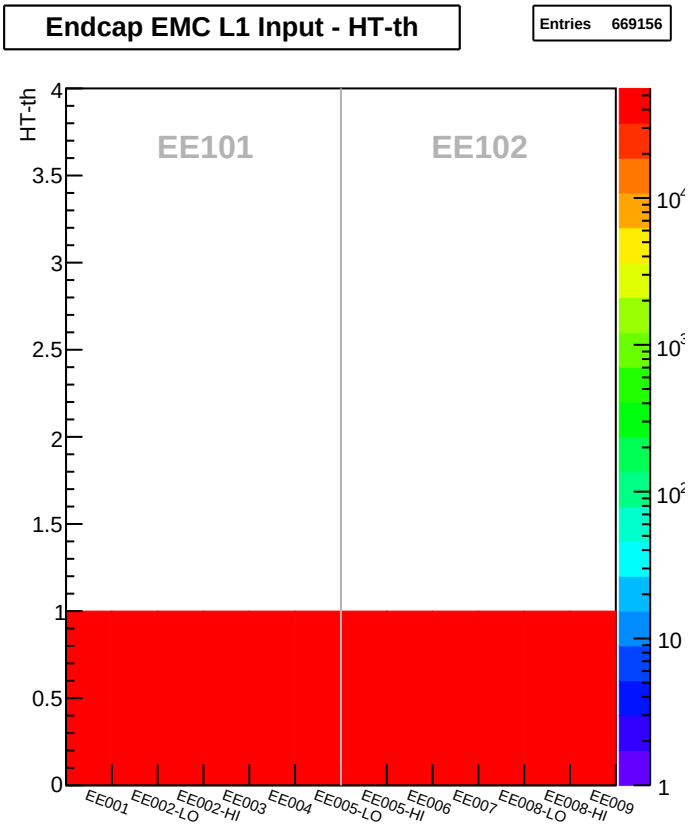

Endcap EMC L0 Input - Patch Sum
Entries 5018670
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

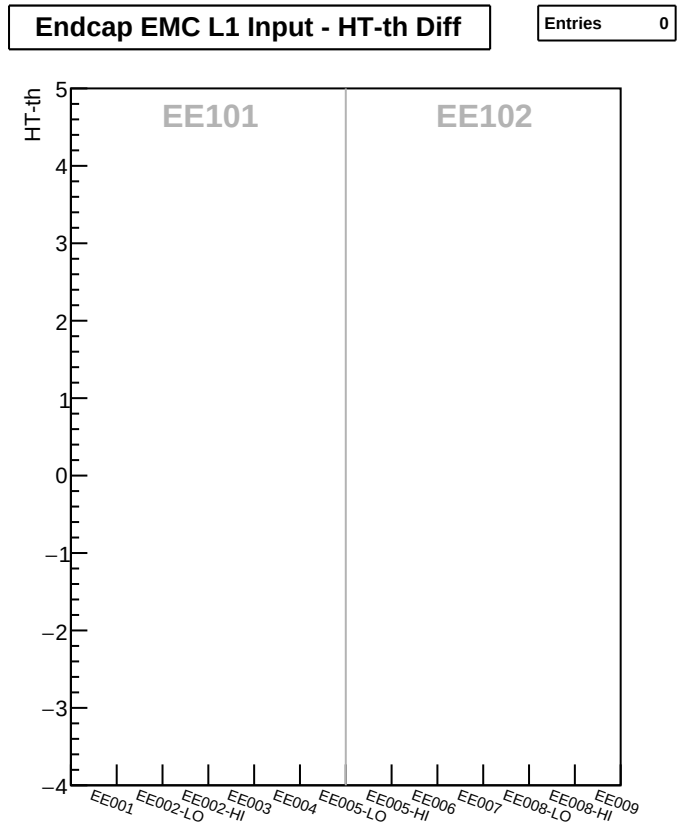

Barrel EMC L1 Input - HT Bits
Entries 2007468
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

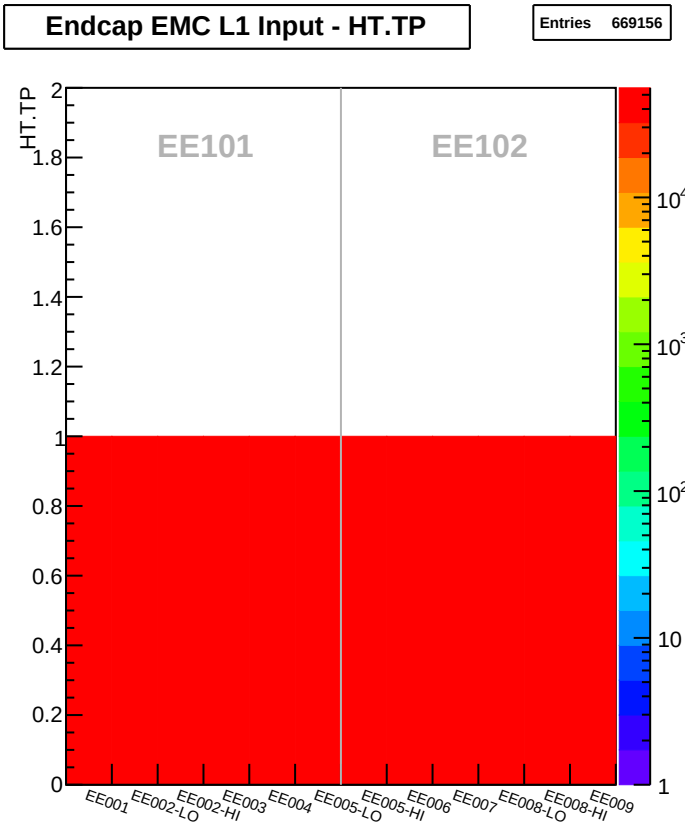

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

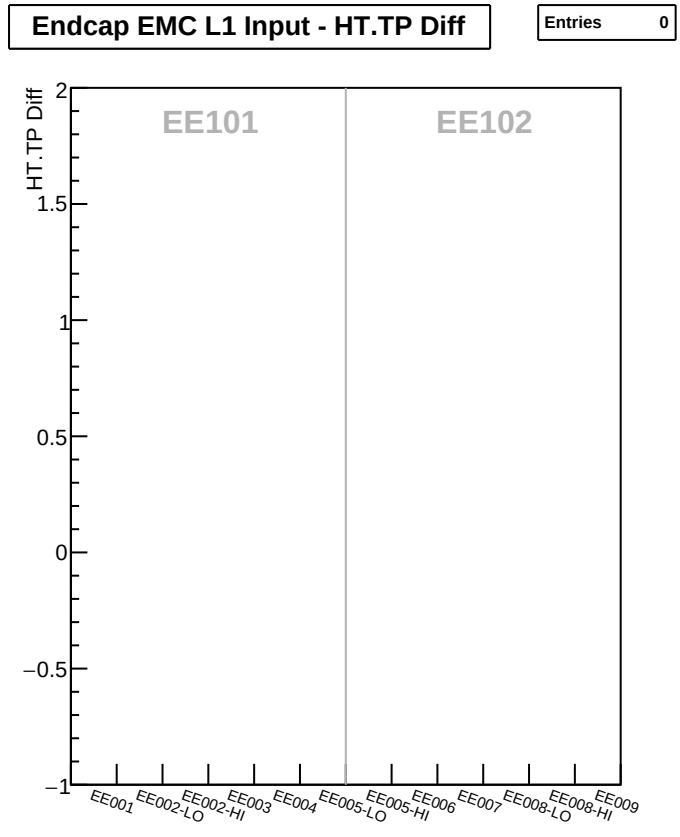

Barrel EMC L1 Input - HT.TP Bit
Entries 2007468
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

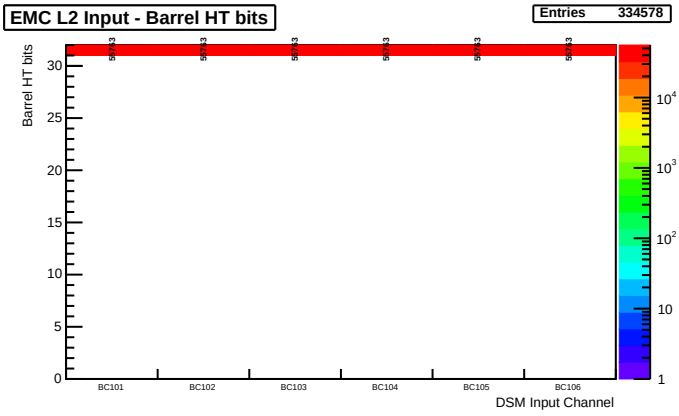

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

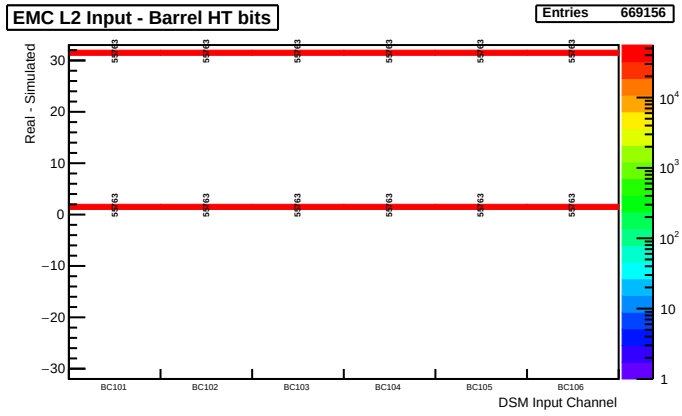

Endcap EMC L1 Input - HT-th
Entries 669156
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

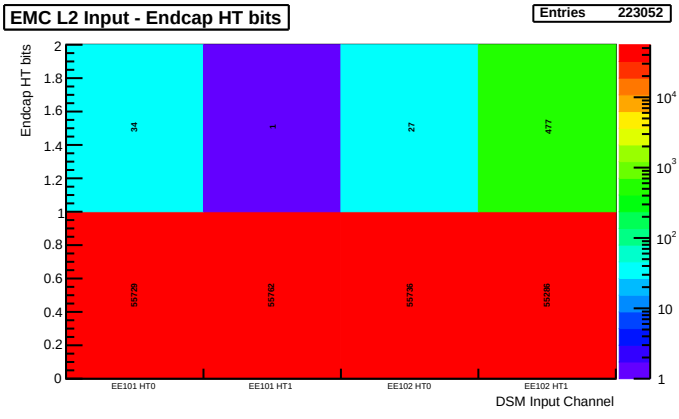

Endcap EMC L1 Input - HT-th Diff
Entries 0
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

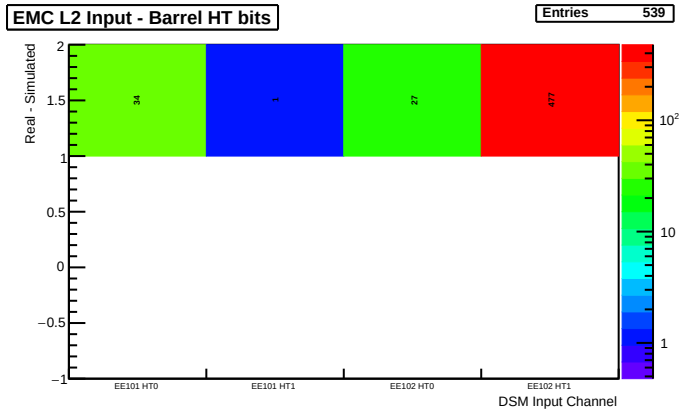

Endcap EMC L1 Input - HT.TP
Entries 669156
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

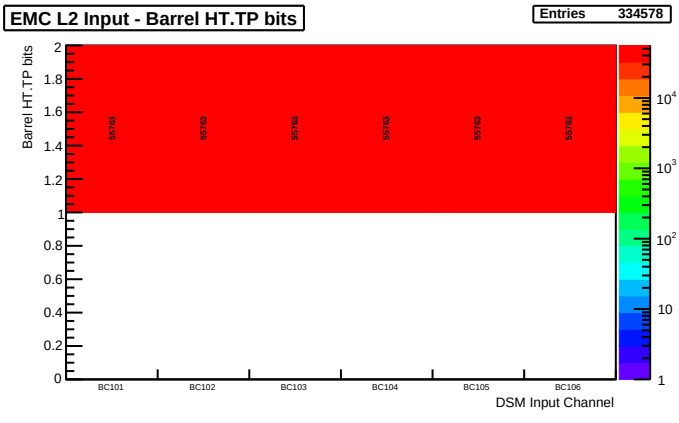

Endcap EMC L1 Input - HT.TP Diff
Entries 0
HT.TP Diff
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

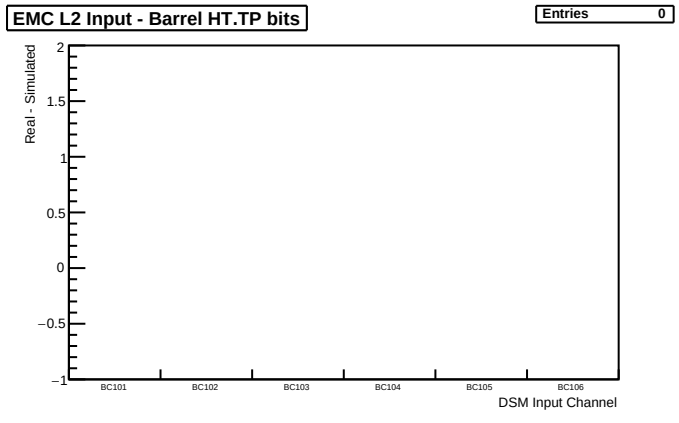

EMC L2 Input - Barrel HT bits
Entries 334578
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
55763

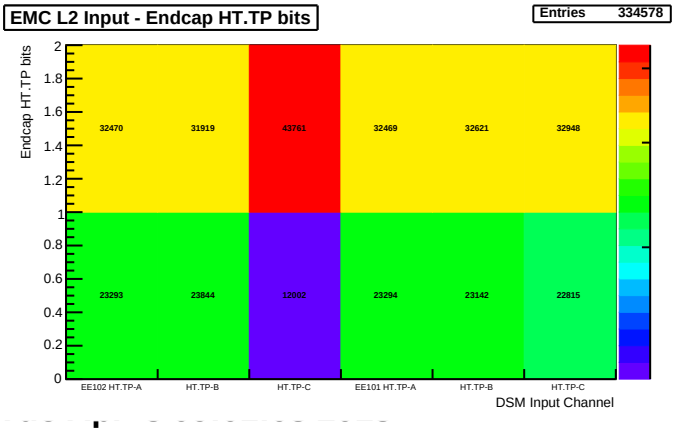

EMC L2 Input - Barrel HT bits
Entries 669156
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
55763

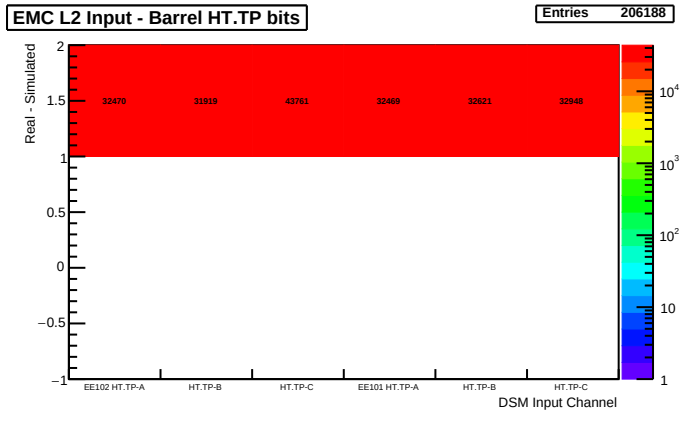

EMC L2 Input - Endcap HT bits
Entries 223052
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1
34
1
27
477
55729
55762
55736
55286

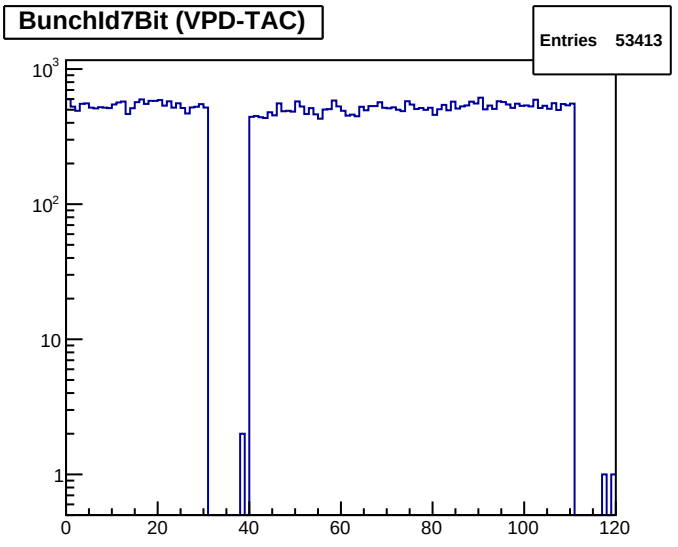

EMC L2 Input - Barrel HT bits
Entries 539
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1
34
1
27
477

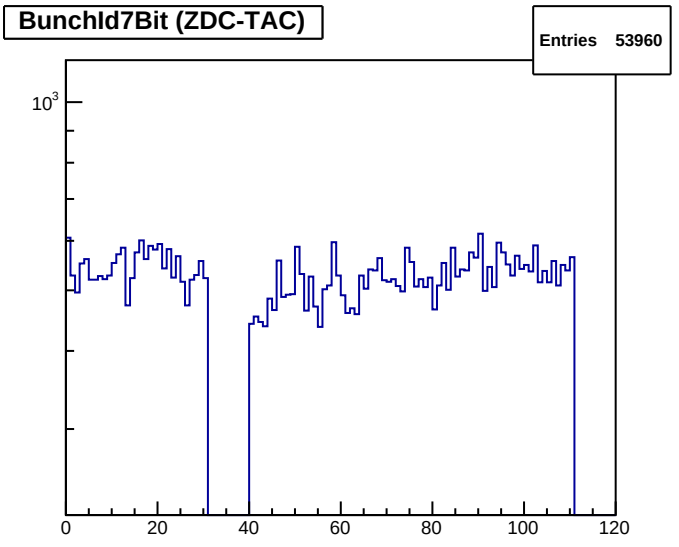

EMC L2 Input - Barrel HT.TP bits
Entries 334578
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
55763

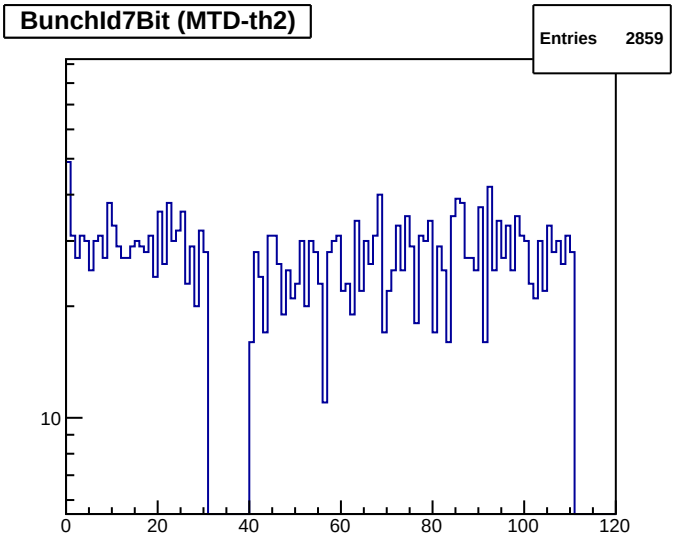

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

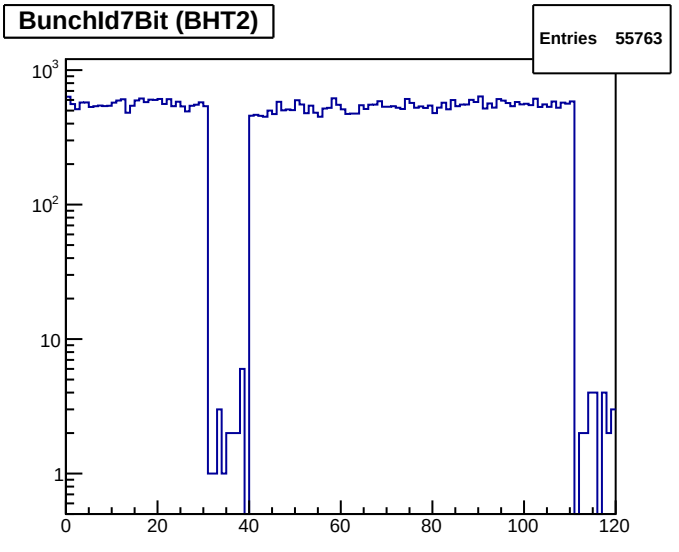

EMC L2 Input - Endcap HT.TP bits
Entries 334578
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C
32470
31919
43761
32469
32621
32948
23293
23844
12002
23294
23142
22815

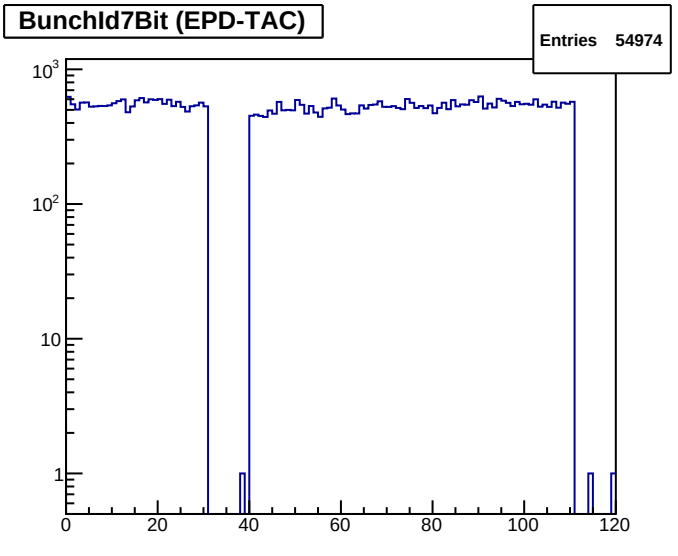

EMC L2 Input - Barrel HT.TP bits
Entries 206188
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C
32470
31919
43761
32469
32621
32948

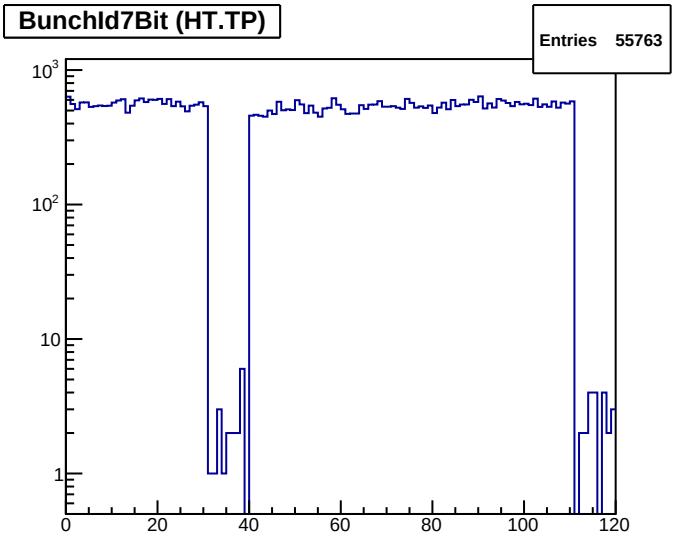

BunchId7Bit (VPD-TAC)
Entries 53413

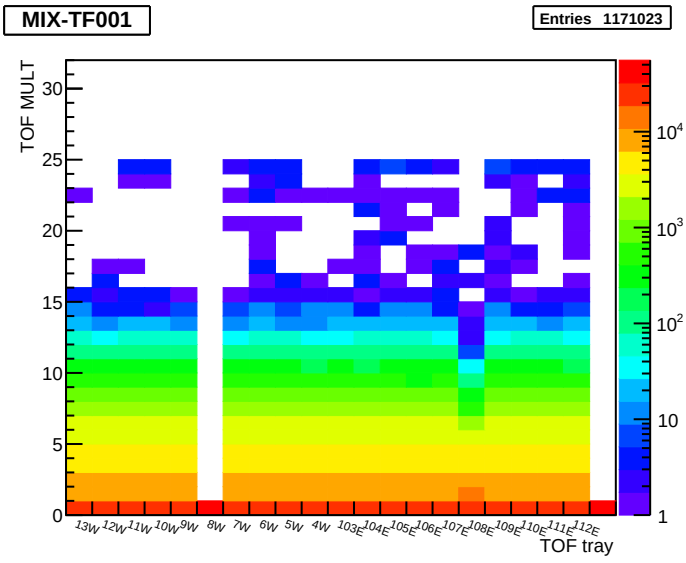

BunchId7Bit (ZDC-TAC)
Entries 53960

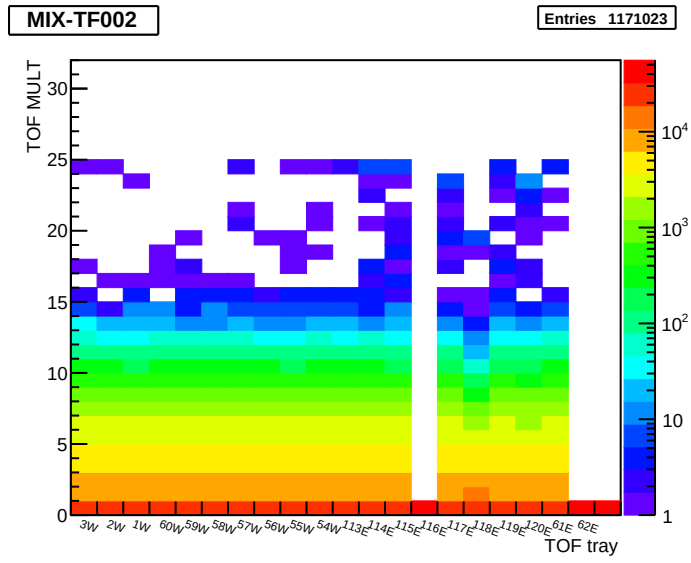

BunchId7Bit (MTD-th2)
Entries 2859

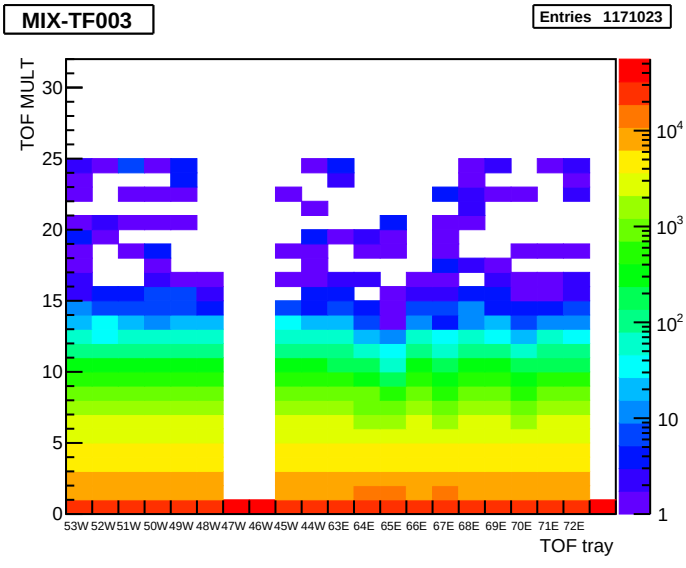

BunchId7Bit (BHT2)
Entries 55763

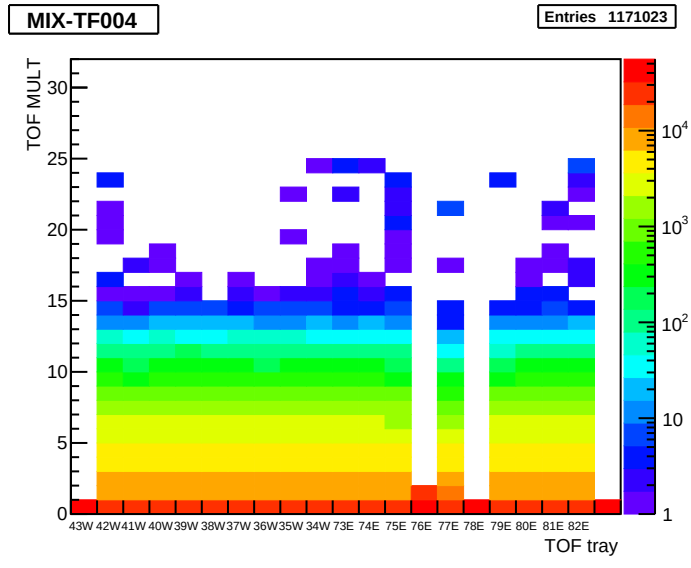

BunchId7Bit (EPD-TAC)
Entries 54974

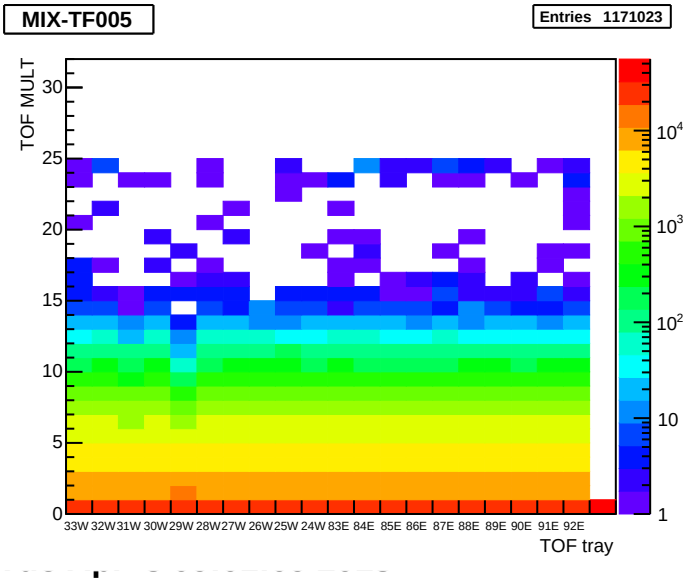

BunchId7Bit (HT.TP)
Entries 55763

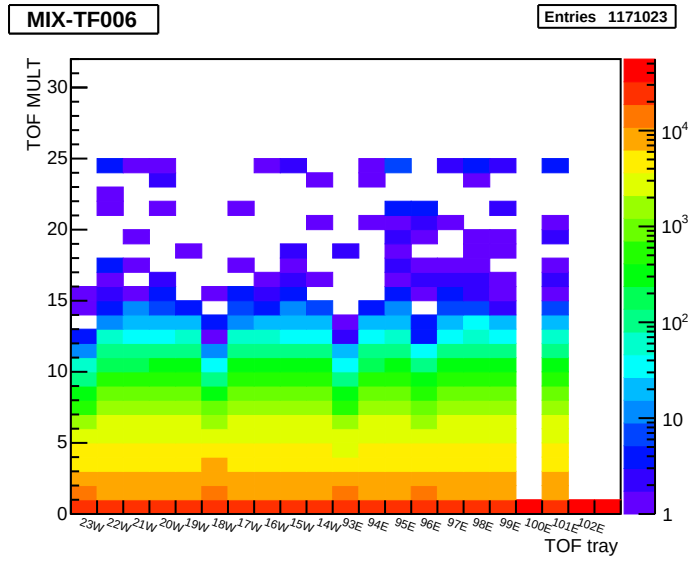

MIX-TF001
Entries 1171023
TOF MULT
TOF tray

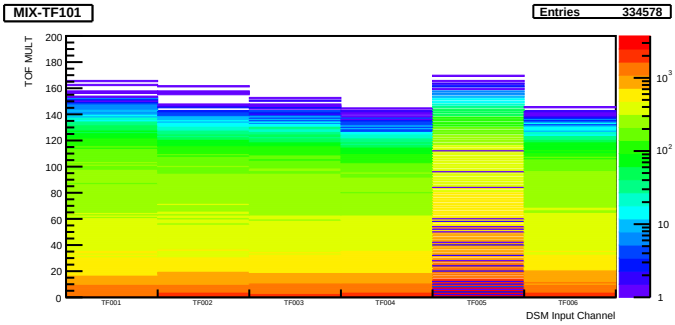

MIX-TF002
Entries 1171023
TOF MULT
TOF tray

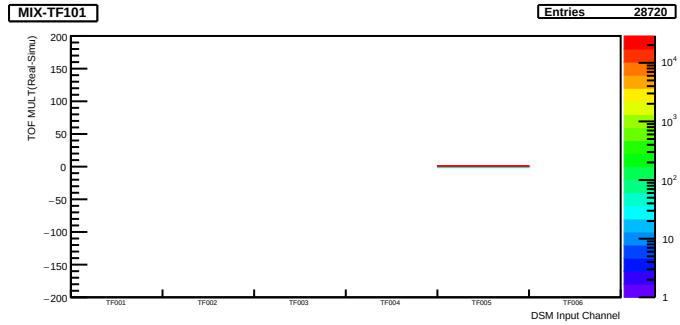

MIX-TF003
Entries 1171023
TOF MULT
TOF tray

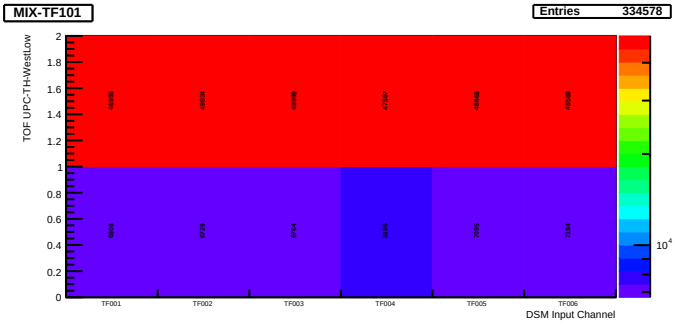

MIX-TF004
Entries 1171023
TOF MULT
TOF tray

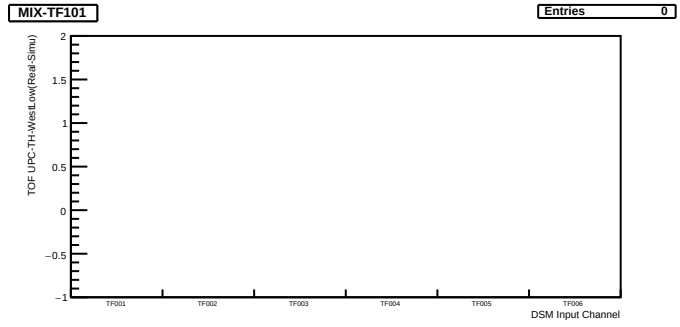

MIX-TF005
Entries 1171023
TOF MULT
TOF tray

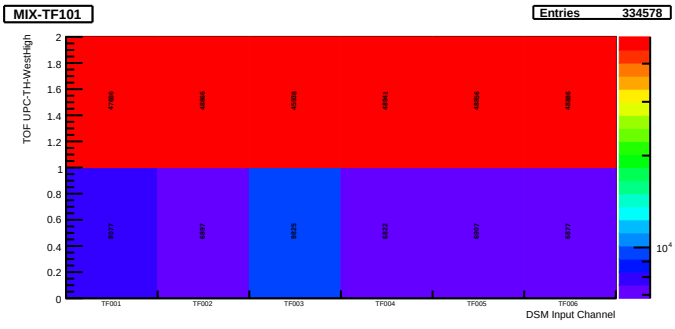

MIX-TF006
Entries 1171023
TOF MULT
TOF tray

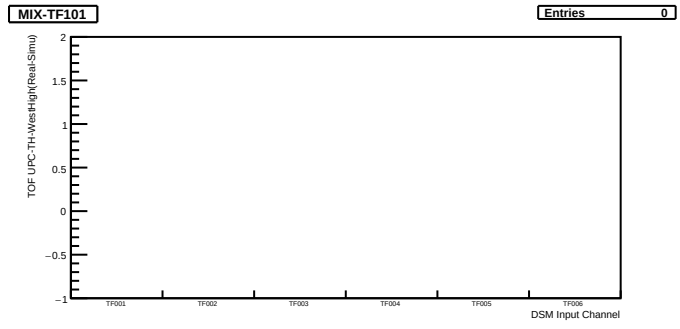

MIX-TF101
Entries 334578
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

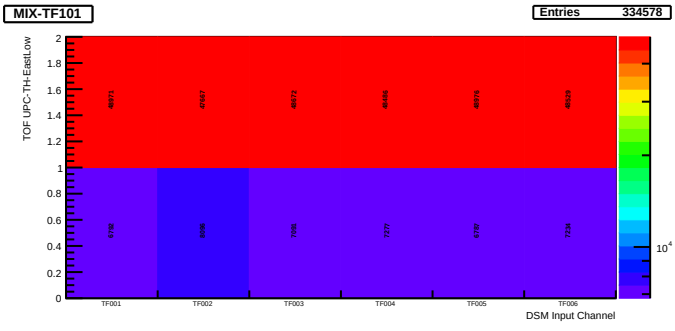

MIX-TF101
Entries 28720
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

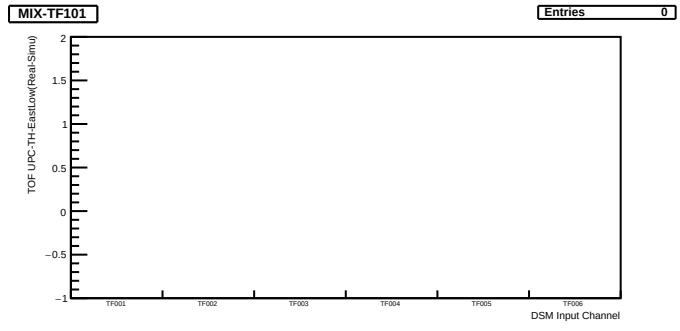

MIX-TF101
Entries 334578
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

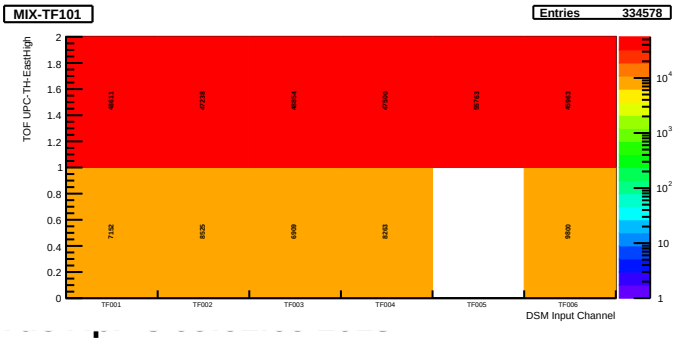

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

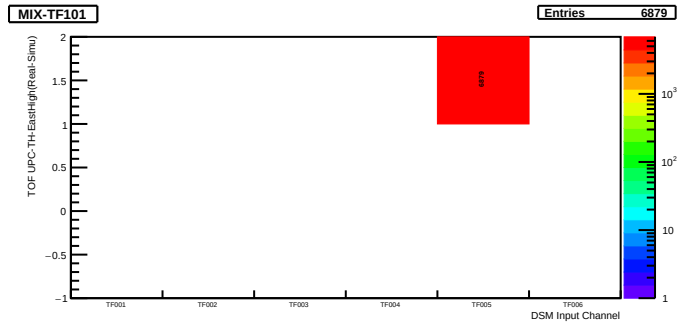

MIX-TF101
Entries 334578
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

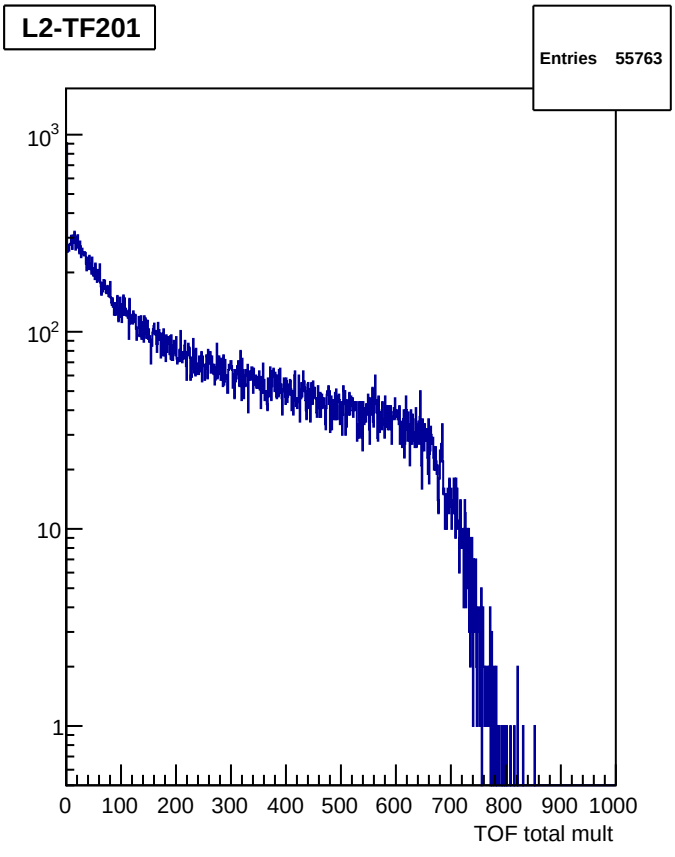

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

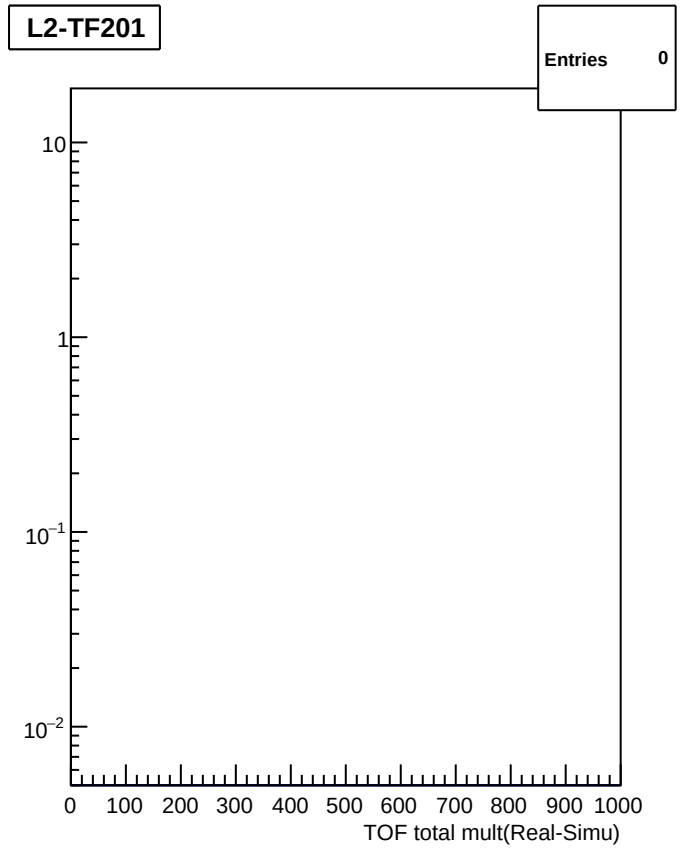

MIX-TF101
Entries 334578
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

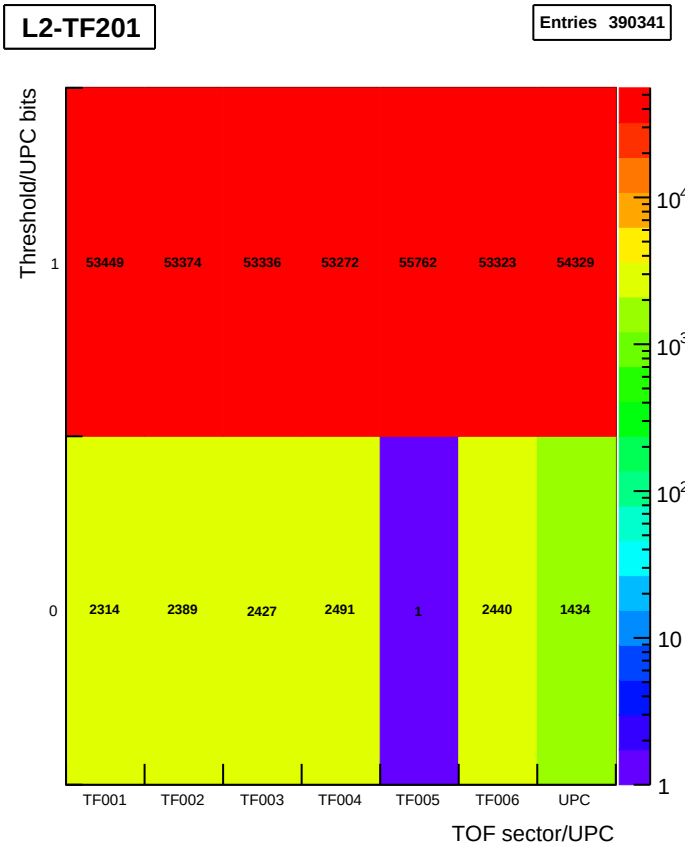

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

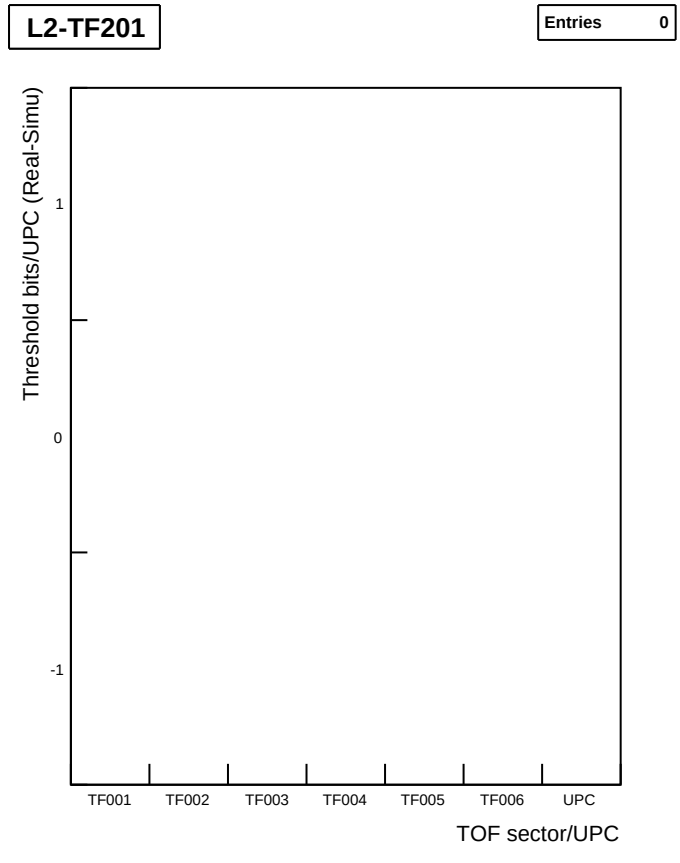

MIX-TF101
Entries 334578
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

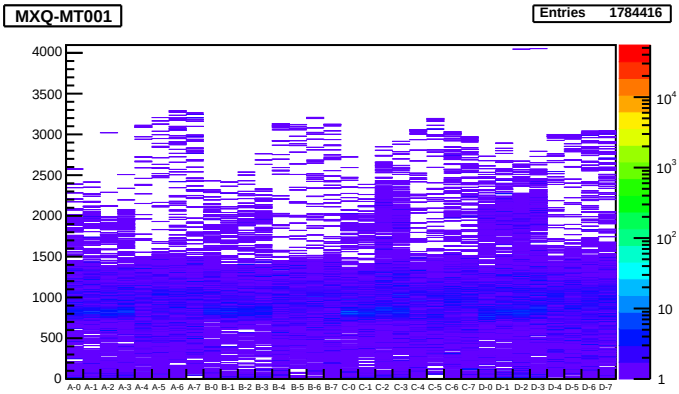

MIX-TF101
Entries 6879
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

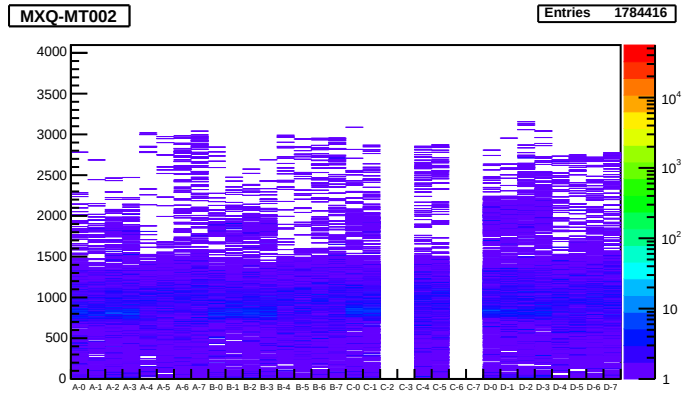

L2-TF201
Entries 55763
TOF total mult

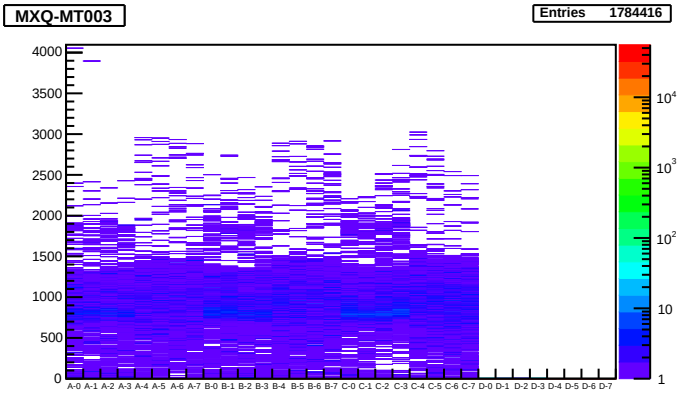

L2-TF201
Entries 0
TOF total mult(Real-Simu)

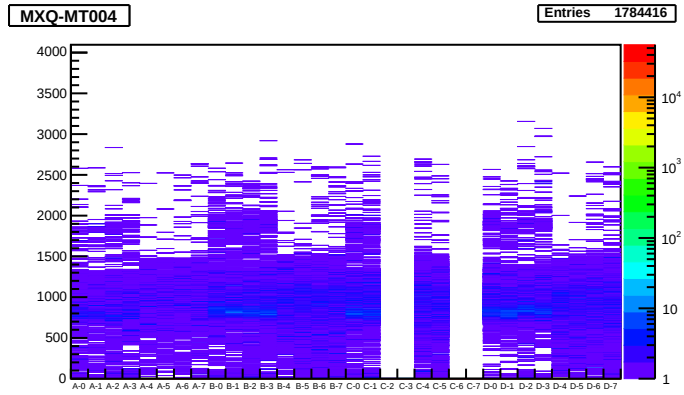

L2-TF201
Entries 390341
Threshold/UPC bits
53449
53374
53336
53272
55762
53323
54329
2314
2389
2427
2491
1
2440
1434
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

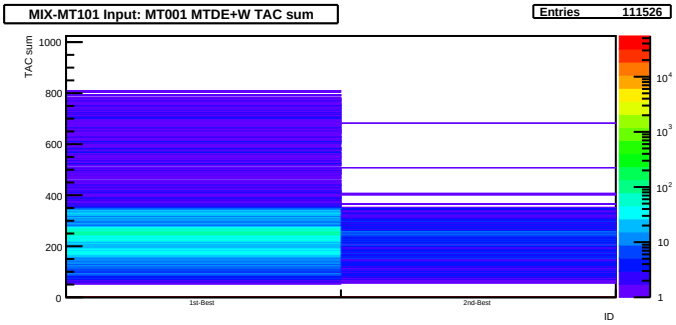

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

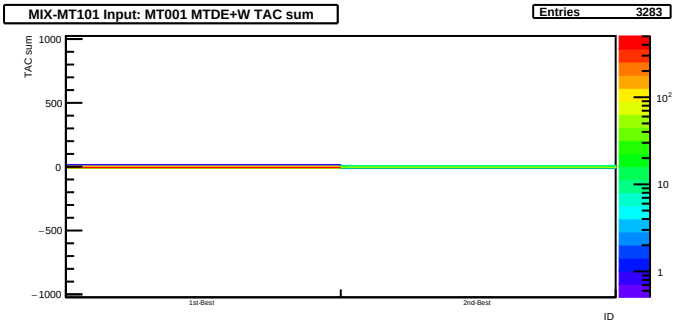

MXQ-MT001
Entries 1784416
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

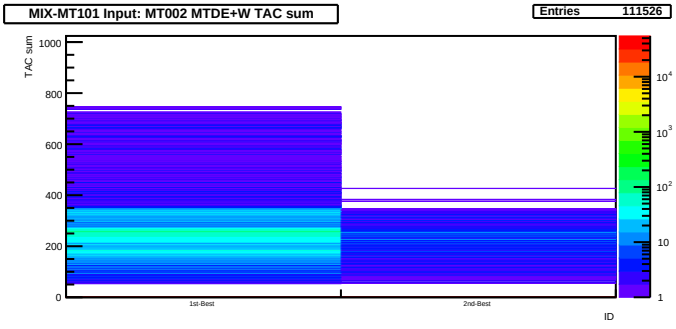

MXQ-MT002
Entries 1784416
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

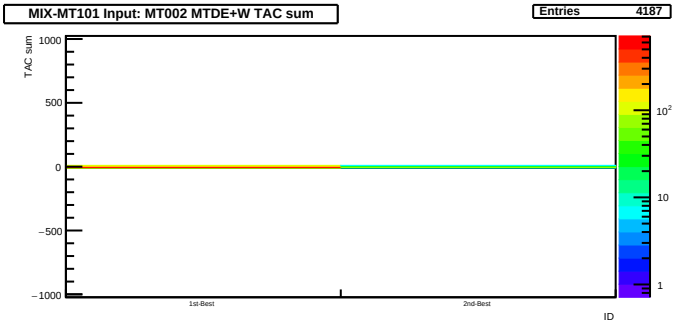

MXQ-MT003
Entries 1784416
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

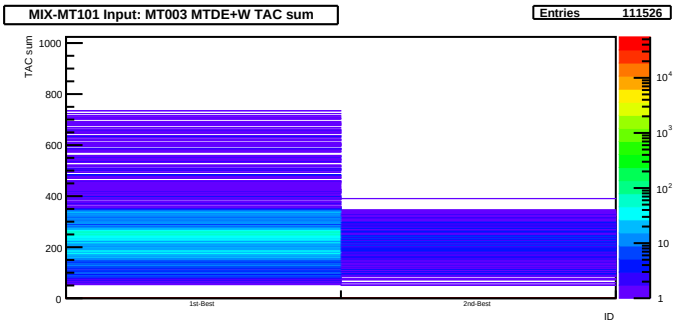

MXQ-MT004
Entries 1784416
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

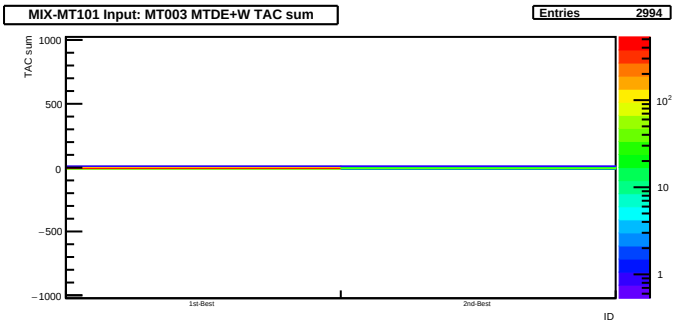

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 111526
TAC sum
ID
1st-Best
2nd-Best

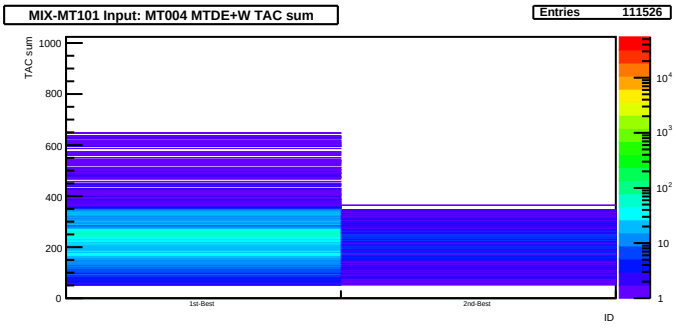

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 3283
TAC sum
ID
1st-Best
2nd-Best

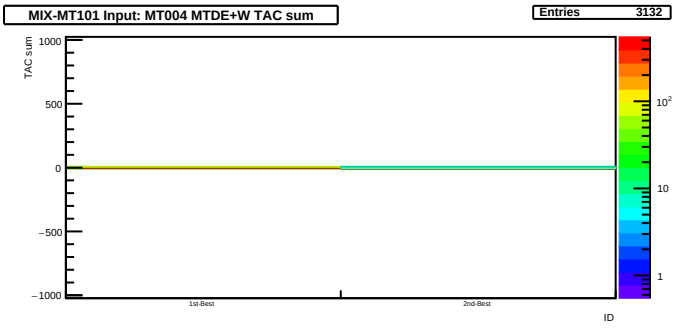

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 111526
TAC sum
ID
1st-Best
2nd-Best

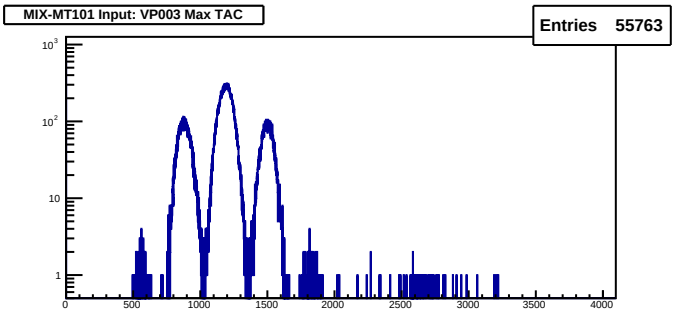

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 4187
TAC sum
ID
1st-Best
2nd-Best

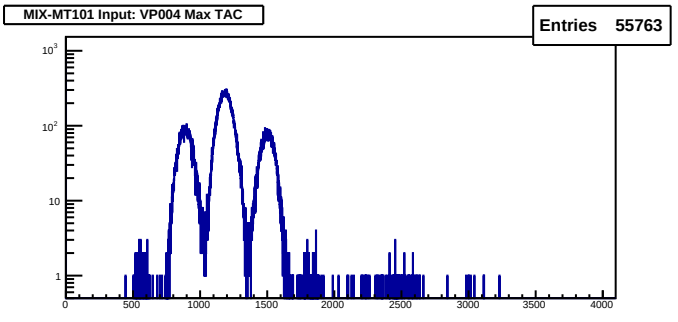

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 111526
TAC sum
ID
1st-Best
2nd-Best

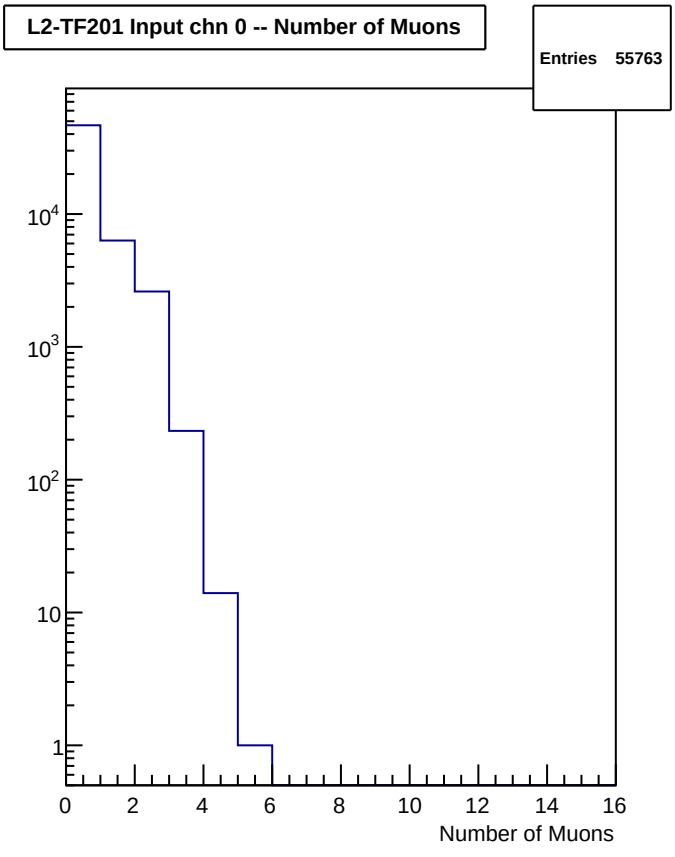

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 2994
TAC sum
ID
1st-Best
2nd-Best

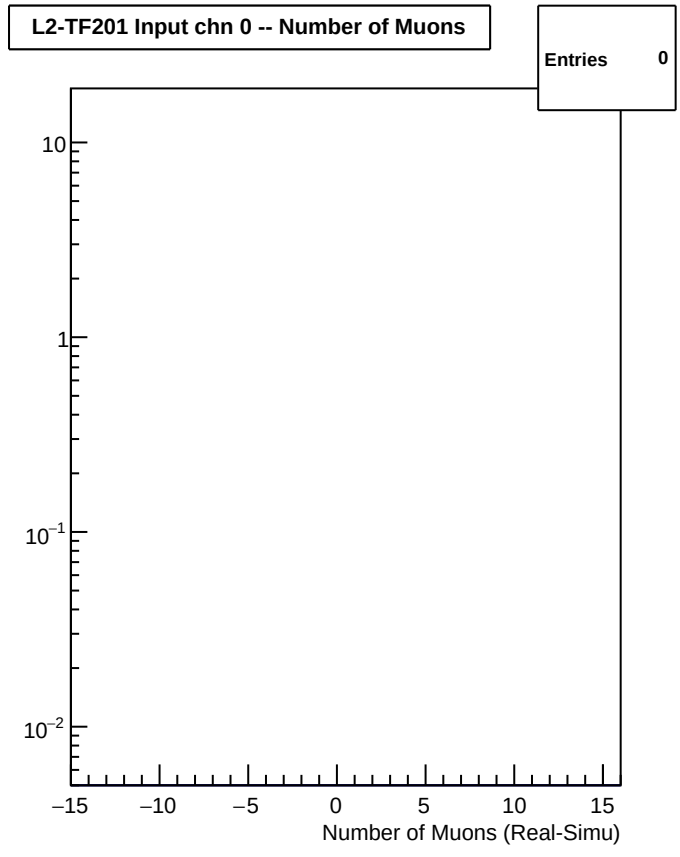

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 111526
TAC sum
ID
1st-Best
2nd-Best

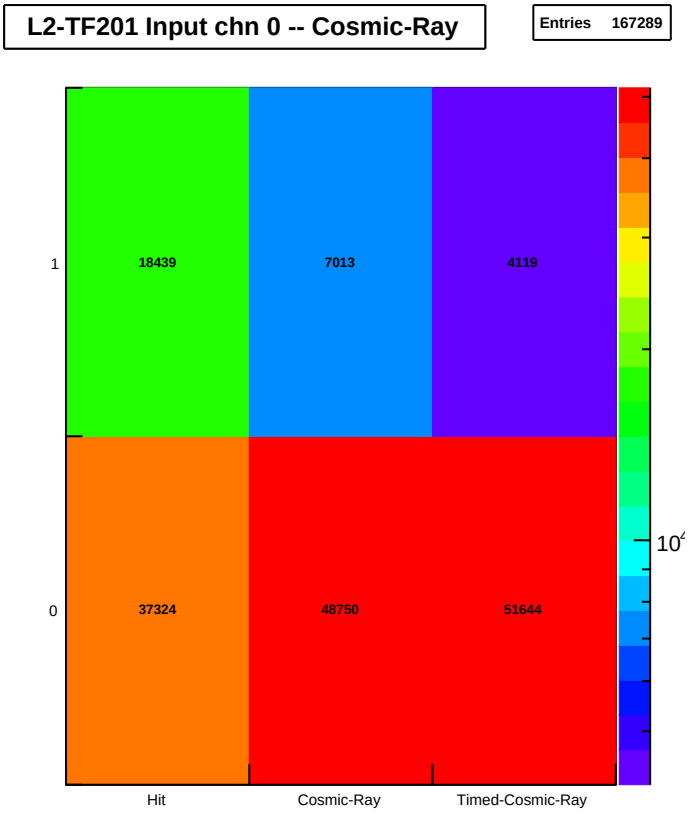

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 3132
TAC sum
ID
1st-Best
2nd-Best

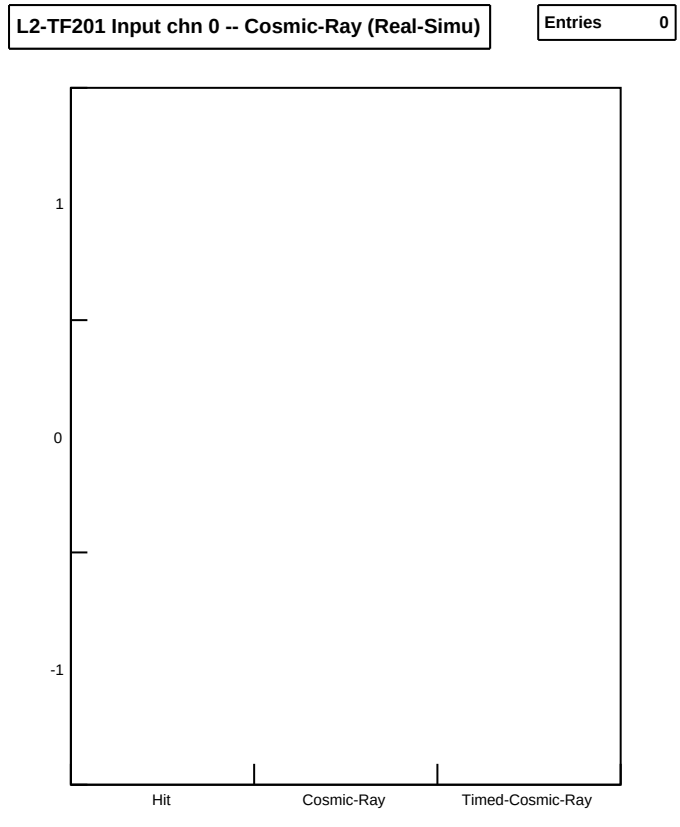

MIX-MT101 Input: VP003 Max TAC
Entries 55763

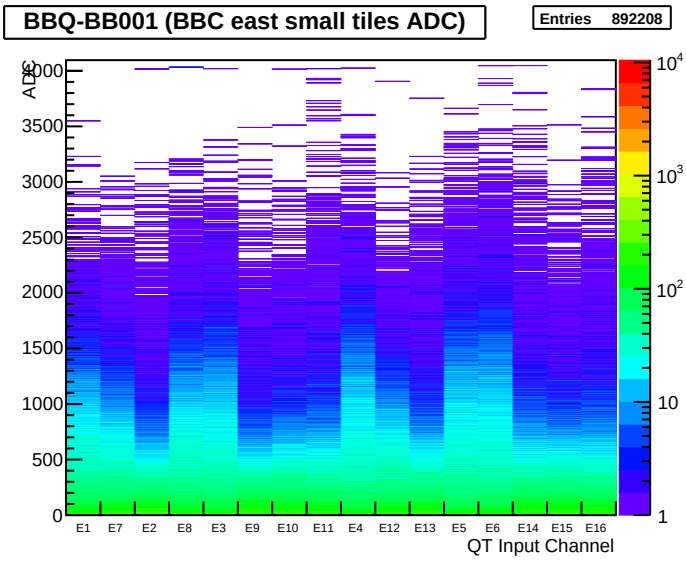

MIX-MT101 Input: VP004 Max TAC
Entries 55763

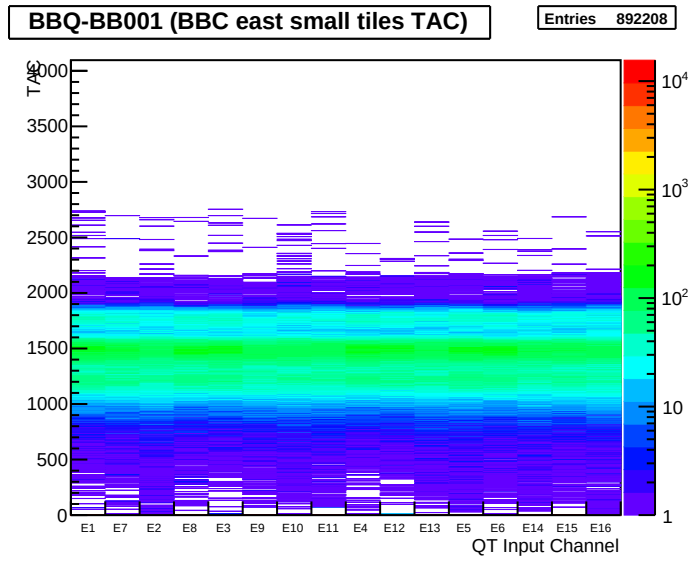

L2-TF201 Input chn 0 -- Number of Muons
Entries 55763
Number of Muons

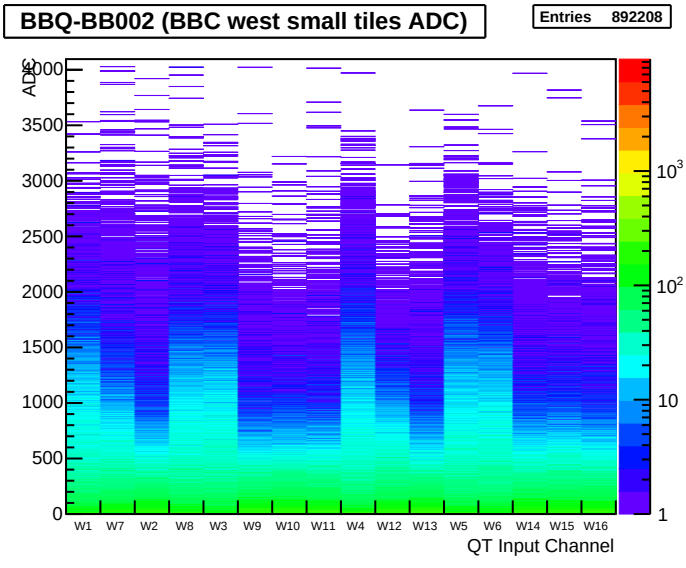

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

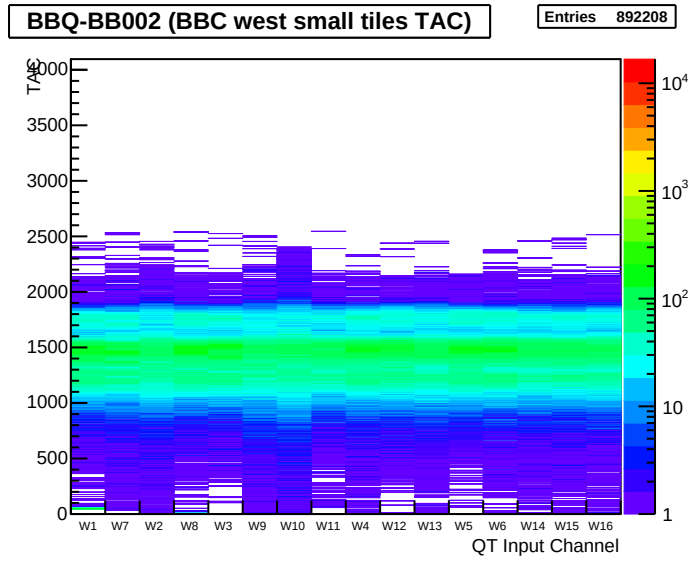

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 167289
18439
7013
4119
37324
48750
51644
Hit
Cosmic-Ray
Timed-Cosmic-Ray

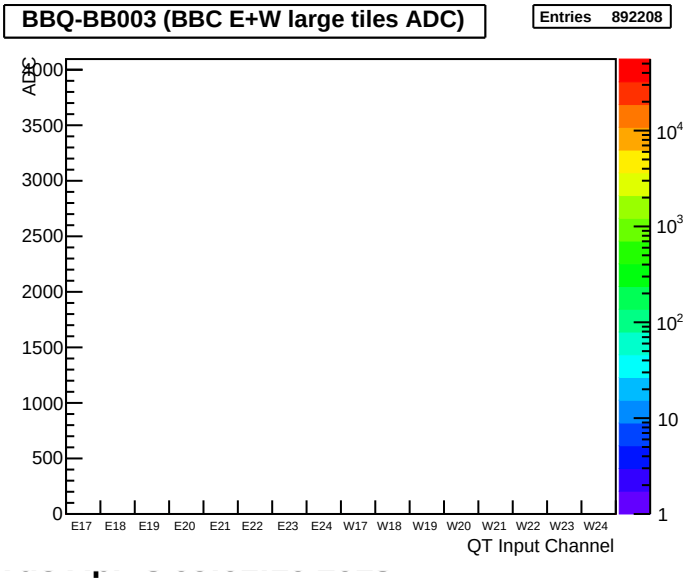

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

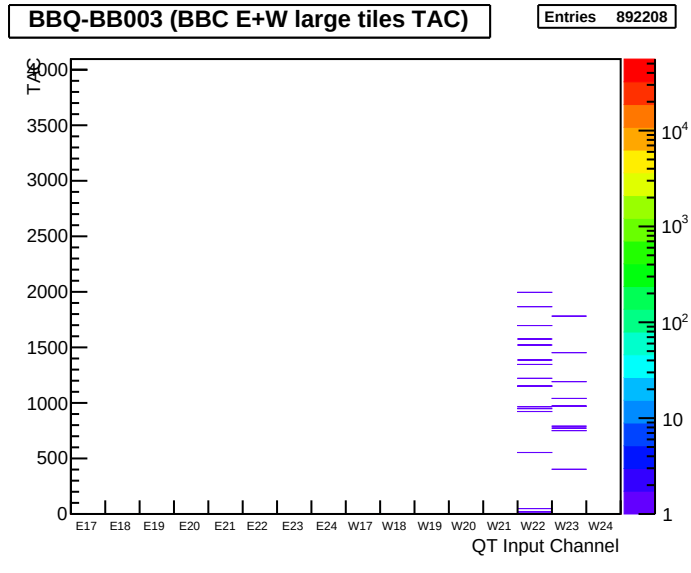

BBQ-BB001 (BBC east small tiles ADC)
Entries 892208
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

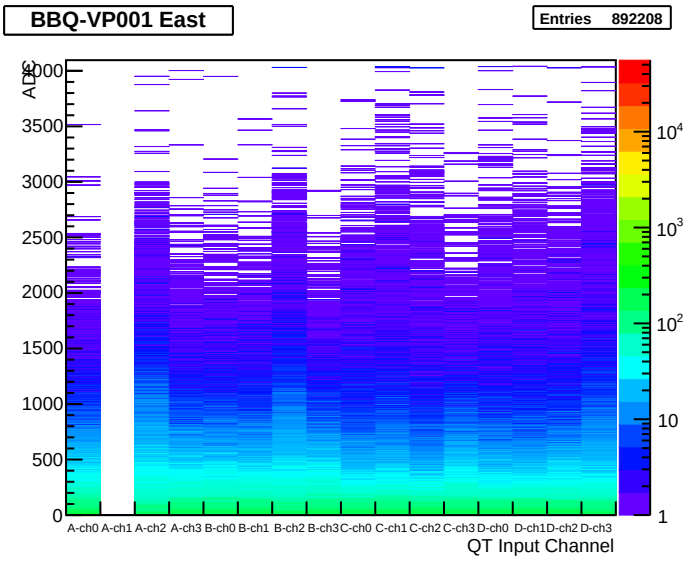

BBQ-BB001 (BBC east small tiles TAC)
Entries 892208
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

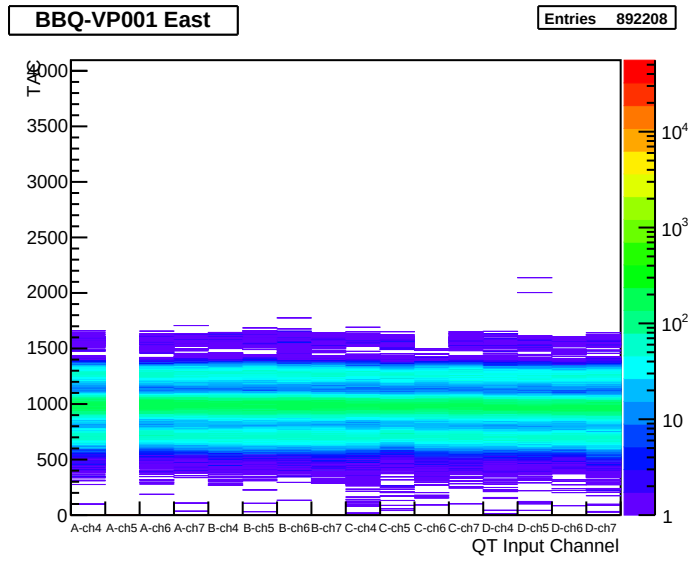

BBQ-BB002 (BBC west small tiles ADC)
Entries 892208
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

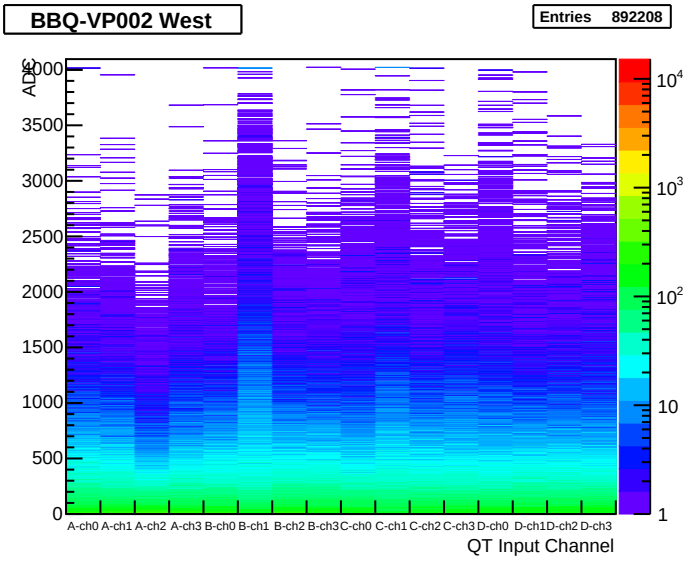

BBQ-BB002 (BBC west small tiles TAC)
Entries 892208
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

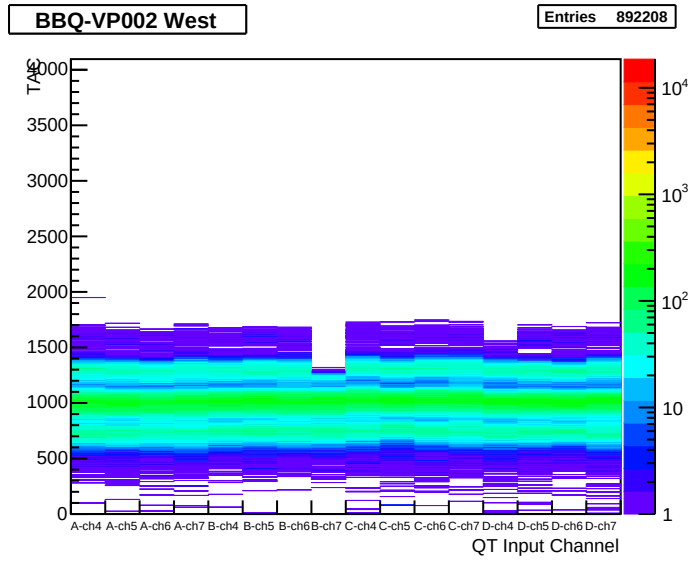

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 892208
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

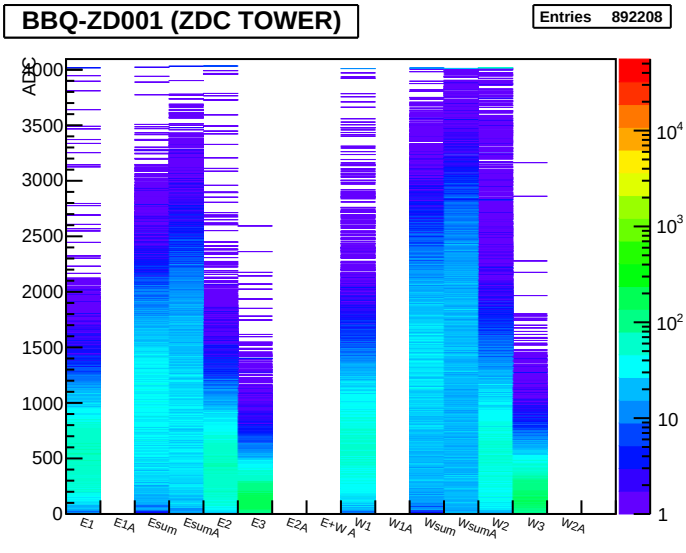

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 892208
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

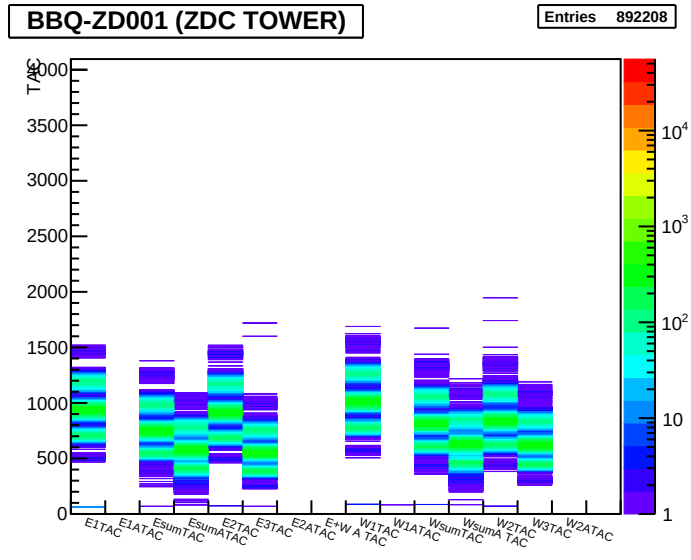

BBQ-VP001 East
Entries 892208
ADC
QT Input Channel

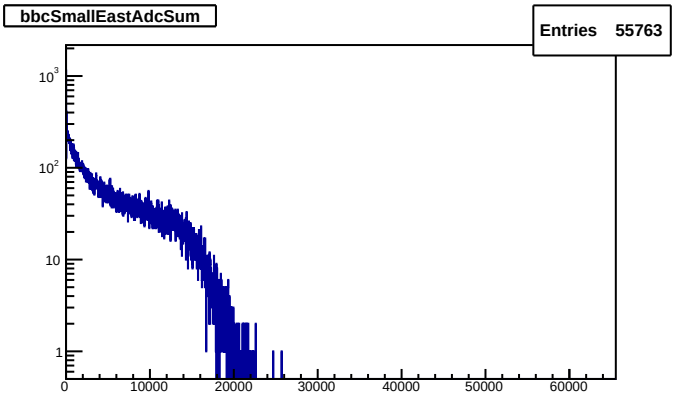

BBQ-VP001 East
Entries 892208
TAC
QT Input Channel

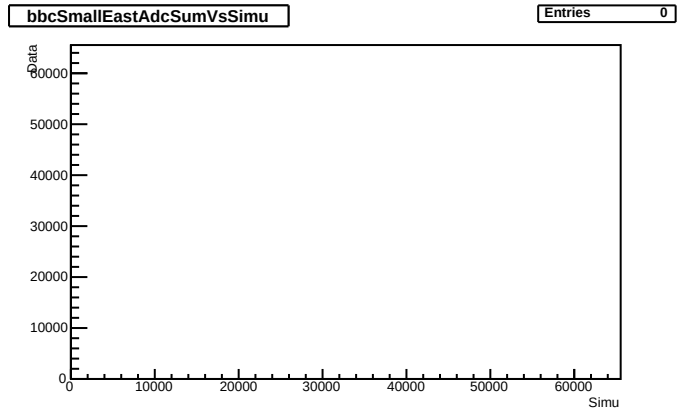

BBQ-VP002 West
Entries 892208
ADC
QT Input Channel

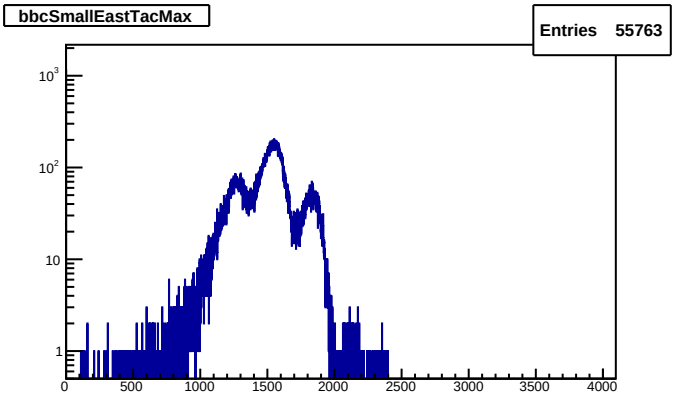

BBQ-VP002 West
Entries 892208
TAC
QT Input Channel

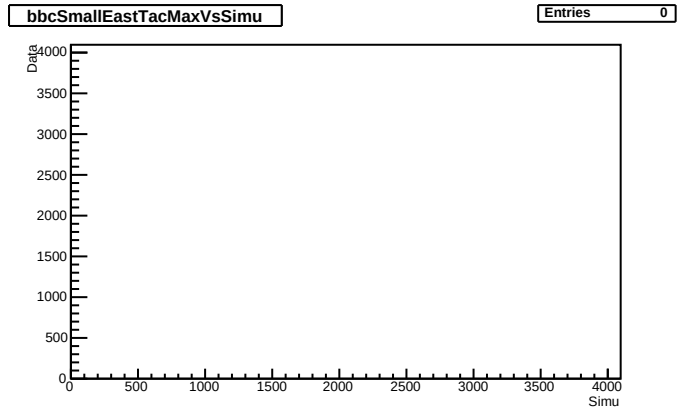

BBQ-ZD001 (ZDC TOWER)
Entries 892208
ADC

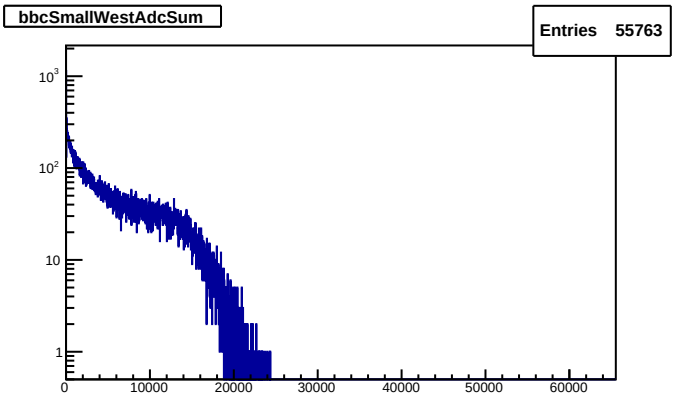

BBQ-ZD001 (ZDC TOWER)
Entries 892208
TAC

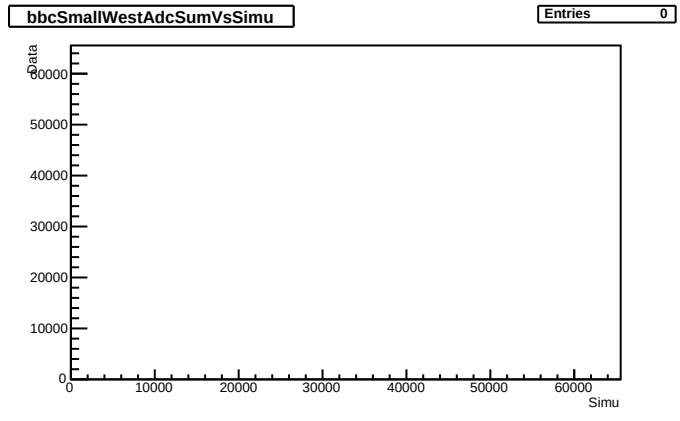

bbcSmallEastAdcSum
Entries 55763

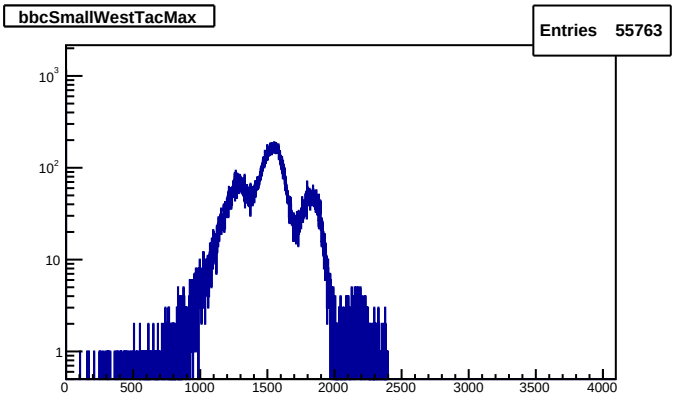

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

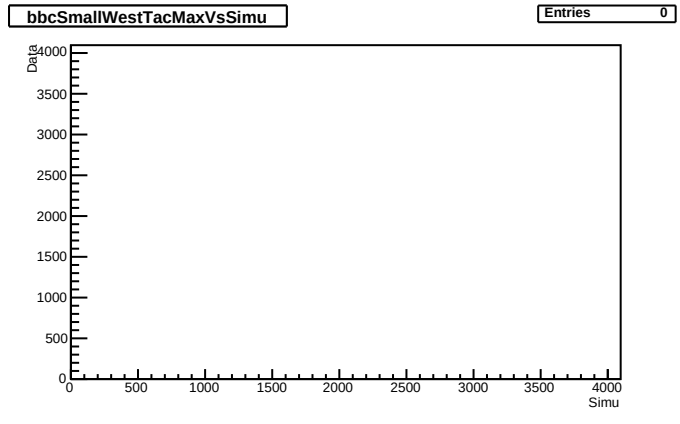

bbcSmallEastTacMax
Entries 55763

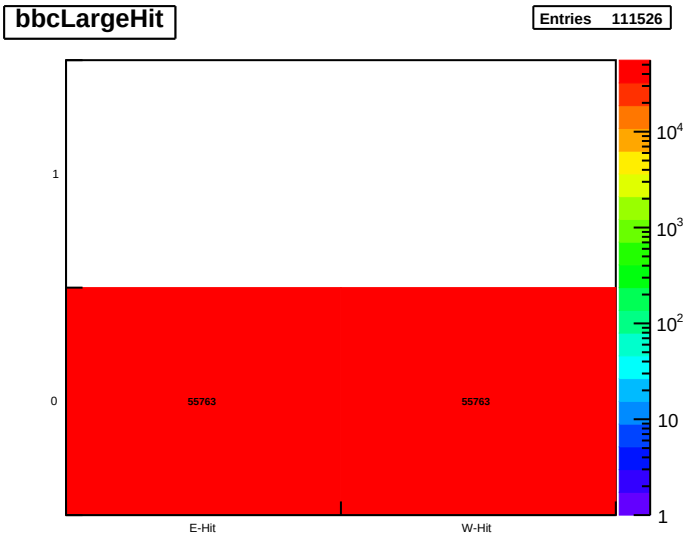

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

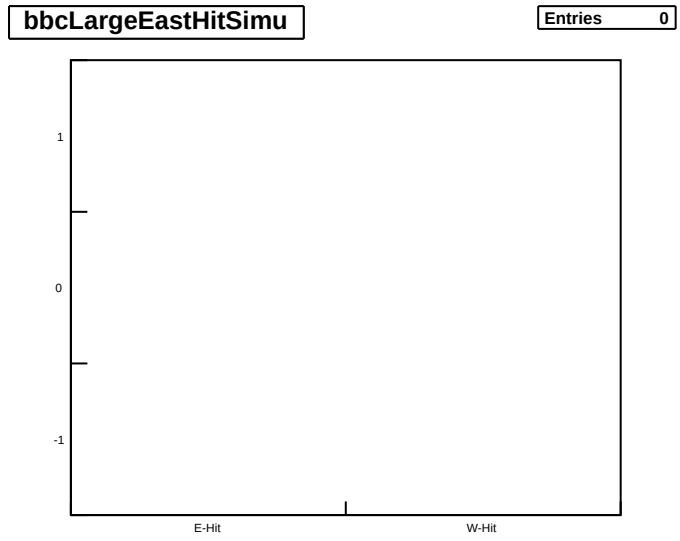

bbcSmallWestAdcSum
Entries 55763

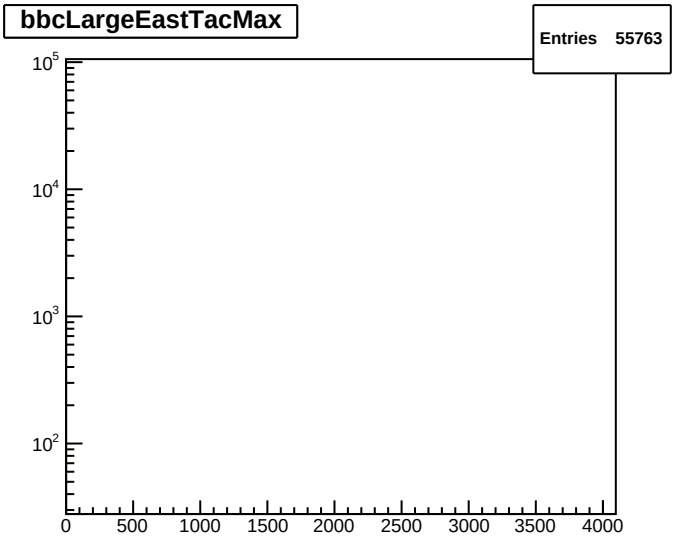

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

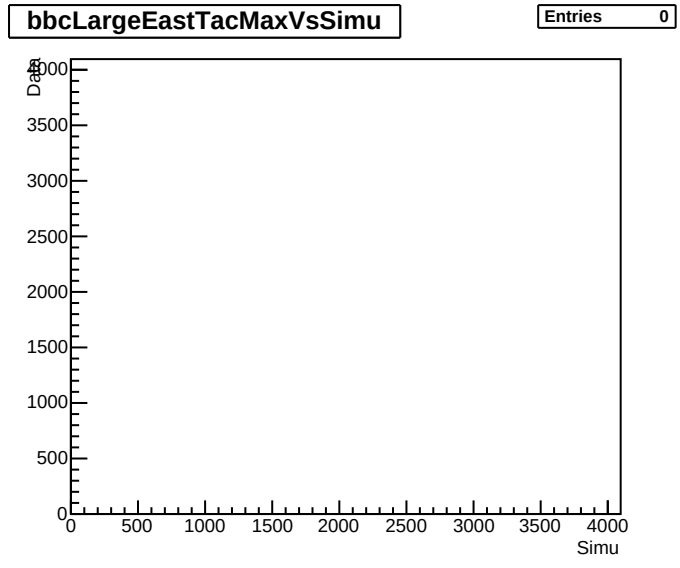

bbcSmallWestTacMax
Entries 55763

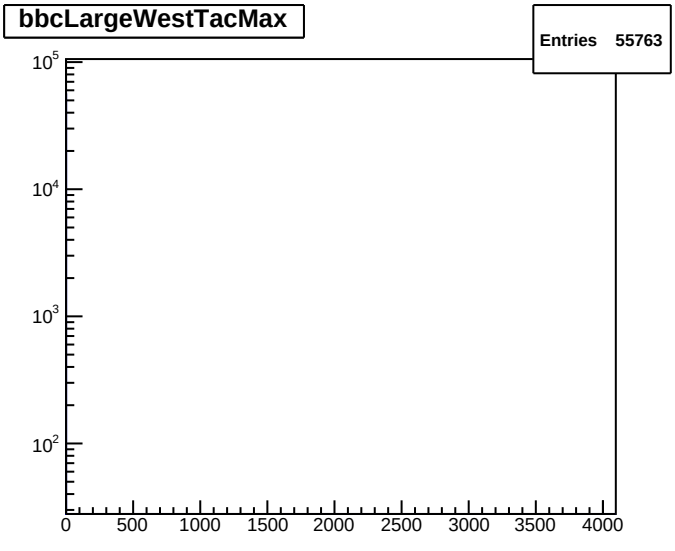

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

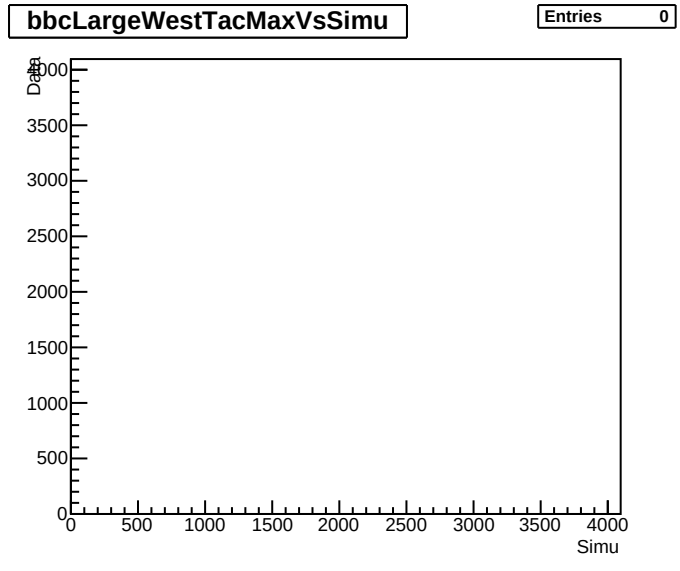

bbcLargeHit
Entries 111526
55763
55763
E-Hit
W-Hit

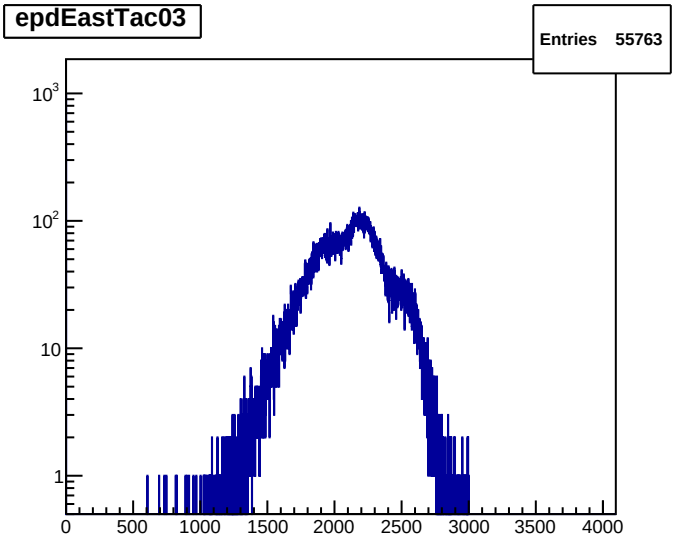

bbcLargeEastHitSimu
Entries 0
E-Hit
W-Hit

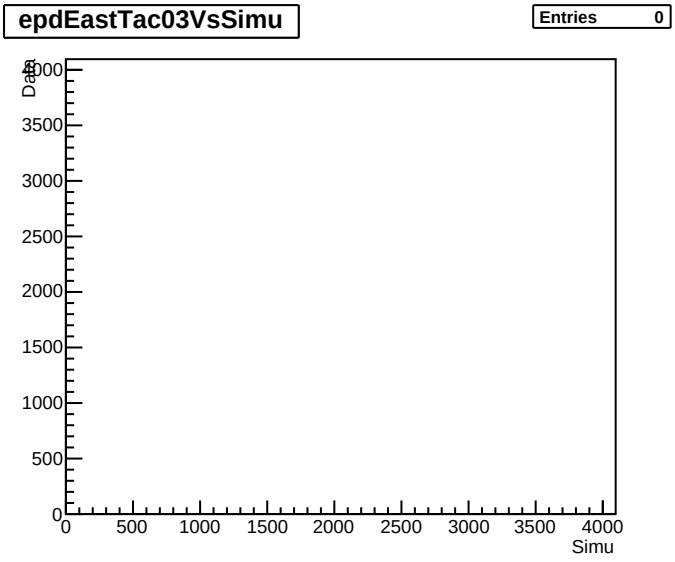

bbcLargeEastTacMax
Entries 55763

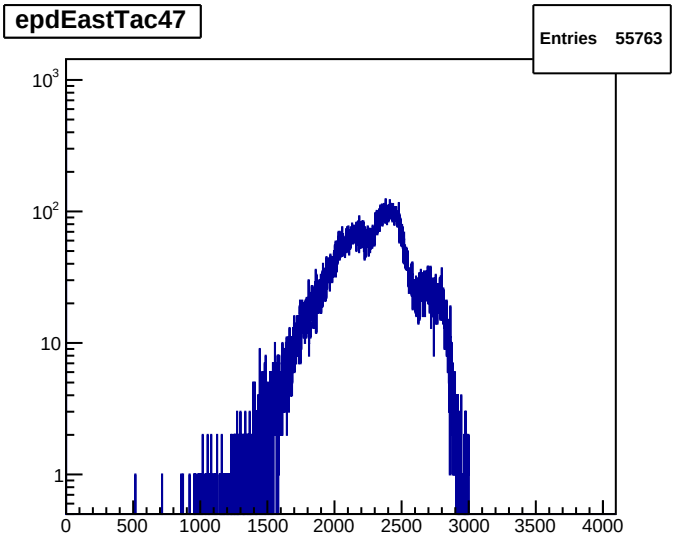

bbcLargeEastTacMaxVsSimu
Entries 0
Data
Simu

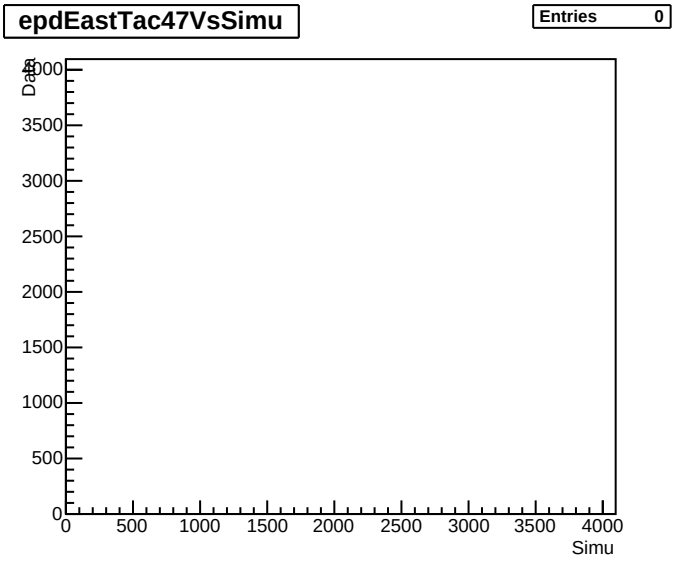

bbcLargeWestTacMax
Entries 55763

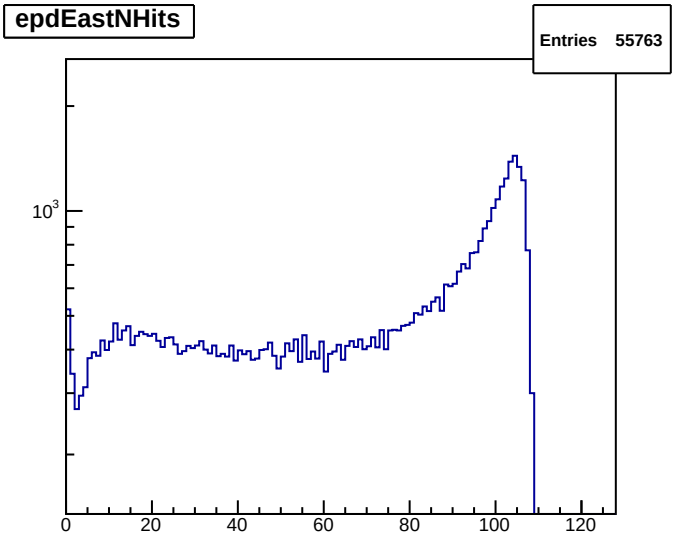

bbcLargeWestTacMaxVsSimu
Entries 0
Data
Simu

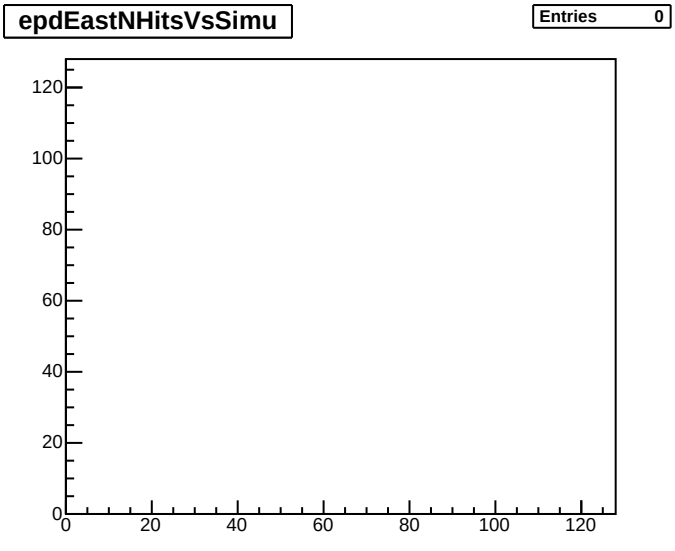

epdEastTac03
Entries 55763
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

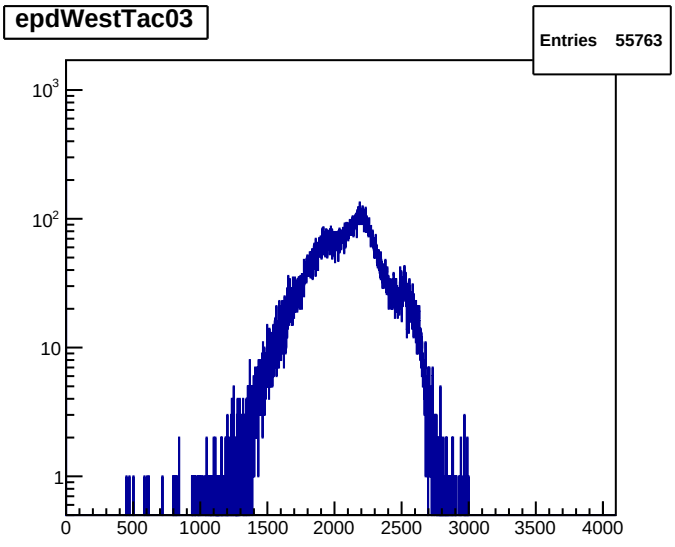

epdEastTac03VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

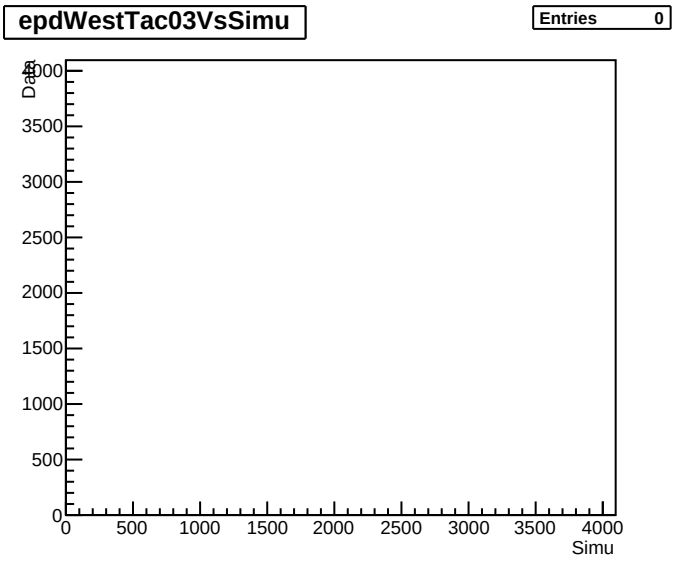

epdEastTac47
Entries 55763
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

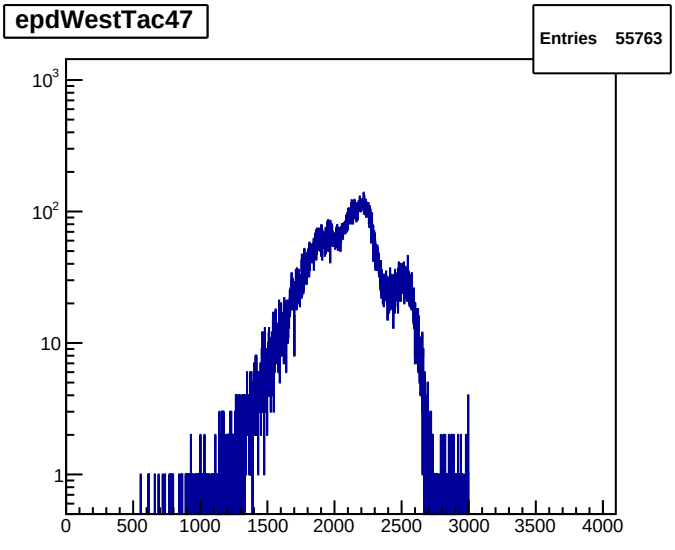

epdEastTac47VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

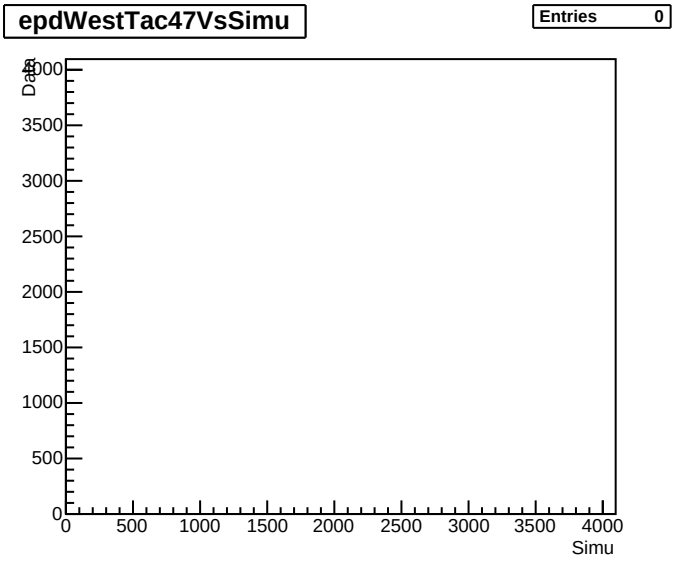

epdEastNHits
Entries 55763
10^3
0 20 40 60 80 100 120

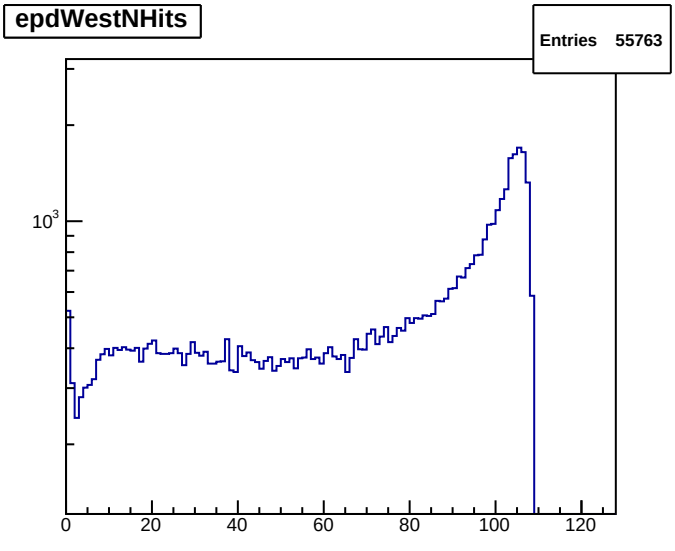

epdEastNHitsVsSimu
Entries 0
120
100
80
60
40
20
0
0 20 40 60 80 100 120

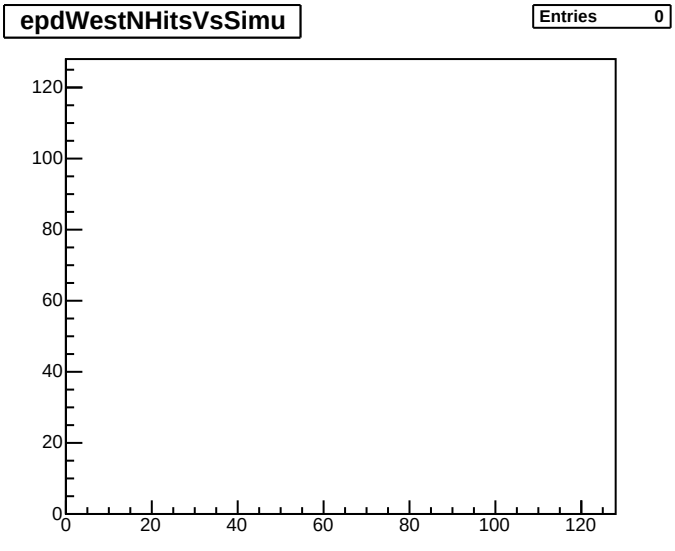

epdWestTac03
Entries 55763
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

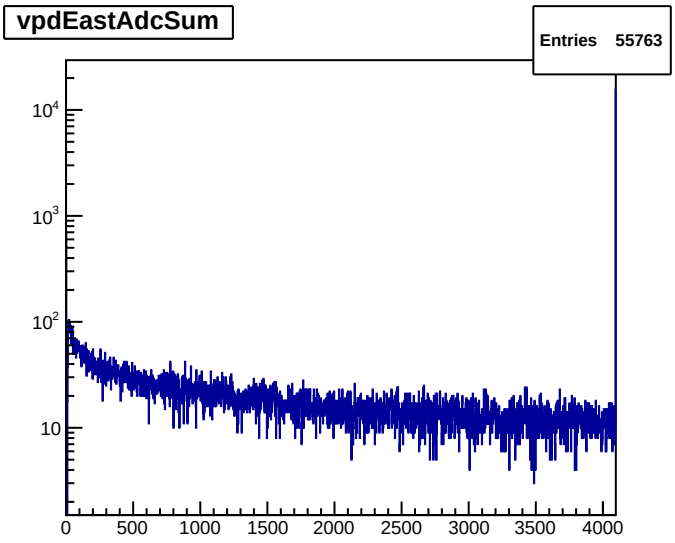

epdWestTac03VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

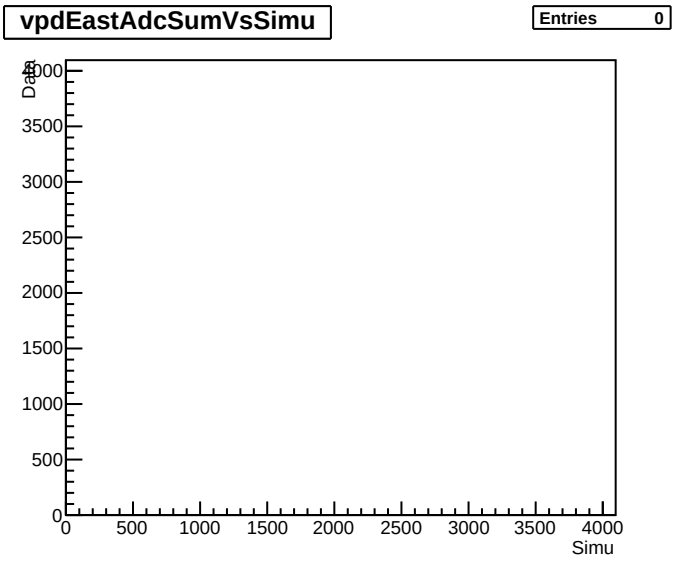

epdWestTac47
Entries 55763
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

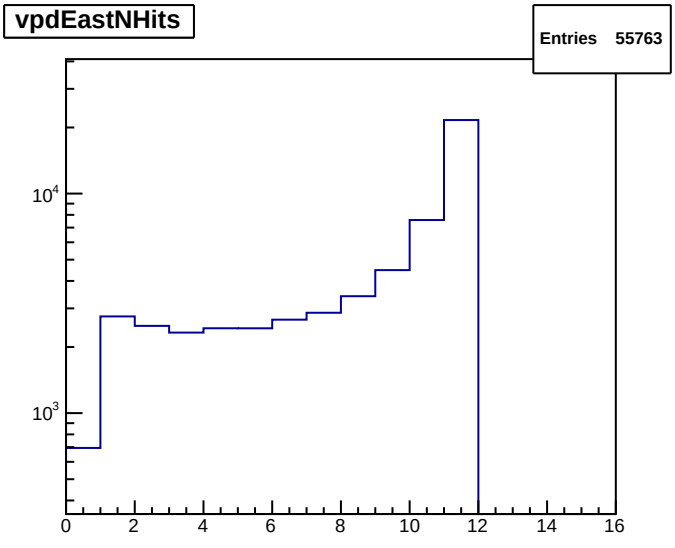

epdWestTac47VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

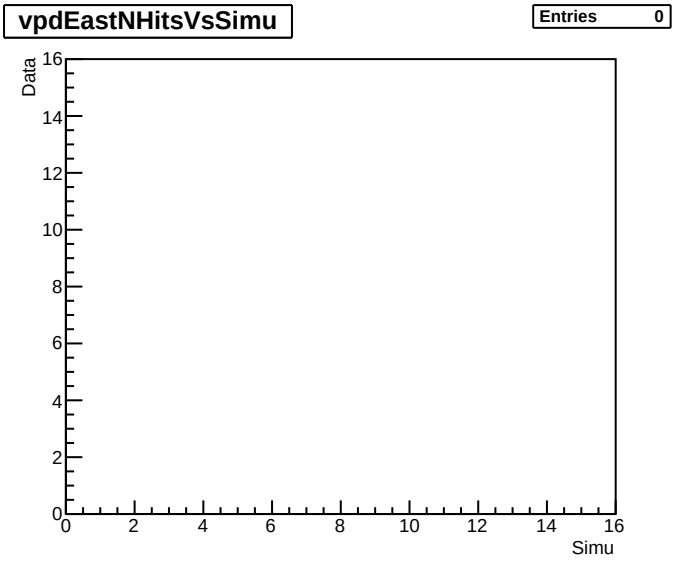

epdWestNHits
Entries 55763
10^3
0 20 40 60 80 100 120

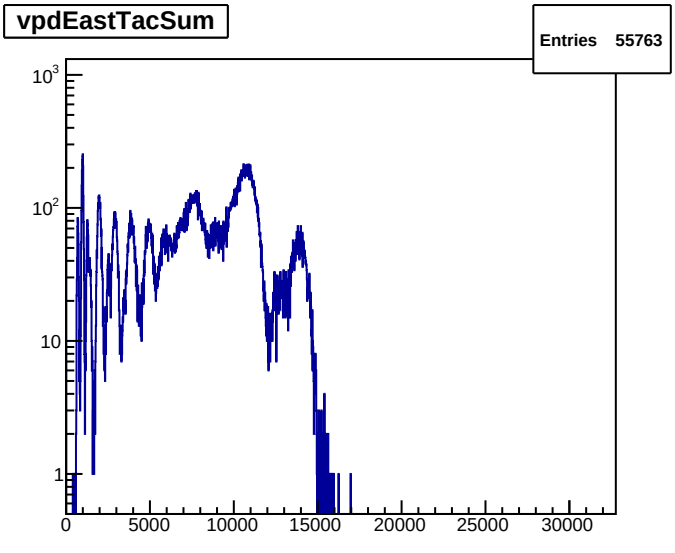

epdWestNHitsVsSimu
Entries 0
120
100
80
60
40
20
0
0 20 40 60 80 100 120

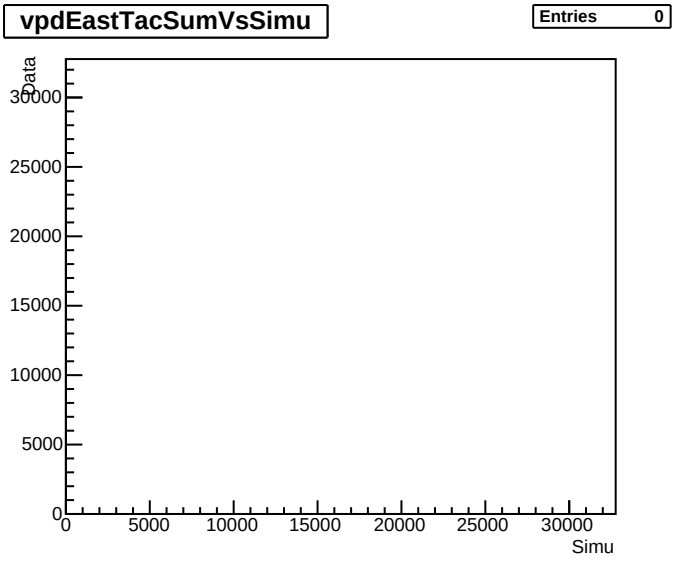

vpdEastAdcSum
Entries 55763

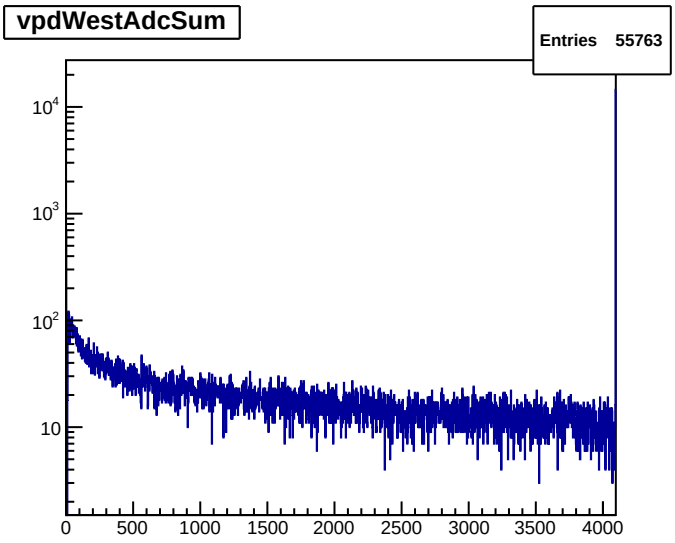

vpdEastAdcSumVsSimu
Entries 0
Data
Simu

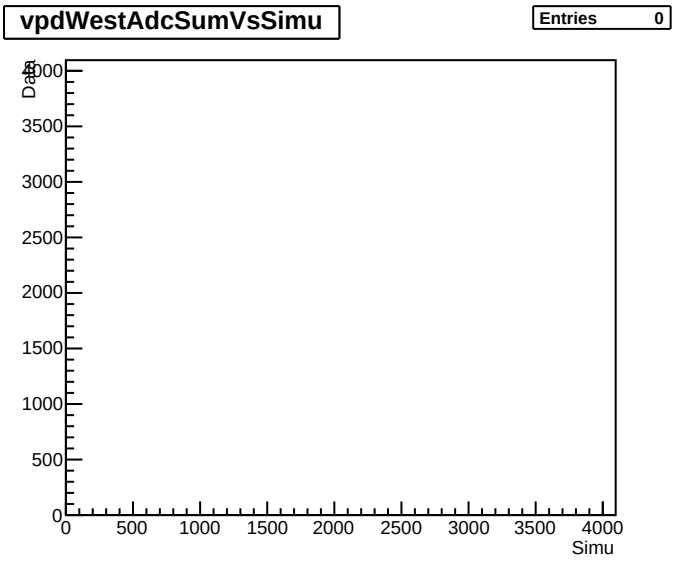

vpdEastNHits
Entries 55763

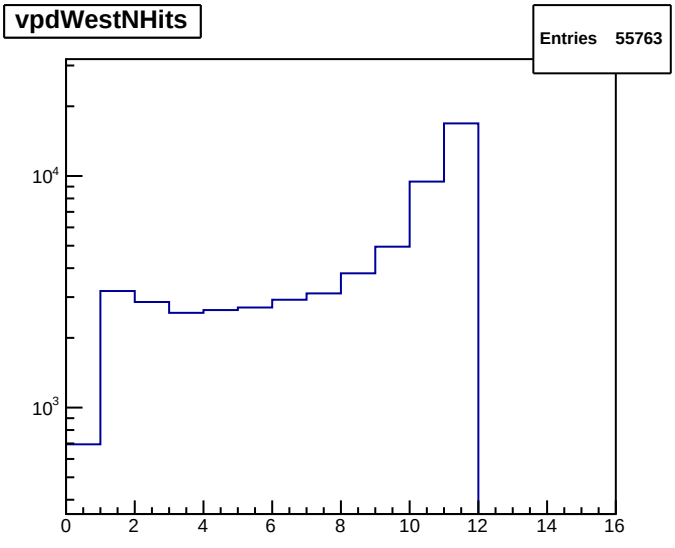

vpdEastNHitsVsSimu
Entries 0
Data
Simu

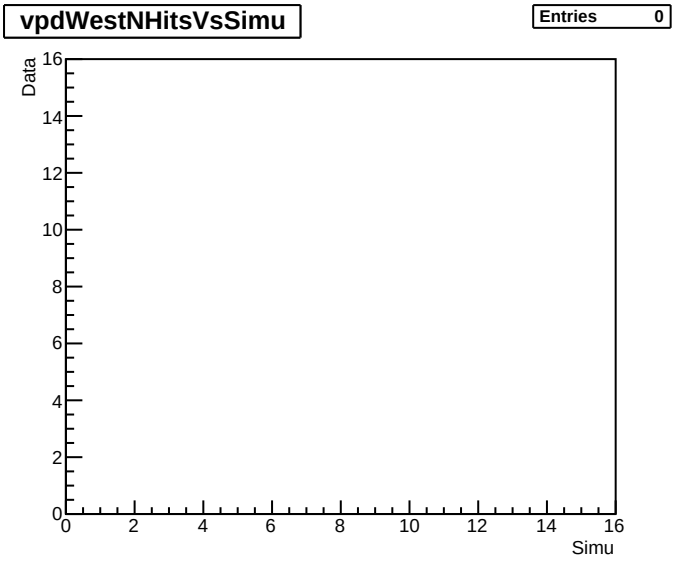

vpdEastTacSum
Entries 55763

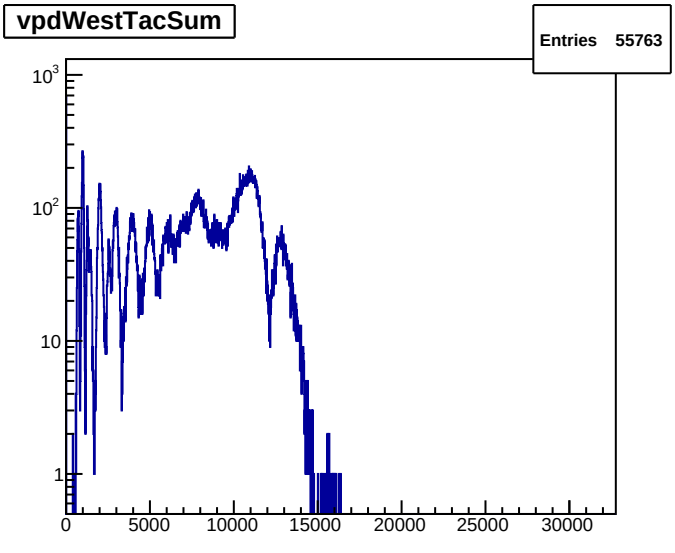

vpdEastTacSumVsSimu
Entries 0
Data
Simu

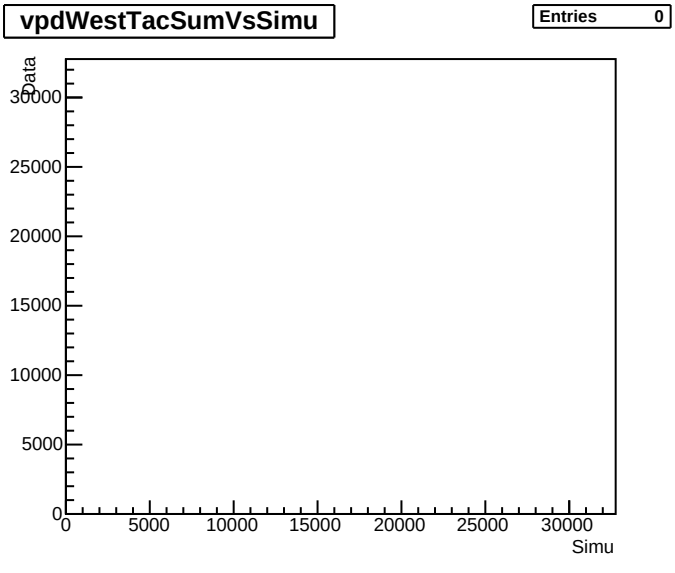

vpdWestAdcSum
Entries 55763

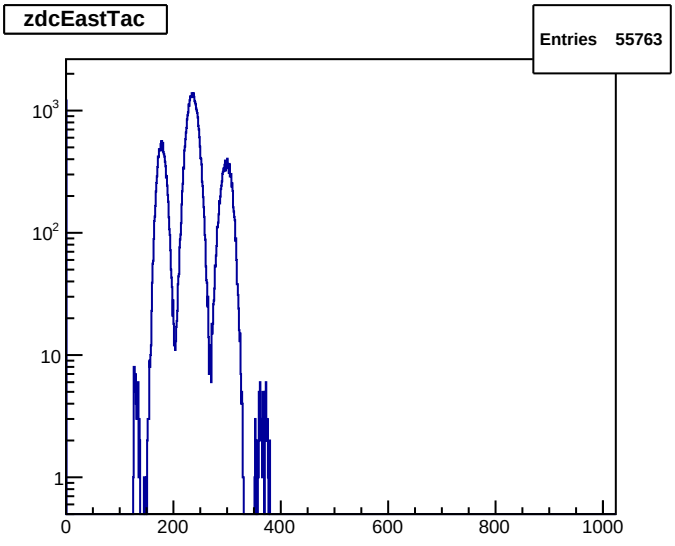

vpdWestAdcSumVsSimu
Entries 0
Data
Simu

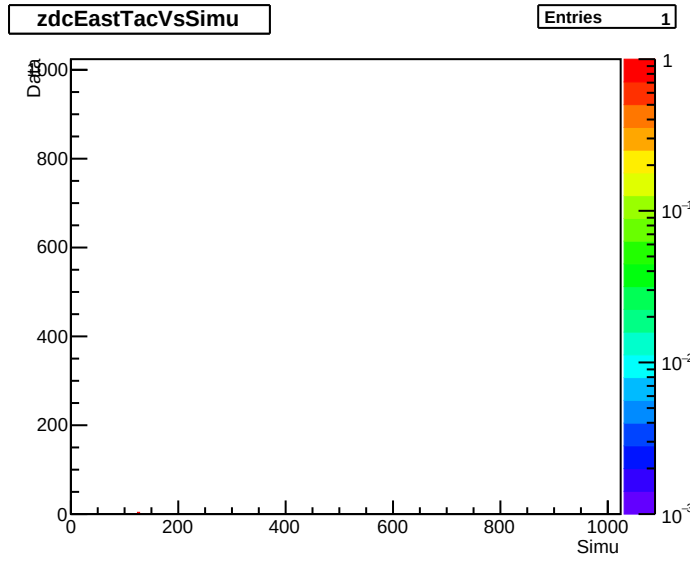

vpdWestNHits
Entries 55763

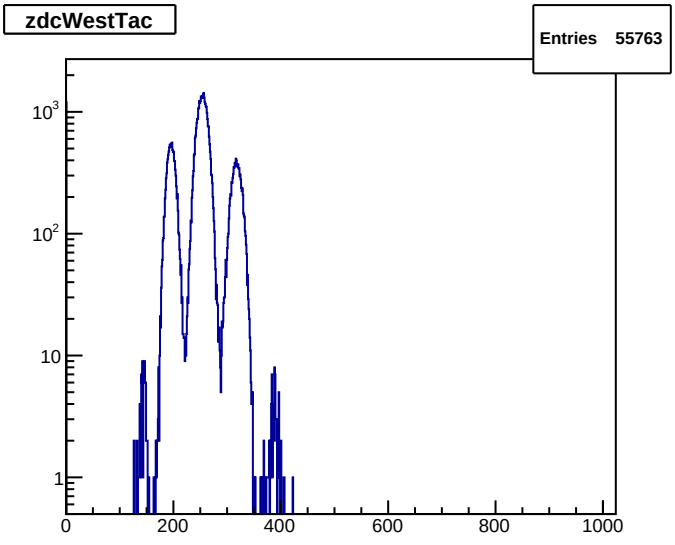

vpdWestNHitsVsSimu
Entries 0
Data
Simu

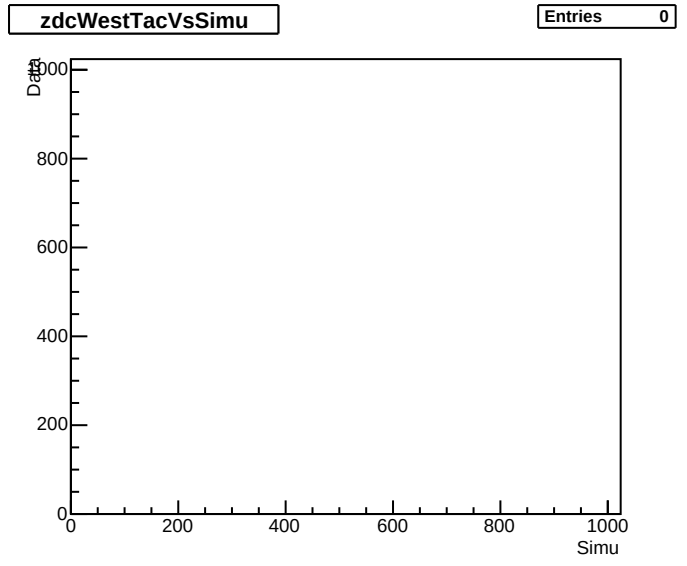

vpdWestTacSum
Entries 55763

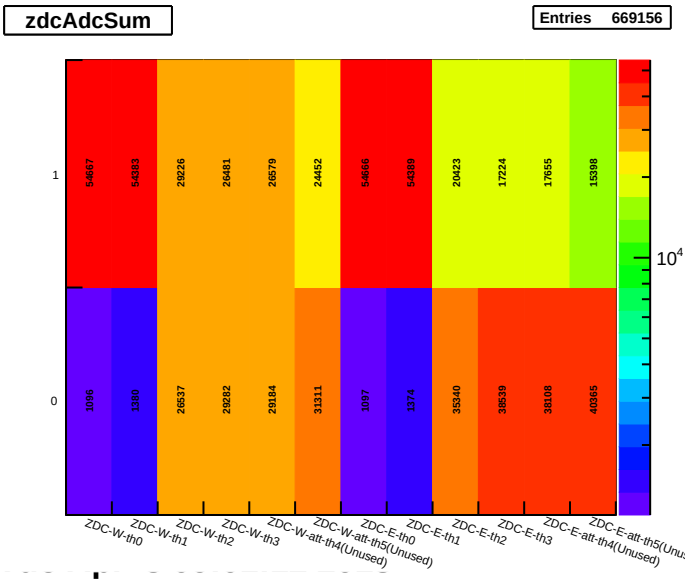

vpdWestTacSumVsSimu
Entries 0
Data
Simu

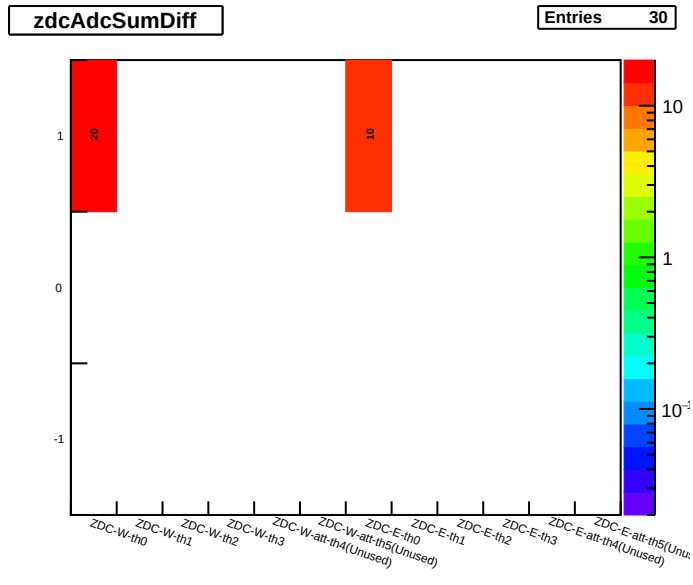

zdcEastTac
Entries 55763

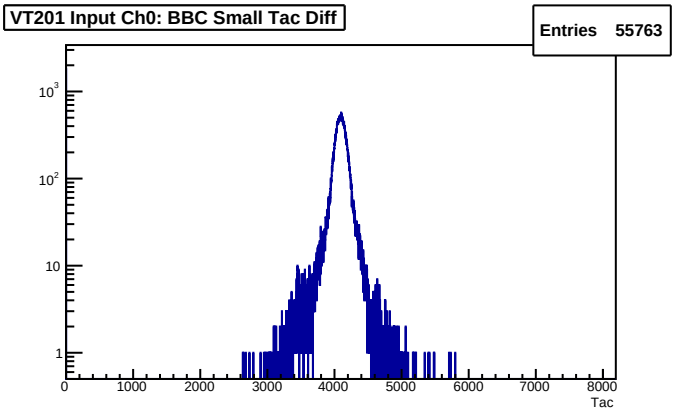

zdcEastTacVsSimu
Entries 1
Data
Simu

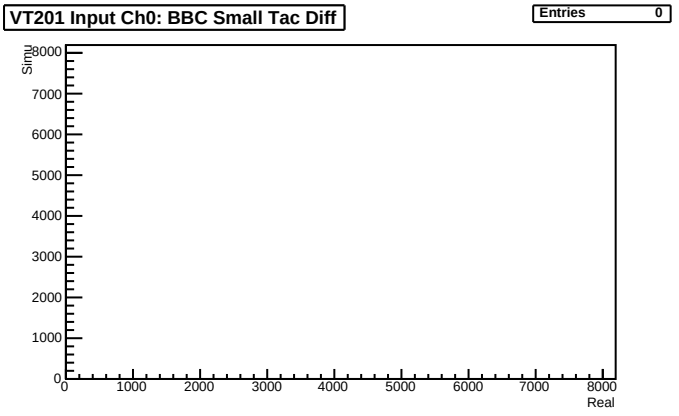

zdcWestTac
Entries 55763

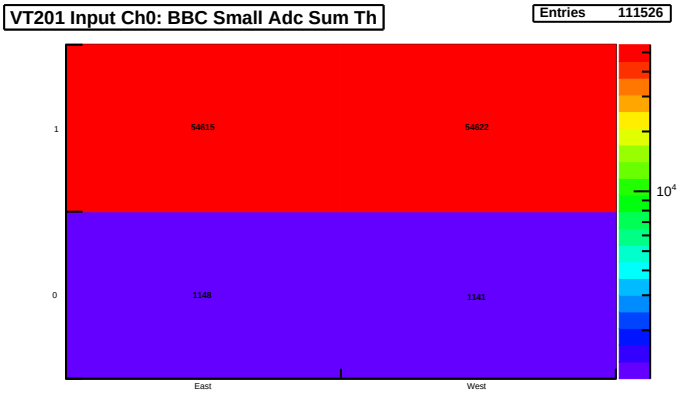

zdcWestTacVsSimu
Entries 0
Data
Simu

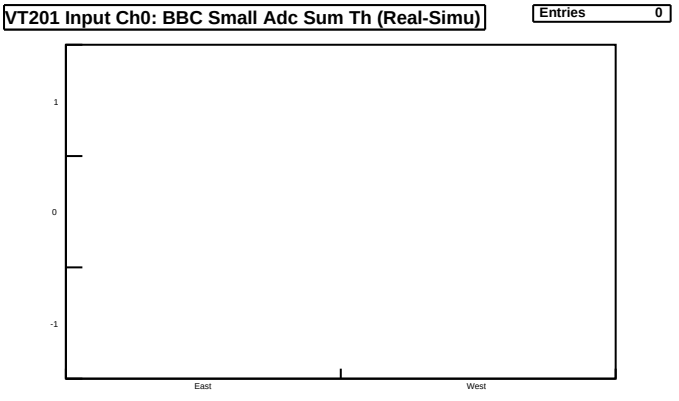

zdcAdcSum
Entries 669156
54667
54683
29226
26481
26679
24452
54666
54689
20423
17224
17655
15398
1096
1380
28637
29282
29184
31311
1097
1374
35340
38639
38108
40965
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

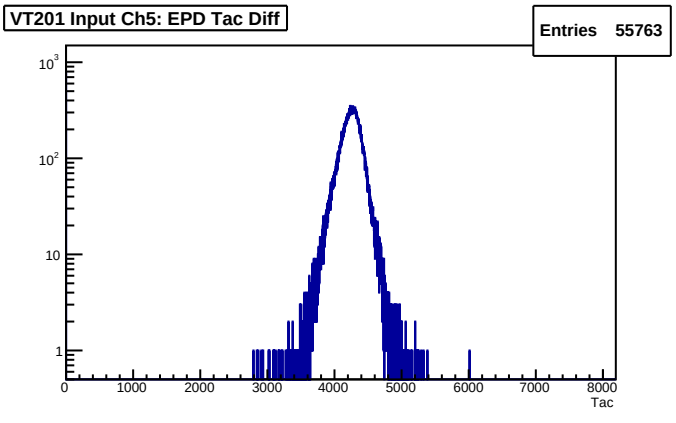

zdcAdcSumDiff
Entries 30
20
10
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

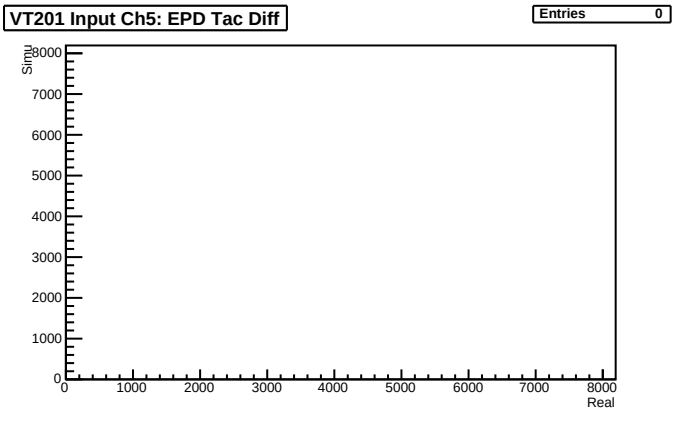

VT201 Input Ch0: BBC Small Tac Diff
Entries 55763
Tac

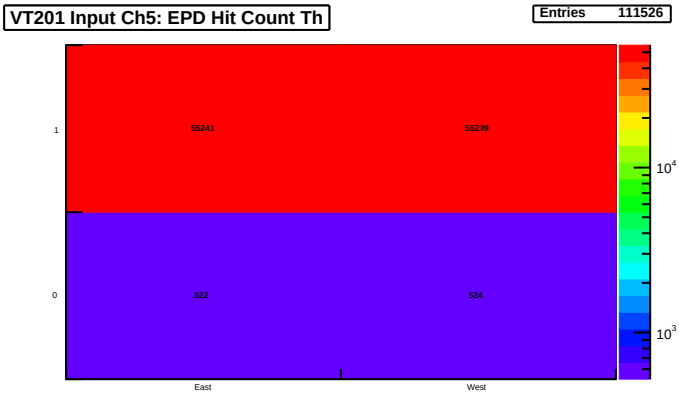

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

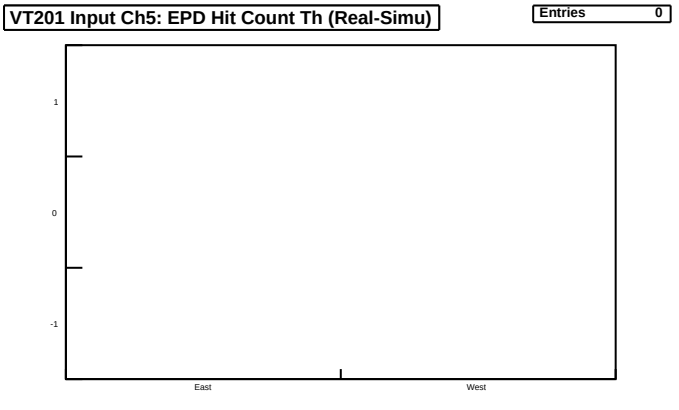

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 111526
54615
54622
1148
1141
East
West

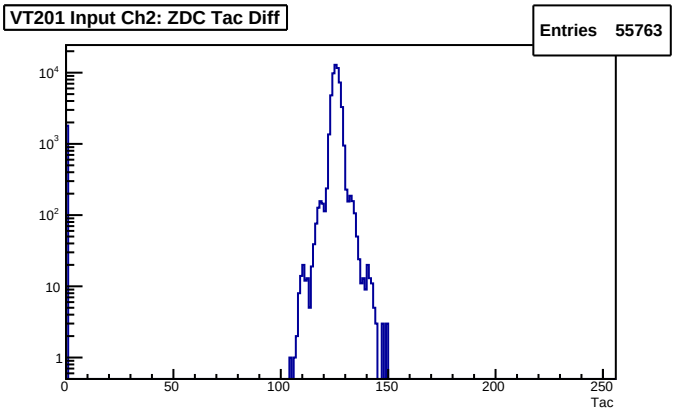

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

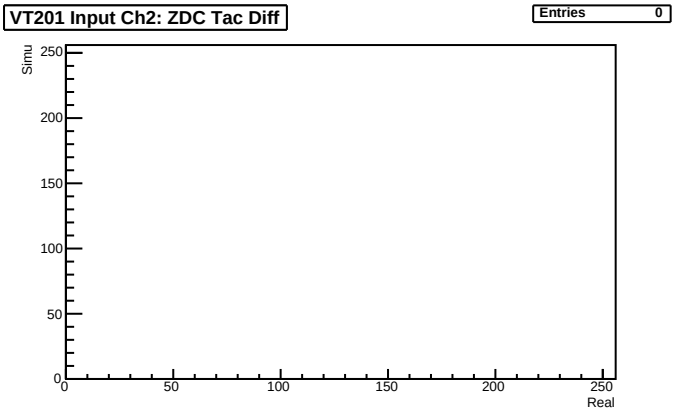

VT201 Input Ch5: EPD Tac Diff
Entries 55763
Tac

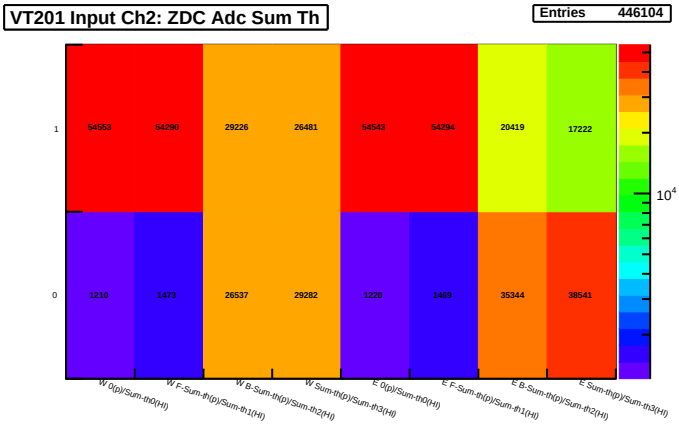

VT201 Input Ch5: EPD Tac Diff
Entries 0
Simu
Real

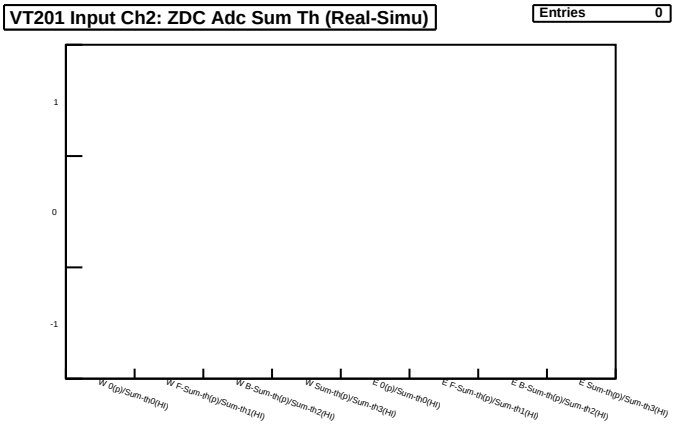

VT201 Input Ch5: EPD Hit Count Th
Entries 111526
55241
55239
522
524
East
West

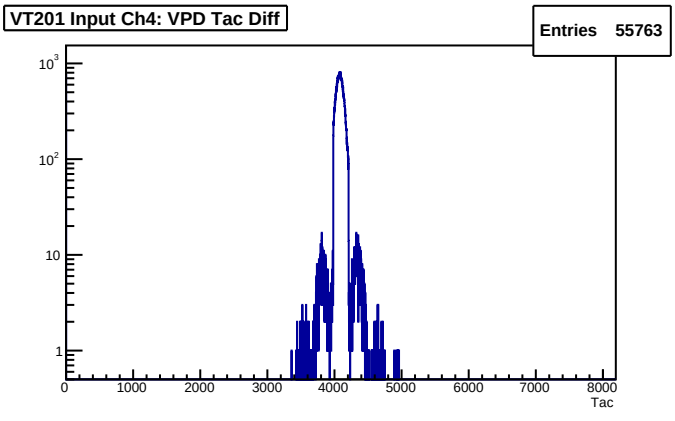

VT201 Input Ch5: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

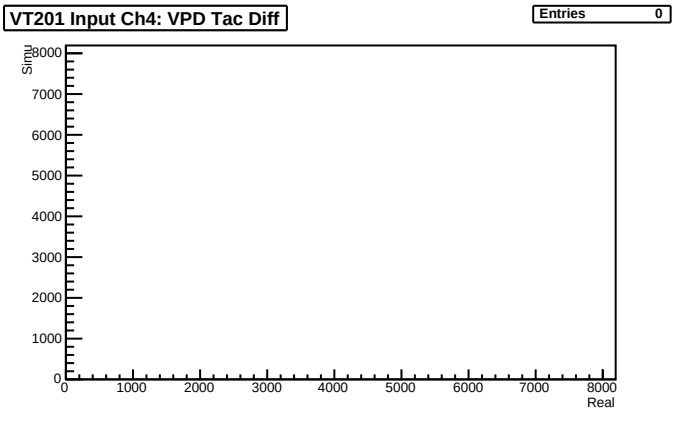

VT201 Input Ch2: ZDC Tac Diff
Entries 55763
Tac

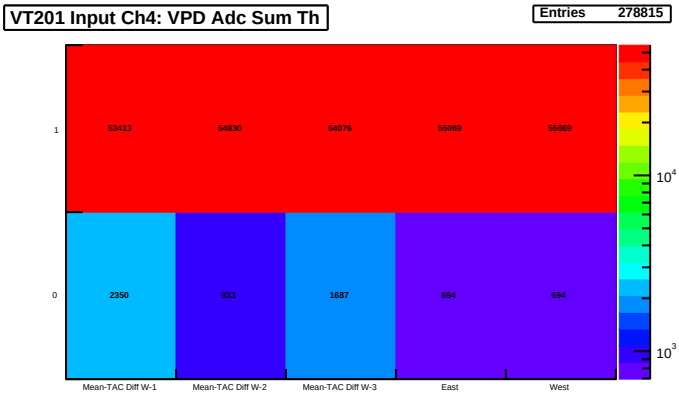

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

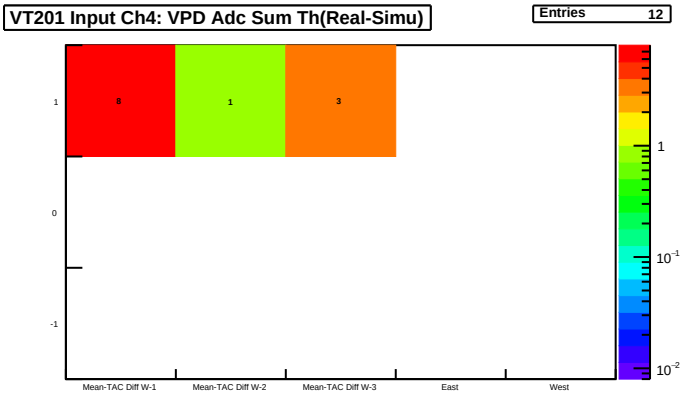

VT201 Input Ch2: ZDC Adc Sum Th
Entries 446104
54553
54290
29226
26481
54543
54294
20419
17222
1210
1473
26537
29282
1220
1469
35344
38541
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

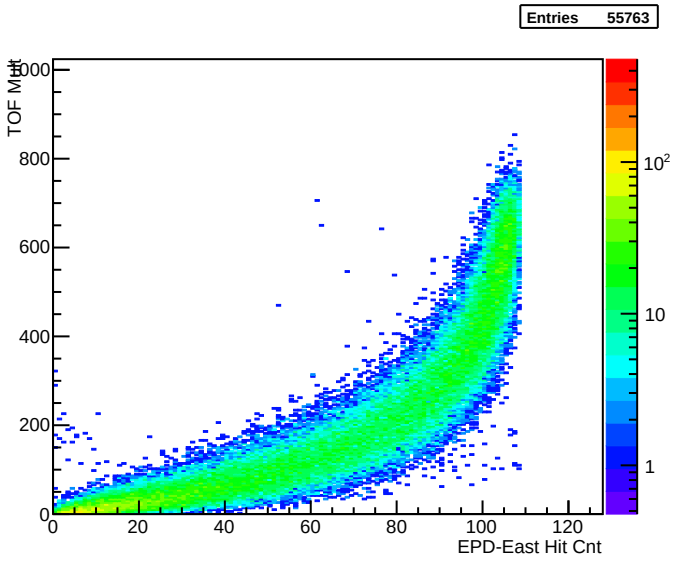

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

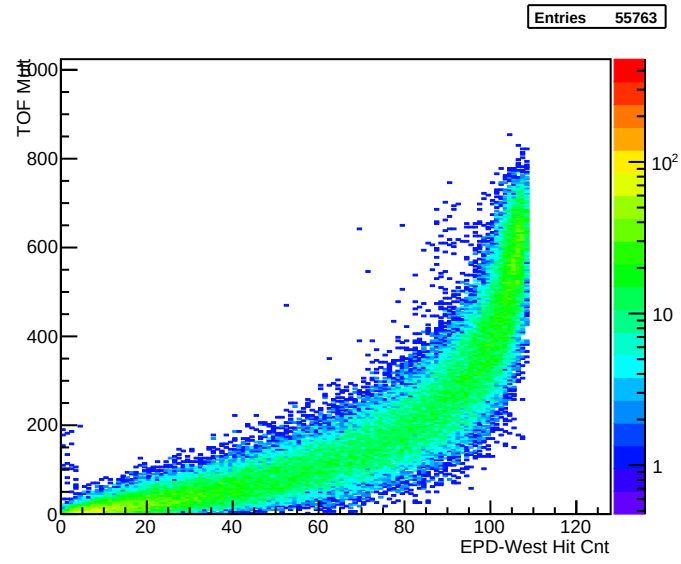

VT201 Input Ch4: VPD Tac Diff
Entries 55763
Tac

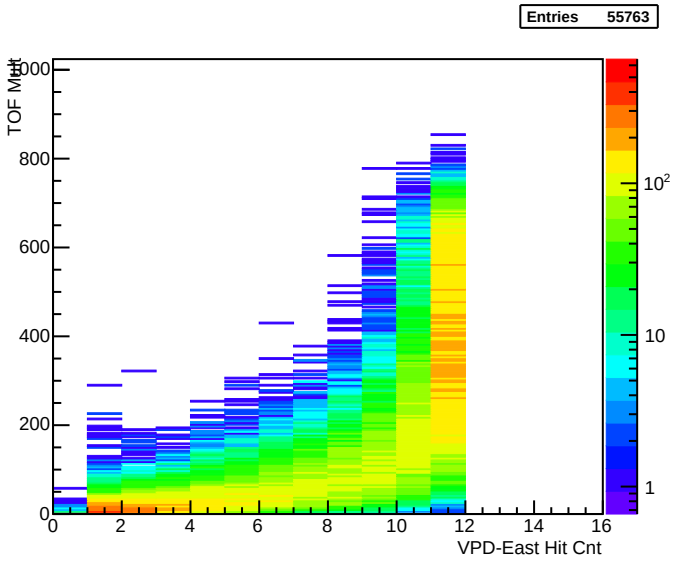

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

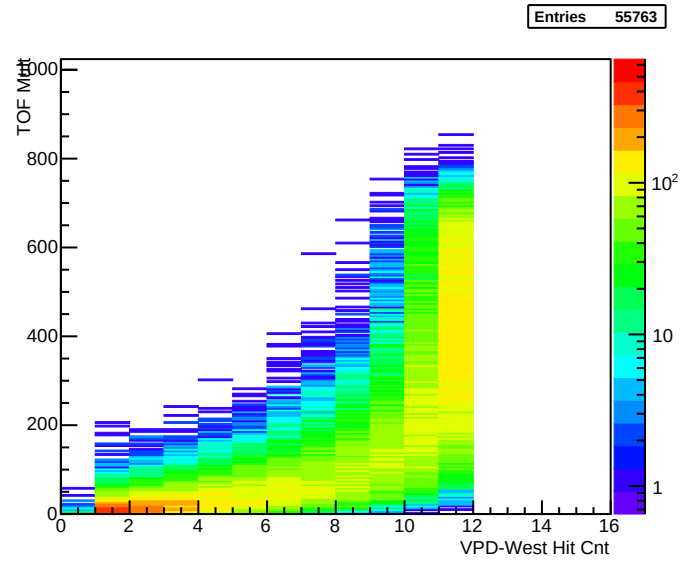

VT201 Input Ch4: VPD Adc Sum Th
Entries 278815
53413
54830
54076
55069
55069
2350
933
1687
694
694
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

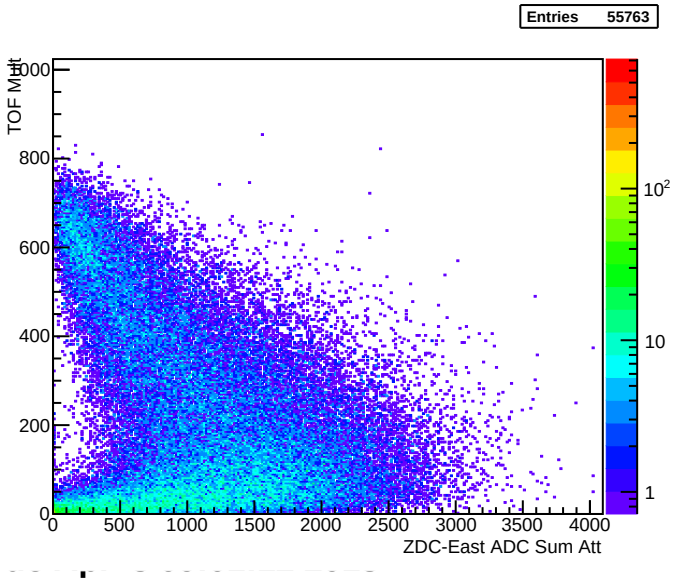

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 12
8
1
3
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

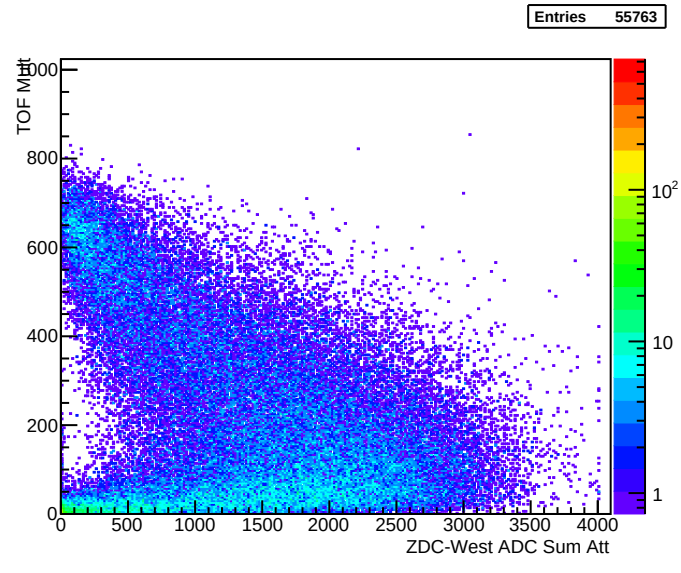

Entries 55763
TOF Mult
EPD-East Hit Cnt

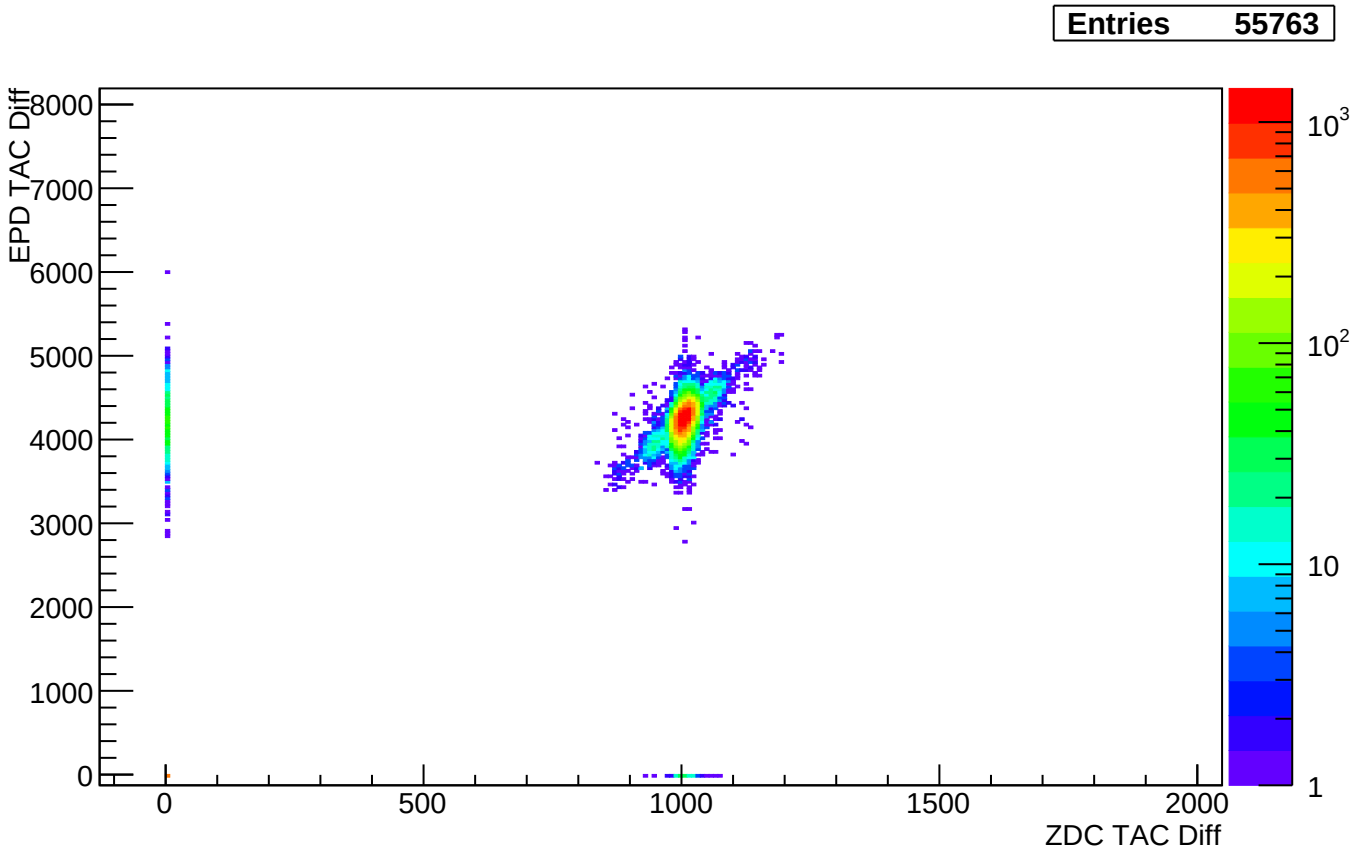

Entries 55763
TOF Mult
EPD-West Hit Cnt

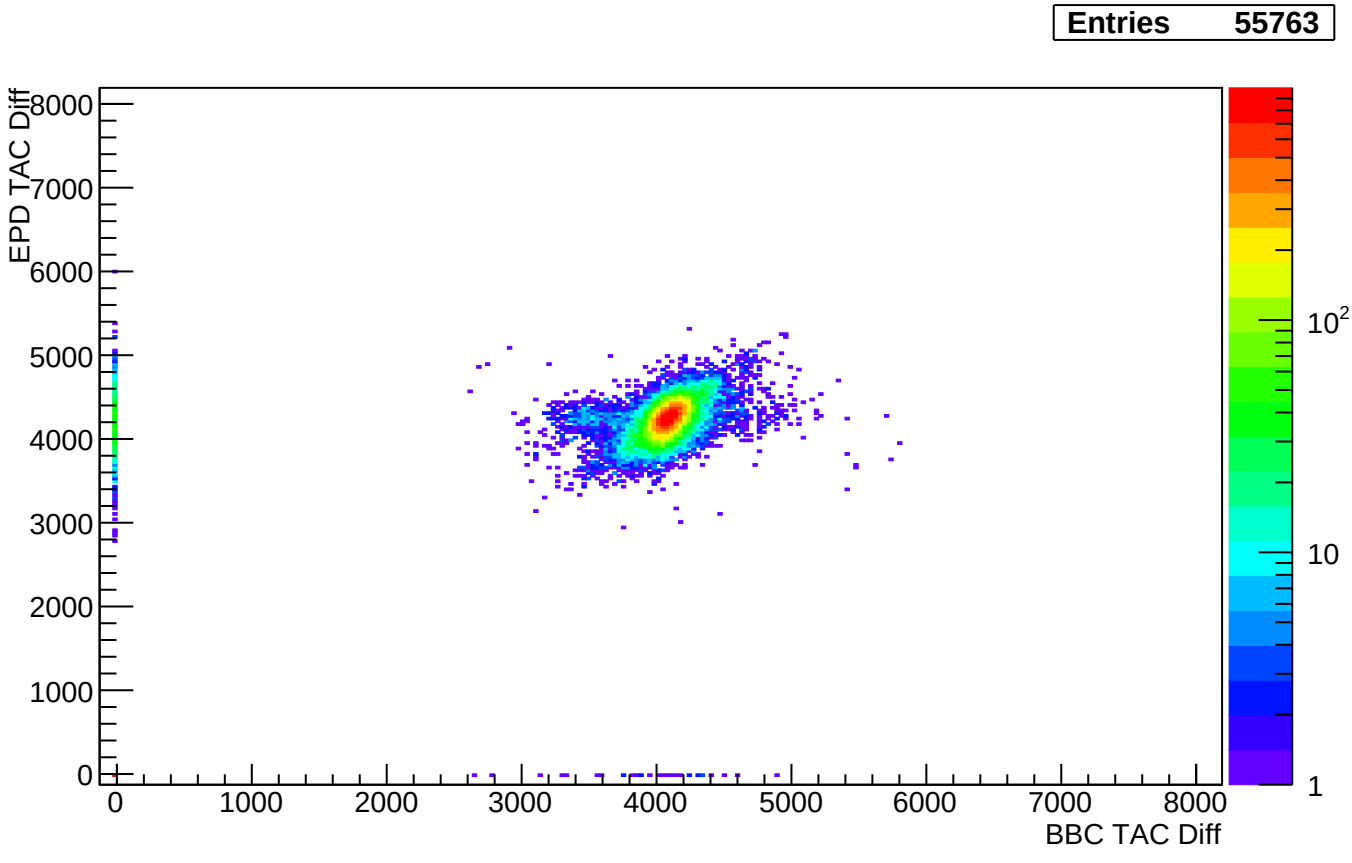

Entries 55763
TOF Mult
VPD-East Hit Cnt

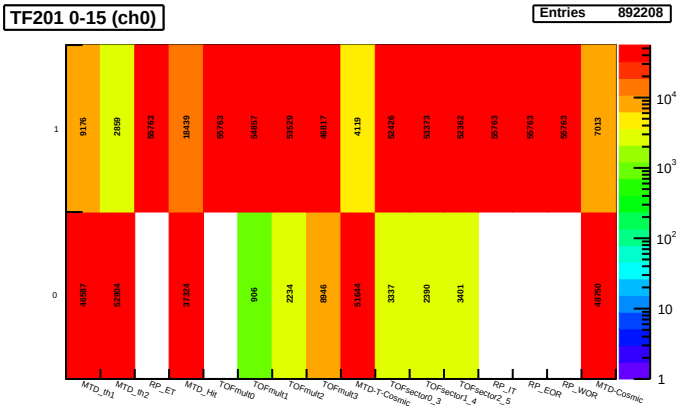

Entries 55763
TOF Mult
VPD-West Hit Cnt

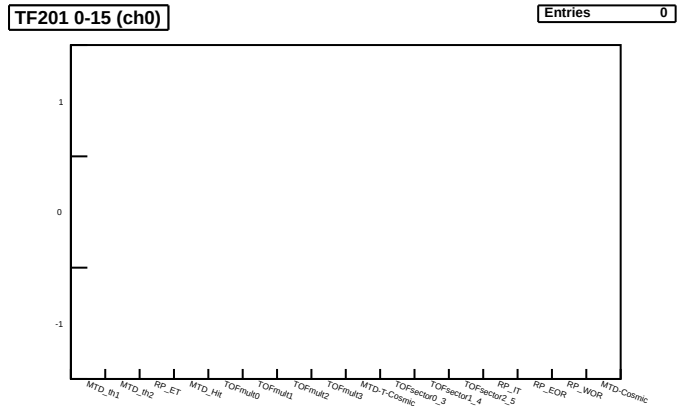

Entries 55763
TOF Mult
ZDC-East ADC Sum Att

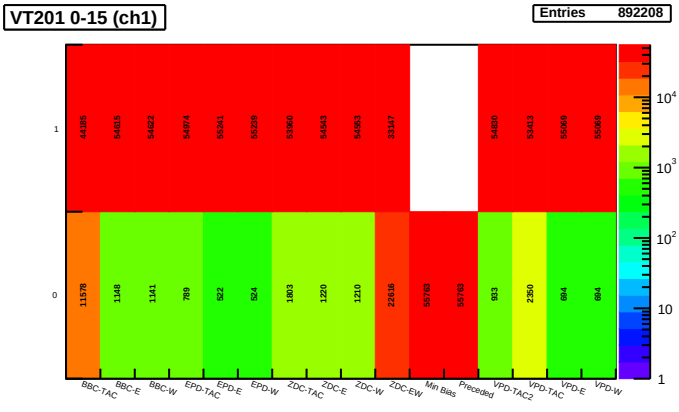

Entries 55763
TOF Mult
ZDC-West ADC Sum Att

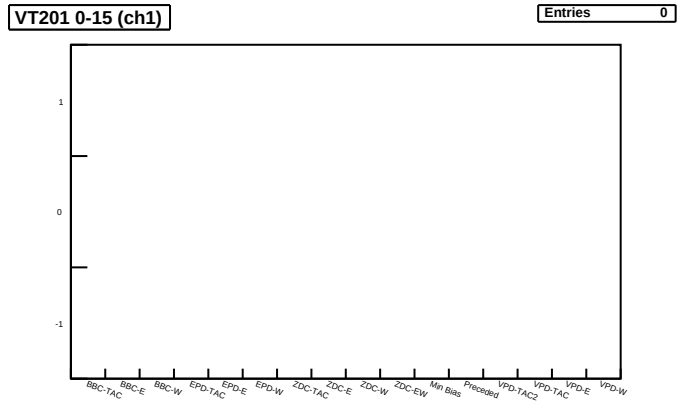

Entries 55763
EPD TAC Diff
ZDC TAC Diff
0
500
1000
1500
2000
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2
10^3

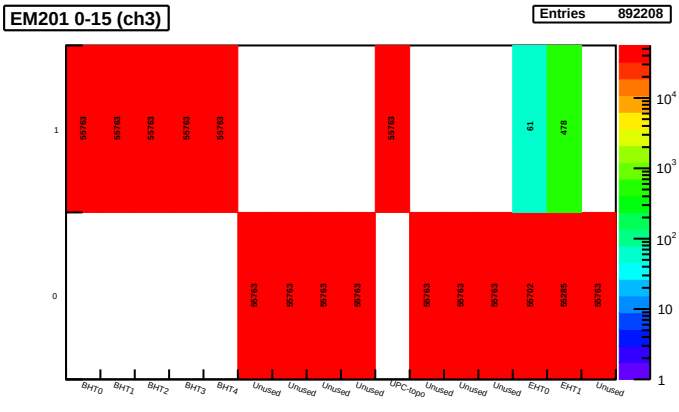

Entries 55763
EPD TAC Diff
BBC TAC Diff
0
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2

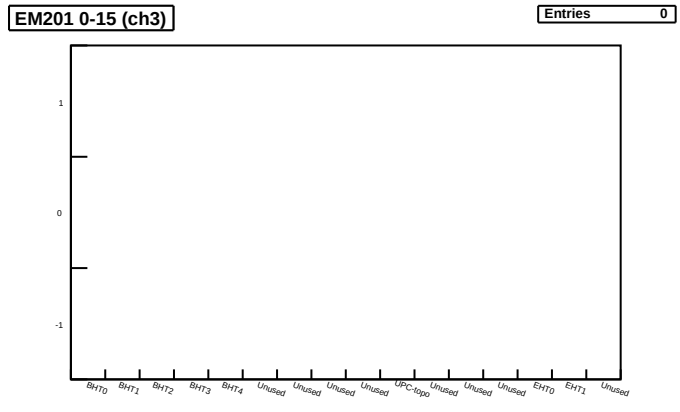

TF201 0-15 (ch0)
Entries 892208
9176 2899 55763 18439 55763 54857 53529 46817 4119 52426 53373 52362 55763 55763 55763 7013
46887 52904 37324 906 2234 8946 51644 3337 2390 3401 48750
MTD_th1 MTD_th2 RP_ET MTD_Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

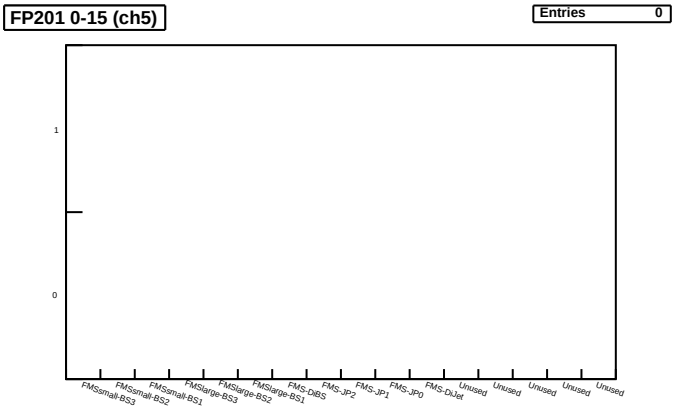

TF201 0-15 (ch0)
Entries 0
MTD_th1 MTD_th2 RP_ET MTD_Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

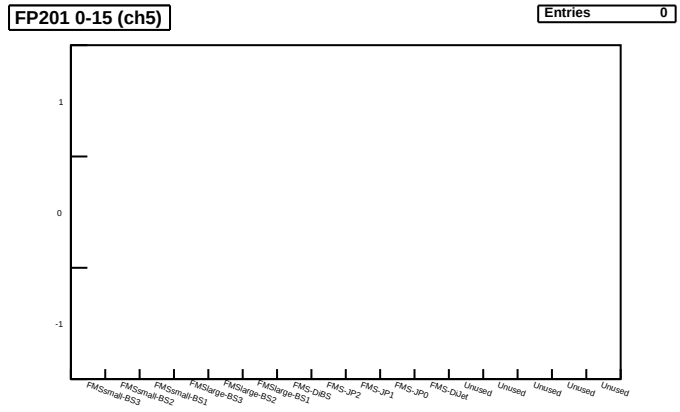

VT201 0-15 (ch1)
Entries 892208
44185 54615 54622 54974 55341 55229 53960 54543 54053 33147 54830 53413 55069 55069
11578 1148 1141 789 822 824 1803 1220 1210 22616 55763 55763 933 2350 694 694
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W ZDC-EW Min Bias Preceded VPD-TAC2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 892208
55763 55763 55763 55763 55763 55763 61 478
55763 55763 55763 55763 55763 55763 55763 55702 55285 55763
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3 FMSsmall-BS2 FMSsmall-BS1 FMSlarge-BS3 FMSlarge-BS2 FMSlarge-BS1 FMS-DiBS FMS-JP2 FMS-JP1 FMS-JP0 FMS-DiJet Unused Unused Unused Unused Unused