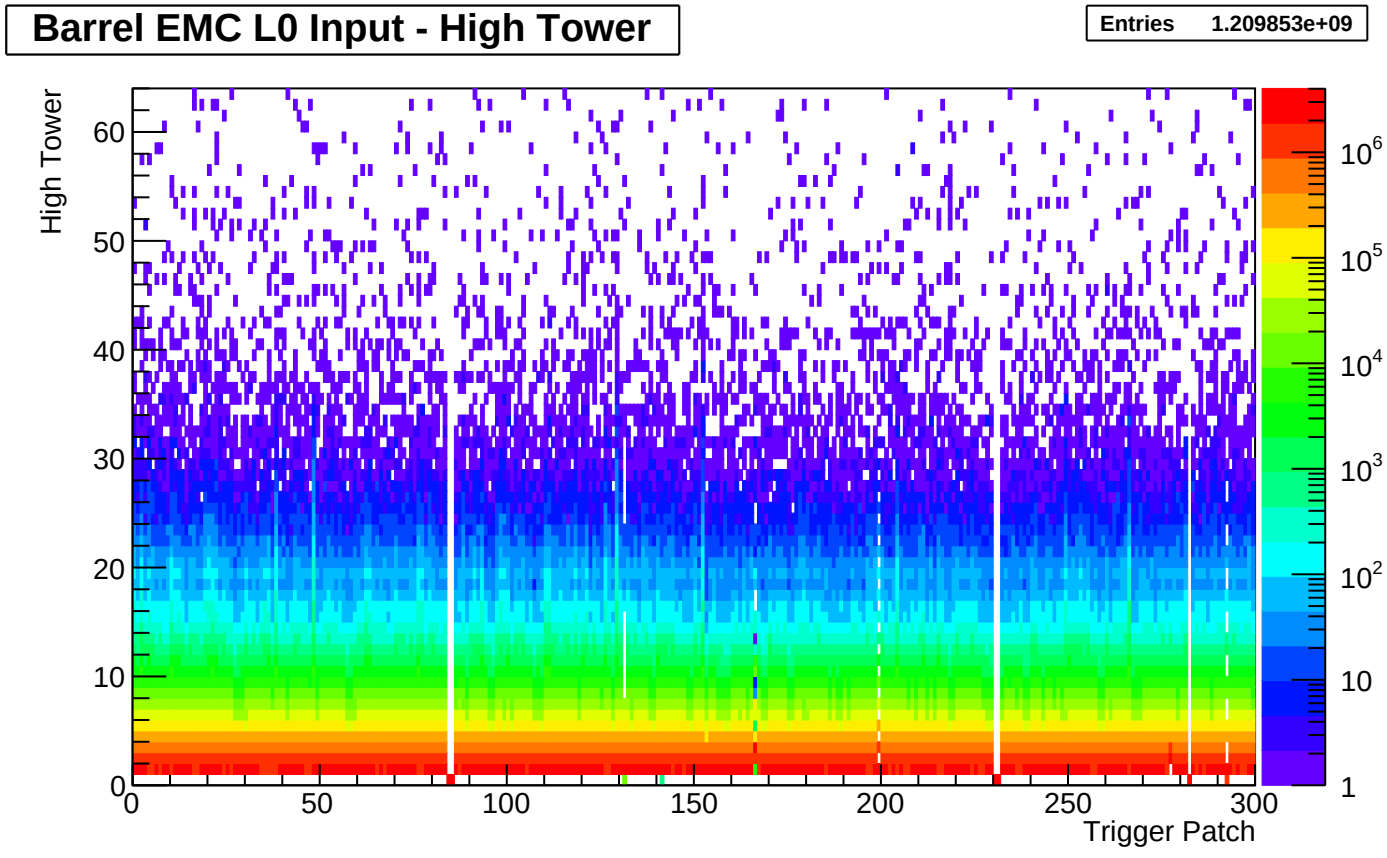
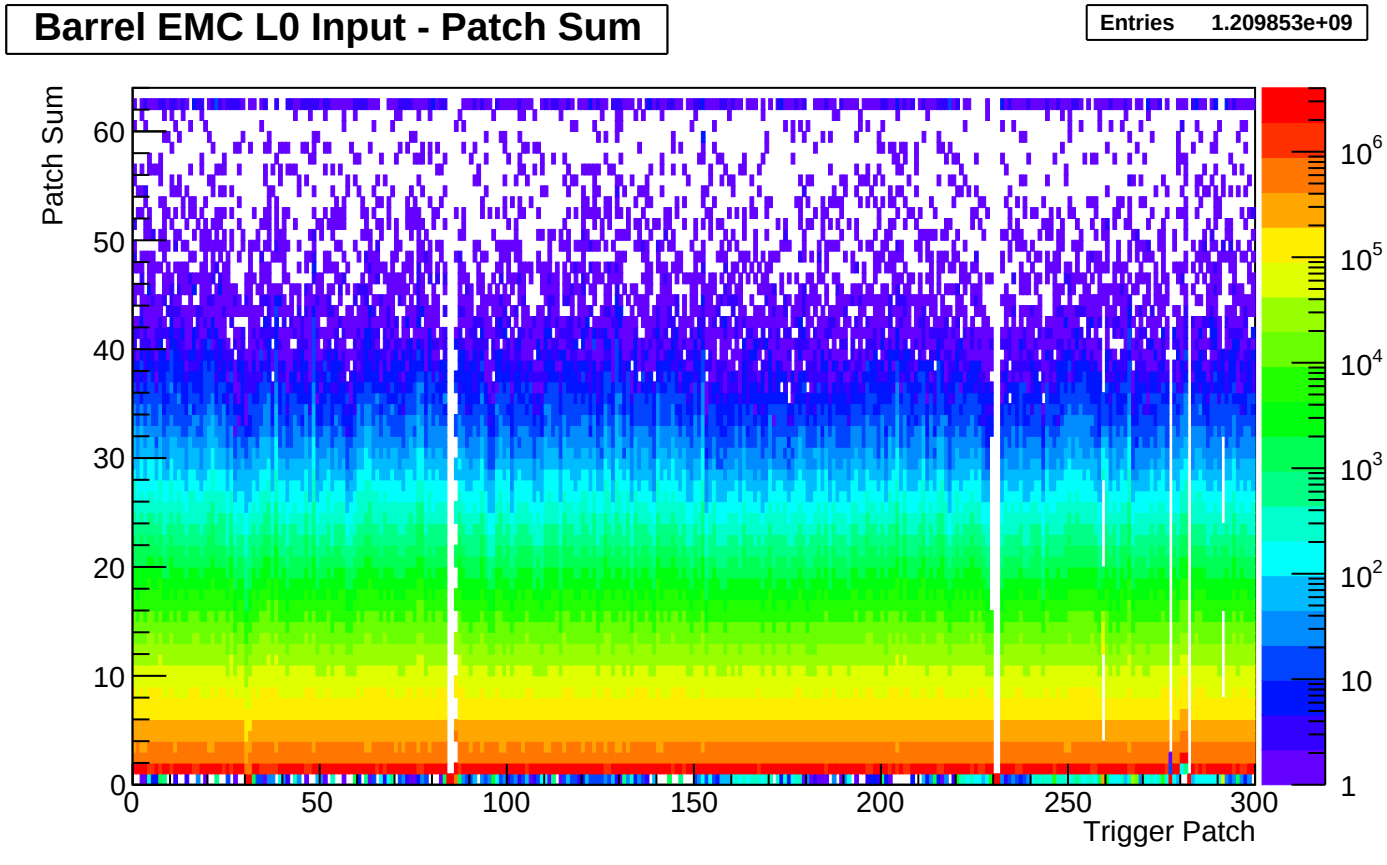
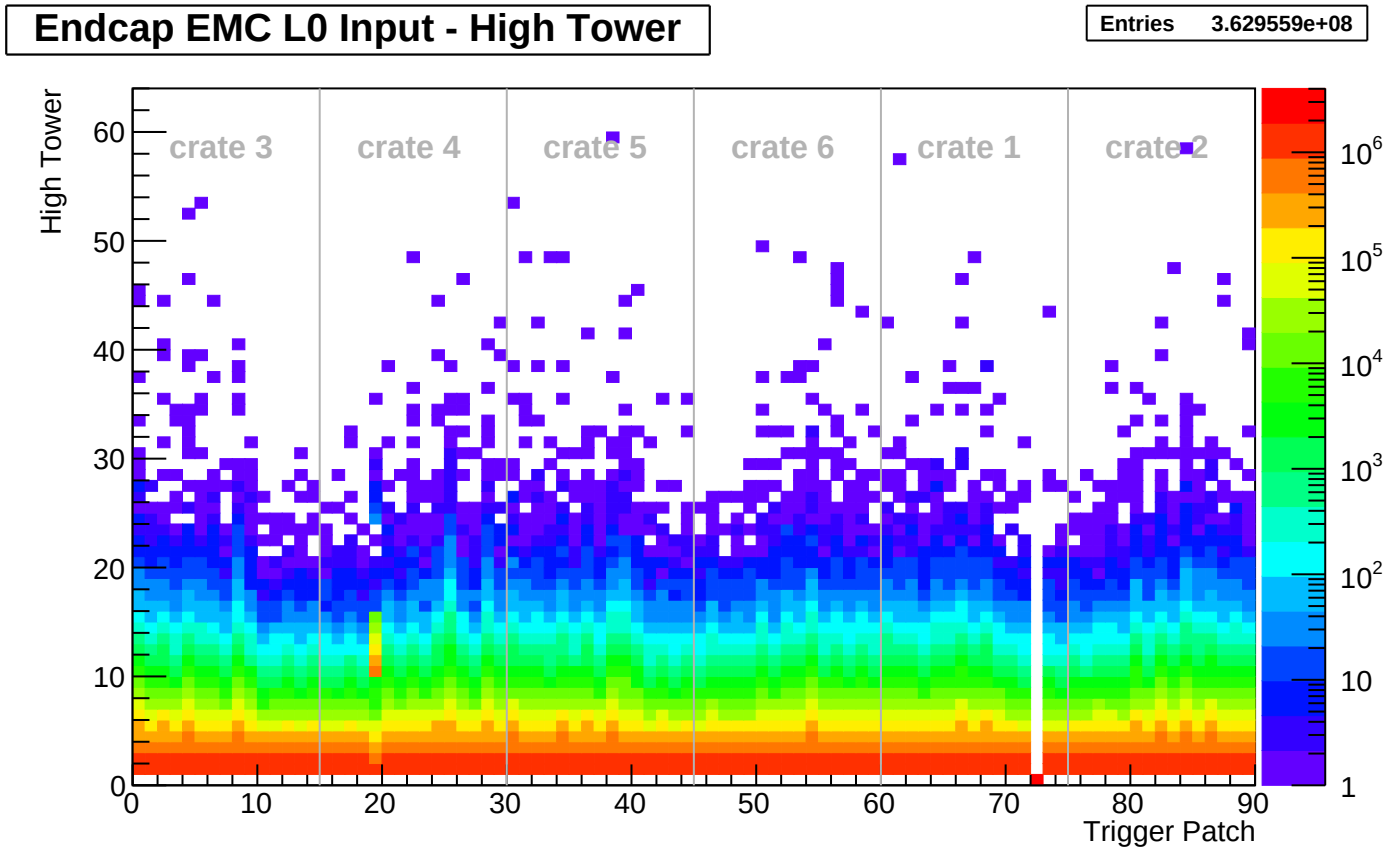
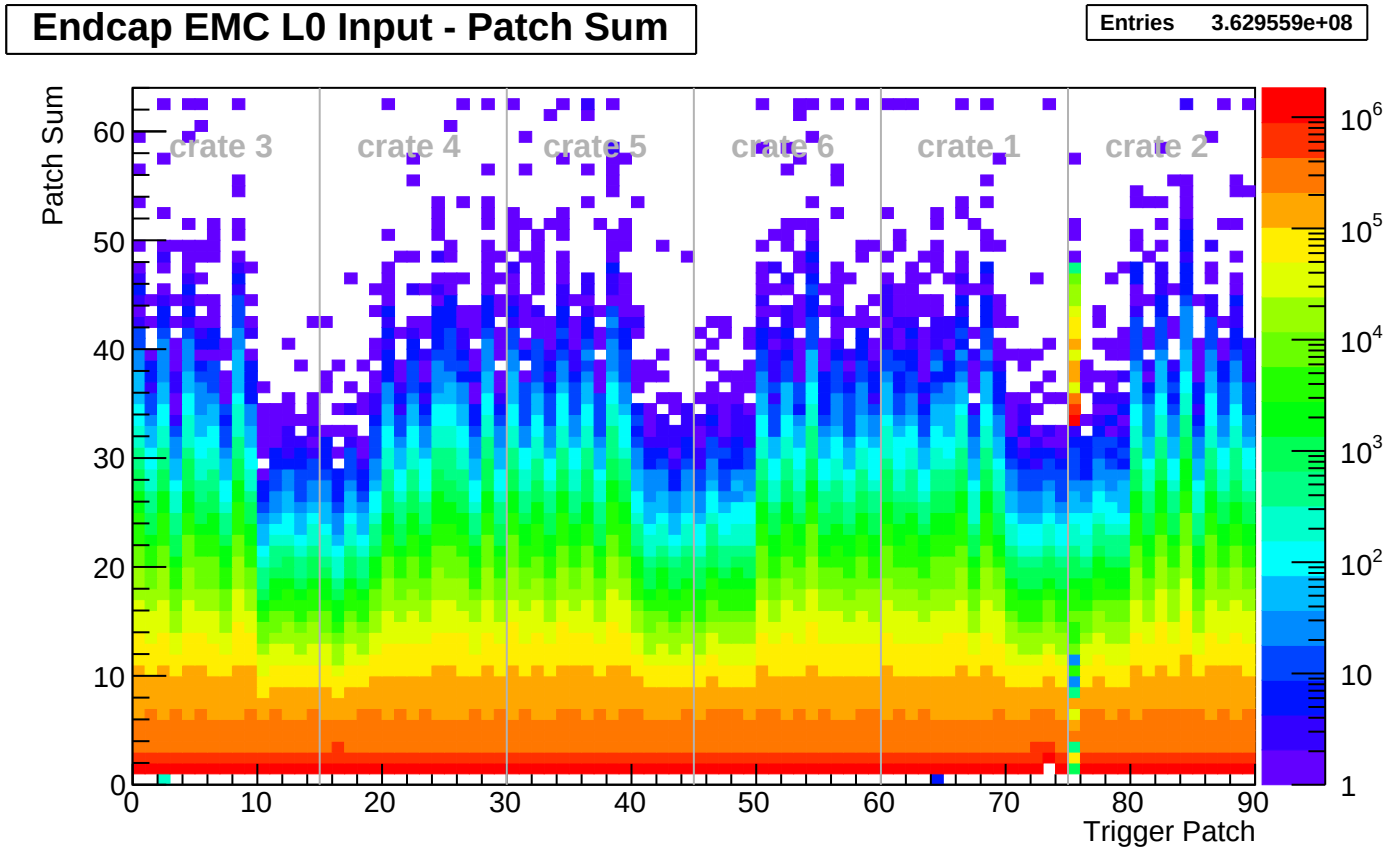
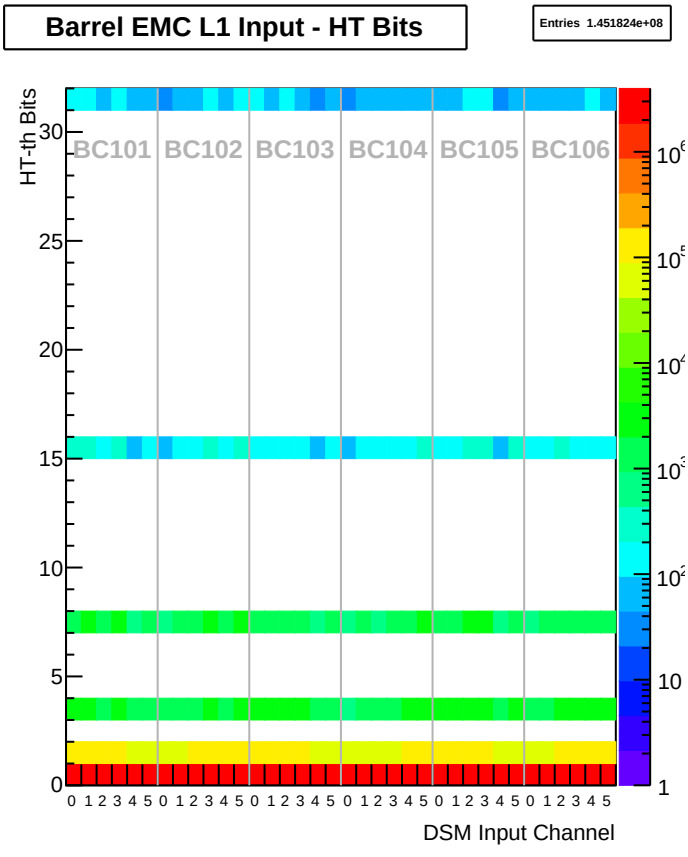
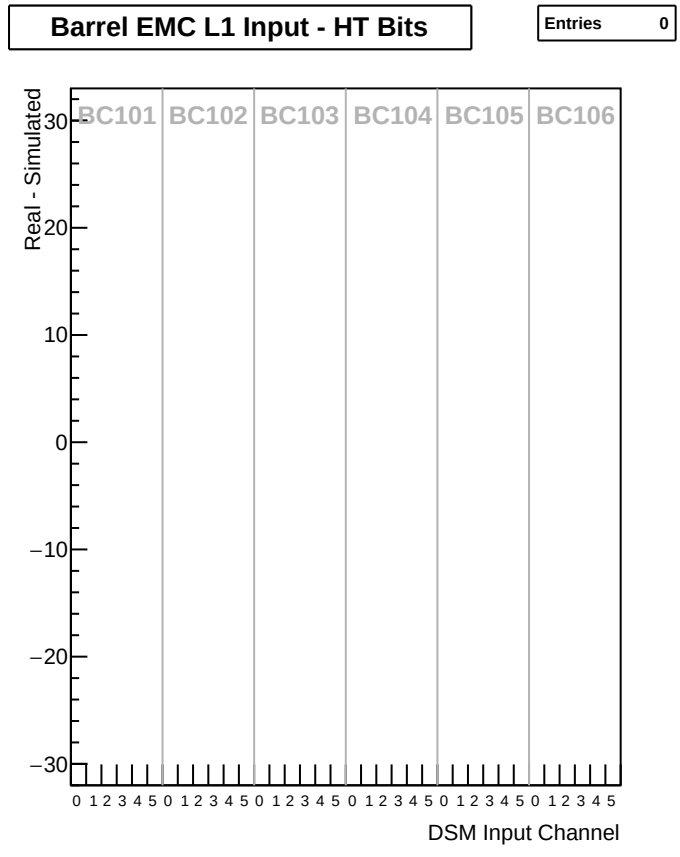

Barrel EMC L0 Input - High Tower
Entries 1.209853e+09
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

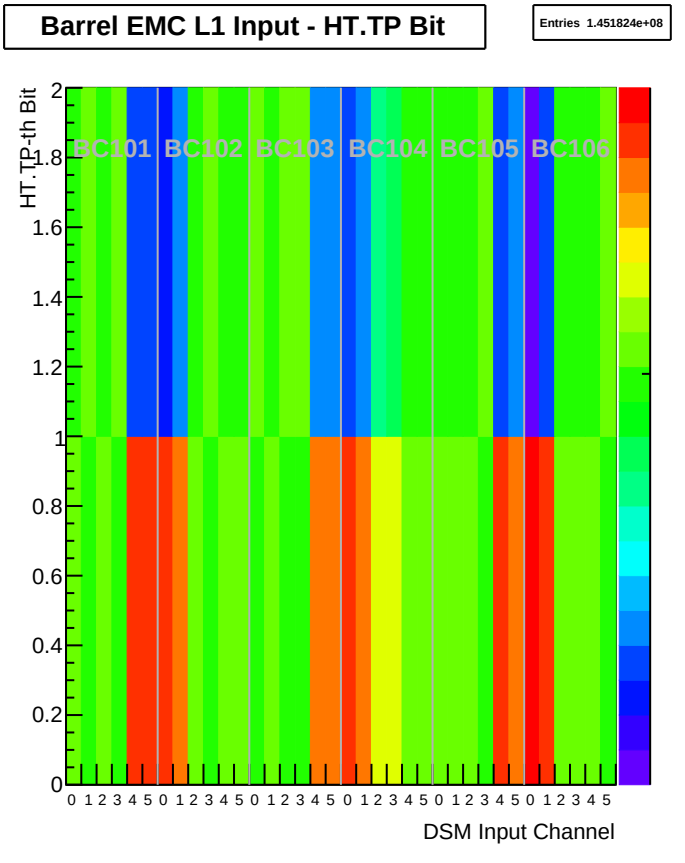

Barrel EMC L0 Input - Patch Sum
Entries 1.209853e+09
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

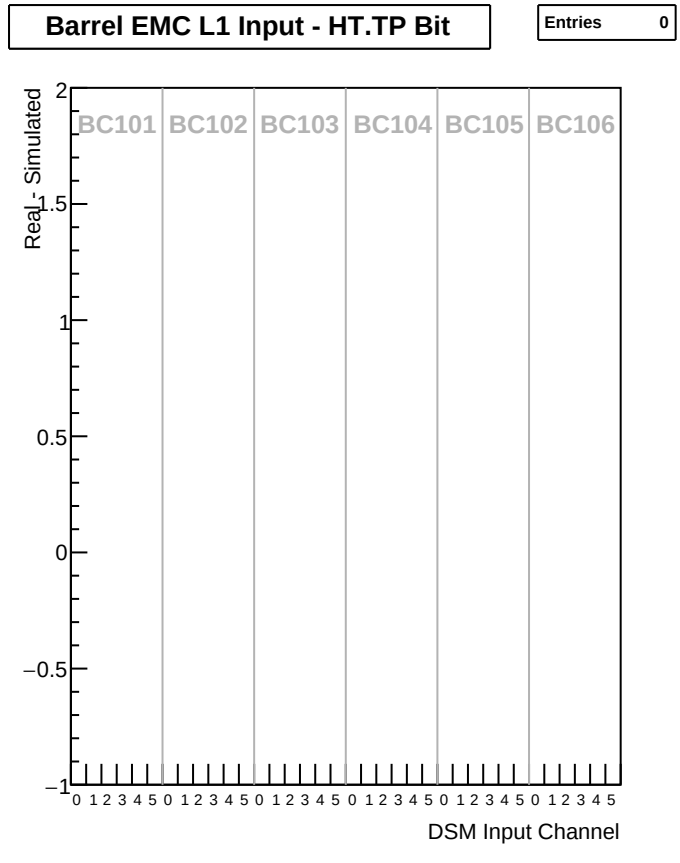

Endcap EMC L0 Input - High Tower
Entries 3.629559e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

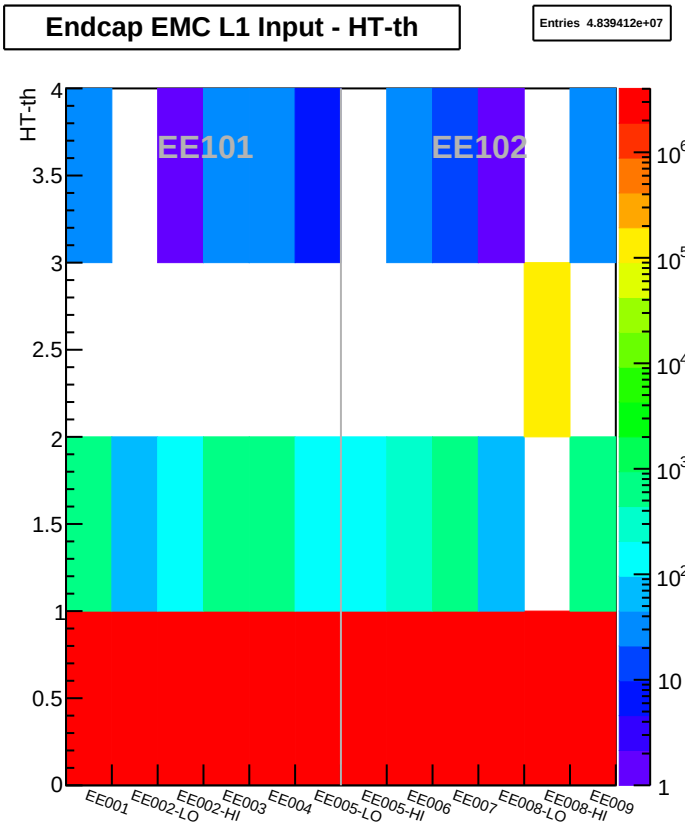

Endcap EMC L0 Input - Patch Sum
Entries 3.629559e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

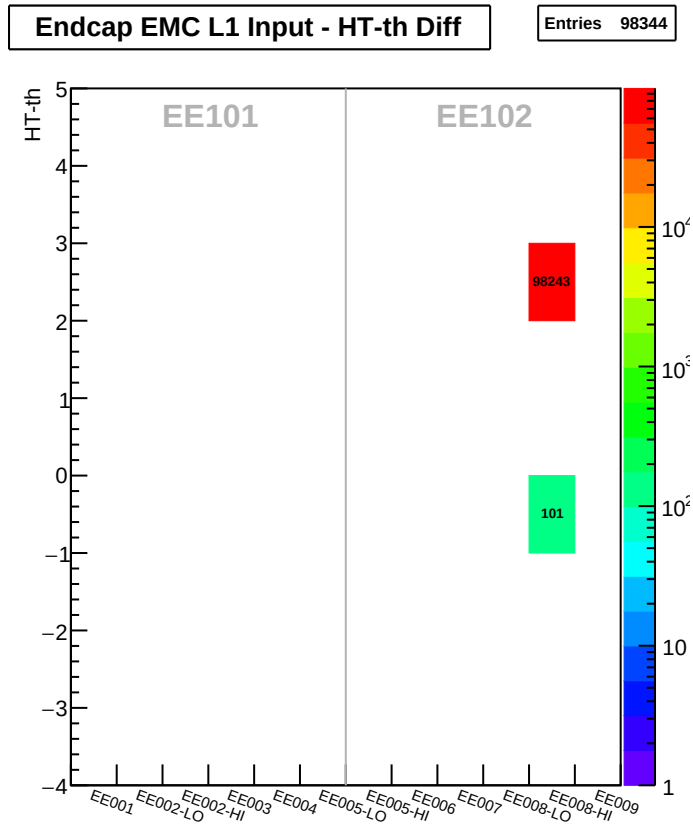

Barrel EMC L1 Input - HT Bits
Entries 1.451824e+08
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

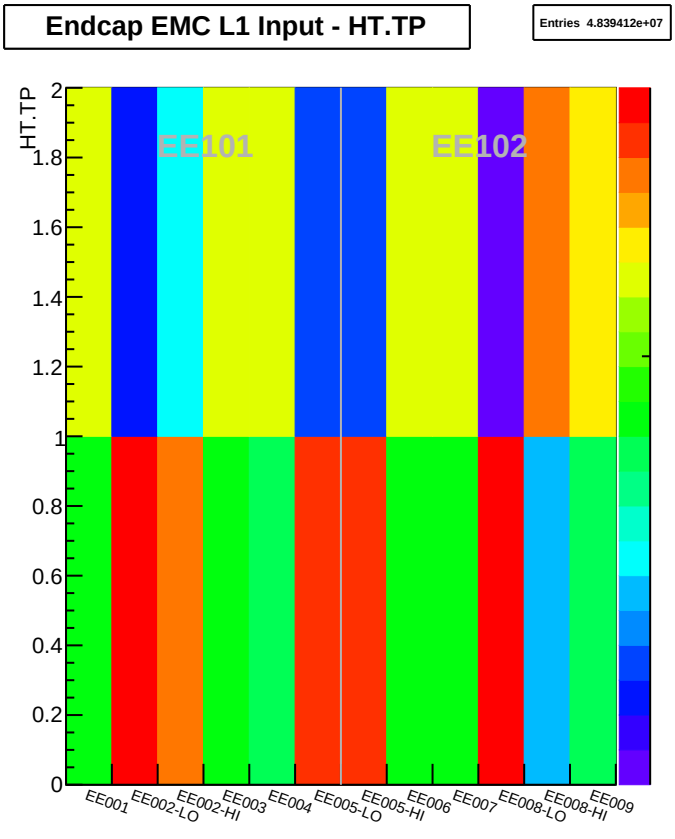

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

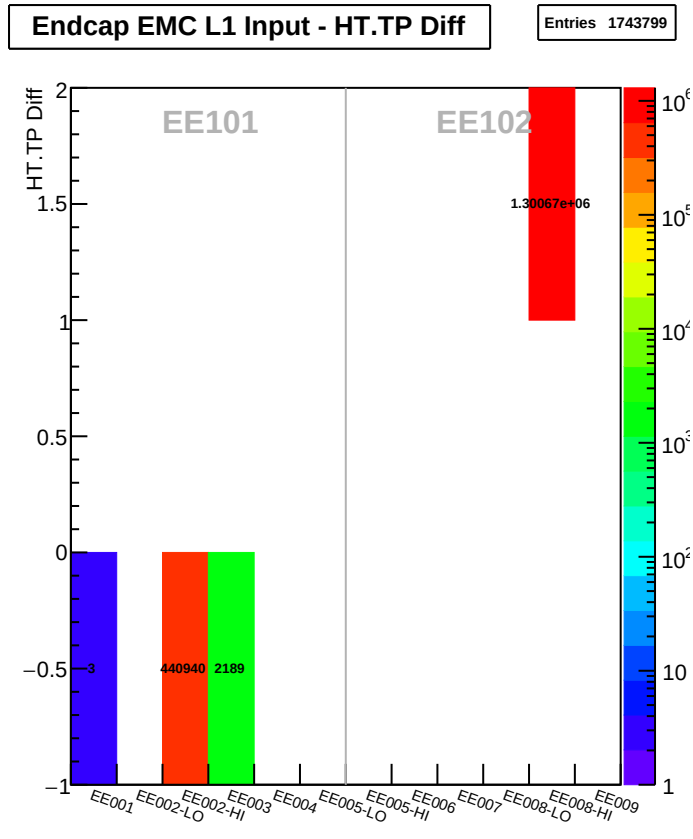

Barrel EMC L1 Input - HT.TP Bit
Entries 1.451824e+08
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

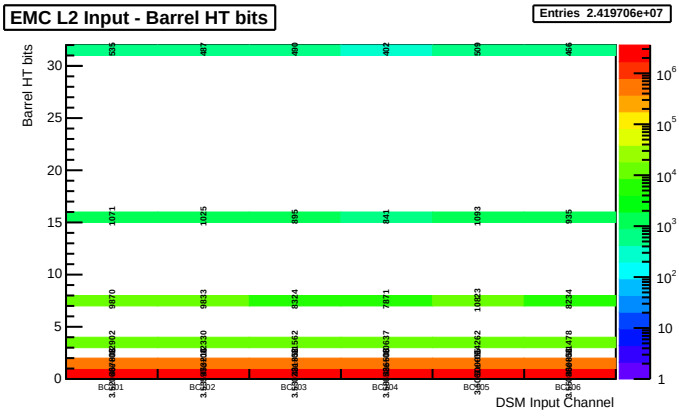

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

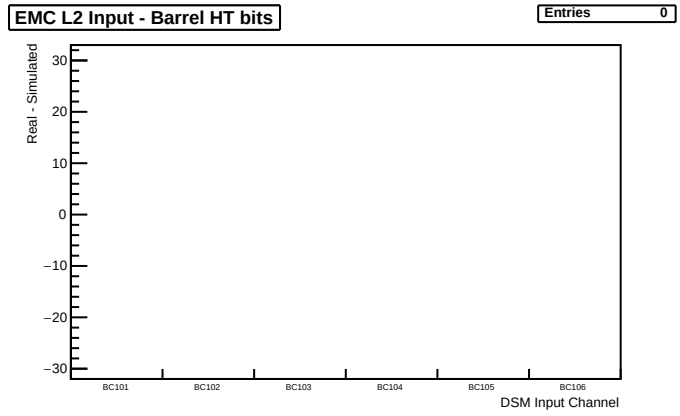

Endcap EMC L1 Input - HT-th
Entries 4.839412e+07
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

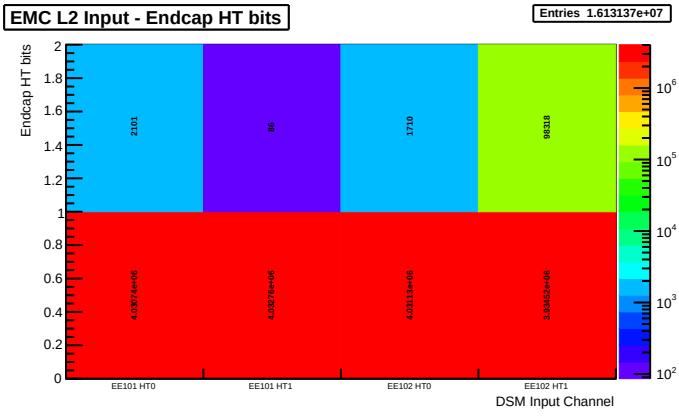

Endcap EMC L1 Input - HT-th Diff
Entries 98344
HT-th
EE101
EE102
98243
101
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

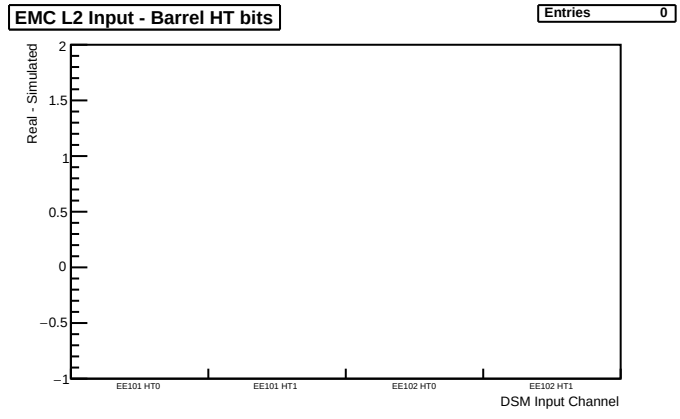

Endcap EMC L1 Input - HT.TP
Entries 4.839412e+07
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

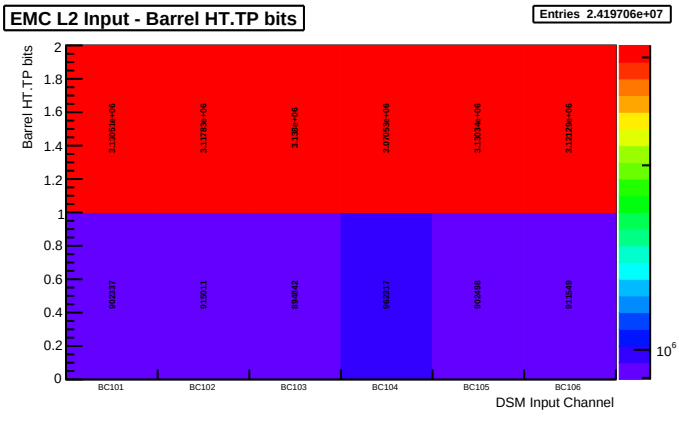

Endcap EMC L1 Input - HT.TP Diff
Entries 1743799
HT.TP Diff
EE101
EE102
1.30067e+06
3
440940
2189
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

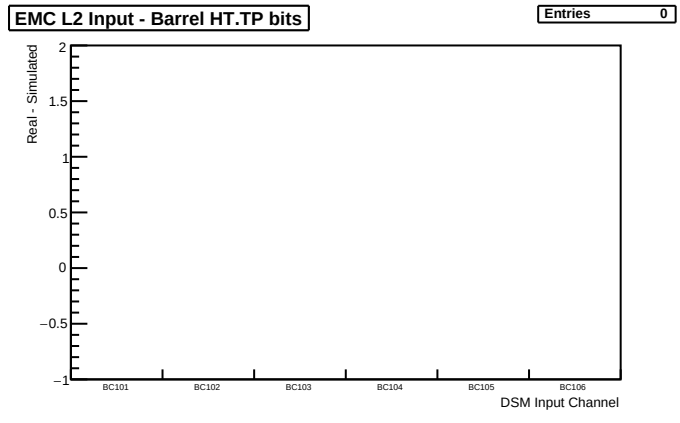

EMC L2 Input - Barrel HT bits
Entries 2.419706e+07
Barrel HT bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

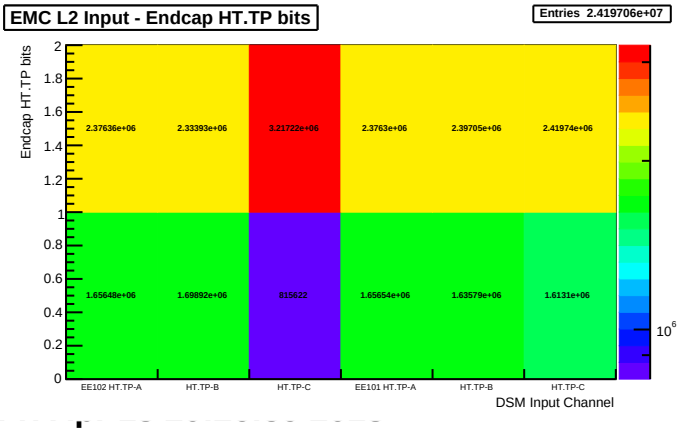

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

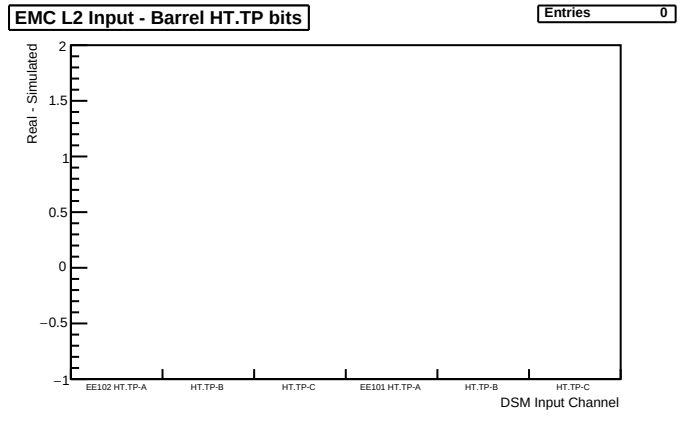

EMC L2 Input - Endcap HT bits
Entries 1.613137e+07
Endcap HT bits
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

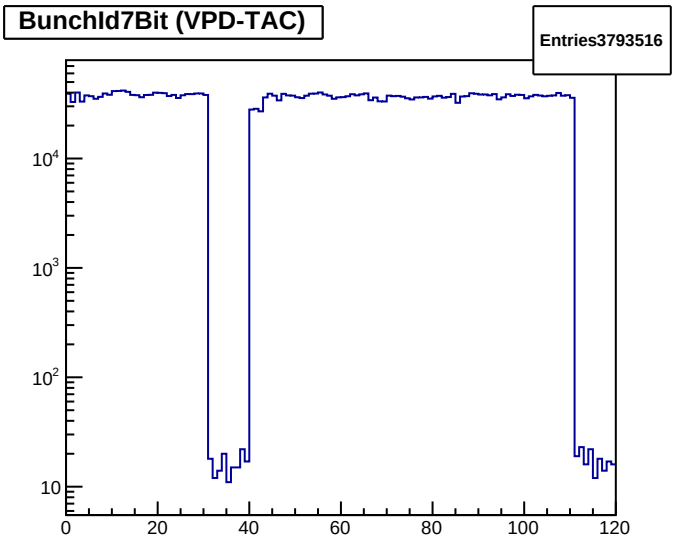

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel
EE101 HT0
EE101 HT1
EE102 HT0
EE102 HT1

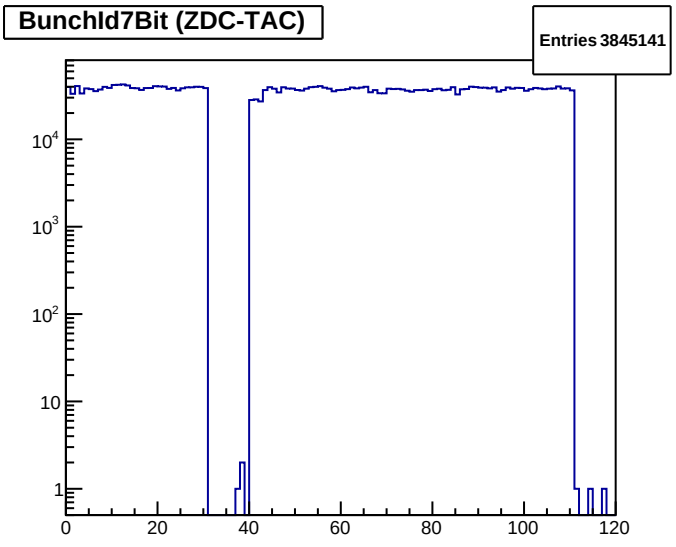

EMC L2 Input - Barrel HT.TP bits
Entries 2.419706e+07
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

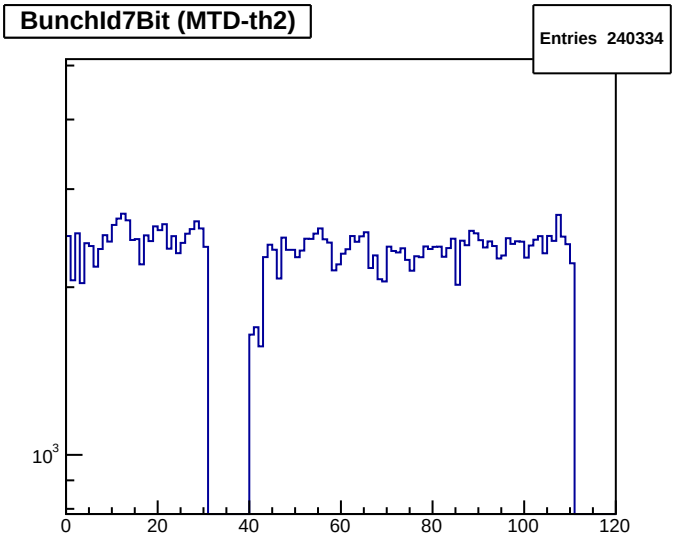

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

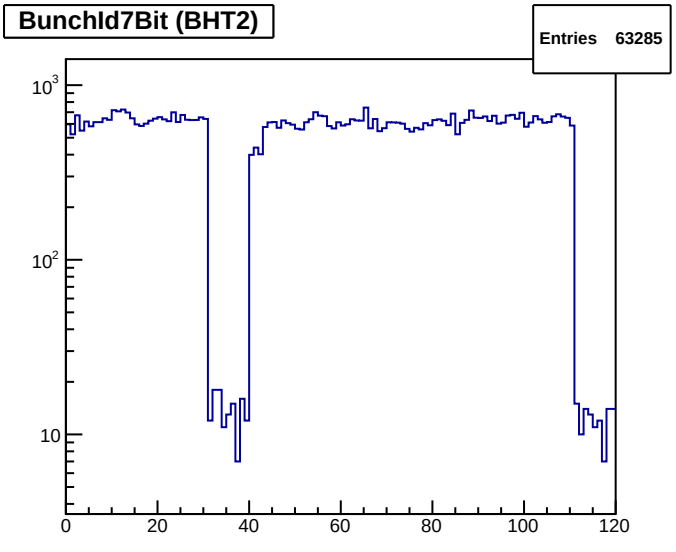

EMC L2 Input - Endcap HT.TP bits
Entries 2.419706e+07
Endcap HT.TP bits
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

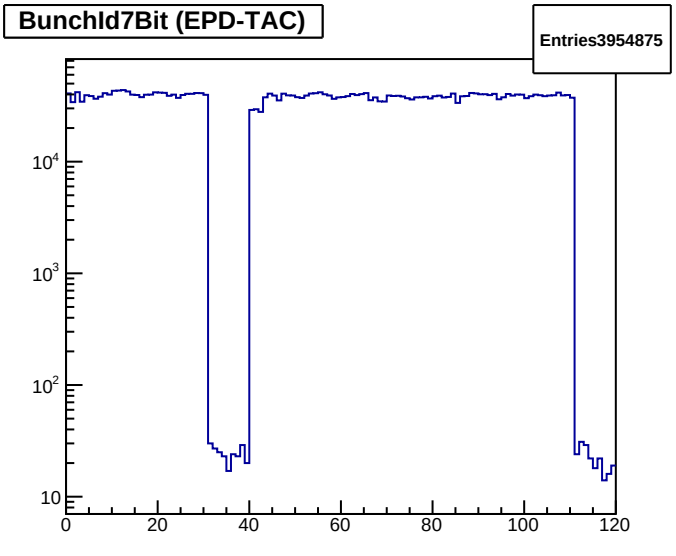

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel
EE102 HT.TP-A
HT.TP-B
HT.TP-C
EE101 HT.TP-A
HT.TP-B
HT.TP-C

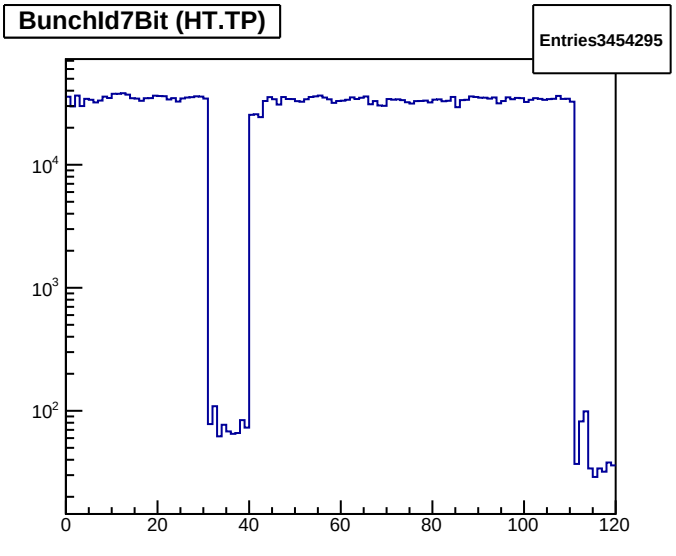

BunchId7Bit (VPD-TAC)
Entries3793516
10^4
10^3
10^2
10
0 20 40 60 80 100 120

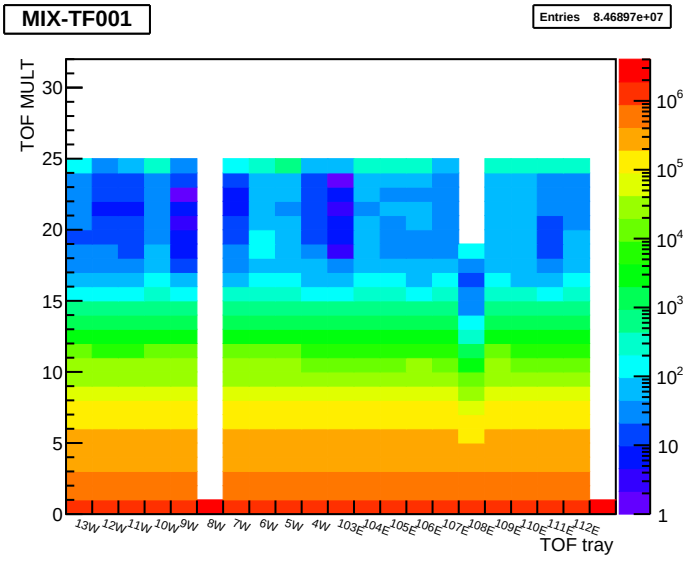

BunchId7Bit (ZDC-TAC)
Entries 3845141
10^4
10^3
10^2
10
1
0 20 40 60 80 100 120

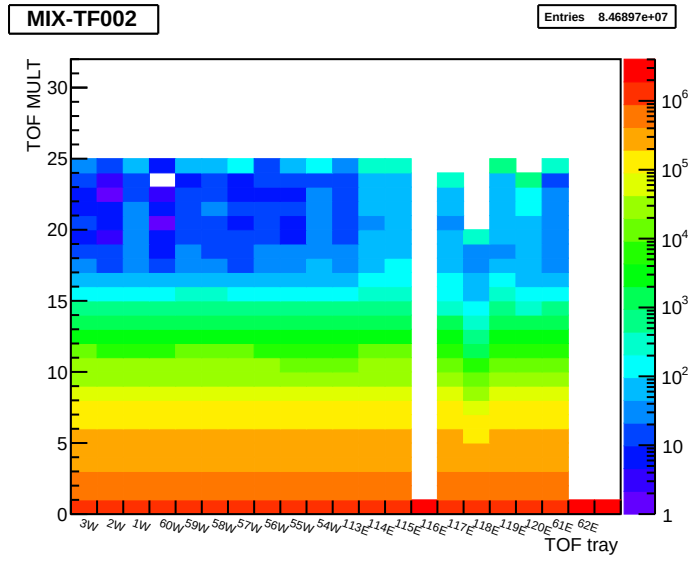

BunchId7Bit (MTD-th2)
Entries 240334
10^3
0 20 40 60 80 100 120

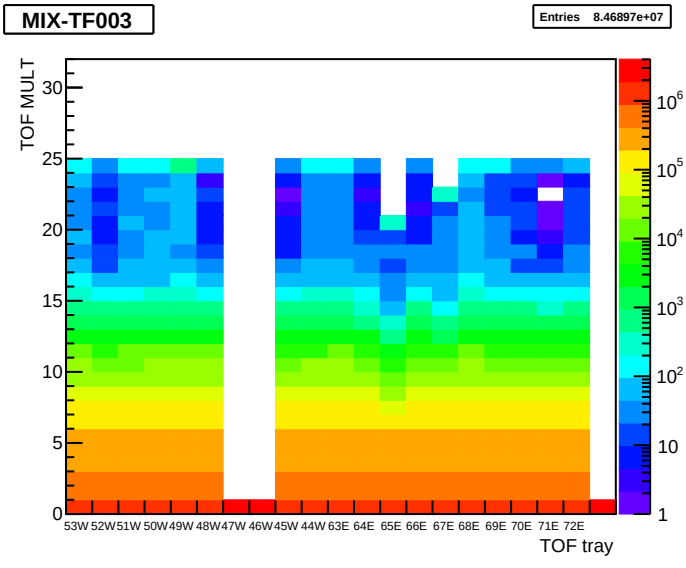

BunchId7Bit (BHT2)
Entries 63285
10^3
10^2
10
0 20 40 60 80 100 120

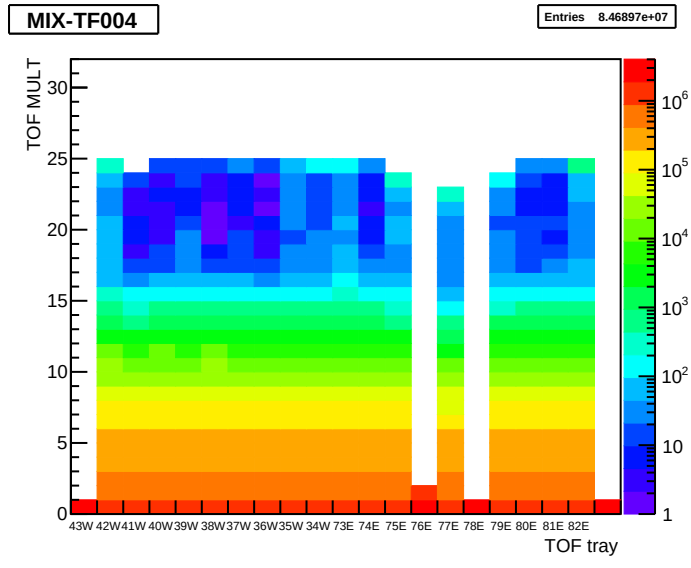

BunchId7Bit (EPD-TAC)
Entries3954875
10^4
10^3
10^2
10
0 20 40 60 80 100 120

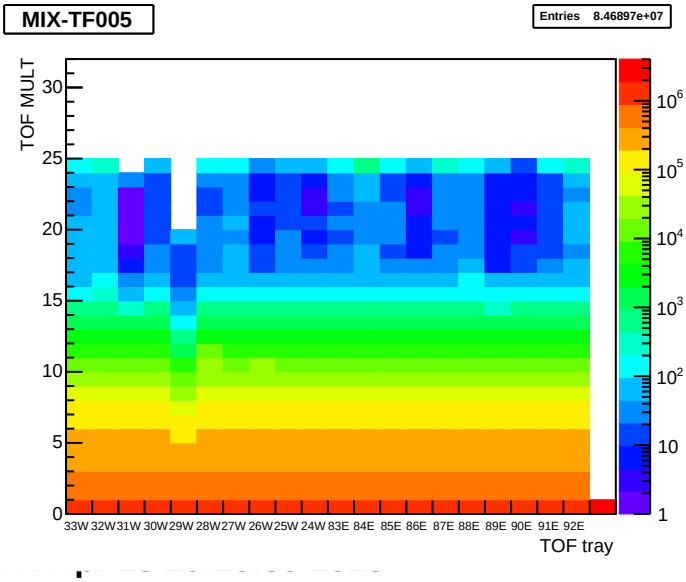

BunchId7Bit (HT.TP)
Entries3454295
10^4
10^3
10^2
0 20 40 60 80 100 120

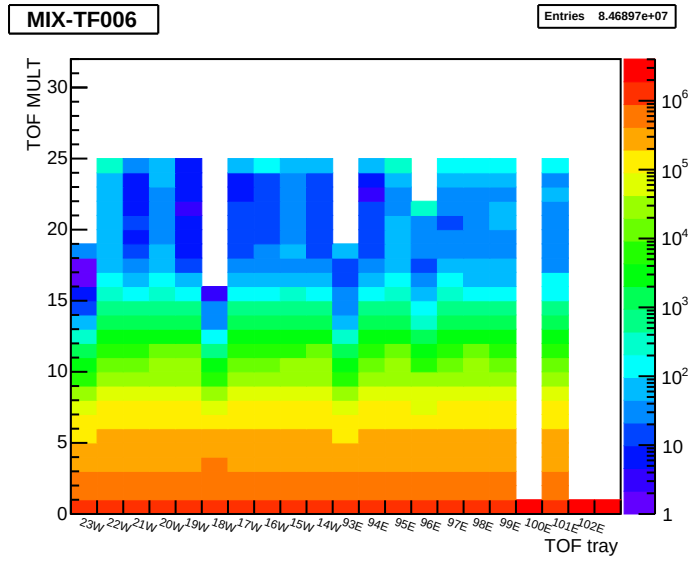

MIX-TF001
Entries 8.46897e+07
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

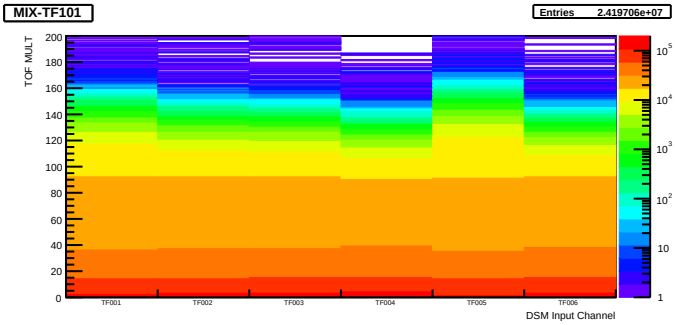

MIX-TF002
Entries 8.46897e+07
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

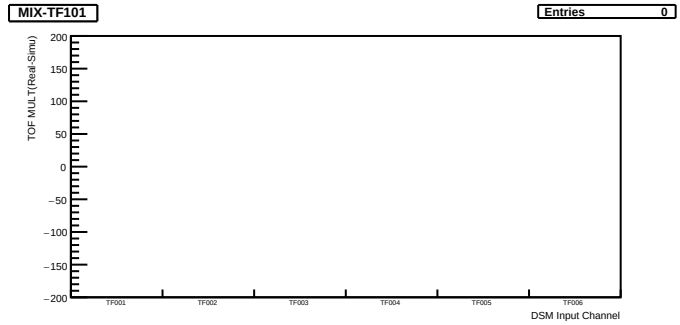

MIX-TF003
Entries 8.46897e+07
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

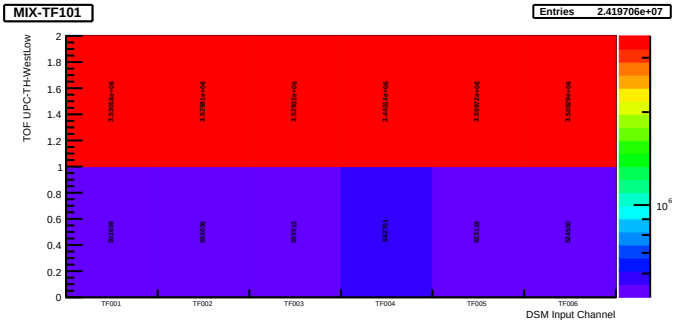

MIX-TF004
Entries 8.46897e+07
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

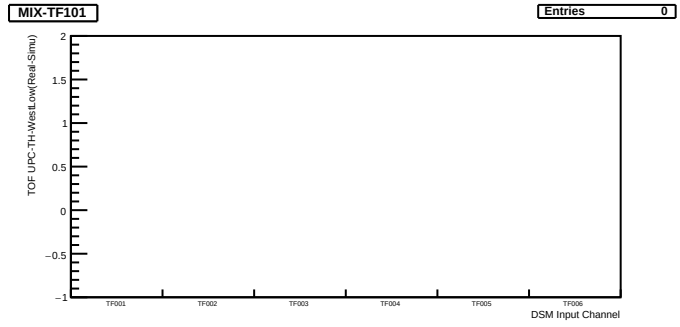

MIX-TF005
Entries 8.46897e+07
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

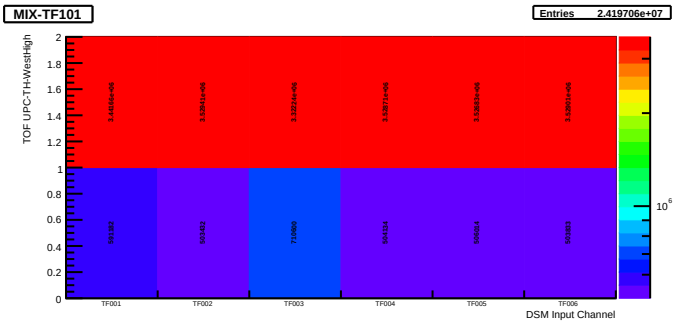

MIX-TF006
Entries 8.46897e+07
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

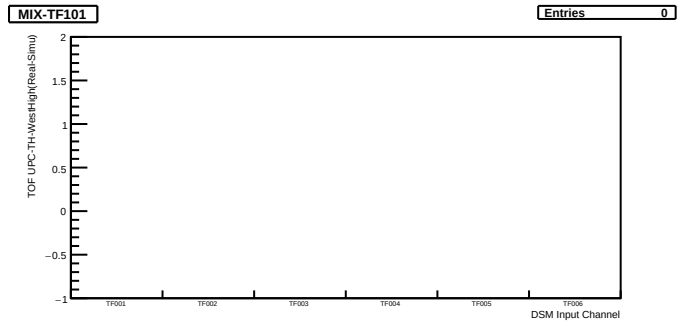

MIX-TF101
Entries 2.419706e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

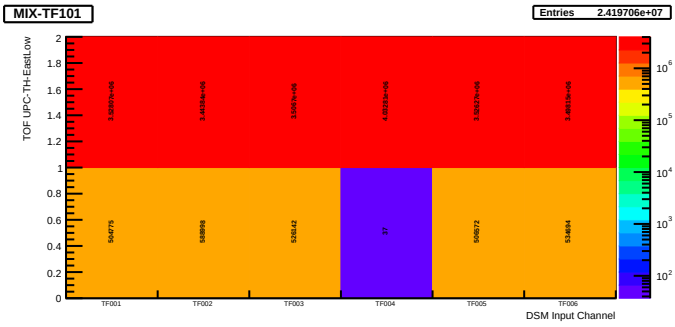

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

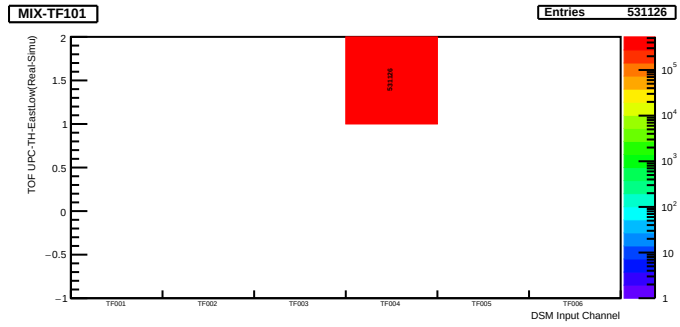

MIX-TF101
Entries 2.419706e+07
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

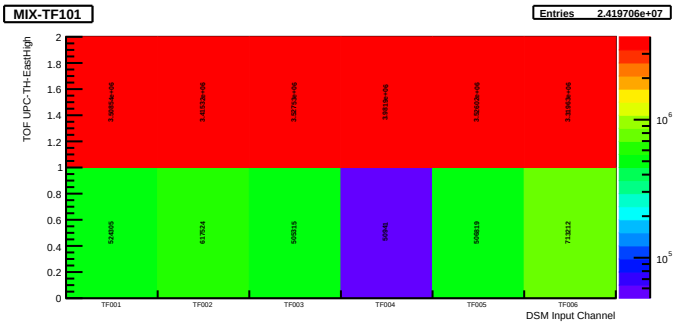

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

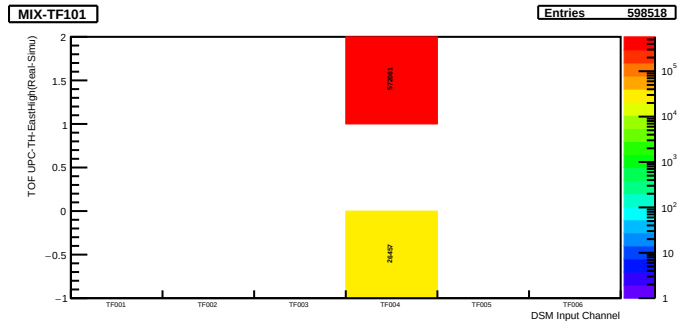

MIX-TF101
Entries 2.419706e+07
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

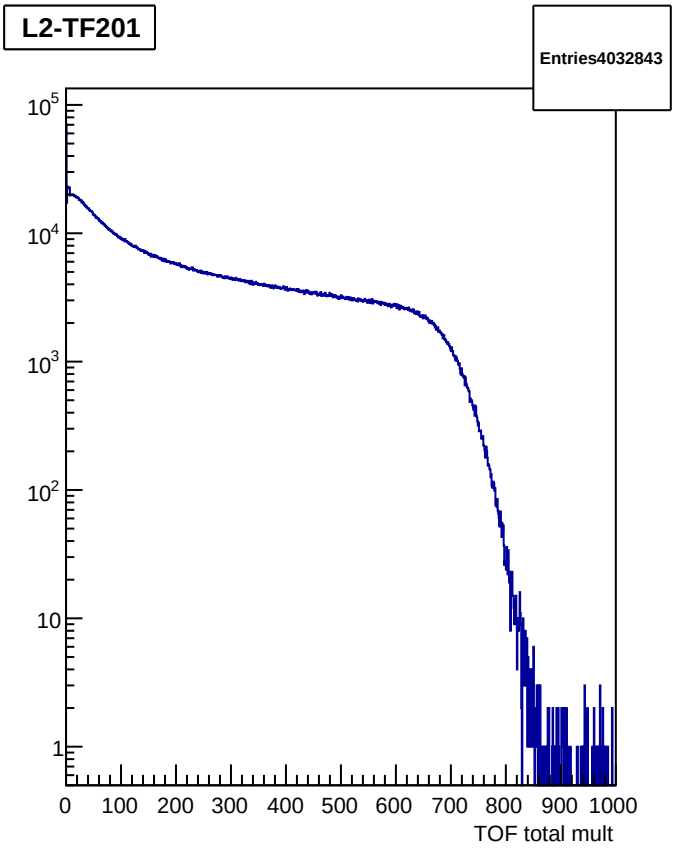

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

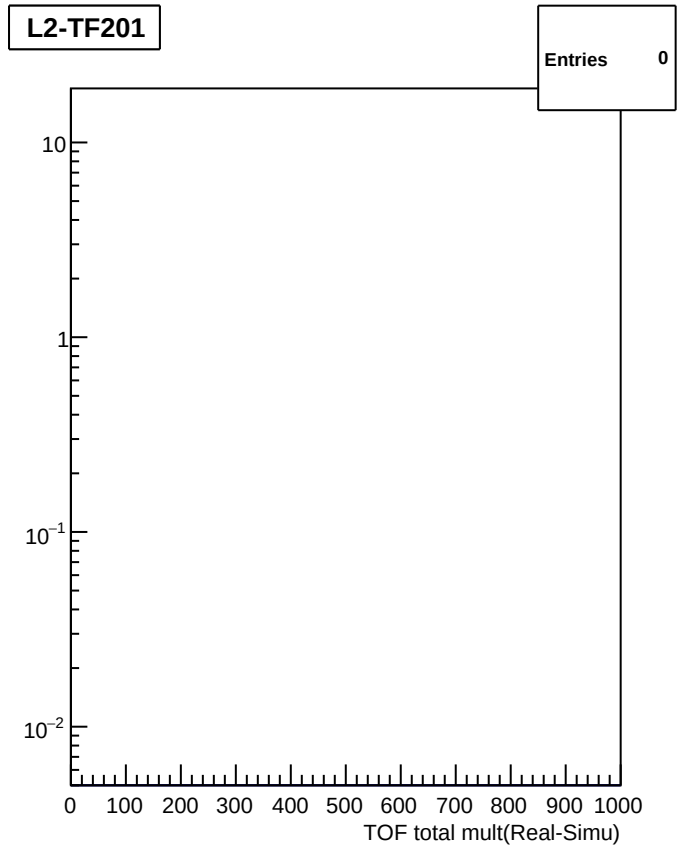

MIX-TF101
Entries 2.419706e+07
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

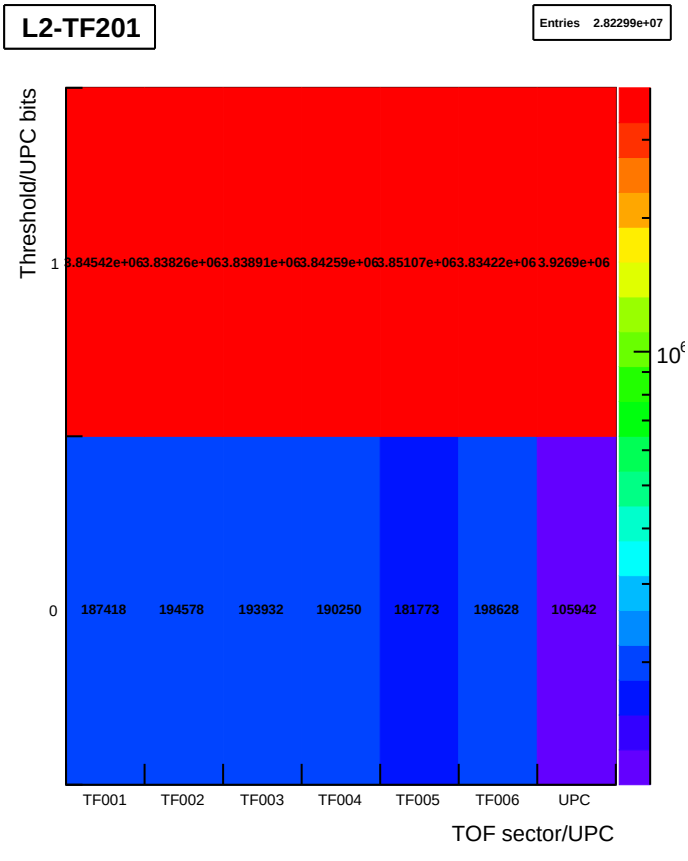

MIX-TF101
Entries 531126
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

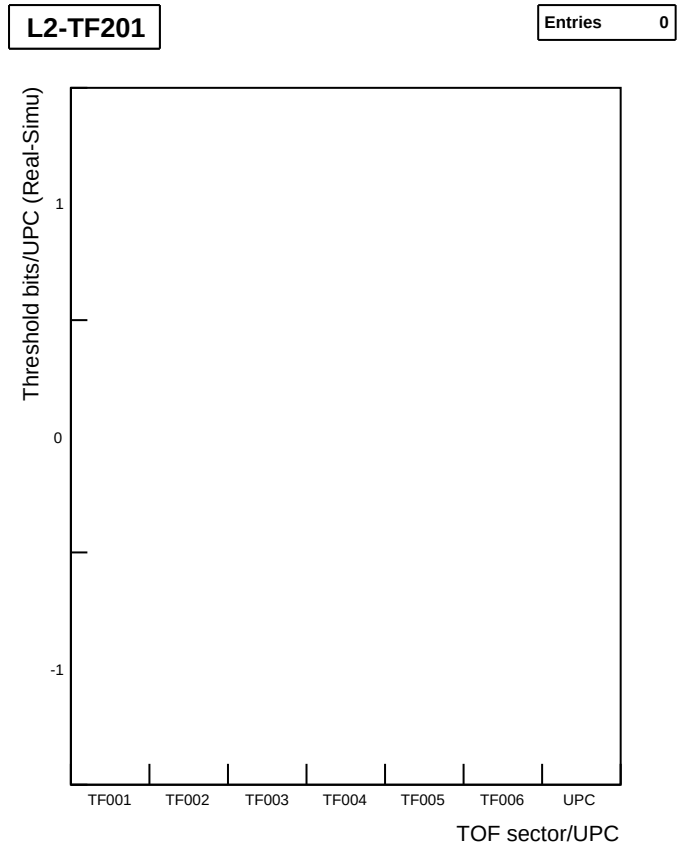

MIX-TF101
Entries 2.419706e+07
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

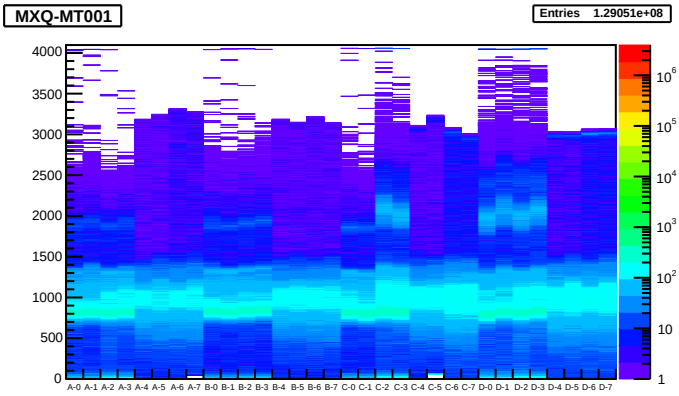

MIX-TF101
Entries 598518
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

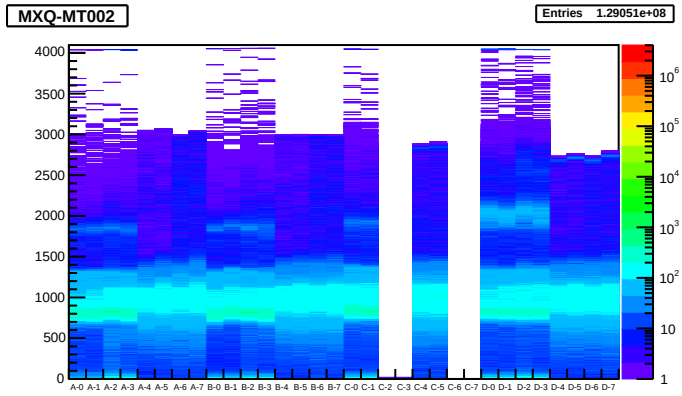

L2-TF201
Entries4032843
TOF total mult

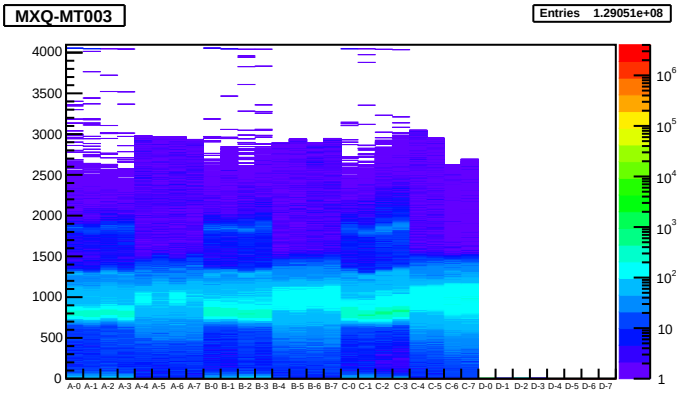

L2-TF201
Entries 0
TOF total mult(Real-Simu)

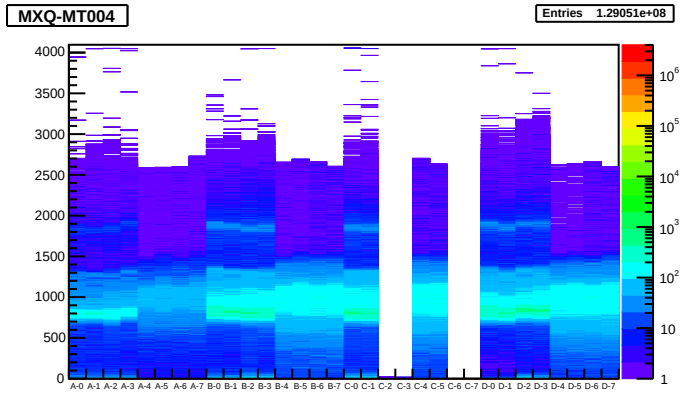

L2-TF201
Entries 2.82299e+07
Threshold/UPC bits
187418
194578
193932
190250
181773
198628
105942
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

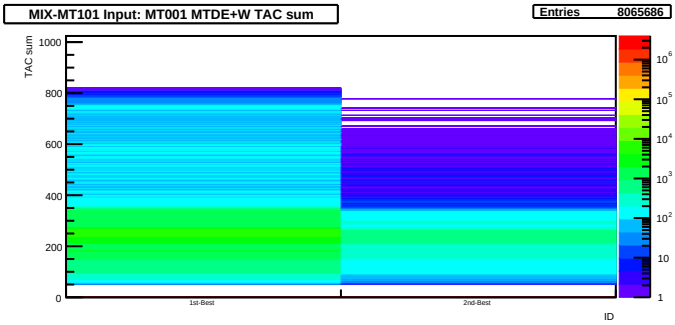

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

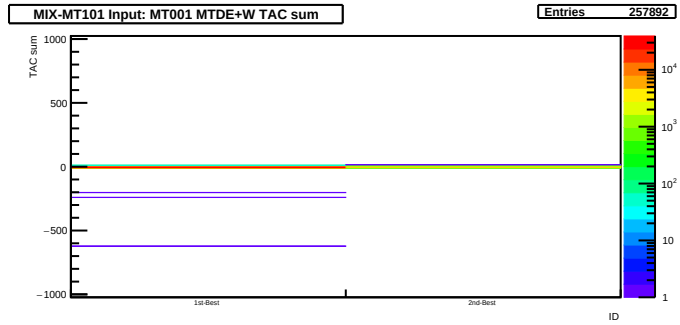

MXQ-MT001
Entries 1.29051e+08

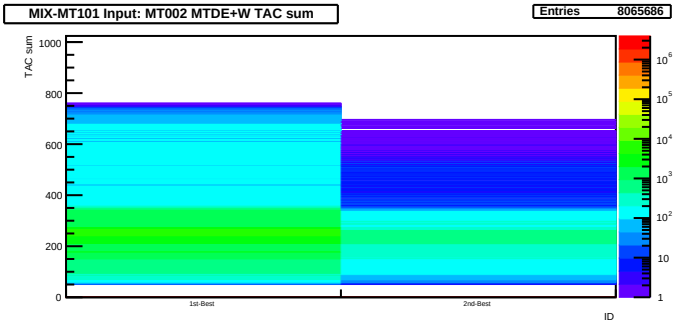

MXQ-MT002
Entries 1.29051e+08

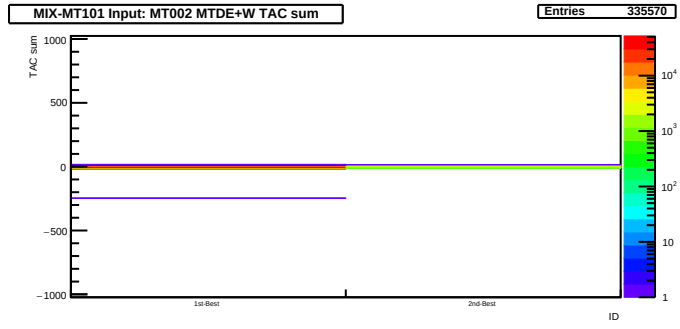

MXQ-MT003
Entries 1.29051e+08

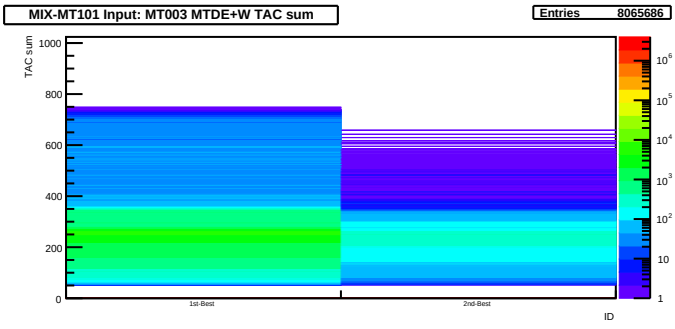

MXQ-MT004
Entries 1.29051e+08

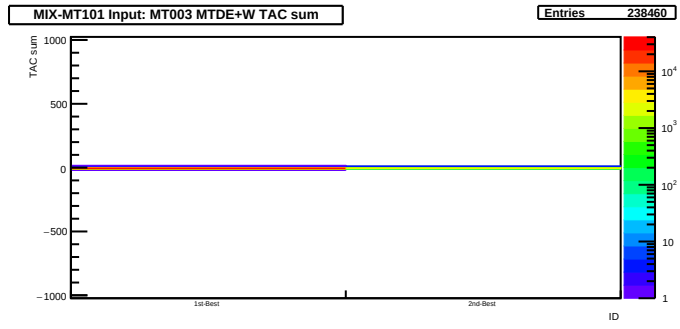

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 8065686
TAC sum
1st-Best
2nd-Best
ID

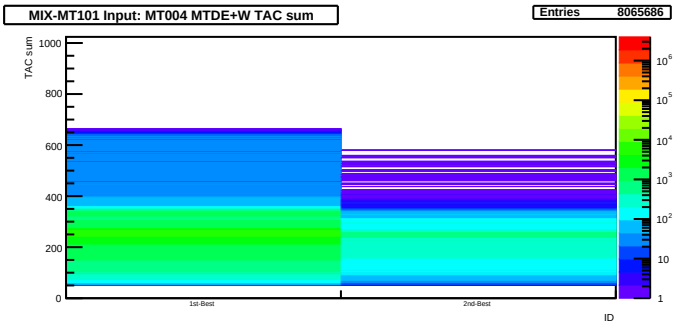

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 257892
TAC sum
1st-Best
2nd-Best
ID

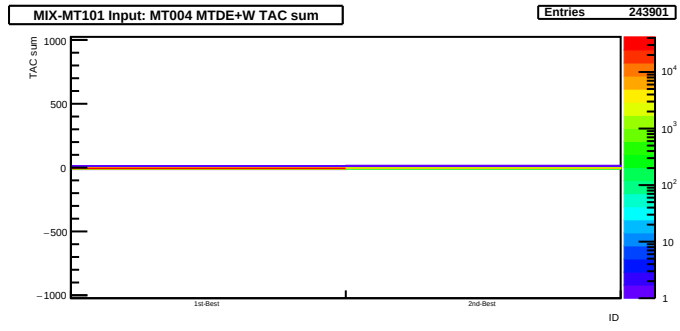

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 8065686
TAC sum
1st-Best
2nd-Best
ID

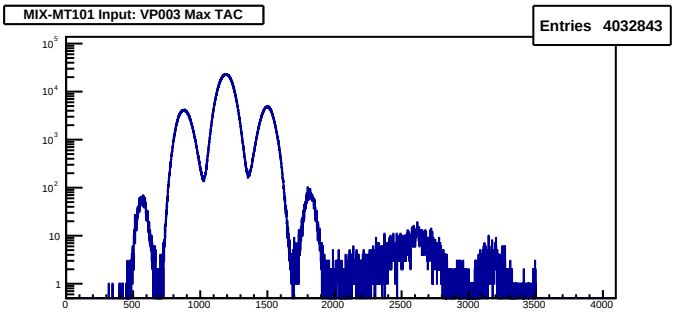

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 335570
TAC sum
1st-Best
2nd-Best
ID

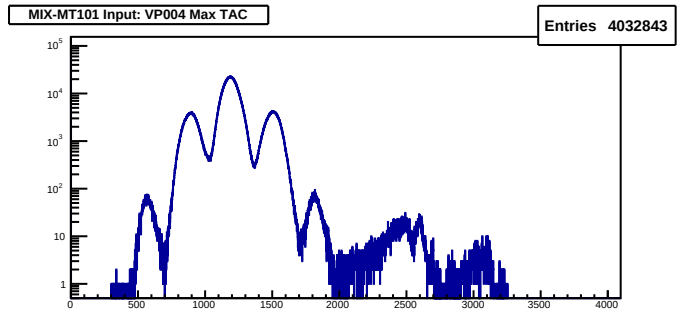

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 8065686
TAC sum
1st-Best
2nd-Best
ID

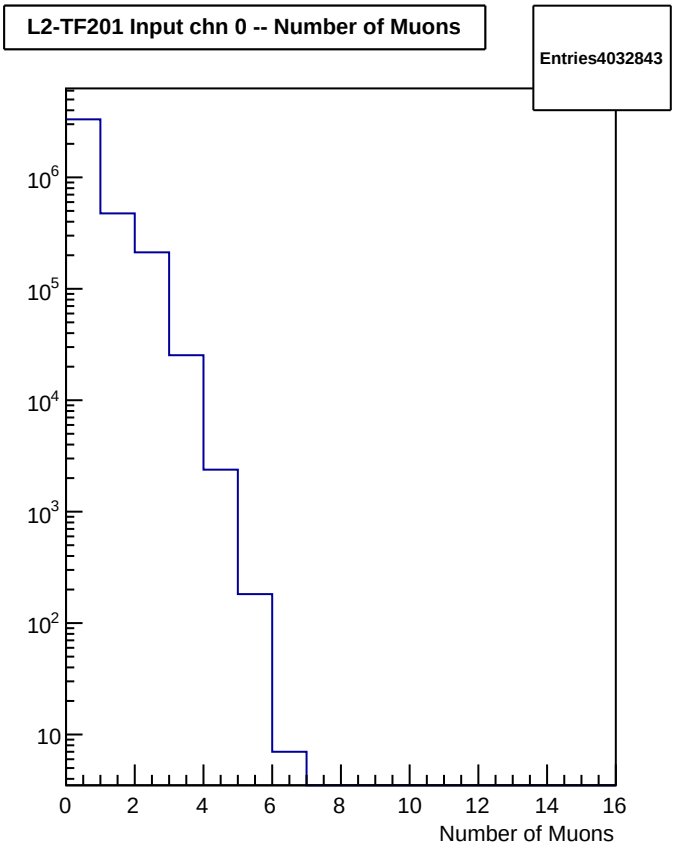

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 238460
TAC sum
1st-Best
2nd-Best
ID

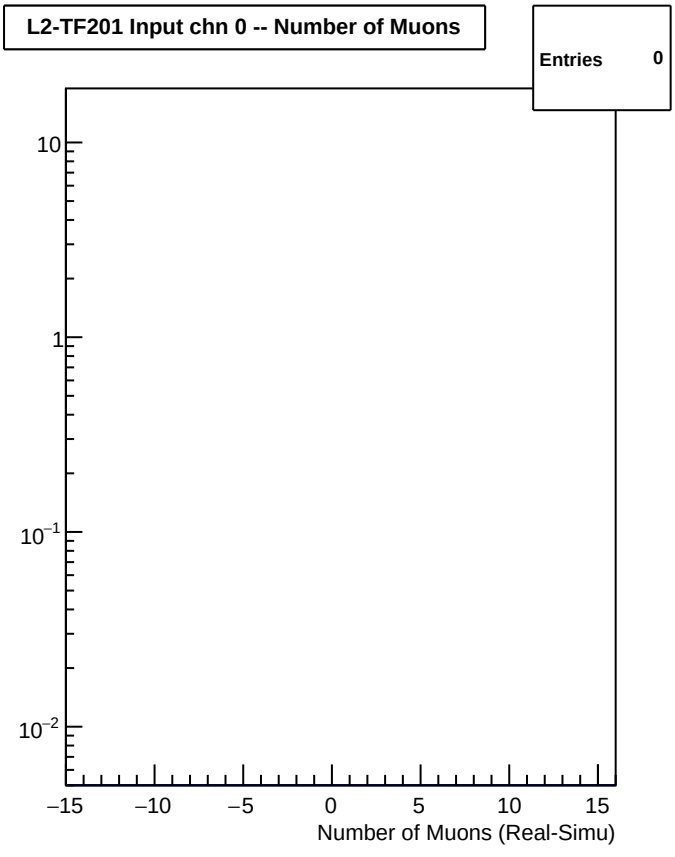

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 8065686
TAC sum
1st-Best
2nd-Best
ID

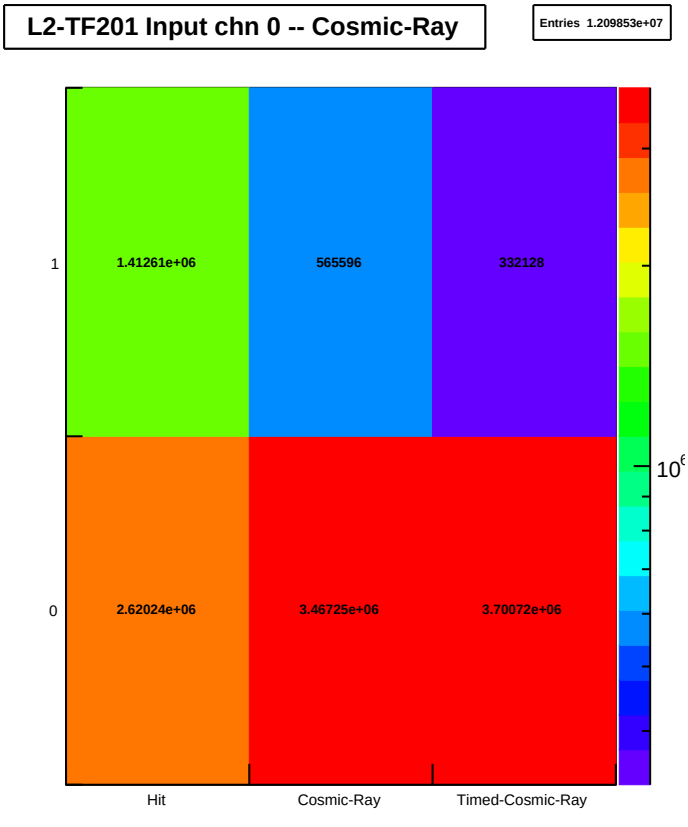

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 243901
TAC sum
1st-Best
2nd-Best
ID

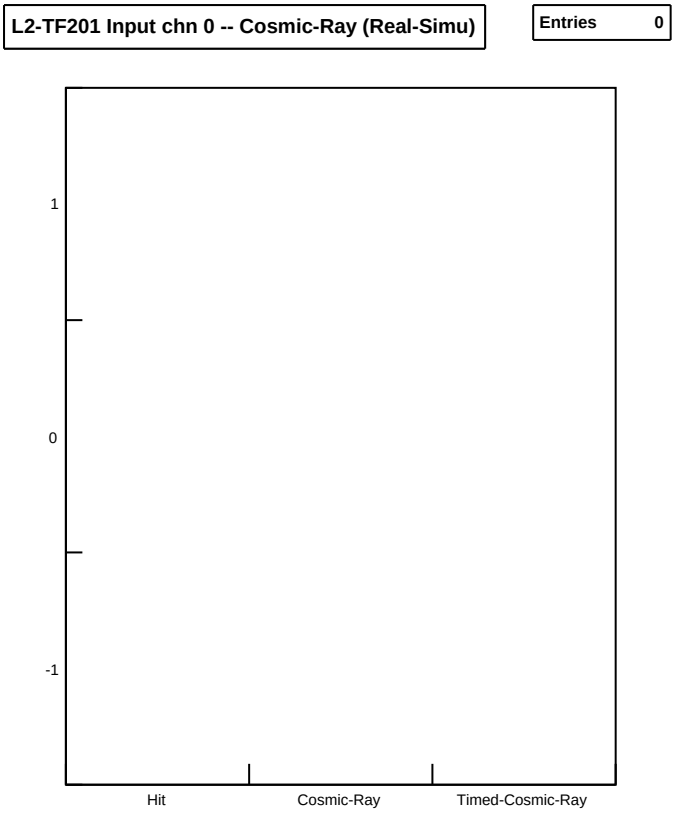

MIX-MT101 Input: VP003 Max TAC
Entries 4032843

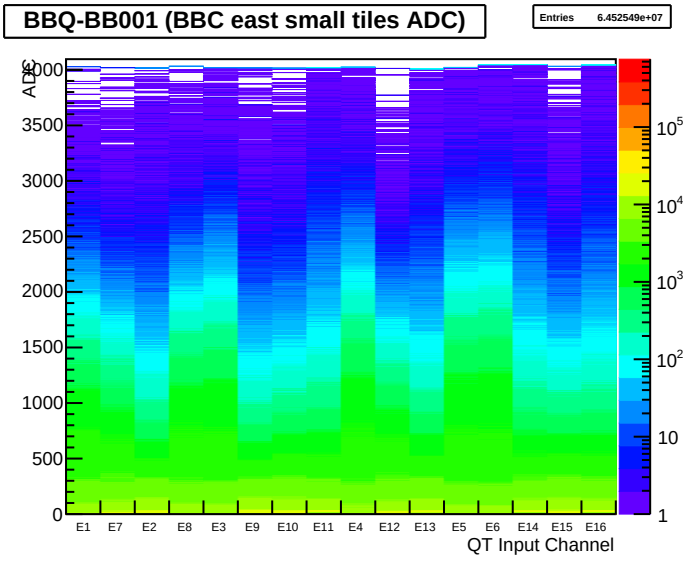

MIX-MT101 Input: VP004 Max TAC
Entries 4032843

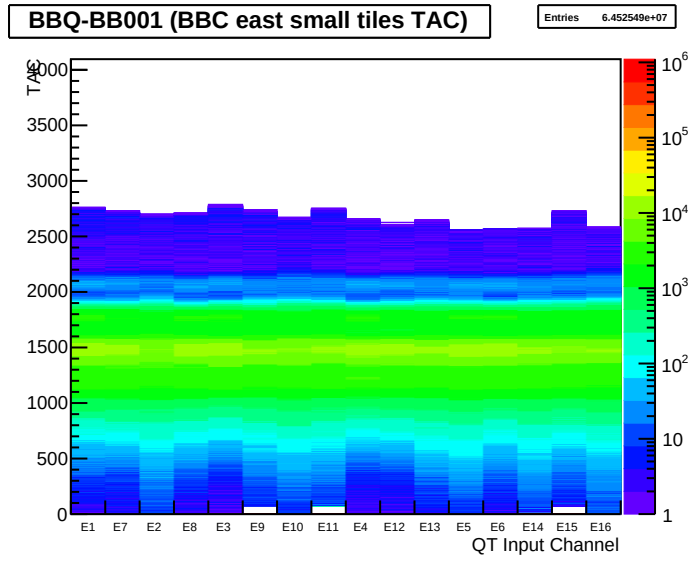

L2-TF201 Input chn 0 -- Number of Muons
Entries4032843
Number of Muons

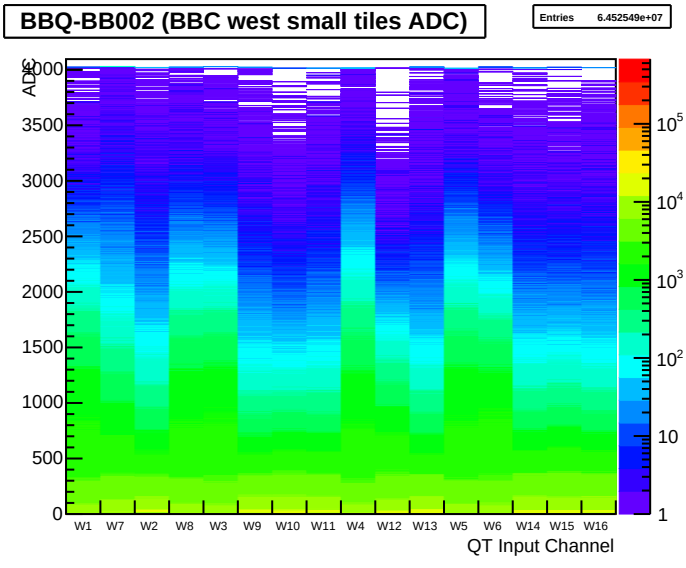

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

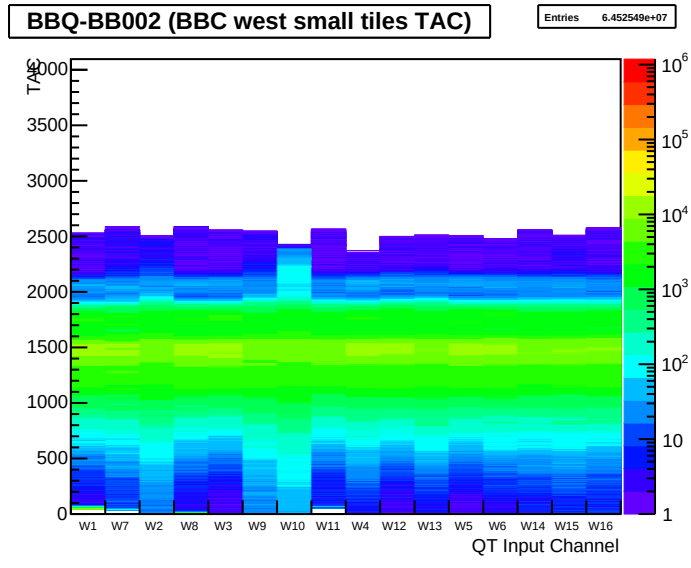

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 1.209853e+07
1.41261e+06
565596
332128
2.62024e+06
3.46725e+06
3.70072e+06
Hit
Cosmic-Ray
Timed-Cosmic-Ray

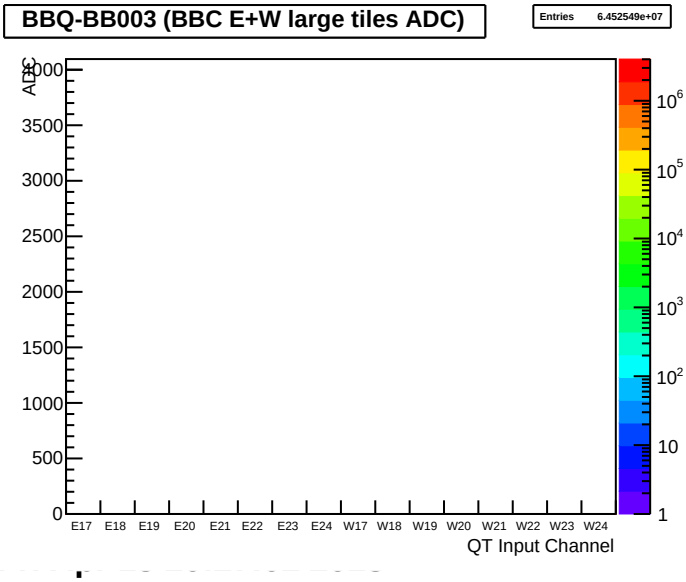

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

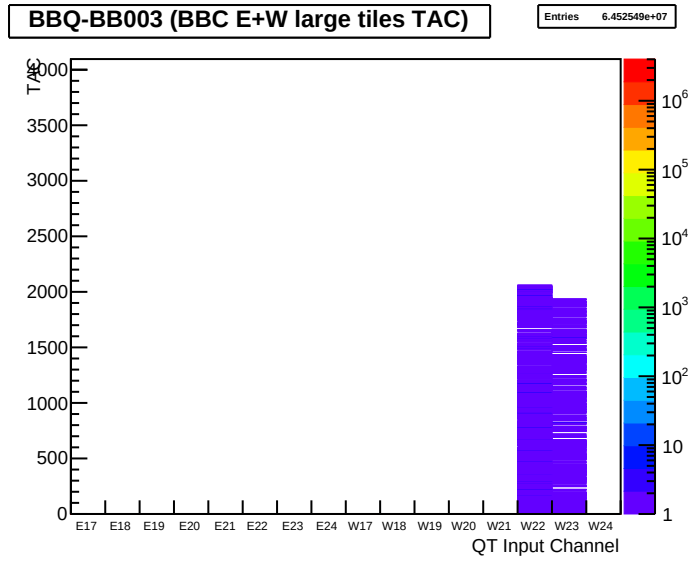

BBQ-BB001 (BBC east small tiles ADC)
Entries 6.452549e+07
ADC
QT Input Channel

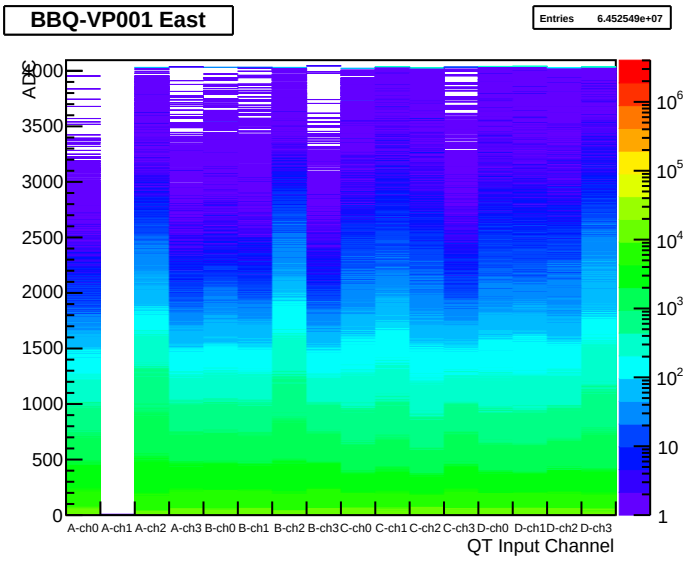

BBQ-BB001 (BBC east small tiles TAC)
Entries 6.452549e+07
TAC
QT Input Channel

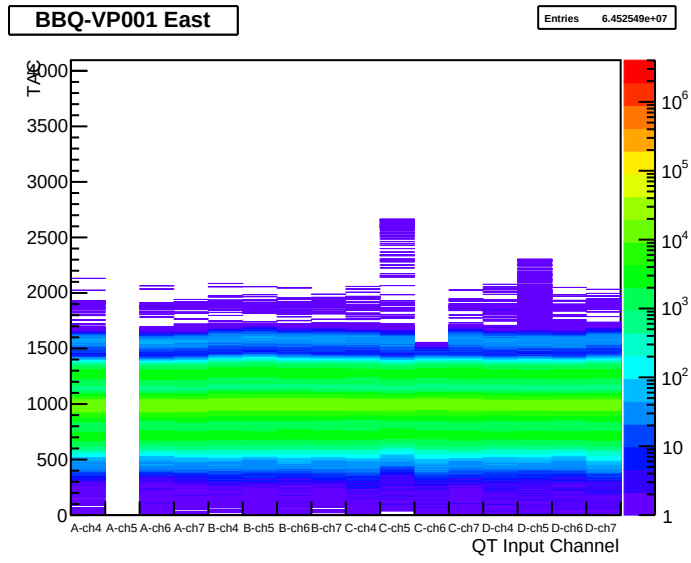

BBQ-BB002 (BBC west small tiles ADC)
Entries 6.452549e+07
ADC
QT Input Channel

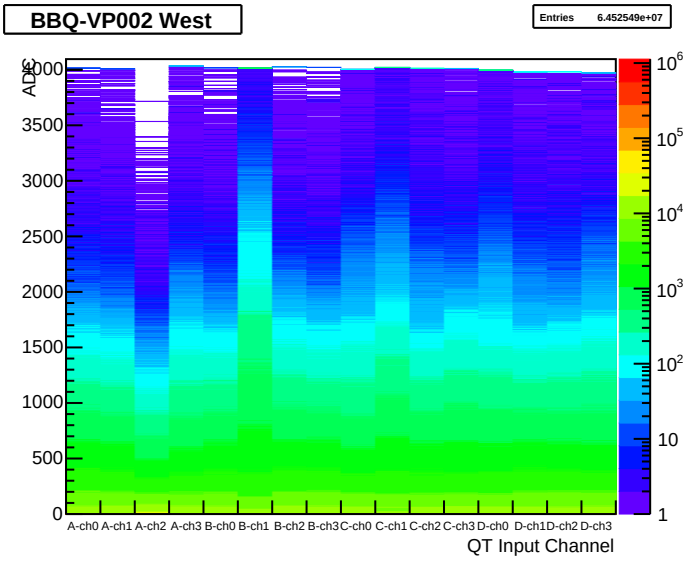

BBQ-BB002 (BBC west small tiles TAC)
Entries 6.452549e+07
TAC
QT Input Channel

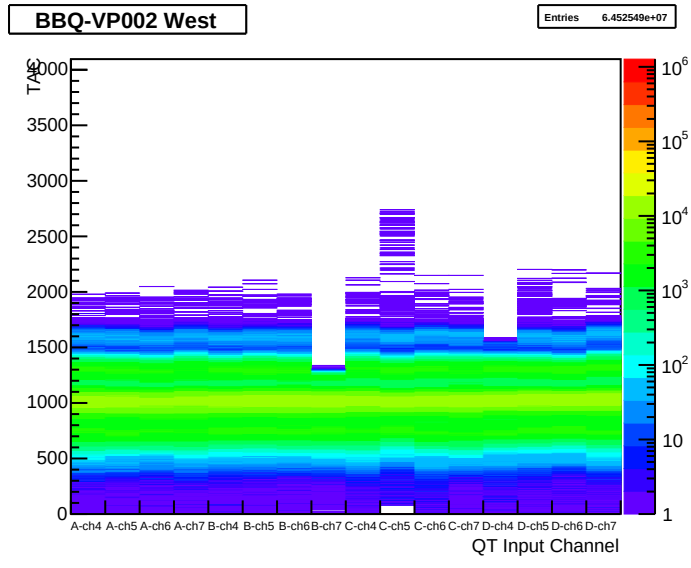

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6.452549e+07
ADC
QT Input Channel

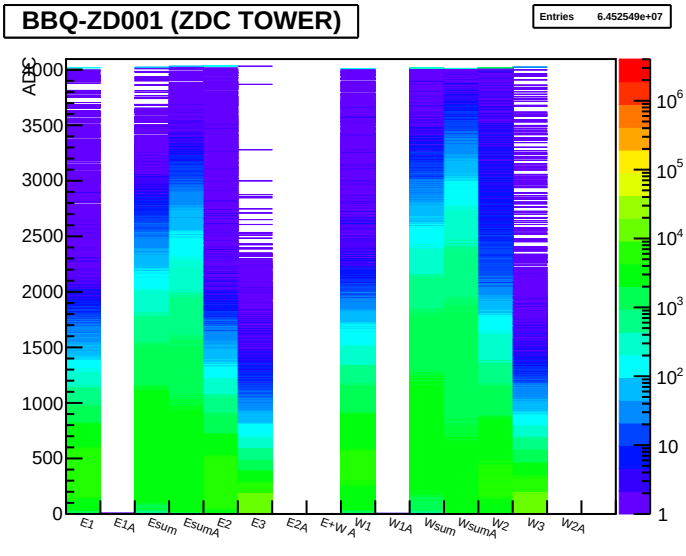

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6.452549e+07
TAC
QT Input Channel

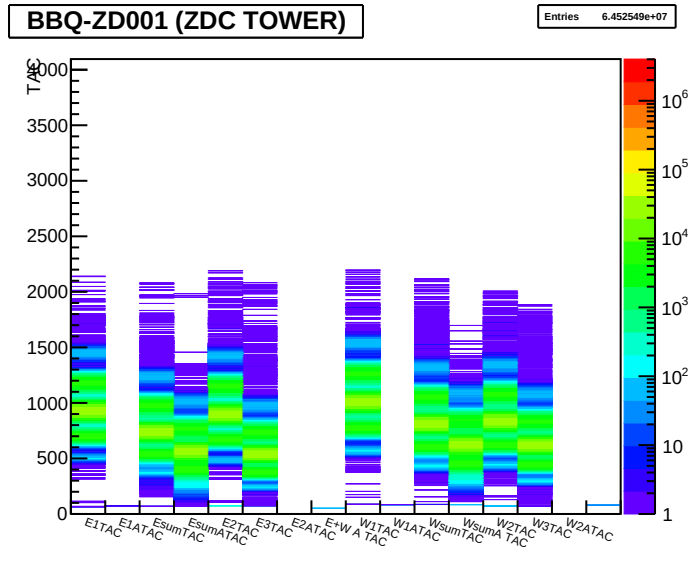

BBQ-VP001 East
Entries 6.452549e+07
ADC
QT Input Channel

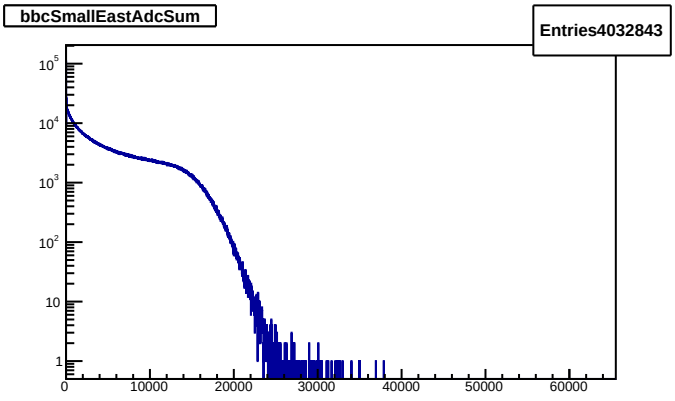

BBQ-VP001 East
Entries 6.452549e+07
TAC
QT Input Channel

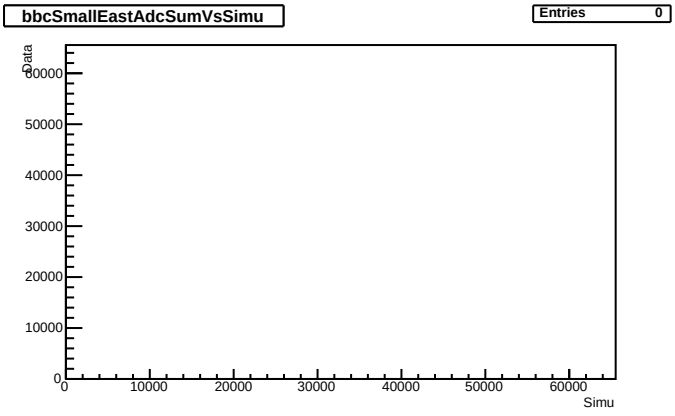

BBQ-VP002 West
Entries 6.452549e+07
ADC
QT Input Channel

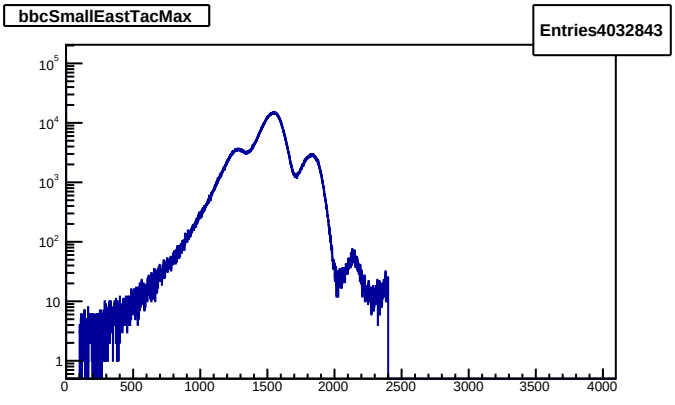

BBQ-VP002 West
Entries 6.452549e+07
TAC
QT Input Channel

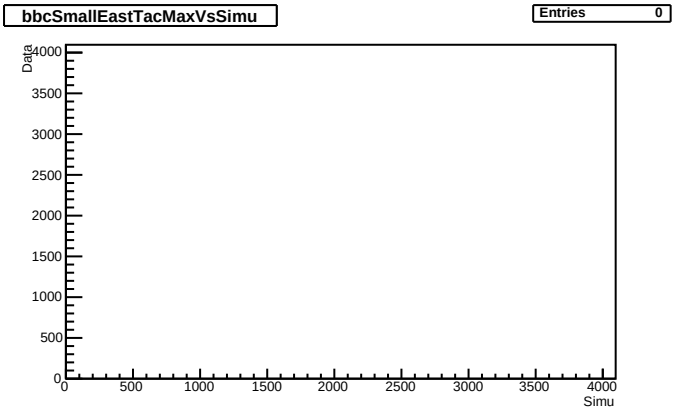

BBQ-ZD001 (ZDC TOWER)
Entries 6.452549e+07
ADC

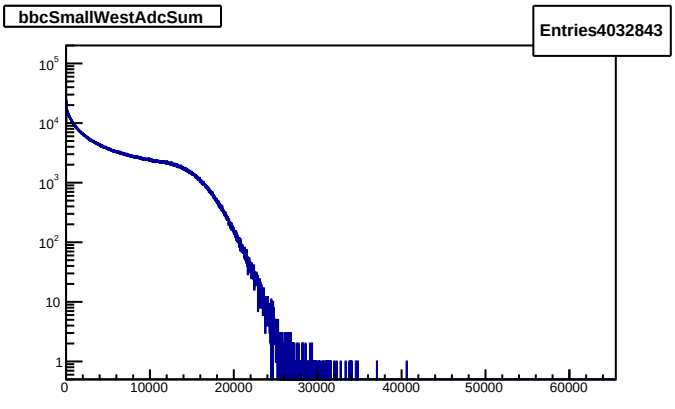

BBQ-ZD001 (ZDC TOWER)
Entries 6.452549e+07
TAC

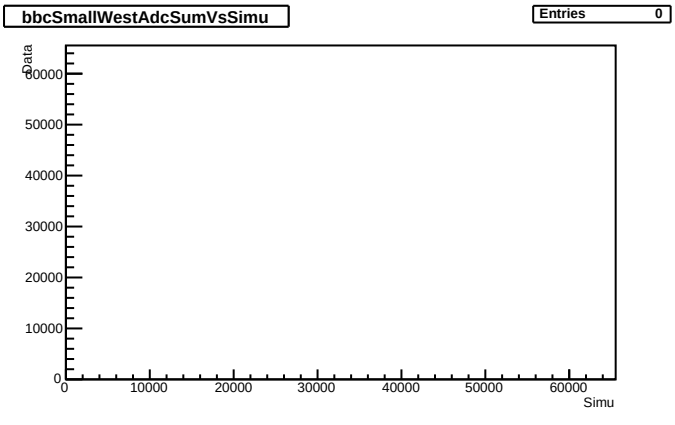

bbcSmallEastAdcSum
Entries4032843

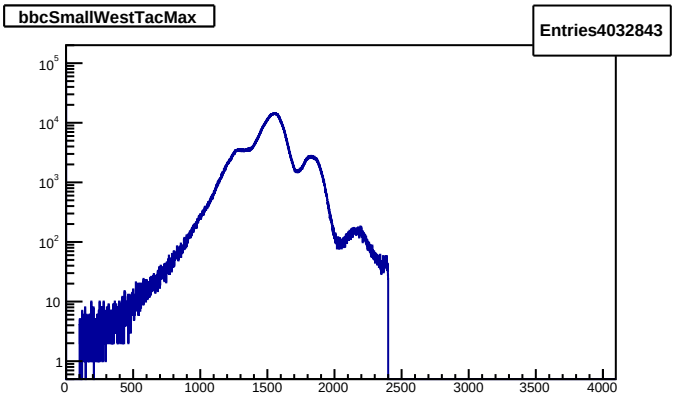

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

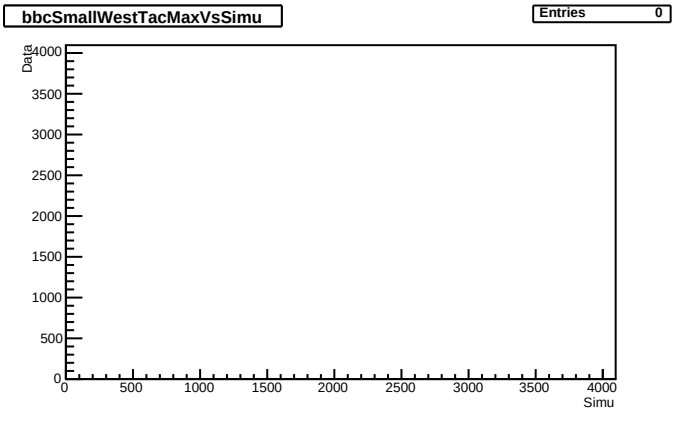

bbcSmallEastTacMax
Entries4032843

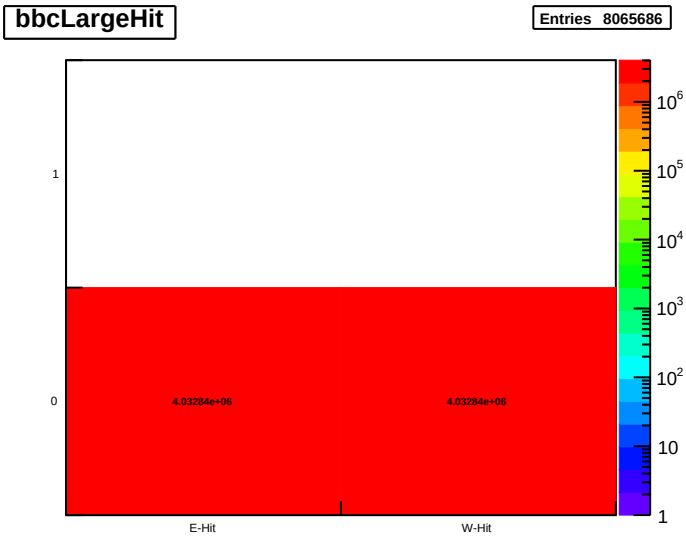

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

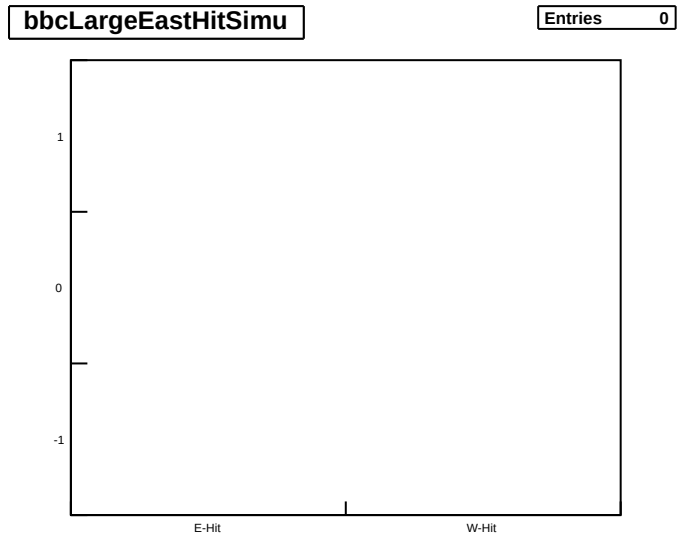

bbcSmallWestAdcSum
Entries4032843

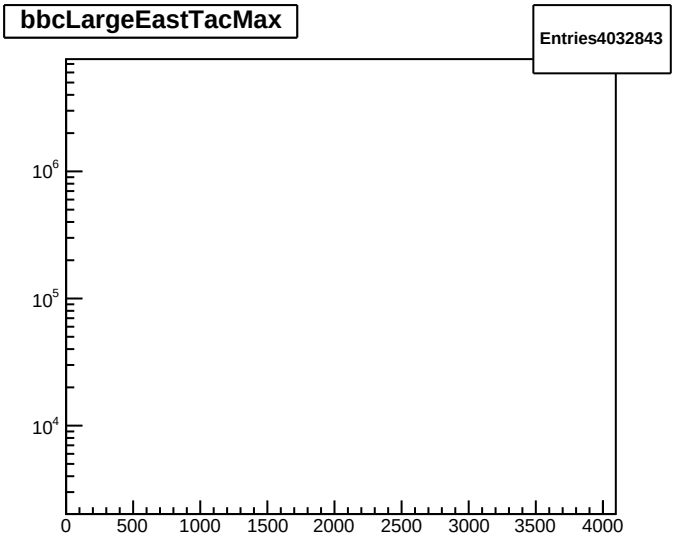

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

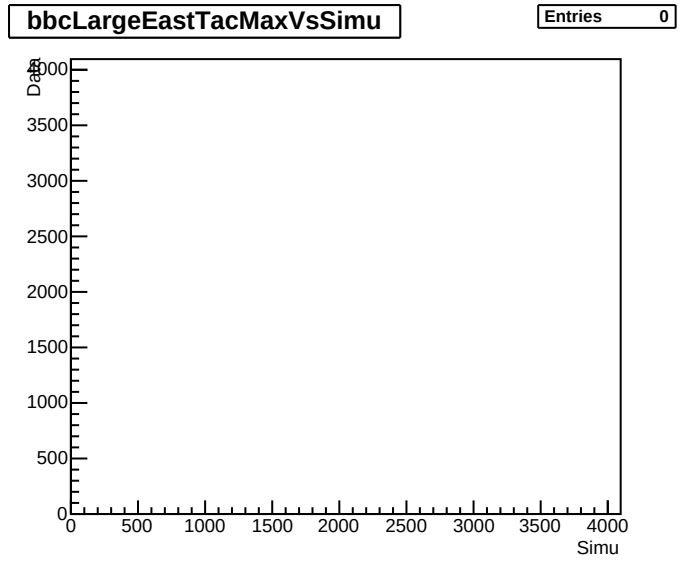

bbcSmallWestTacMax
Entries4032843

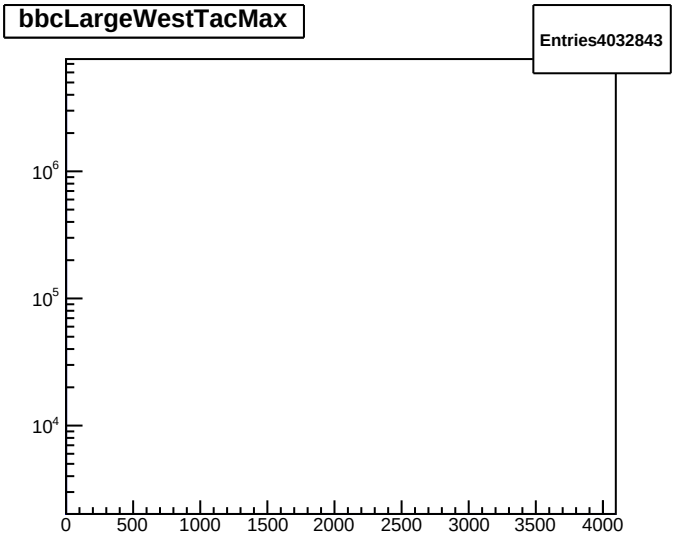

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

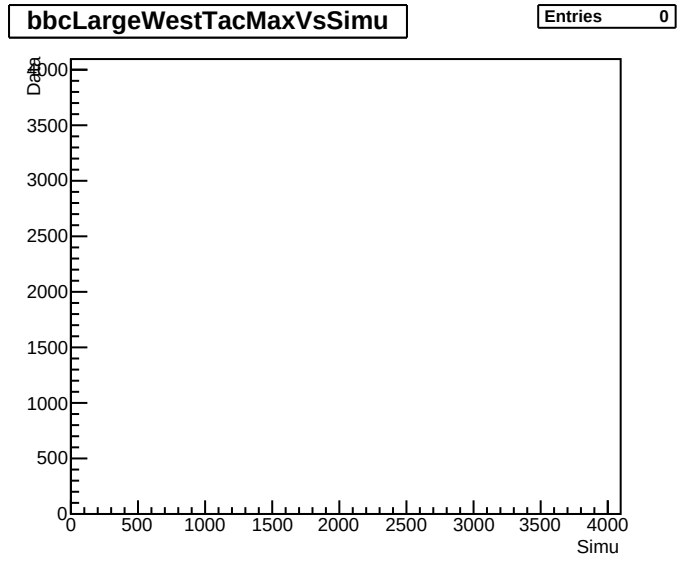

bbcLargeHit
Entries 8065686
1
0
4.03284e+06
4.03284e+06
E-Hit
W-Hit
10^6
10^5
10^4
10^3
10^2
10
1

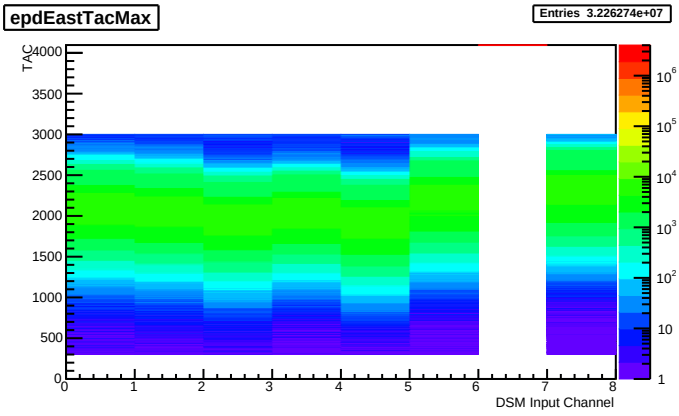

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

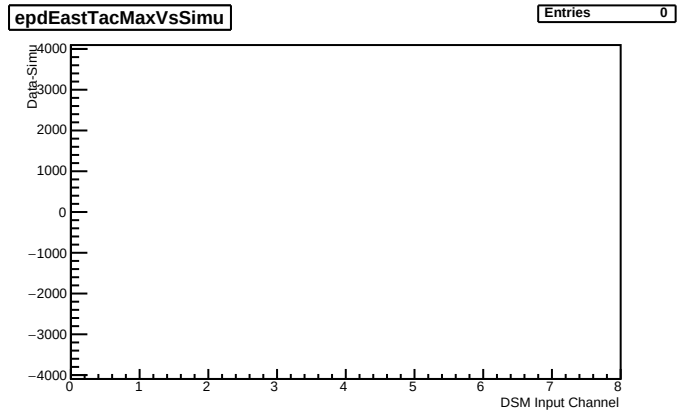

bbcLargeEastTacMax
Entries4032843
10^6
10^5
10^4
0
500
1000
1500
2000
2500
3000
3500
4000

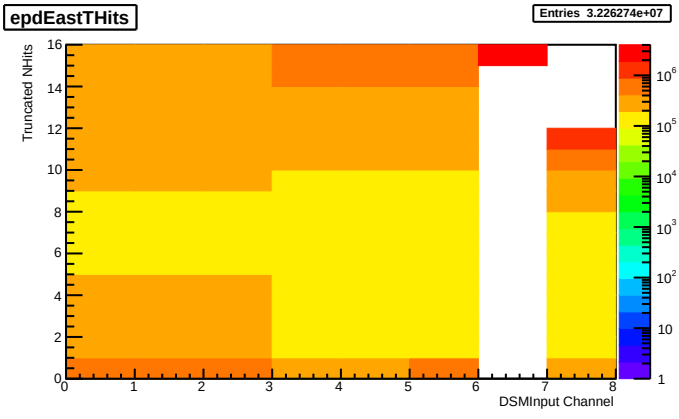

bbcLargeEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

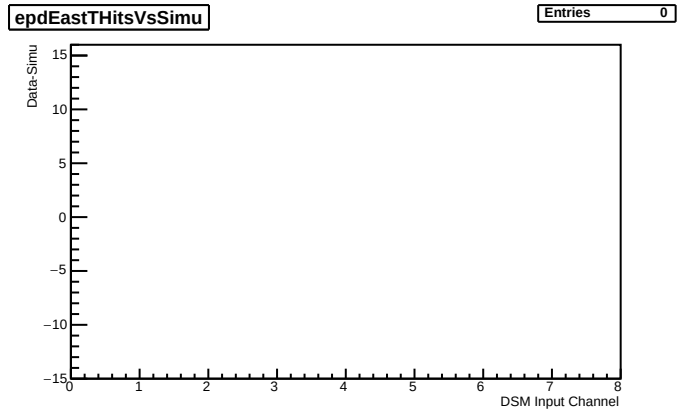

bbcLargeWestTacMax
Entries4032843
10^6
10^5
10^4
0
500
1000
1500
2000
2500
3000
3500
4000

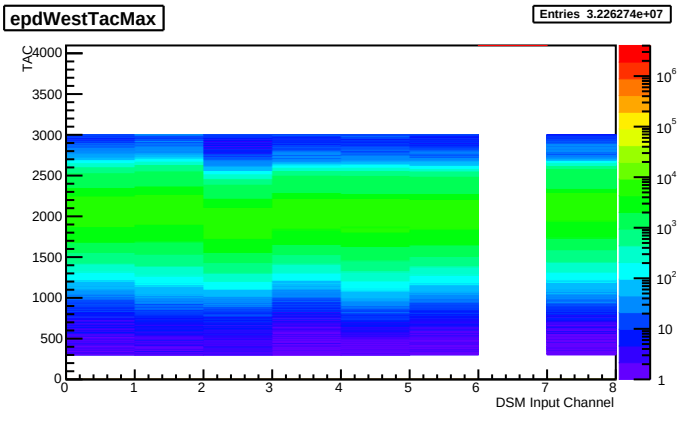

bbcLargeWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

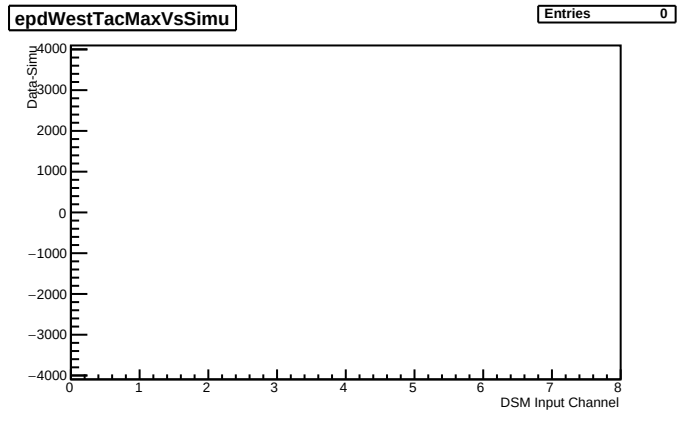

epdEastTacMax
Entries 3.226274e+07
TAC
DSM Input Channel

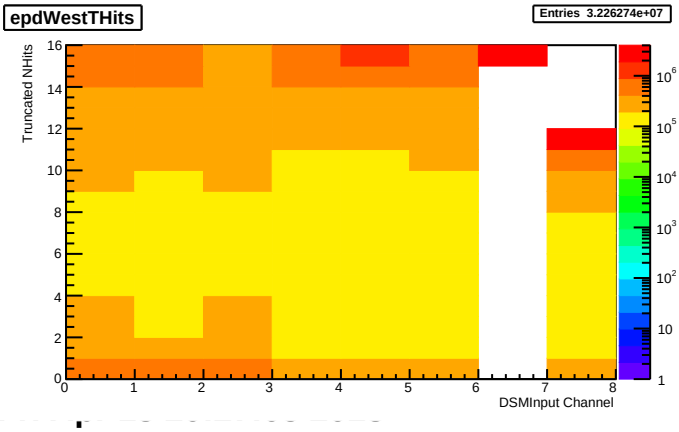

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

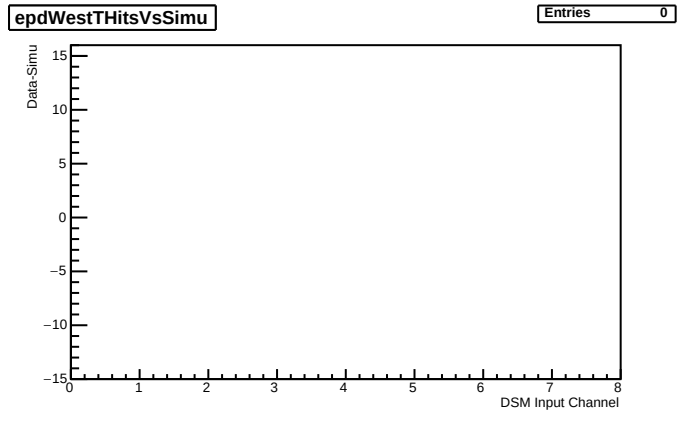

epdEastTHits
Entries 3.226274e+07
Truncated NHits
DSMInput Channel

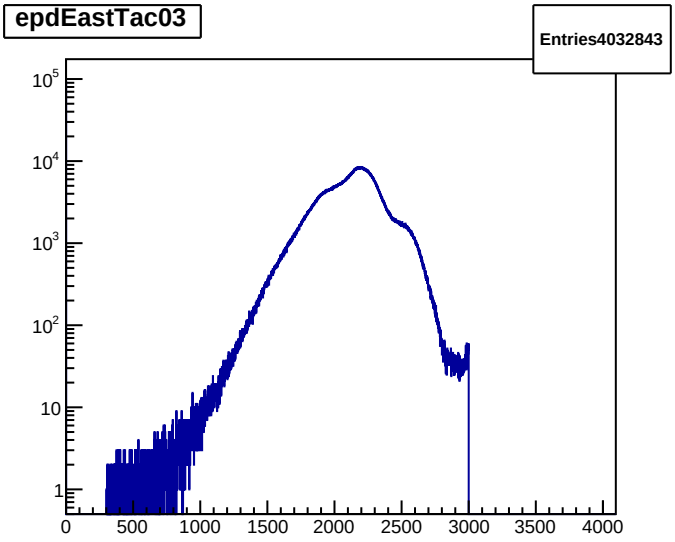

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

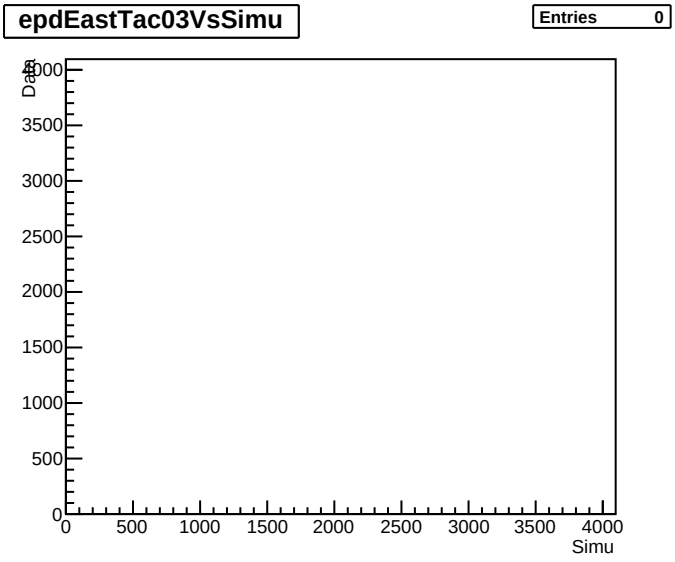

epdWestTacMax
Entries 3.226274e+07
TAC
DSM Input Channel

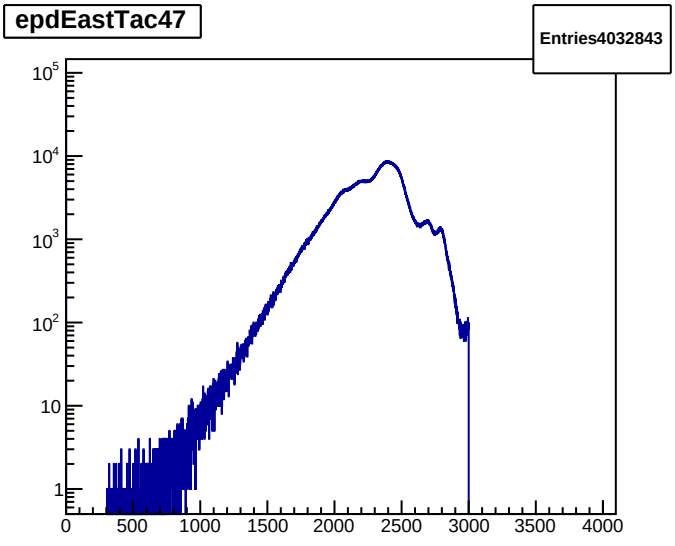

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

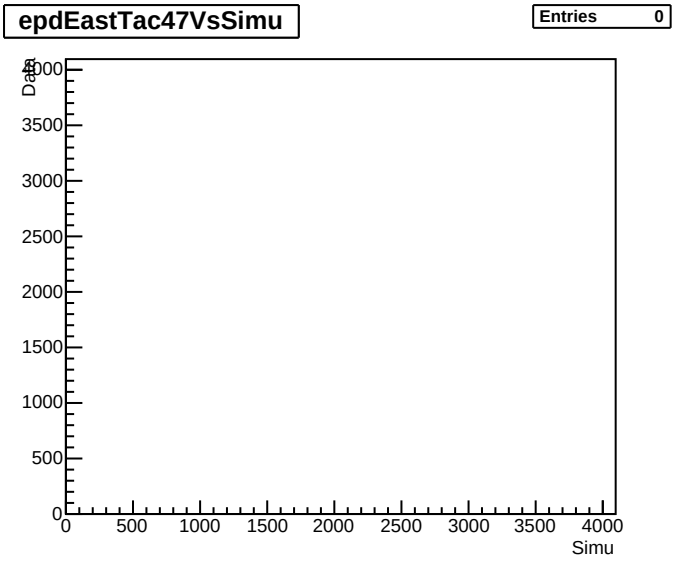

epdWestTHits
Entries 3.226274e+07
Truncated NHits
DSMInput Channel

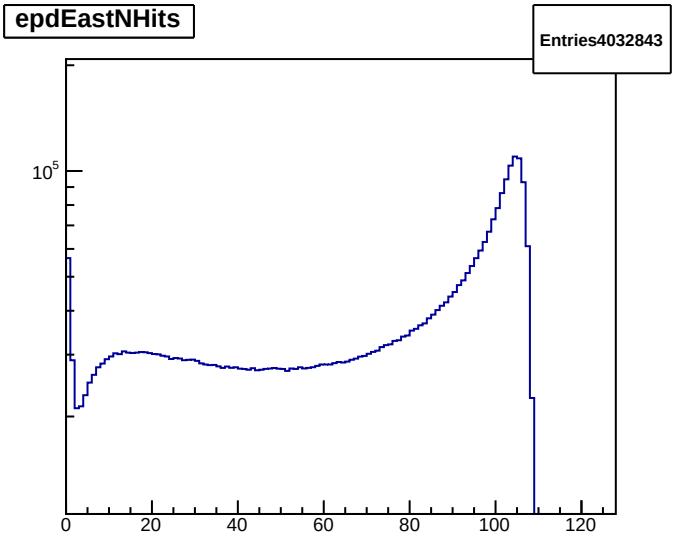

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

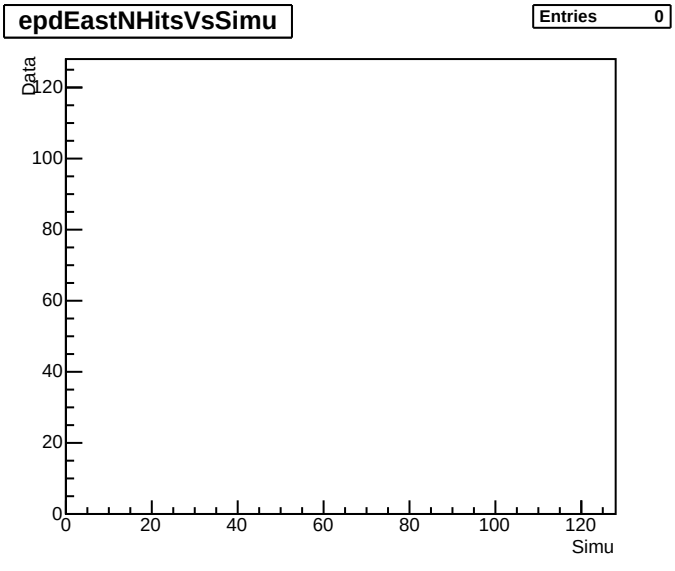

epdEastTac03
Entries4032843
10^5
10^4
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

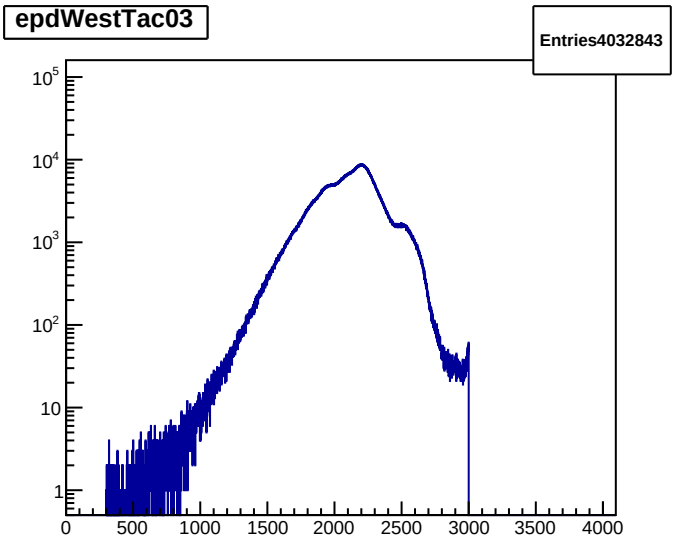

epdEastTac03VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

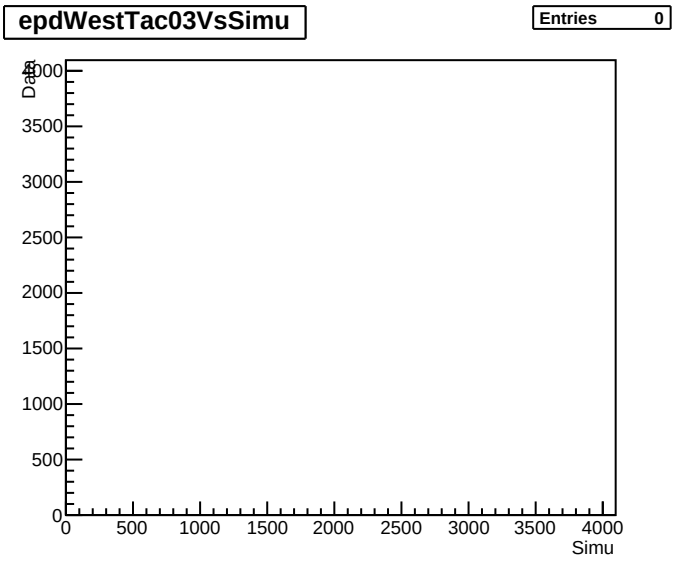

epdEastTac47
Entries4032843
10^5
10^4
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

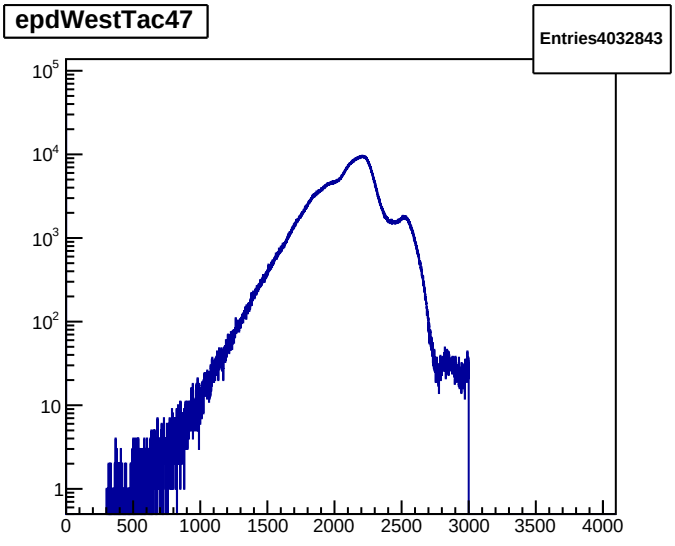

epdEastTac47VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

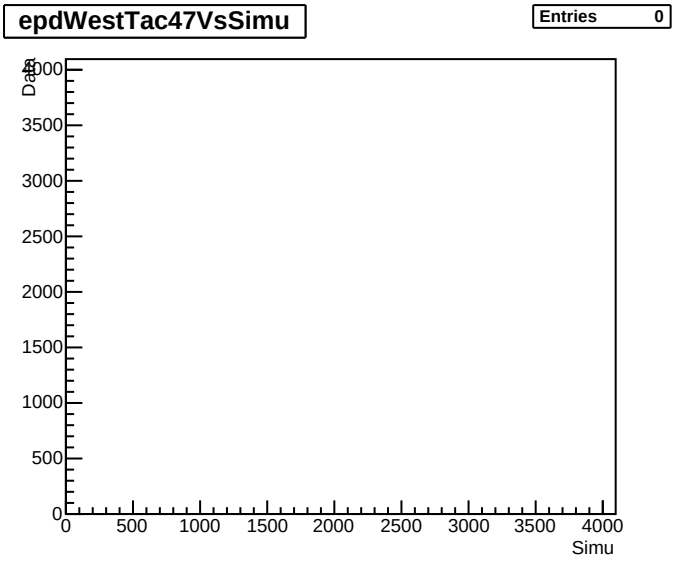

epdEastNHits
Entries4032843
10^5
0 20 40 60 80 100 120

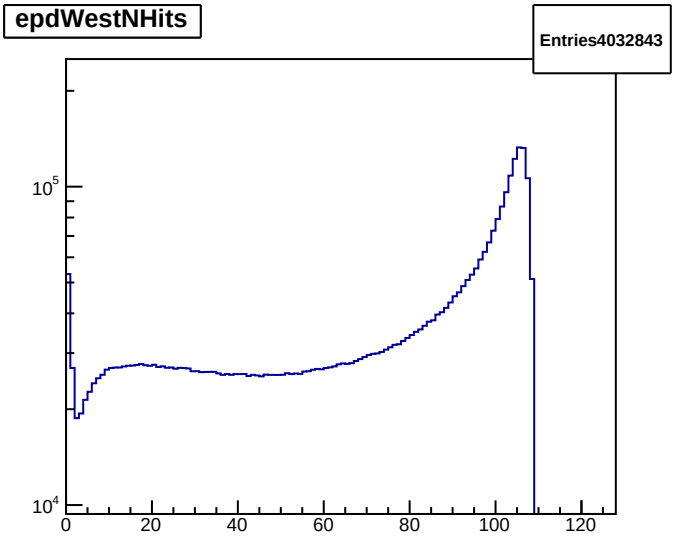

epdEastNHitsVsSimu
Entries 0
Data
120
100
80
60
40
20
0
0 20 40 60 80 100 120
Simu

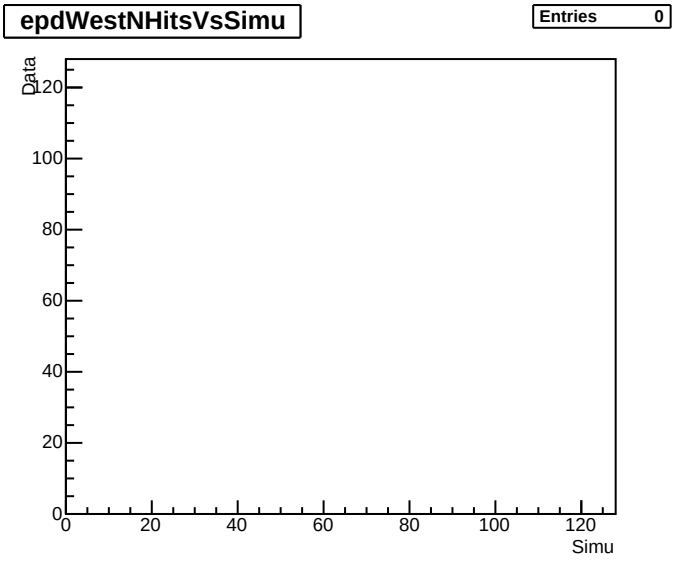

epdWestTac03
Entries4032843

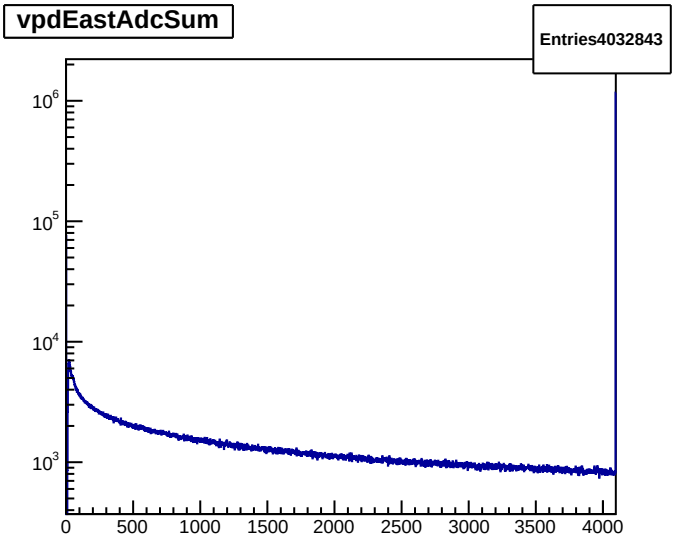

epdWestTac03VsSimu
Entries 0
Data
Simu

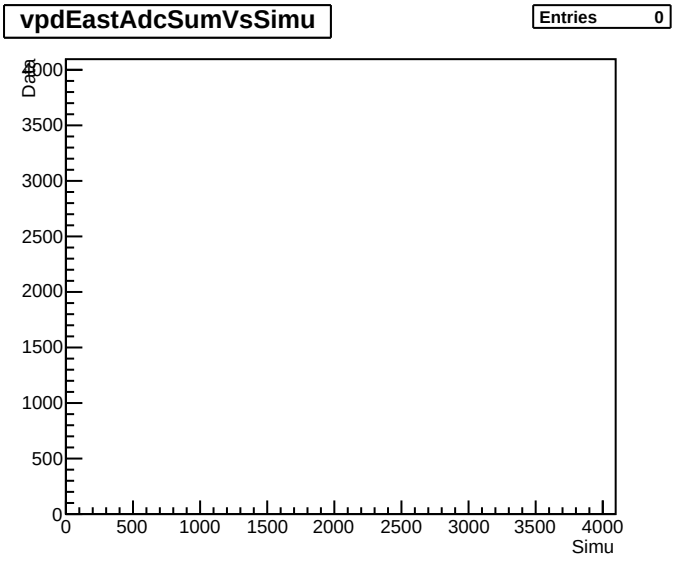

epdWestTac47
Entries4032843

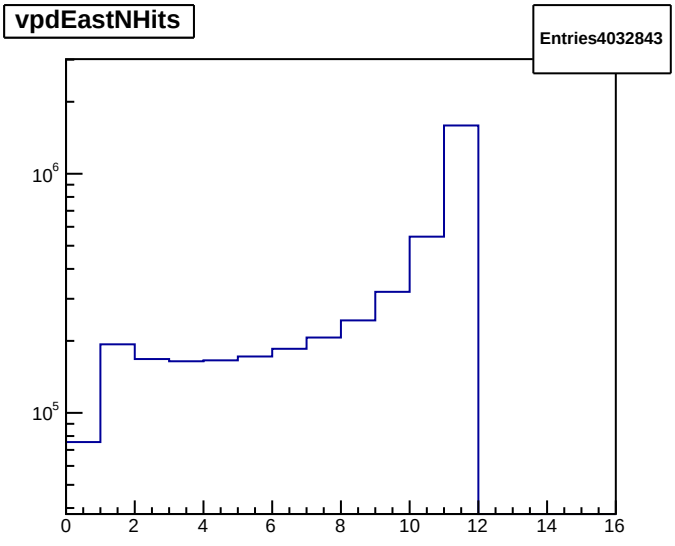

epdWestTac47VsSimu
Entries 0
Data
Simu

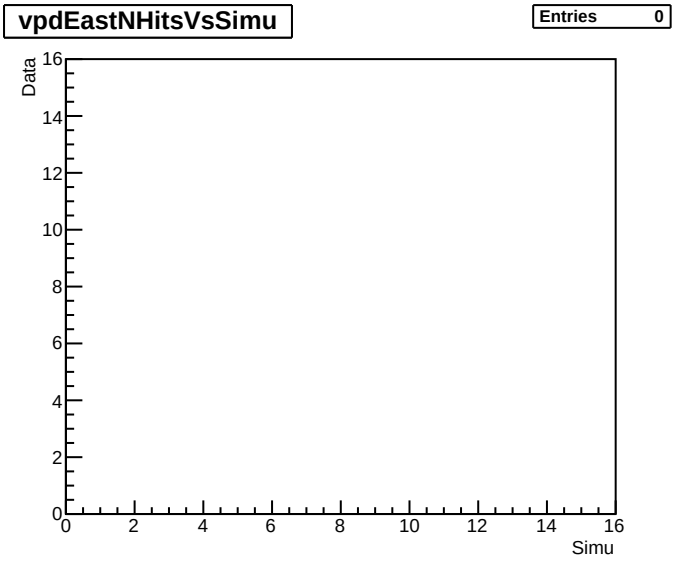

epdWestNHits
Entries4032843

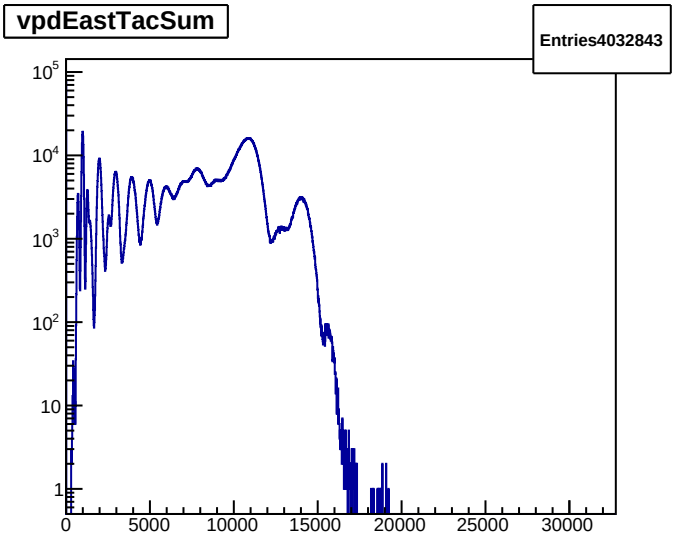

epdWestNHitsVsSimu
Entries 0
Data
Simu

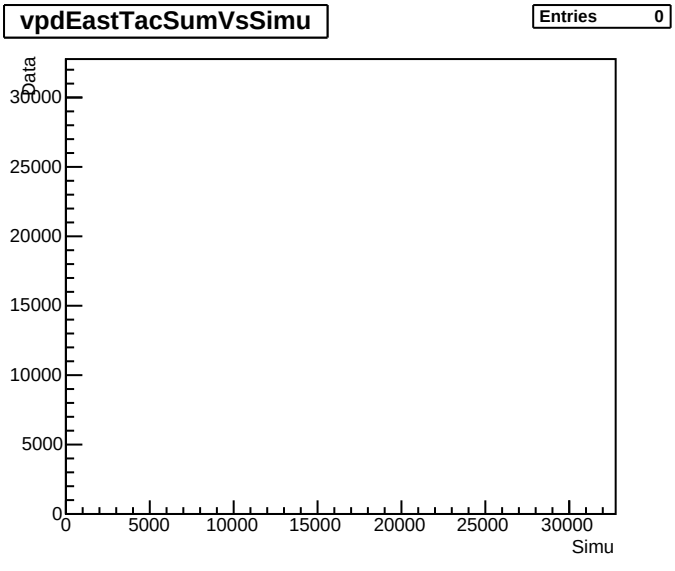

vpdEastAdcSum
Entries4032843
10^6
10^5
10^4
10^3
0 500 1000 1500 2000 2500 3000 3500 4000

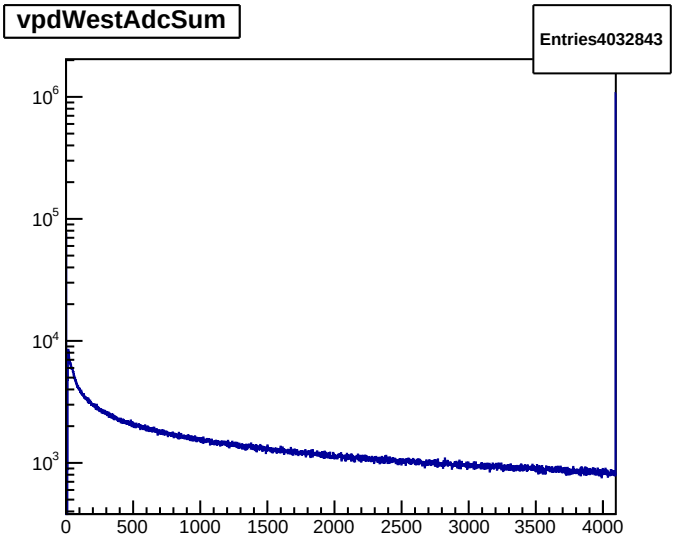

vpdEastAdcSumVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

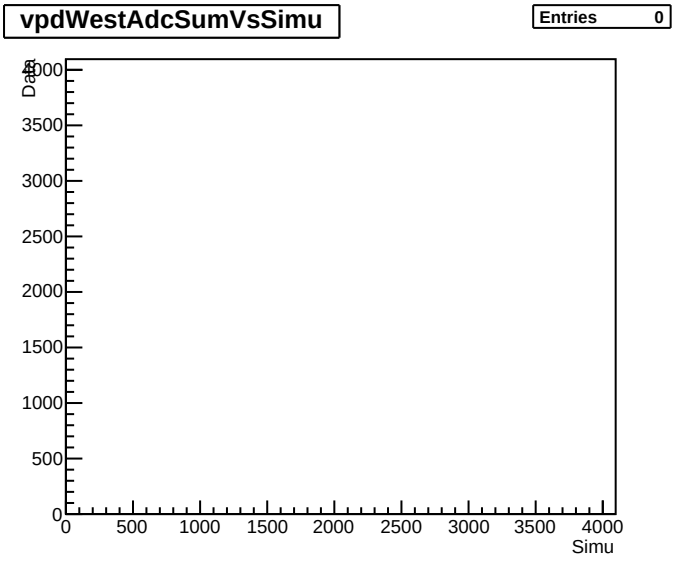

vpdEastNHits
Entries4032843
10^6
10^5
0 2 4 6 8 10 12 14 16

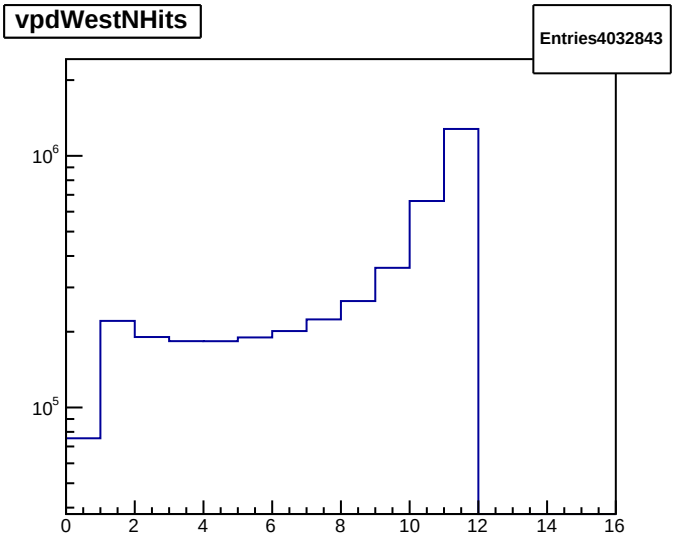

vpdEastNHitsVsSimu
Entries 0
Data
16
14
12
10
8
6
4
2
0
0 2 4 6 8 10 12 14 16
Simu

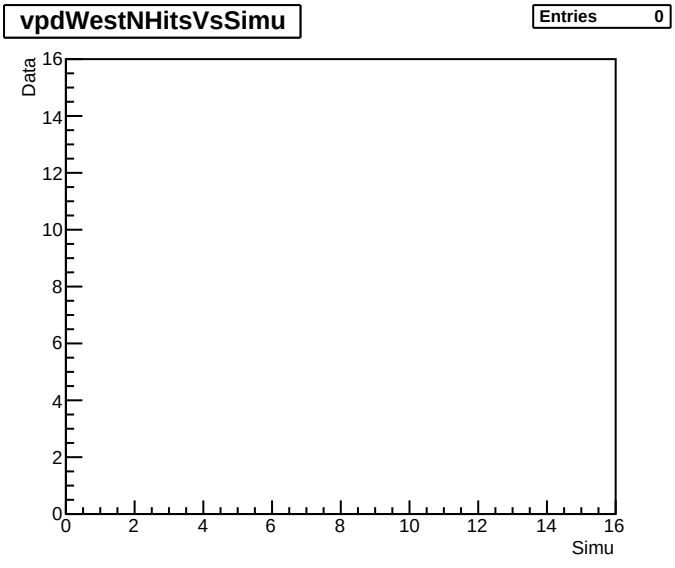

vpdEastTacSum
Entries4032843
10^5
10^4
10^3
10^2
10
1
0 5000 10000 15000 20000 25000 30000

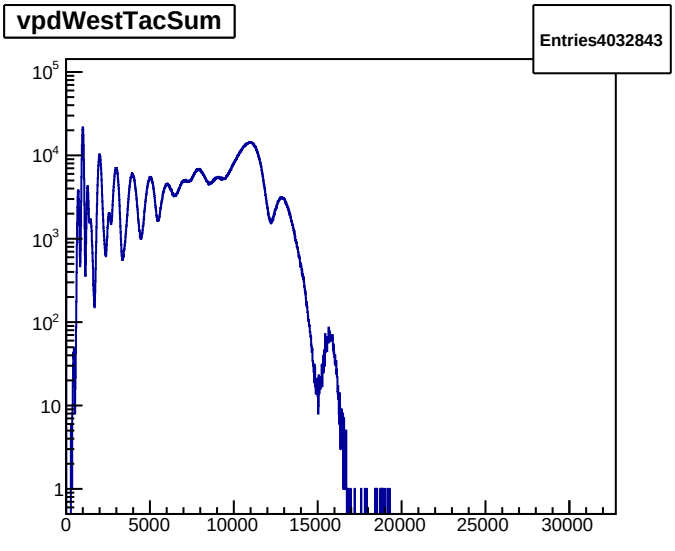

vpdEastTacSumVsSimu
Entries 0
Data
30000
25000
20000
15000
10000
5000
0
0 5000 10000 15000 20000 25000 30000
Simu

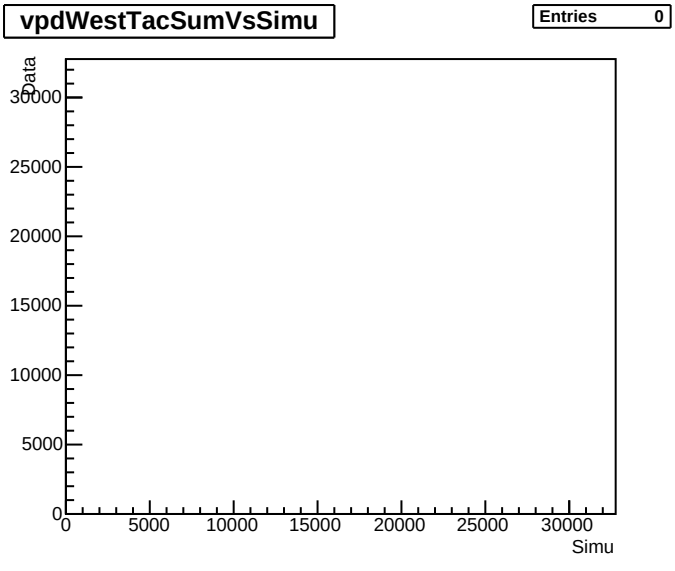

vpdWestAdcSum
Entries4032843

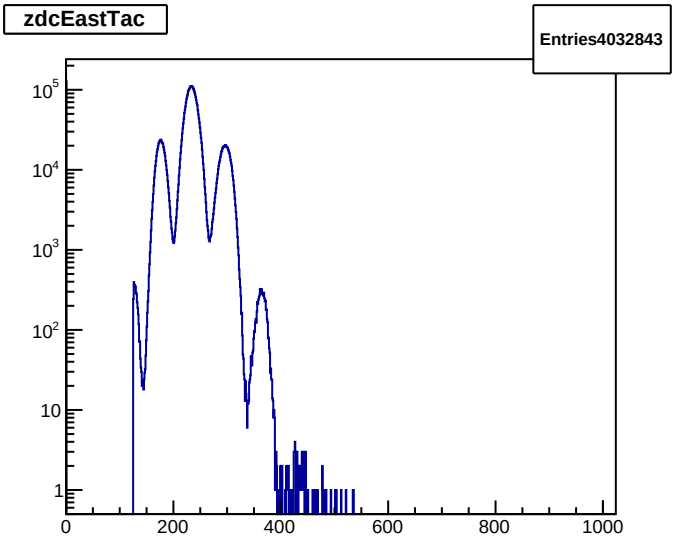

vpdWestAdcSumVsSimu
Entries 0
Data
Simu

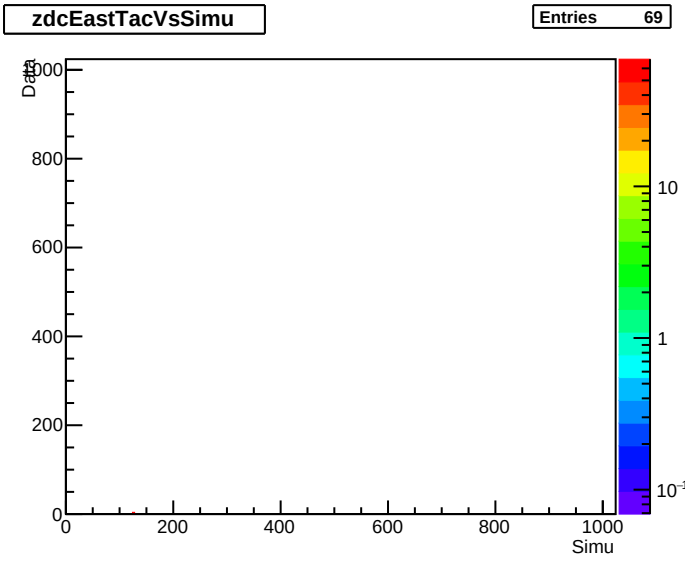

vpdWestNHits
Entries4032843

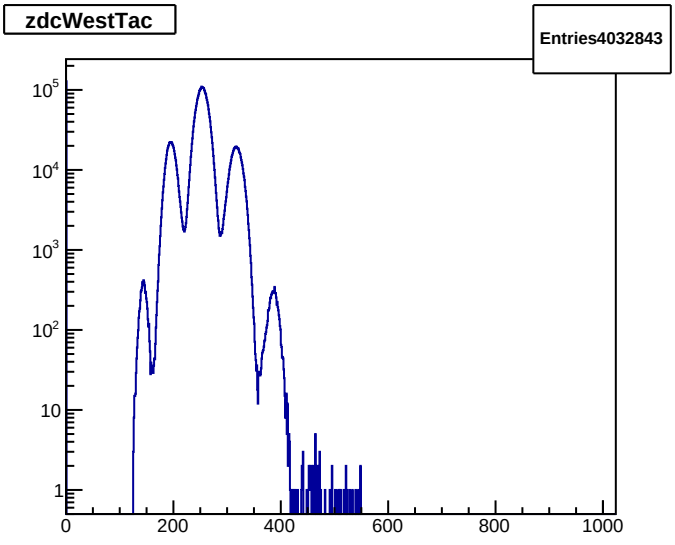

vpdWestNHitsVsSimu
Entries 0
Data
Simu

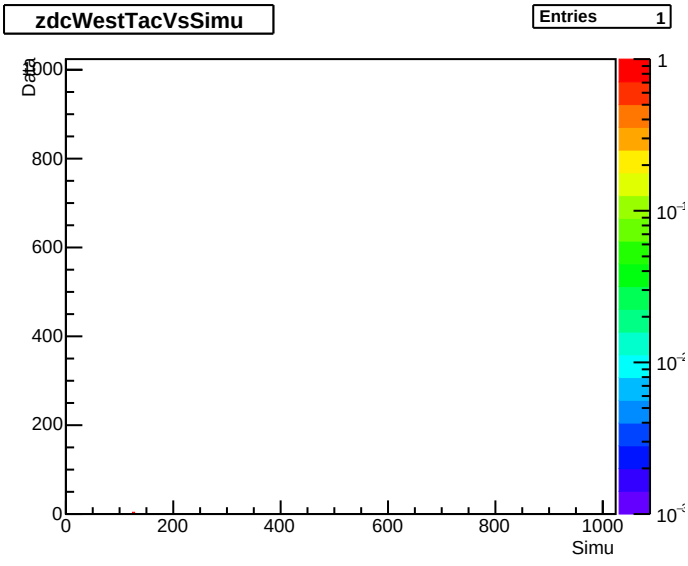

vpdWestTacSum
Entries4032843

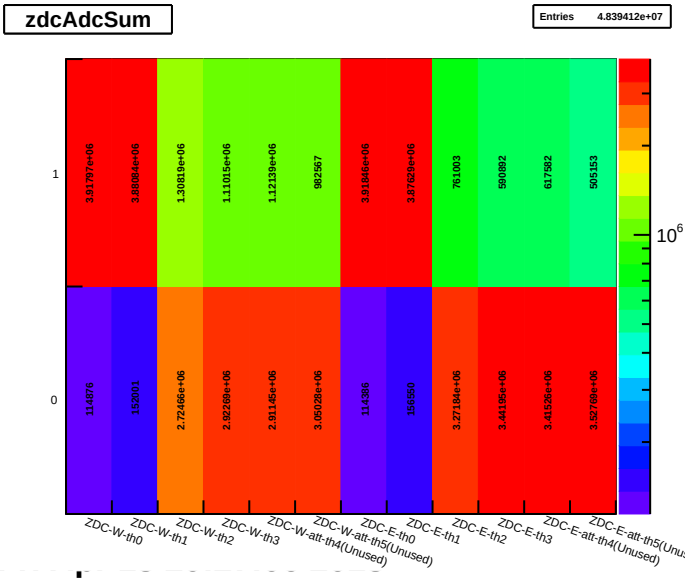

vpdWestTacSumVsSimu
Entries 0
Data
Simu

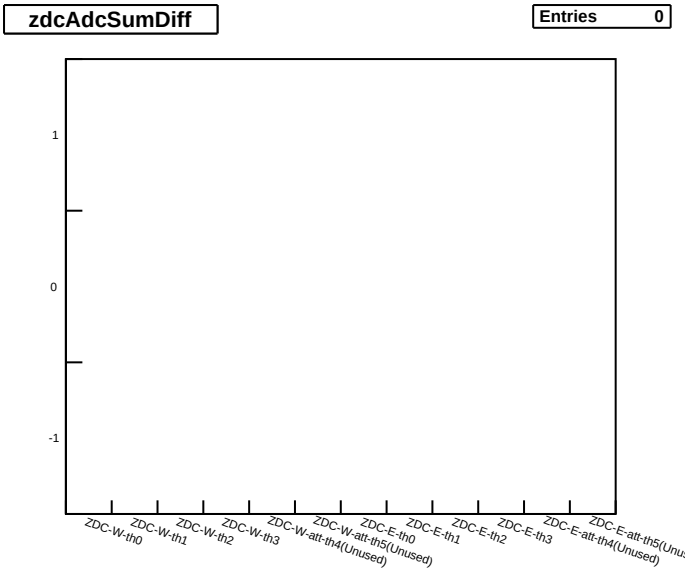

zdcEastTac
Entries4032843

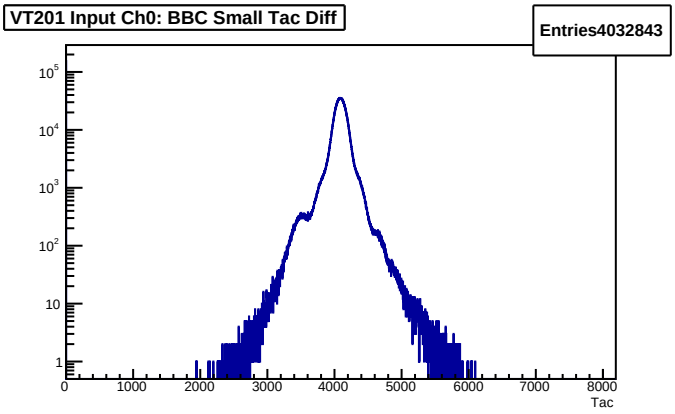

zdcEastTacVsSimu
Entries 69
Data
Simu

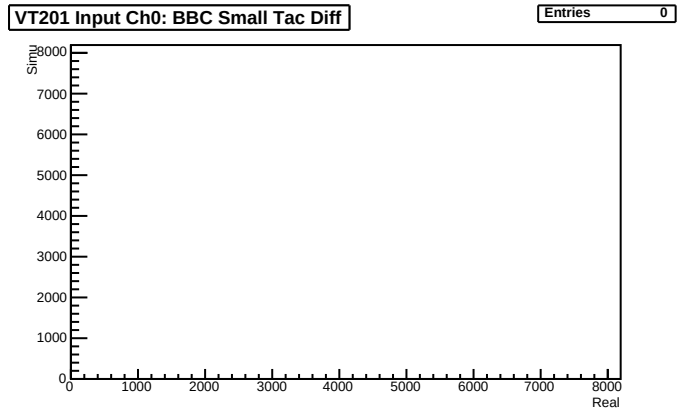

zdcWestTac
Entries4032843

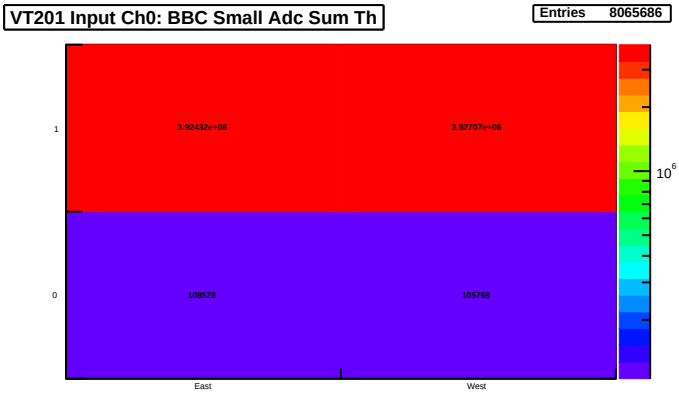

zdcWestTacVsSimu
Entries 1
Data
Simu

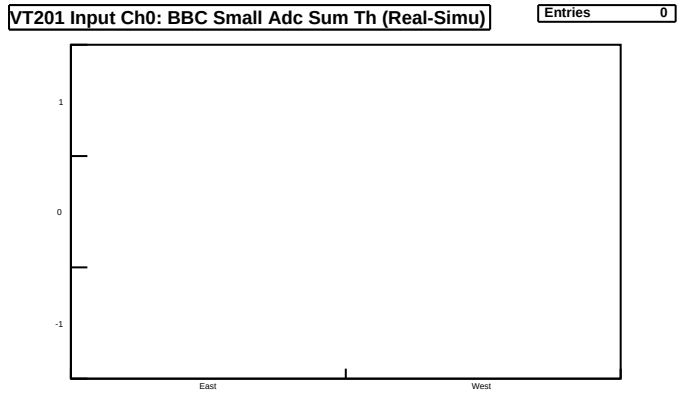

zdcAdcSum
Entries 4.839412e+07
3.91797e+06
3.88004e+06
1.30819e+06
1.11015e+06
1.12139e+06
982567
3.91946e+06
3.87629e+06
761003
590892
617582
505153
114076
152001
2.72466e+06
2.92269e+06
2.91145e+06
3.05028e+06
114386
156550
3.27184e+06
3.44195e+06
3.41526e+06
3.52769e+06
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

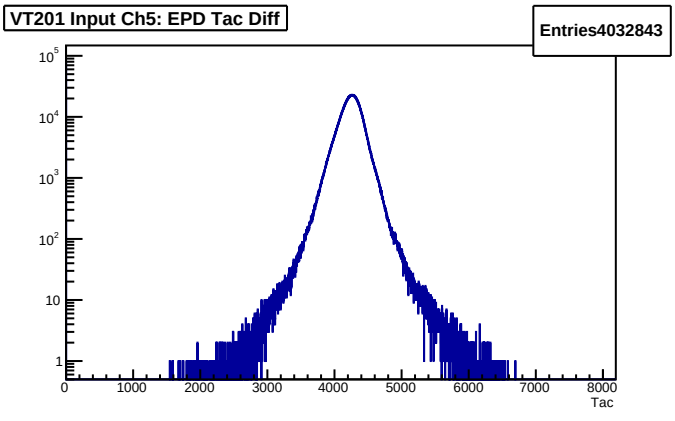

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

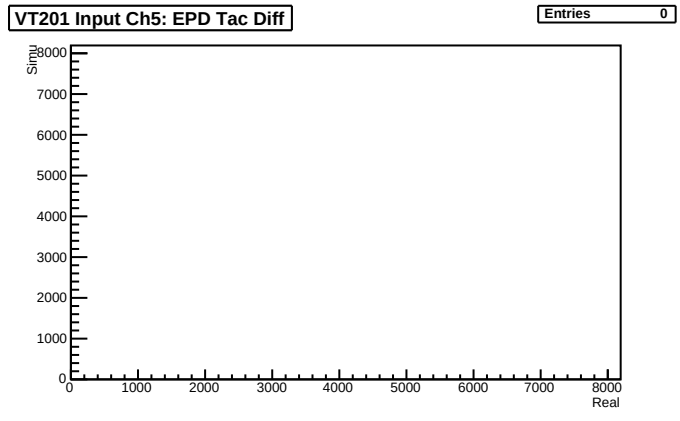

VT201 Input Ch0: BBC Small Tac Diff
Entries4032843
Tac

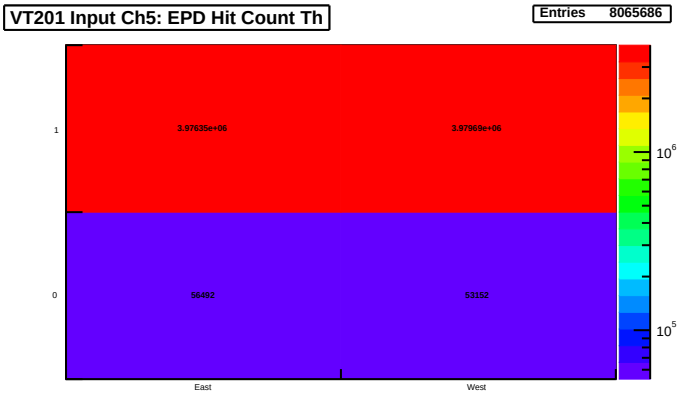

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

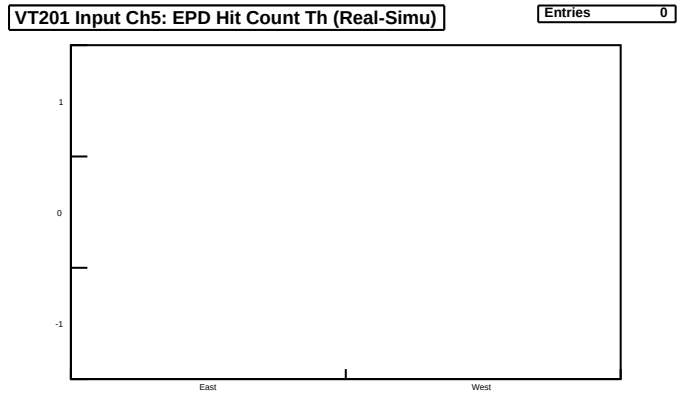

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 8065686
3.92432e+06
3.92707e+06
108528
105769
East
West

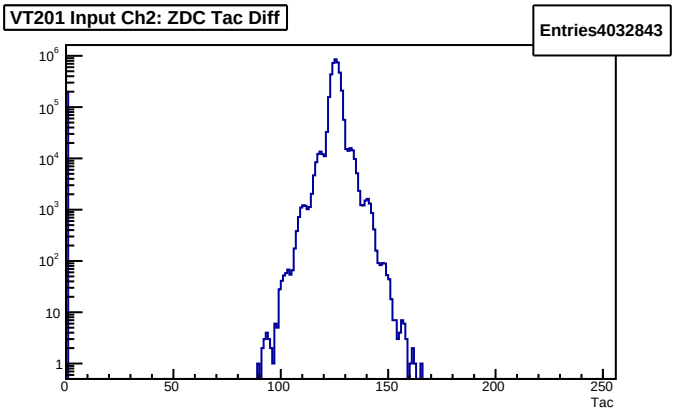

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

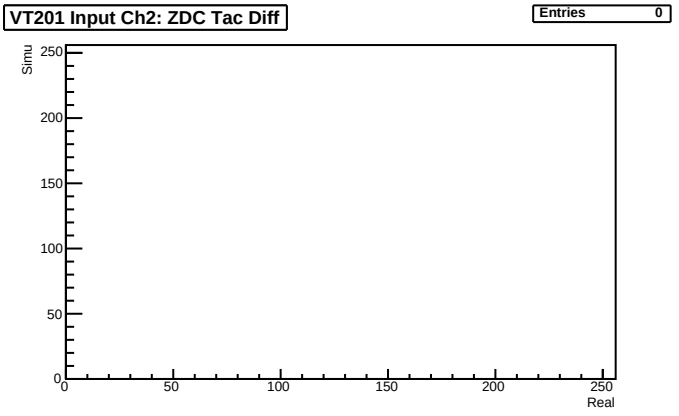

VT201 Input Ch5: EPD Tac Diff
Entries4032843
Tac

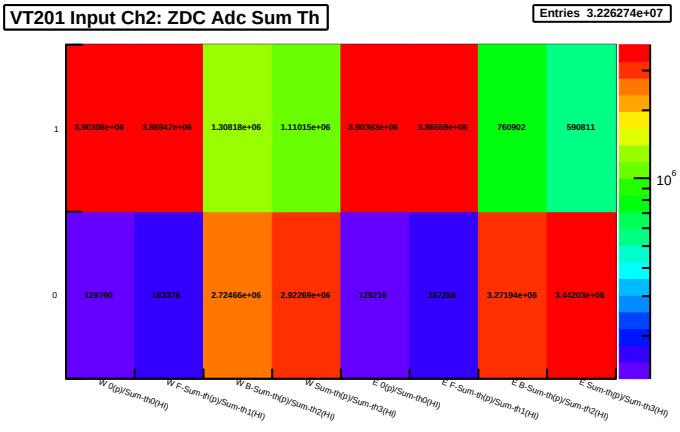

VT201 Input Ch5: EPD Tac Diff
Entries 0
Simu
Real

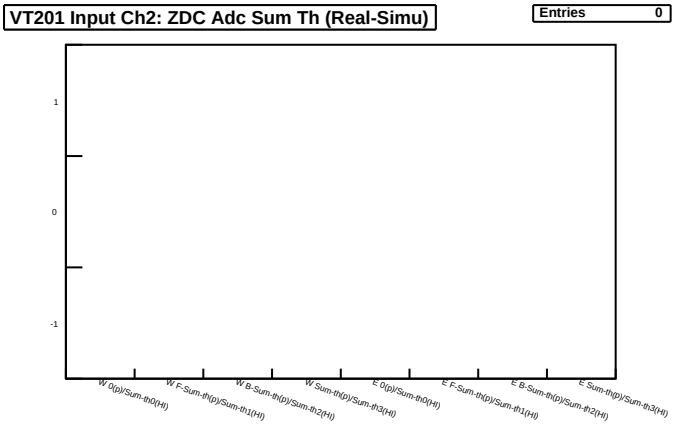

VT201 Input Ch5: EPD Hit Count Th
Entries 8065686
3.97635e+06
3.97969e+06
56492
53152
East
West

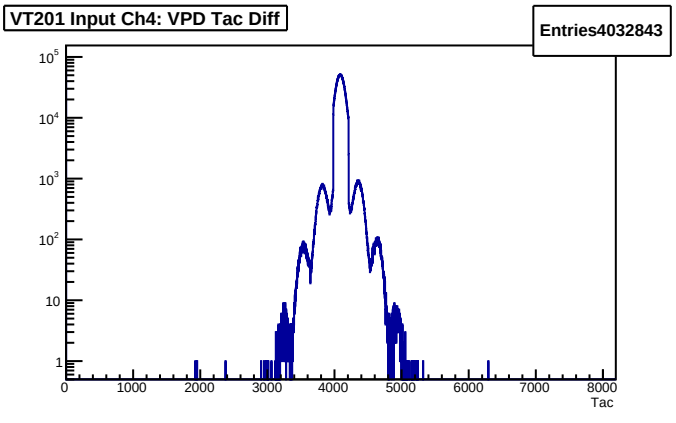

VT201 Input Ch5: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

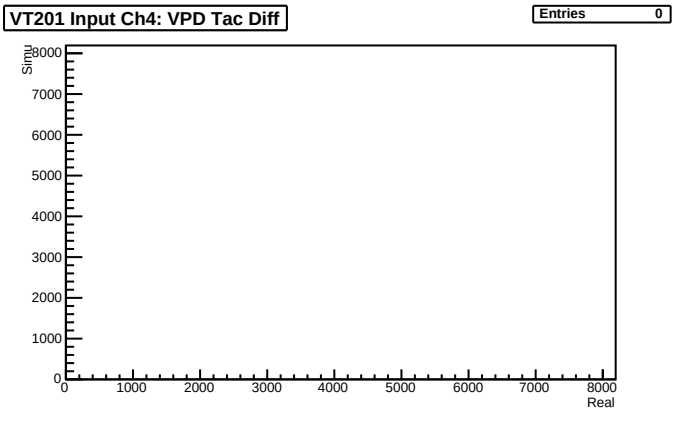

VT201 Input Ch2: ZDC Tac Diff
Entries4032843
Tac

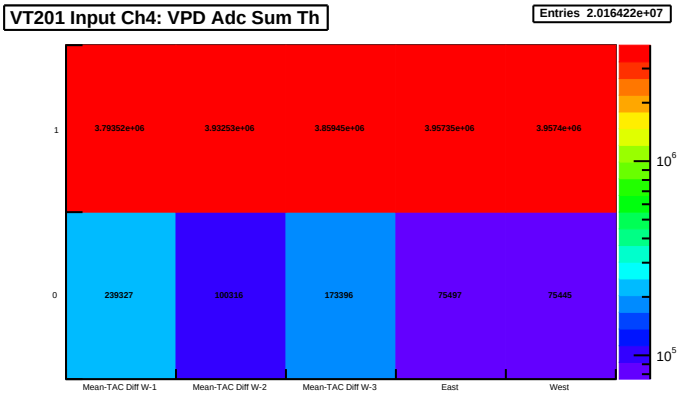

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

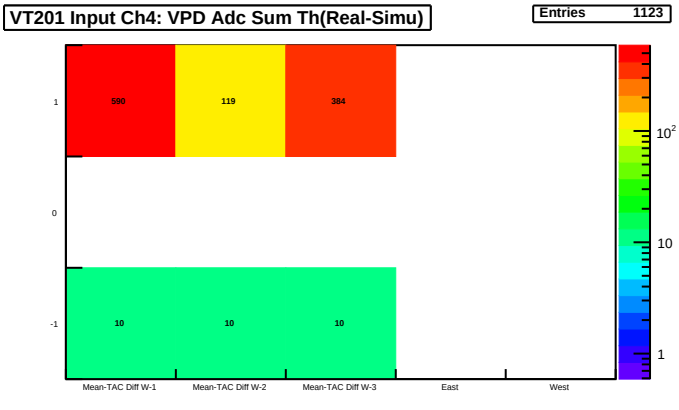

VT201 Input Ch2: ZDC Adc Sum Th
Entries 3.226274e+07
3.90308e+06
3.86947e+06
1.30818e+06
1.11015e+06
3.90363e+06
3.86559e+06
760902
590811
129760
163376
2.72466e+06
2.92269e+06
129216
167256
3.27194e+06
3.44203e+06
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

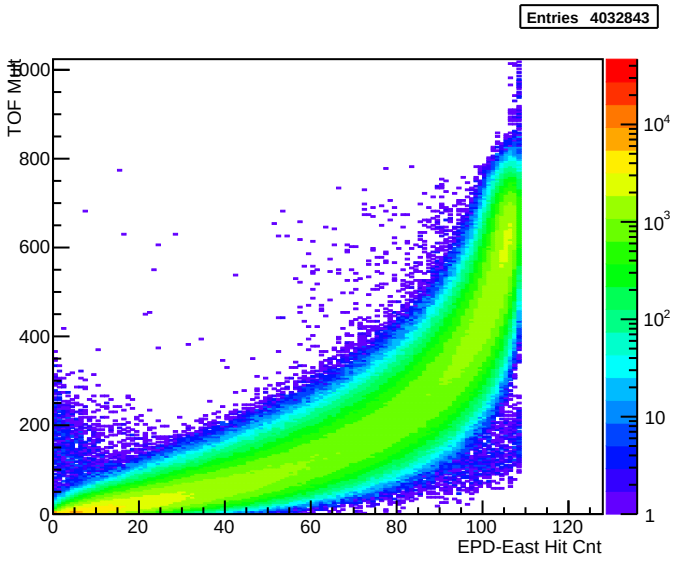

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

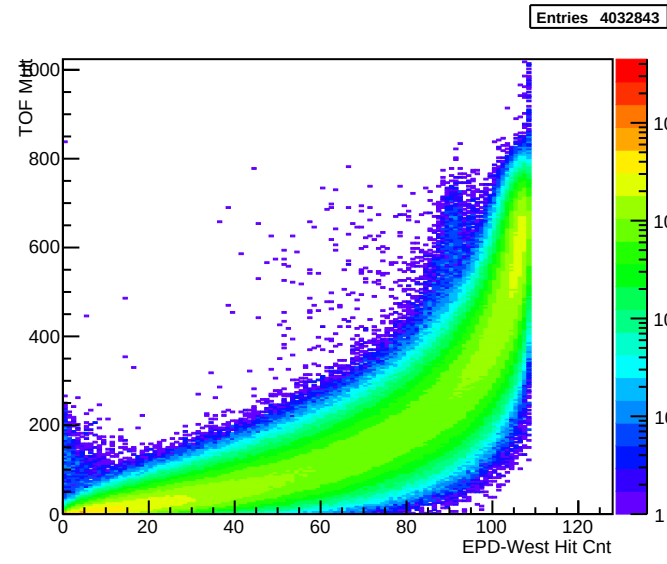

VT201 Input Ch4: VPD Tac Diff
Entries4032843
Tac

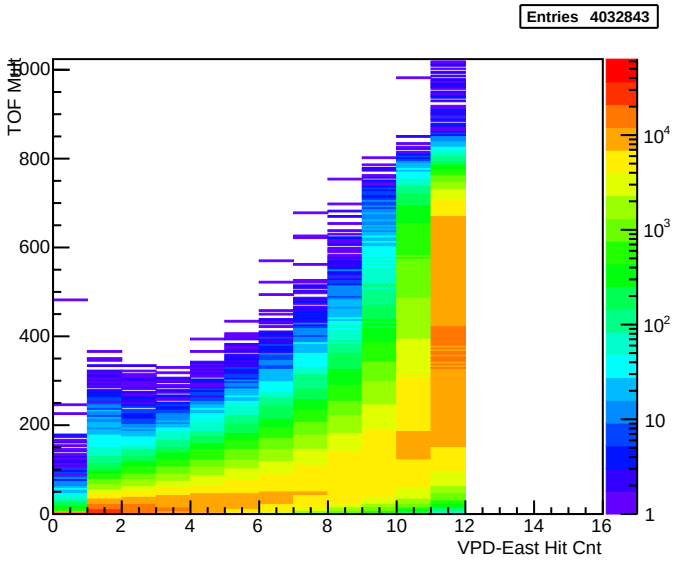

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

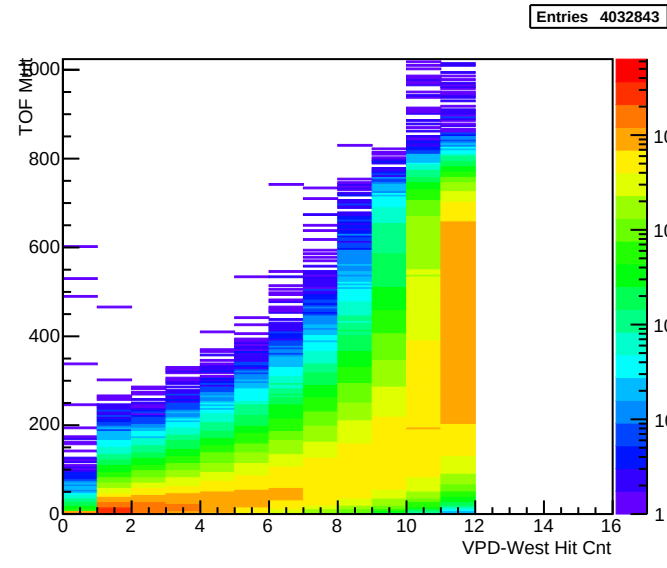

VT201 Input Ch4: VPD Adc Sum Th
Entries 2.016422e+07
3.79352e+06
3.93253e+06
3.85945e+06
3.95735e+06
3.9574e+06
239327
100316
173396
75497
75445
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

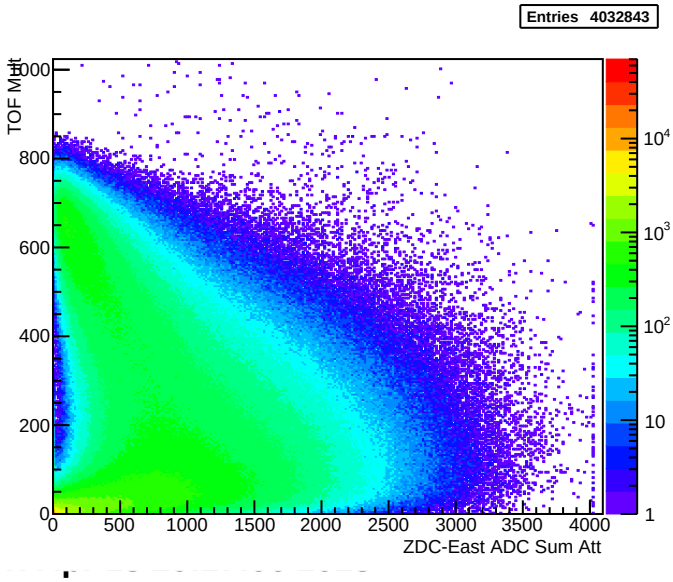

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 1123
590
119
384
10
10
10
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

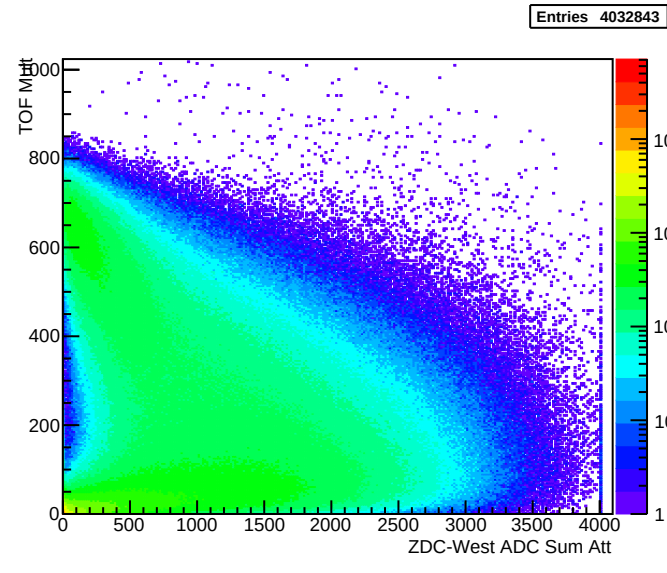

Entries 4032843
TOF Mult
EPD-East Hit Cnt

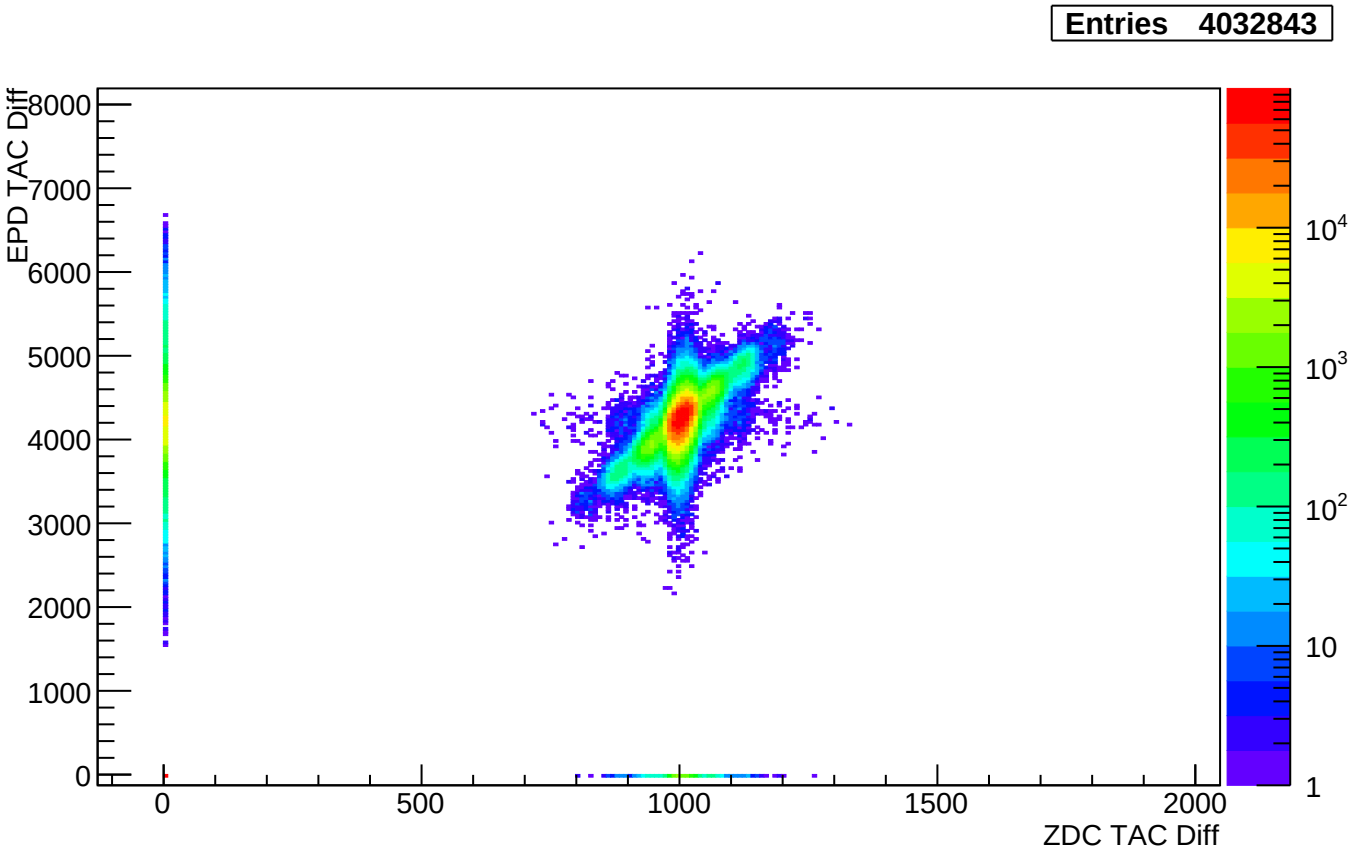

Entries 4032843
TOF Mult
EPD-West Hit Cnt

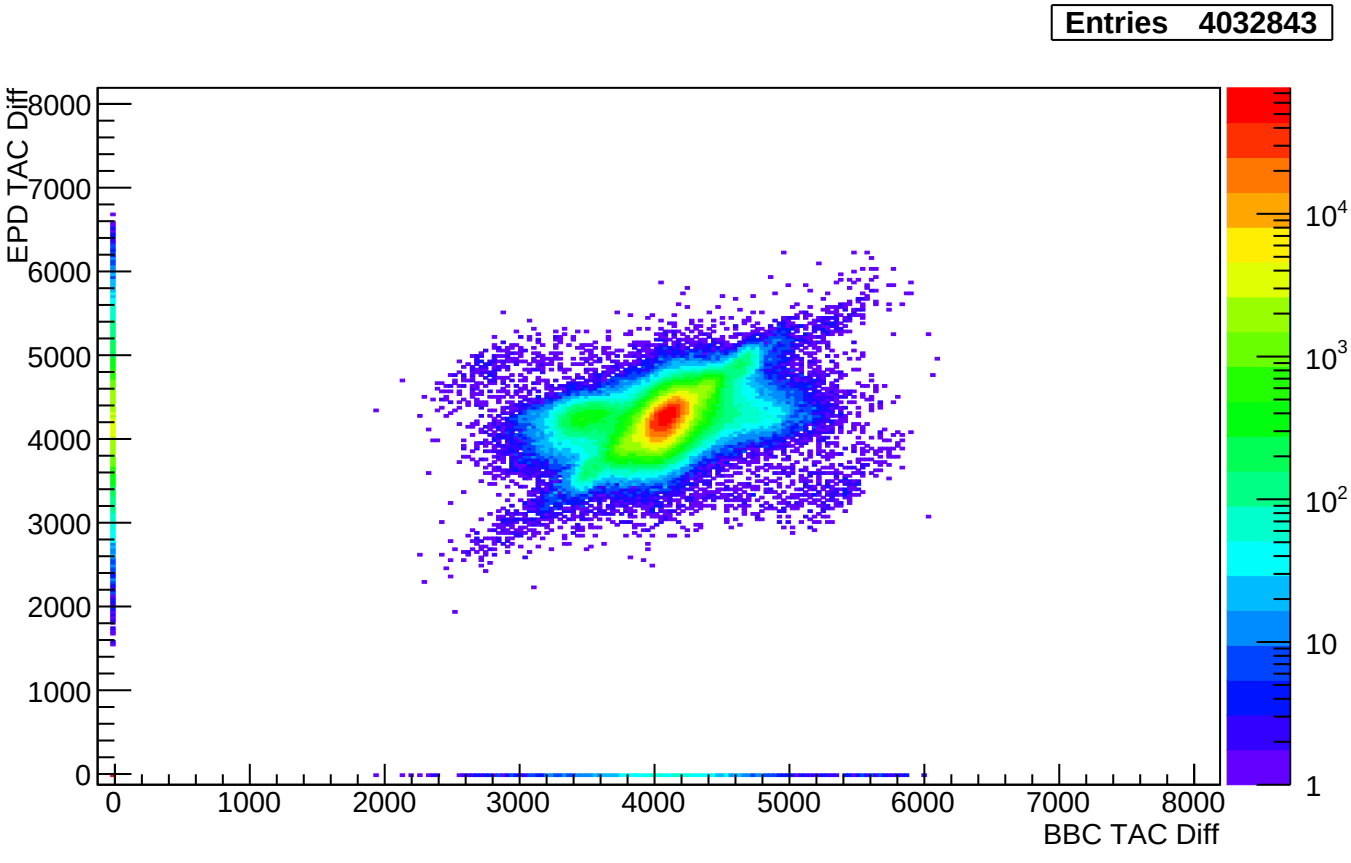

Entries 4032843
TOF Mult
VPD-East Hit Cnt

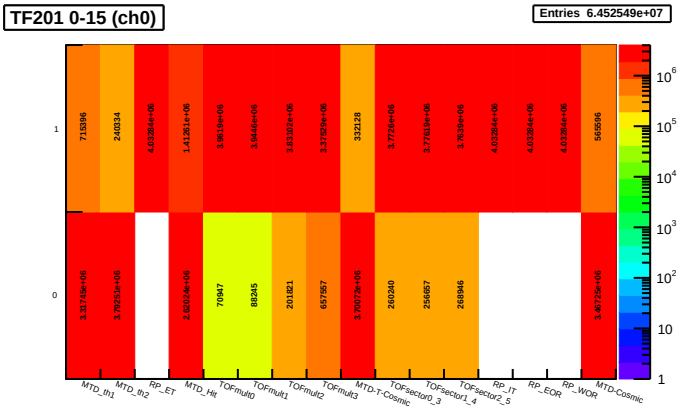

Entries 4032843
TOF Mult
VPD-West Hit Cnt

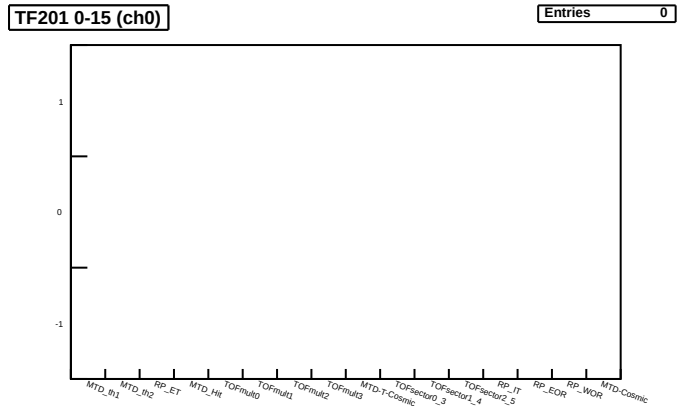

Entries 4032843
TOF Mult
ZDC-East ADC Sum Att

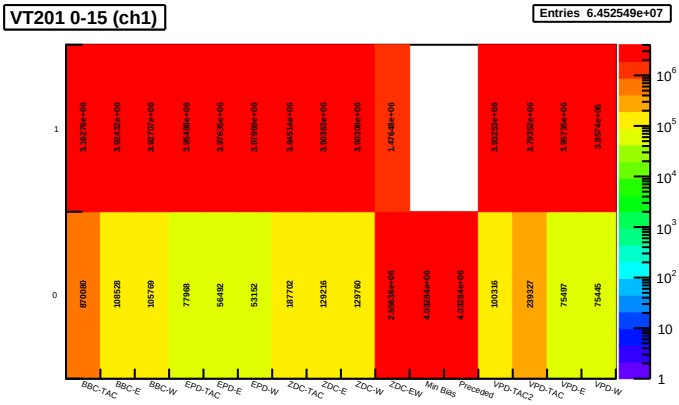

Entries 4032843
TOF Mult
ZDC-West ADC Sum Att

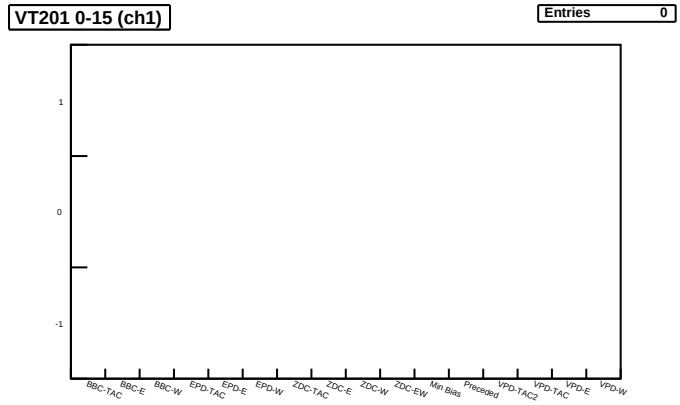

Entries 4032843
EPD TAC Diff
ZDC TAC Diff
0
500
1000
1500
2000
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2
10^3
10^4

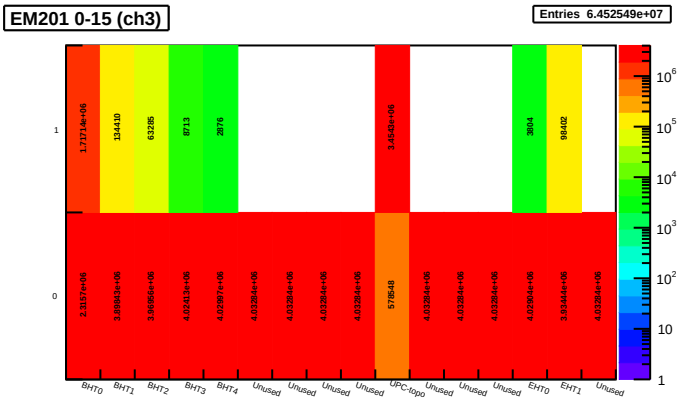

Entries 4032843
EPD TAC Diff
BBC TAC Diff
0
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2
10^3
10^4

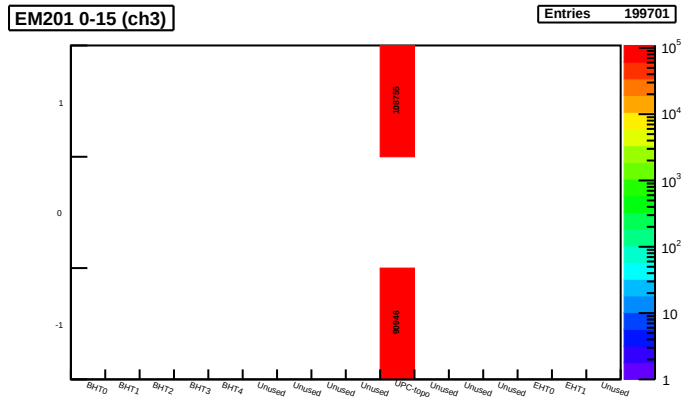

TF201 0-15 (ch0)
Entries 6.452549e+07
MTD_th1
MTD_th2
RP_ET
MTD_Hit
TOFmult0
TOFmult1
TOFmult2
TOFmult3
MTD-T-Cosmic
TOFsector0_3
TOFsector1_4
TOFsector2_5
RP_IT
RP_EOR
RP_WOR
MTD-Cosmic

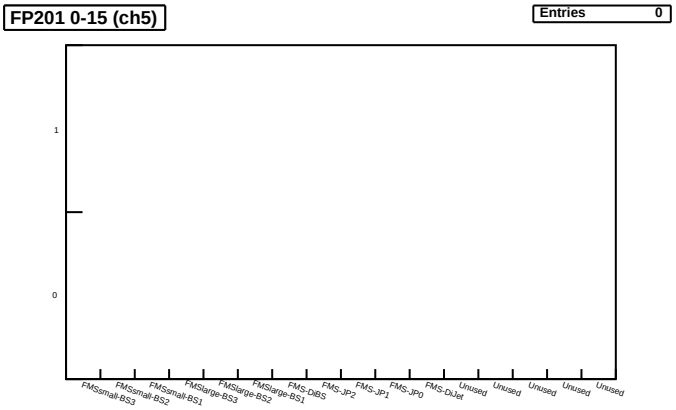

TF201 0-15 (ch0)
Entries 0
MTD_th1
MTD_th2
RP_ET
MTD_Hit
TOFmult0
TOFmult1
TOFmult2
TOFmult3
MTD-T-Cosmic
TOFsector0_3
TOFsector1_4
TOFsector2_5
RP_IT
RP_EOR
RP_WOR
MTD-Cosmic

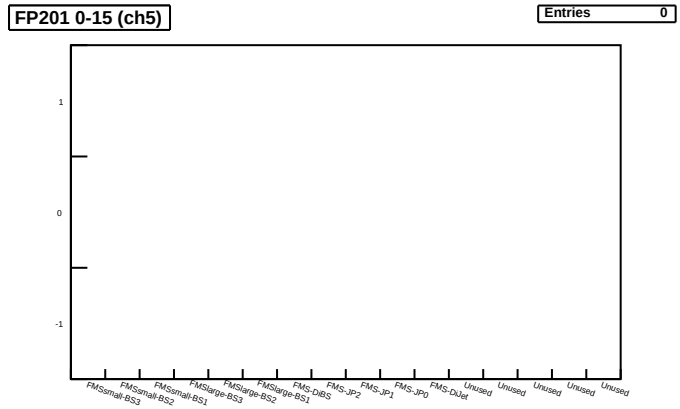

VT201 0-15 (ch1)
Entries 6.452549e+07
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-EW
Min Bias
Preceded
VPD-TAC2
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-EW
Min Bias
Preceded
VPD-TAC2
VPD-TAC
VPD-E
VPD-W


EM201 0-15 (ch3)
Entries 6.452549e+07
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1


EM201 0-15 (ch3)
Entries 199701
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused