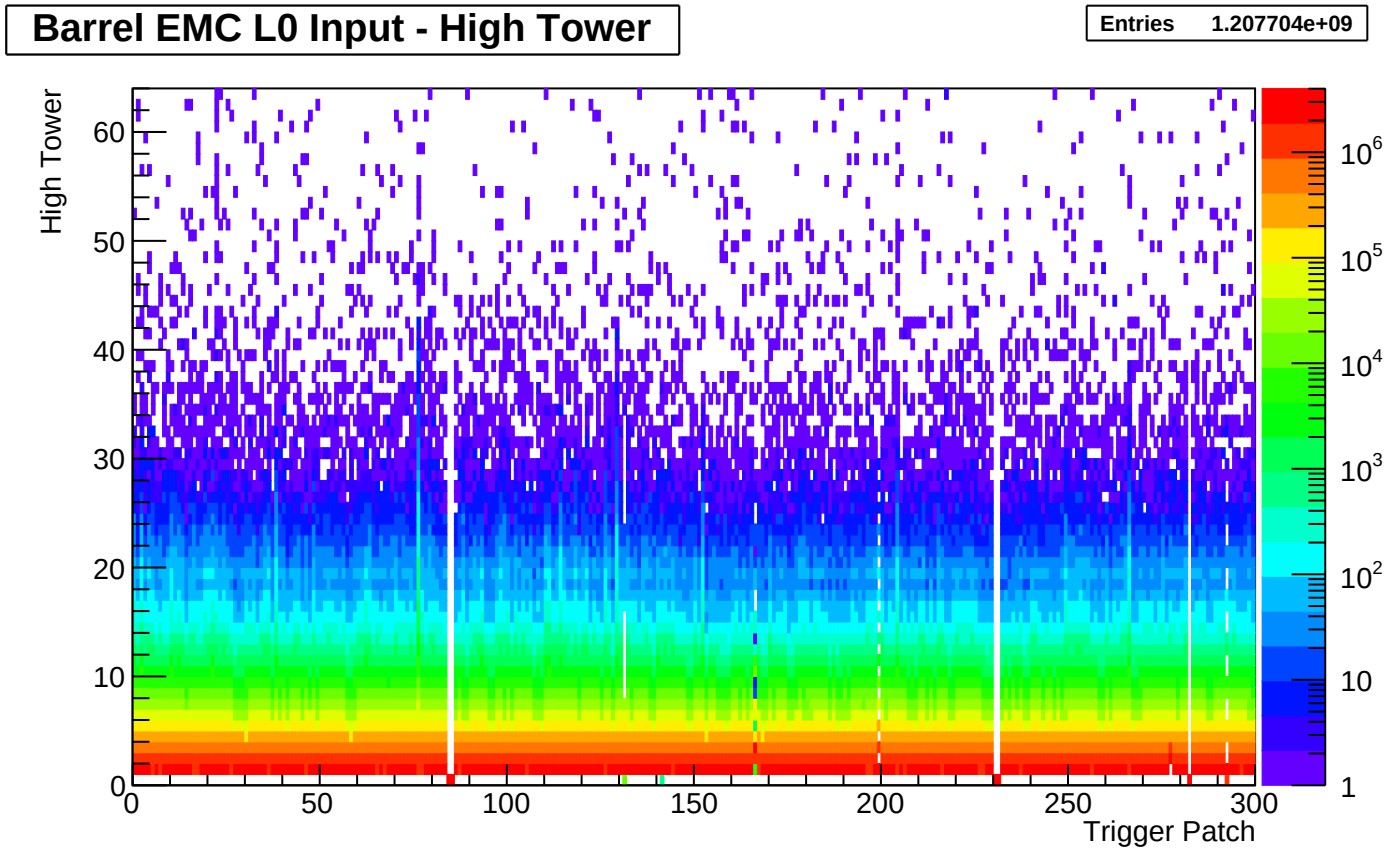
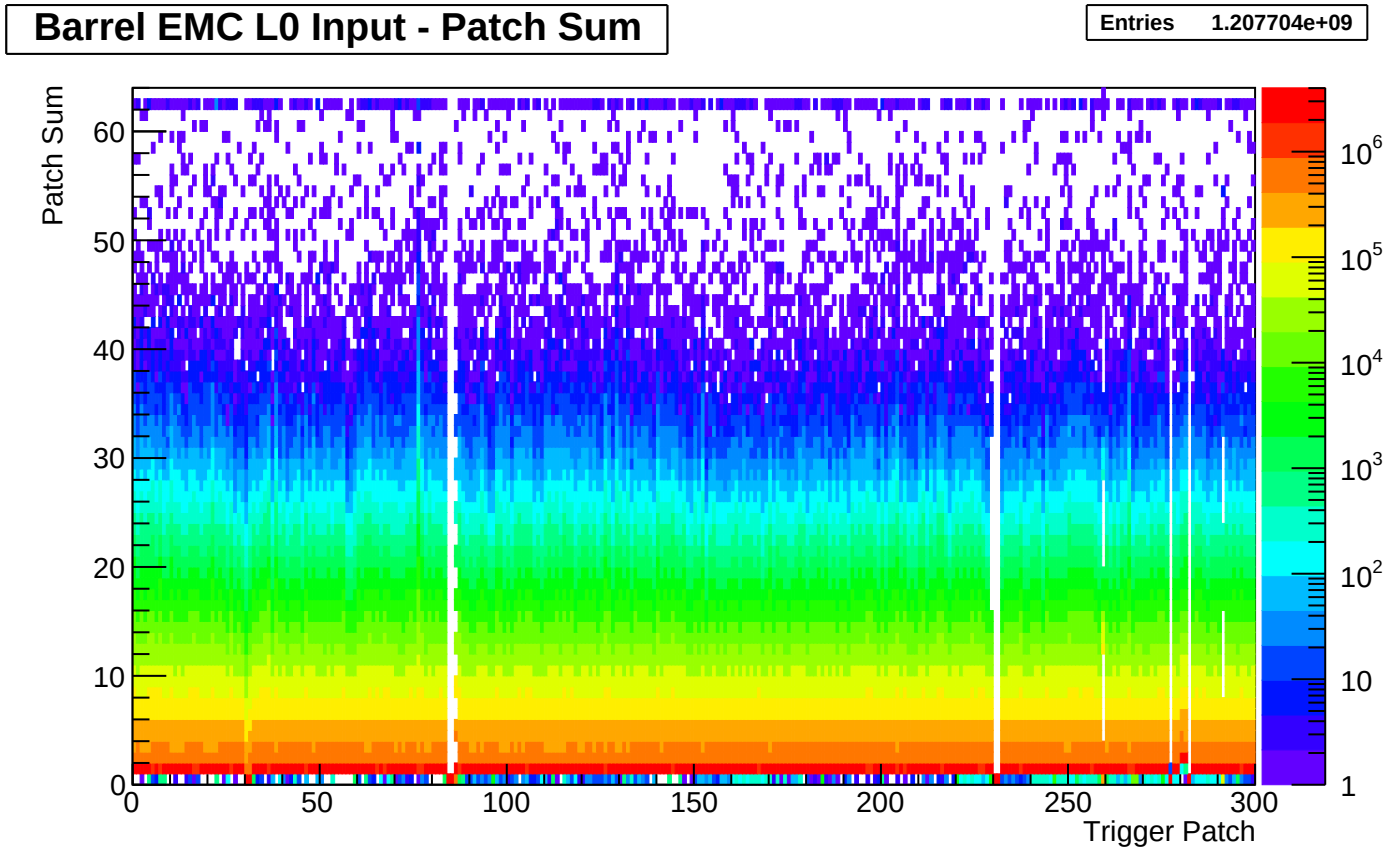
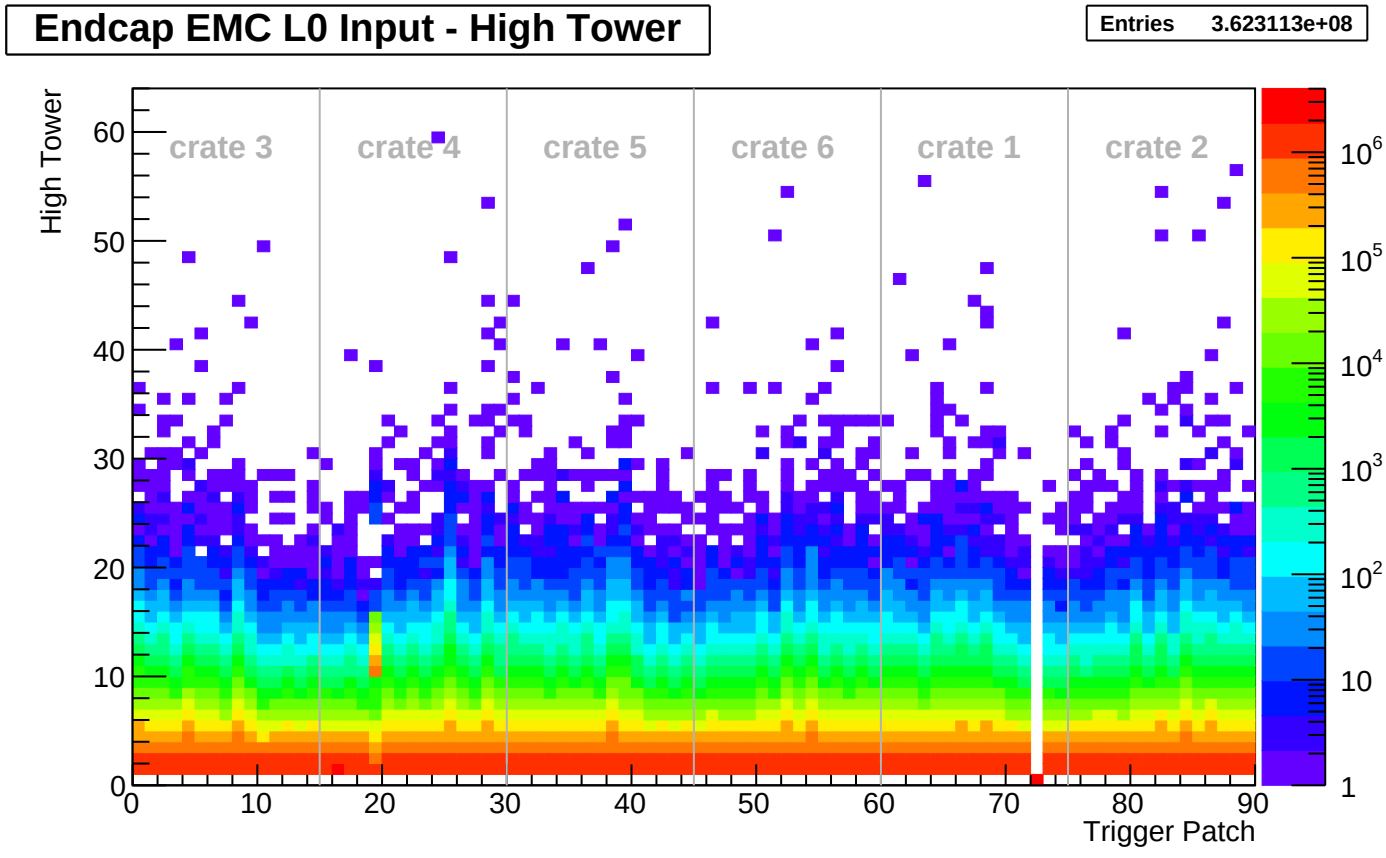
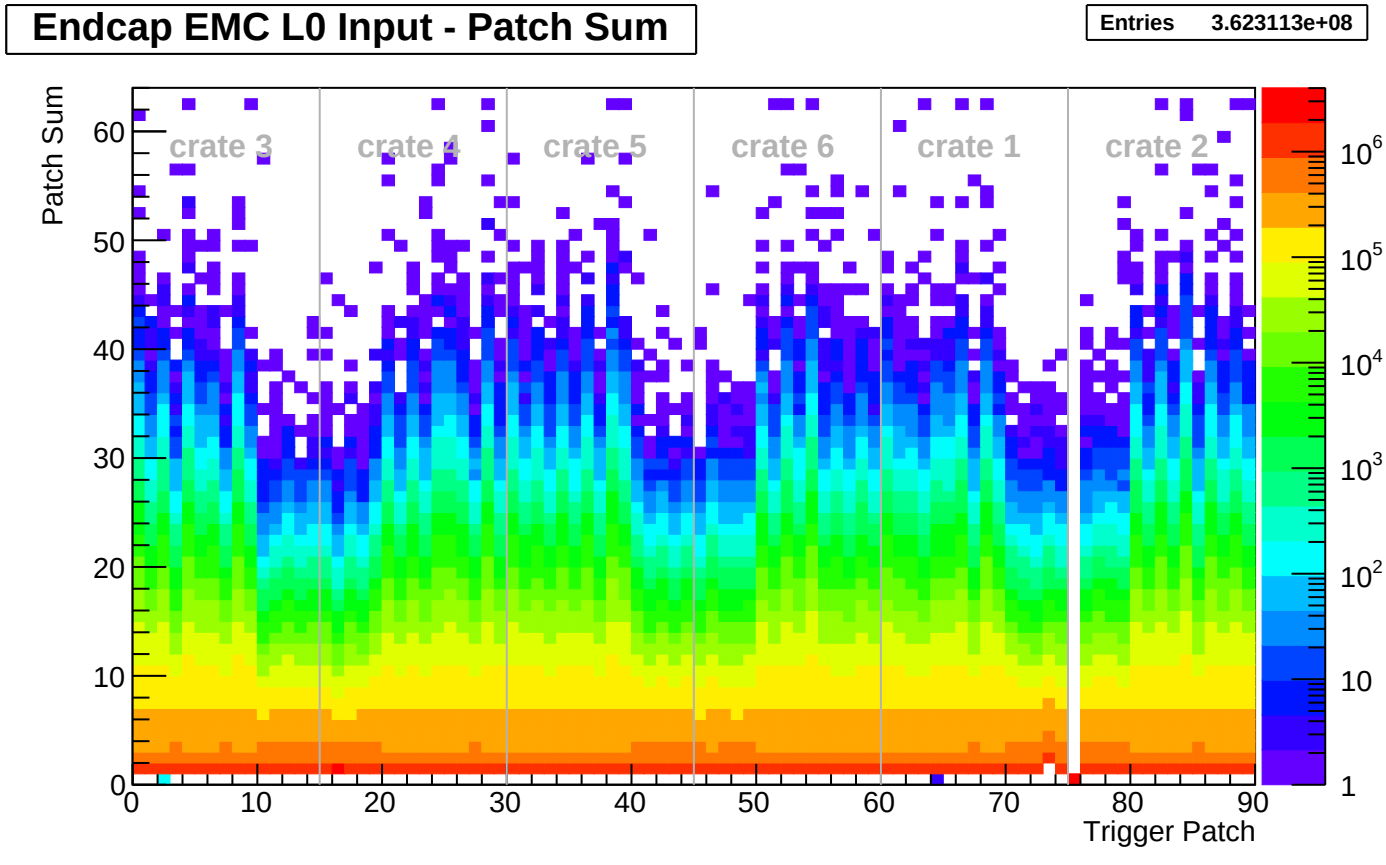
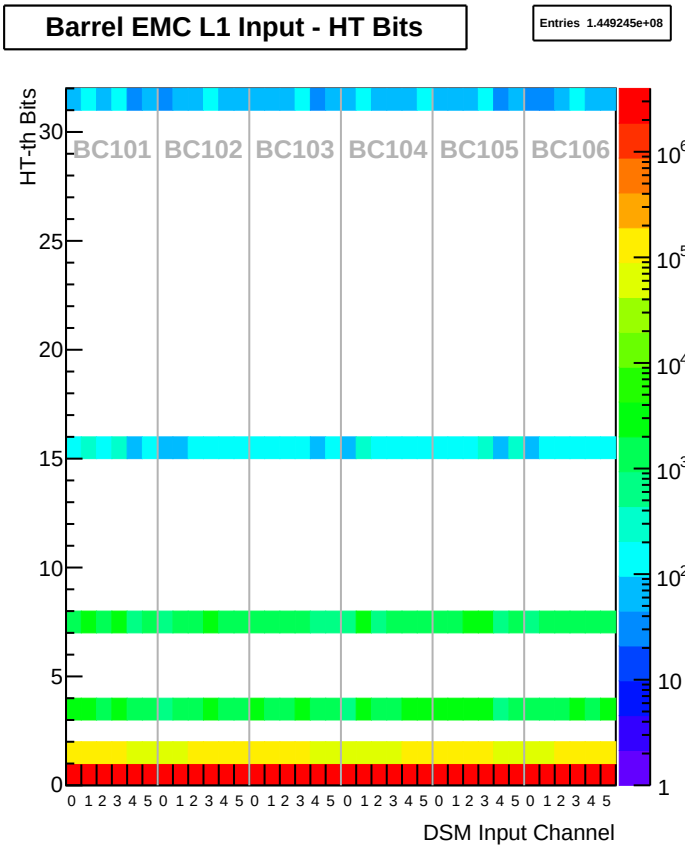
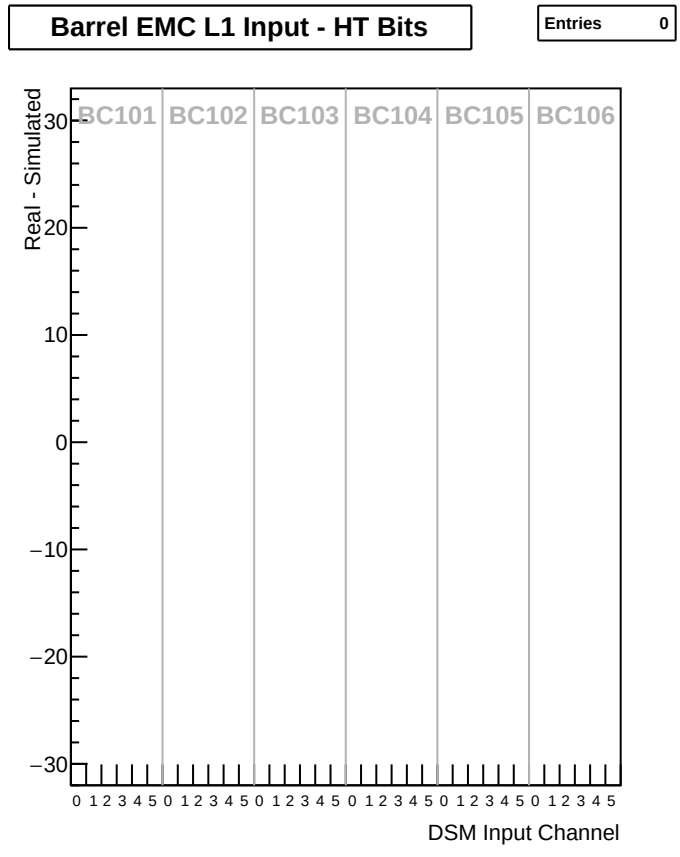

Barrel EMC L0 Input - High Tower
Entries 1.207704e+09
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

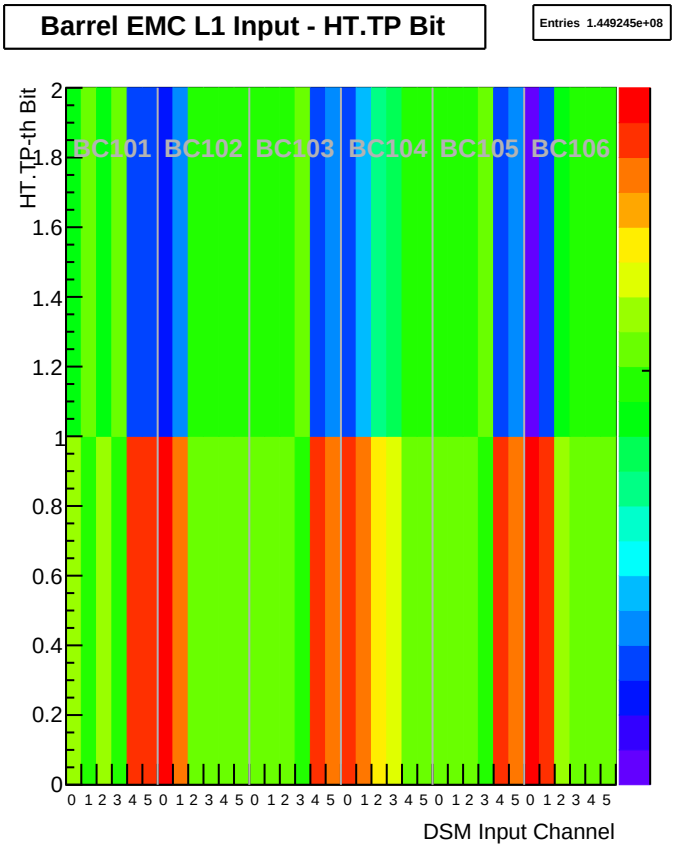

Barrel EMC L0 Input - Patch Sum
Entries 1.207704e+09
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

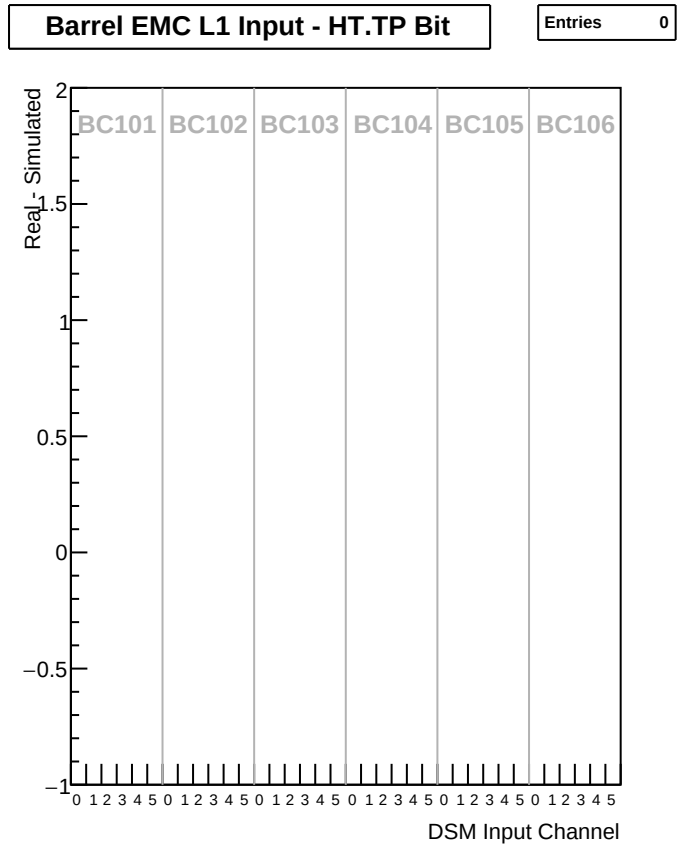

Endcap EMC L0 Input - High Tower
Entries 3.623113e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

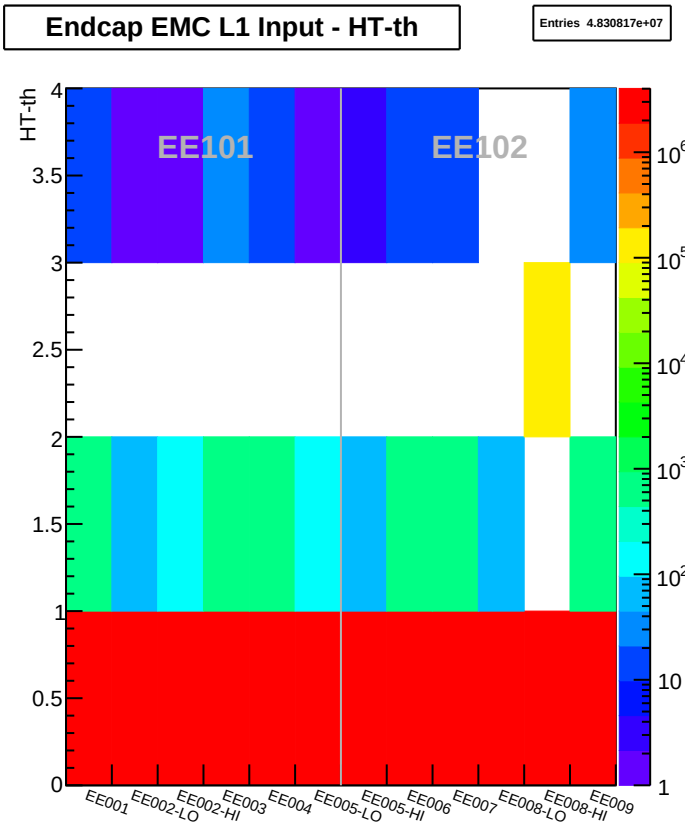

Endcap EMC L0 Input - Patch Sum
Entries 3.623113e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

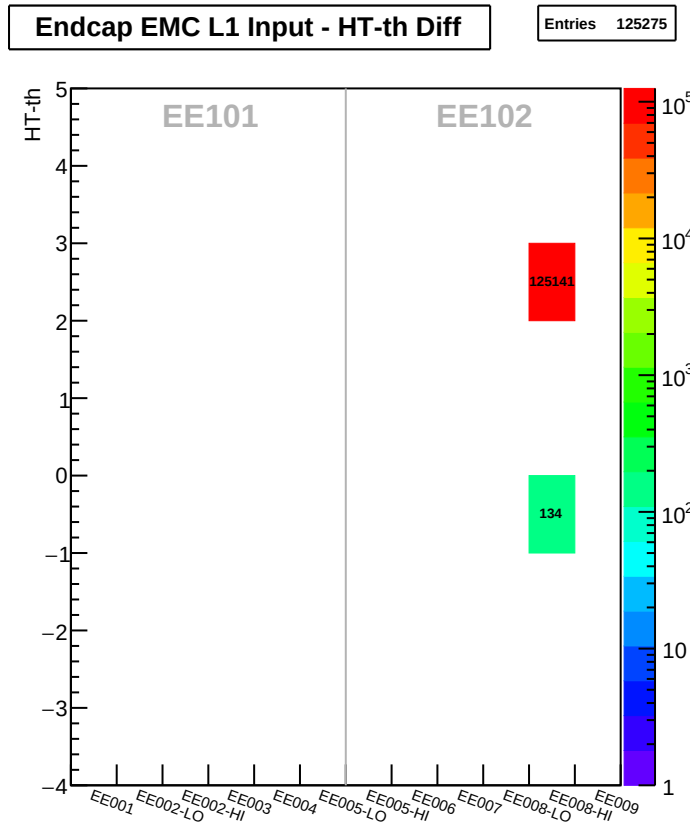

Barrel EMC L1 Input - HT Bits
Entries 1.449245e+08
HT-th Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

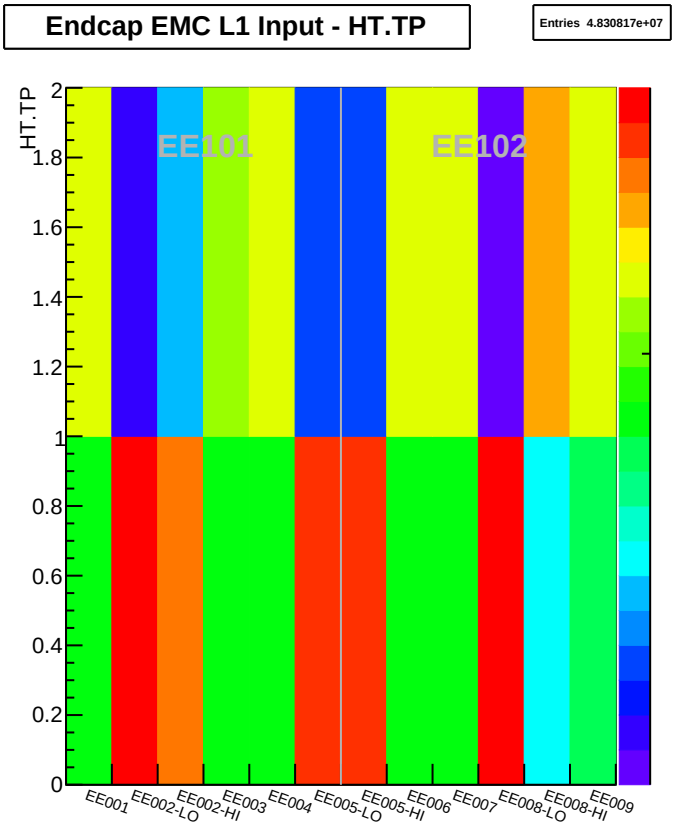

Barrel EMC L1 Input - HT Bits
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

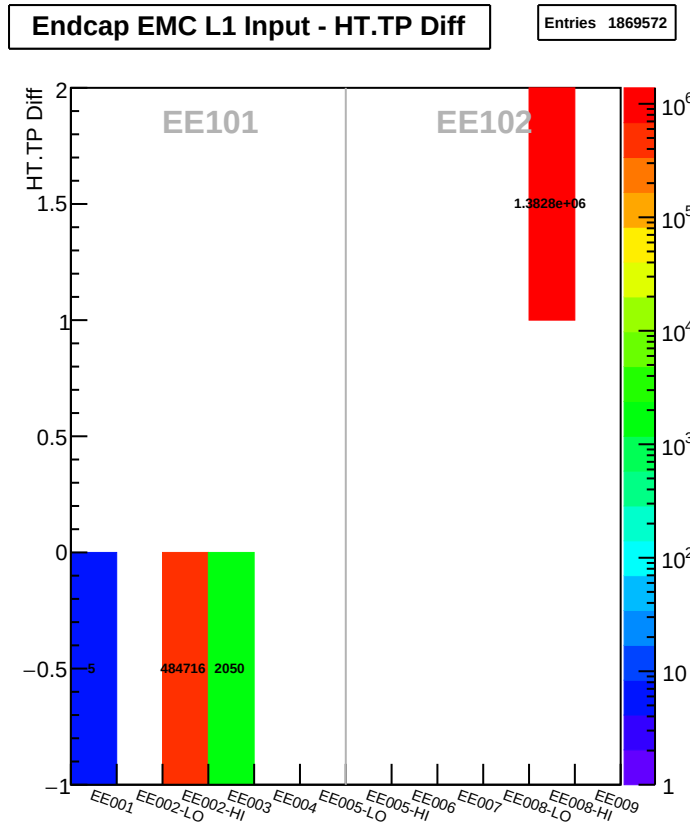

Barrel EMC L1 Input - HT.TP Bit
Entries 1.449245e+08
HT.TP-th Bit
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

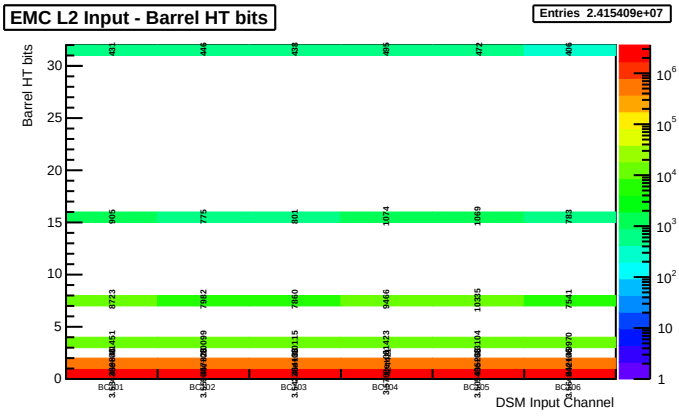

Barrel EMC L1 Input - HT.TP Bit
Entries 0
Real - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

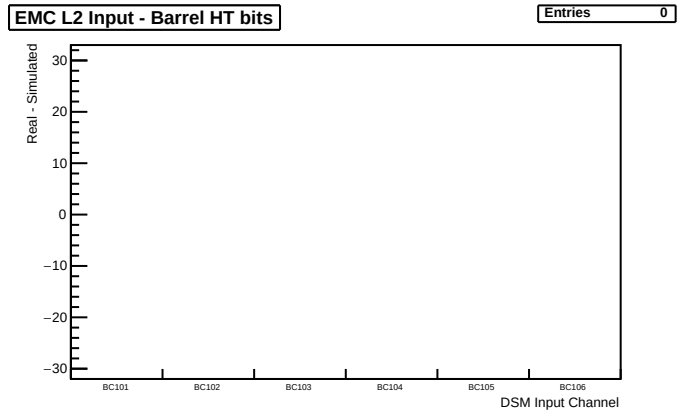

Endcap EMC L1 Input - HT-th
Entries 4.830817e+07
HT-th
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

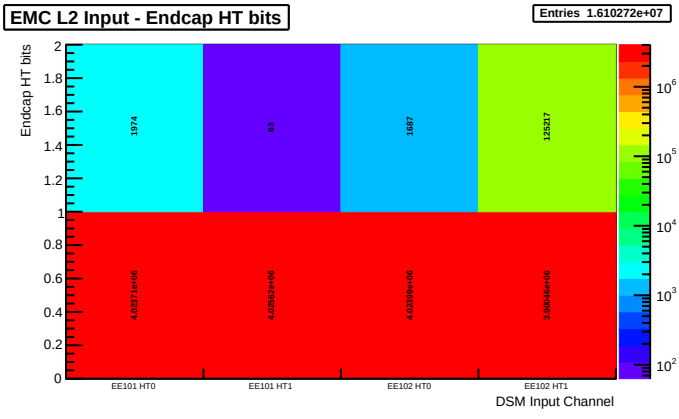

Endcap EMC L1 Input - HT-th Diff
Entries 125275
HT-th
EE101
EE102
125141
134
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

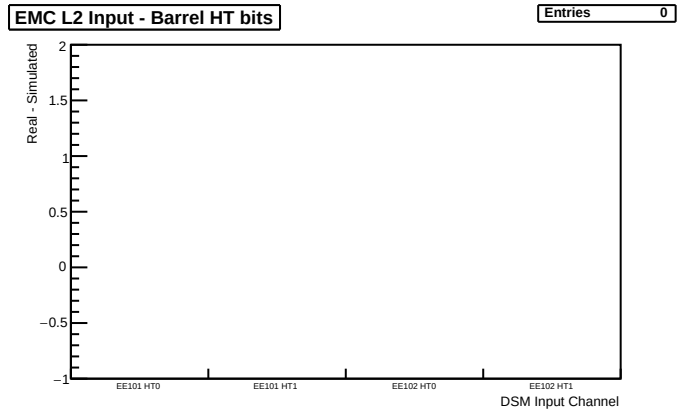

Endcap EMC L1 Input - HT.TP
Entries 4.830817e+07
HT.TP
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

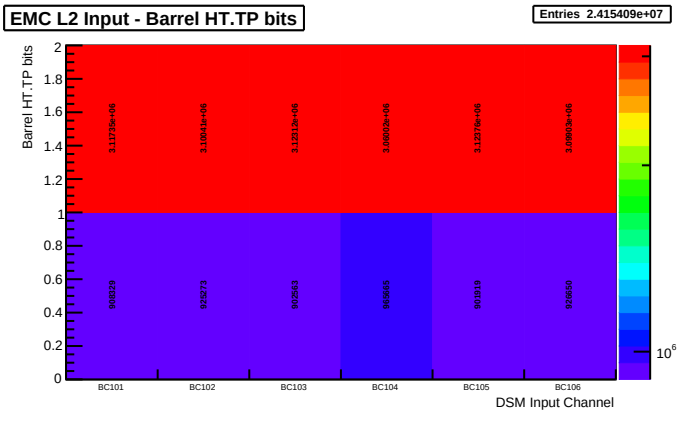

Endcap EMC L1 Input - HT.TP Diff
Entries 1869572
HT.TP Diff
EE101
EE102
1.3828e+06
-5
484716
2050
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

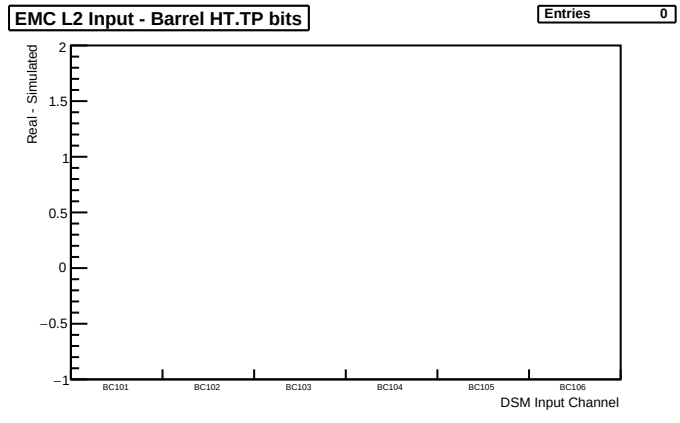

EMC L2 Input - Barrel HT bits
Entries 2.415409e+07
Barrel HT bits
DSM Input Channel

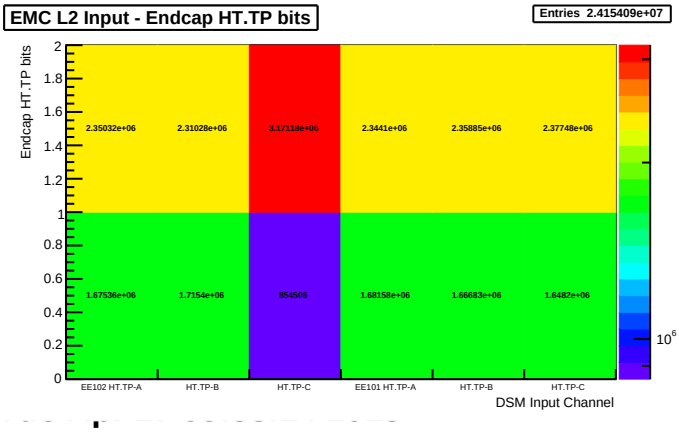

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel

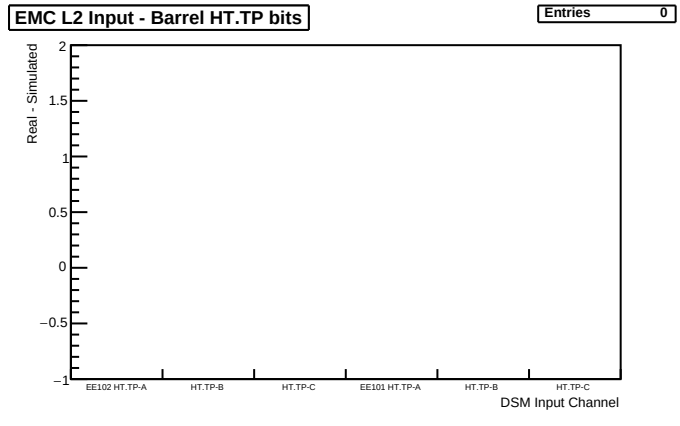

EMC L2 Input - Endcap HT bits
Entries 1.610272e+07
Endcap HT bits
DSM Input Channel

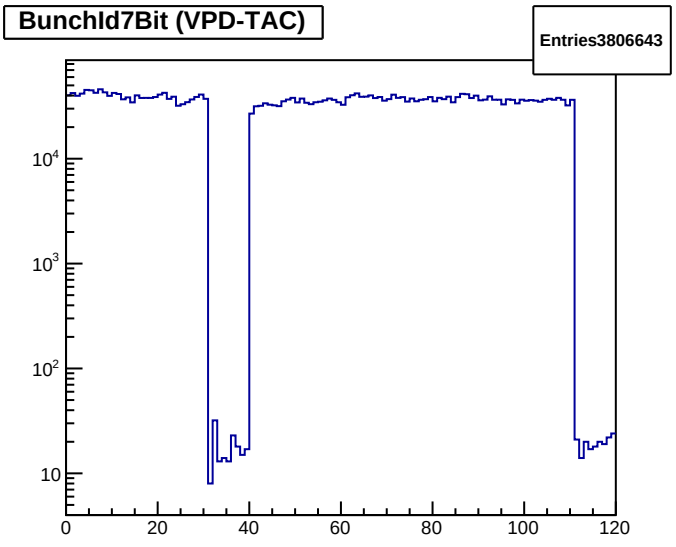

EMC L2 Input - Barrel HT bits
Entries 0
Real - Simulated
DSM Input Channel

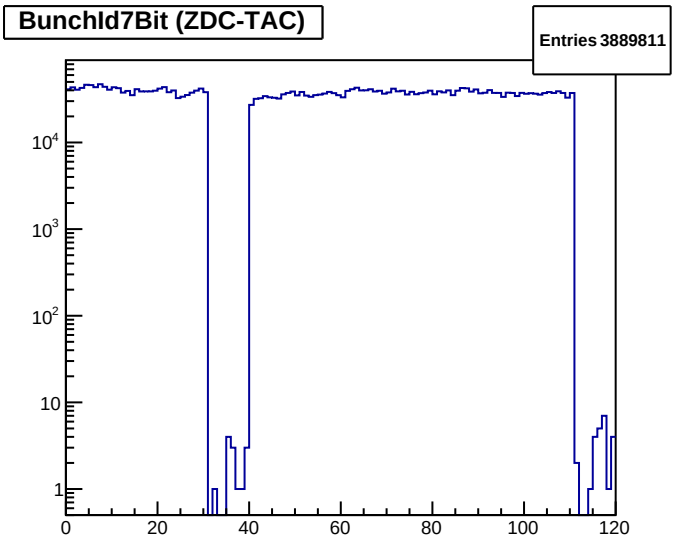

EMC L2 Input - Barrel HT.TP bits
Entries 2.415409e+07
Barrel HT.TP bits
DSM Input Channel

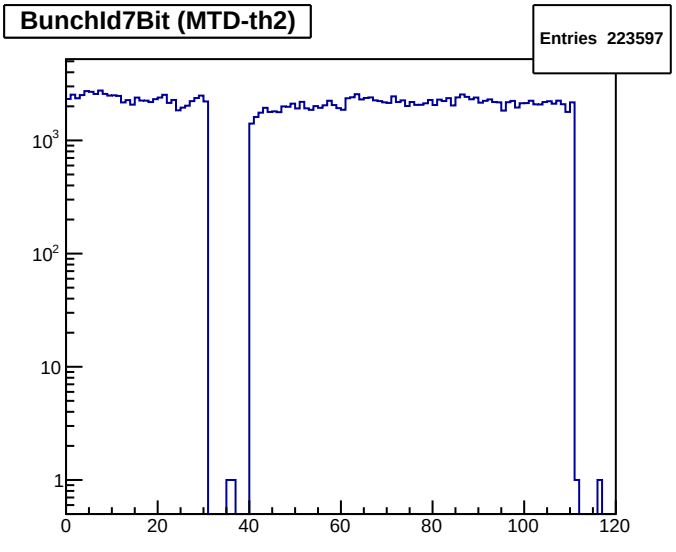

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel

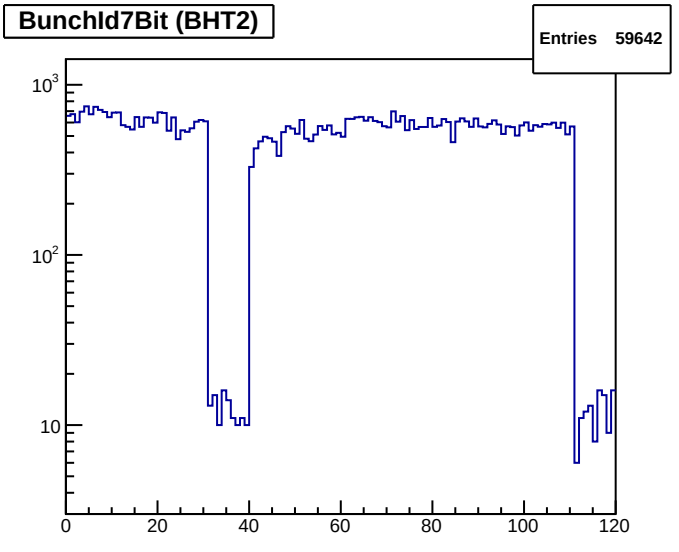

EMC L2 Input - Endcap HT.TP bits
Entries 2.415409e+07
Endcap HT.TP bits
DSM Input Channel

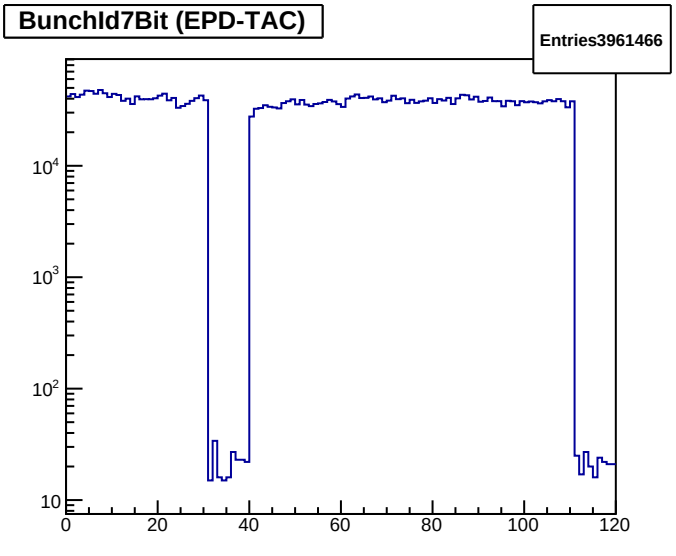

EMC L2 Input - Barrel HT.TP bits
Entries 0
Real - Simulated
DSM Input Channel

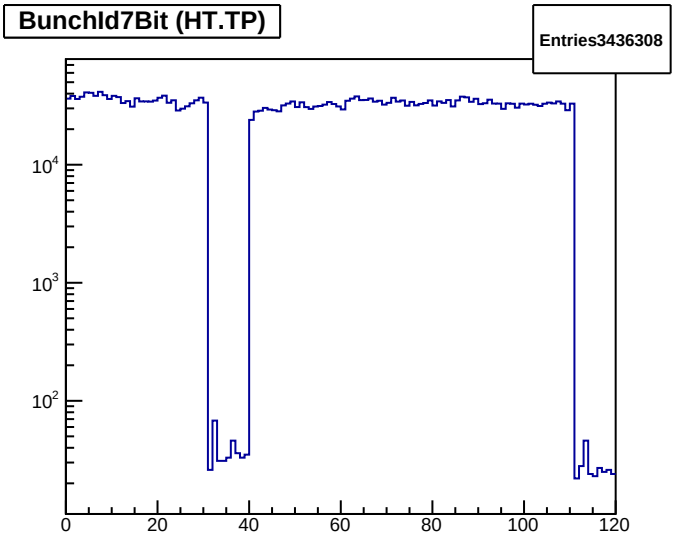

BunchId7Bit (VPD-TAC)
Entries3806643

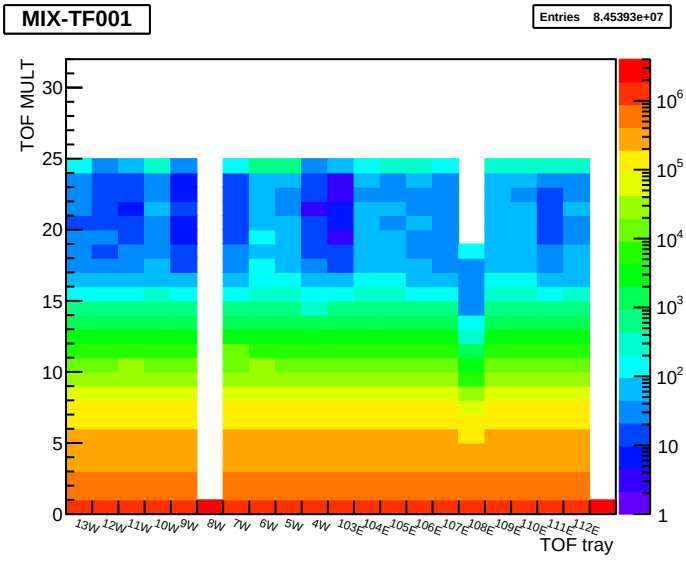

BunchId7Bit (ZDC-TAC)
Entries 3889811

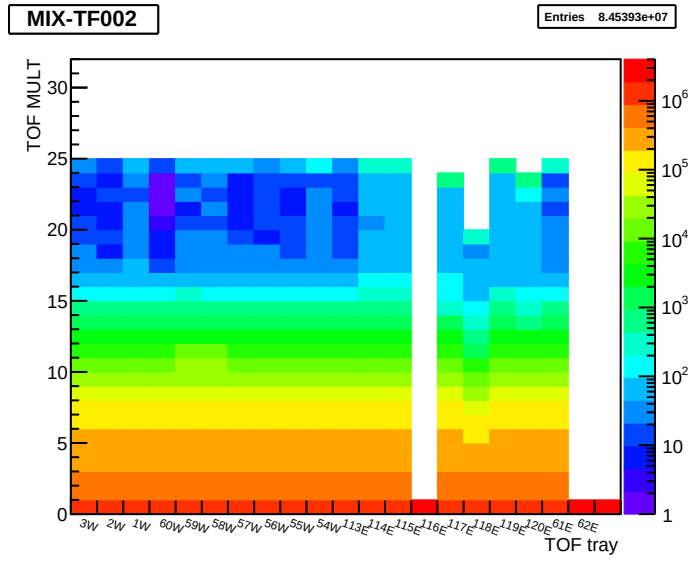

BunchId7Bit (MTD-th2)
Entries 223597

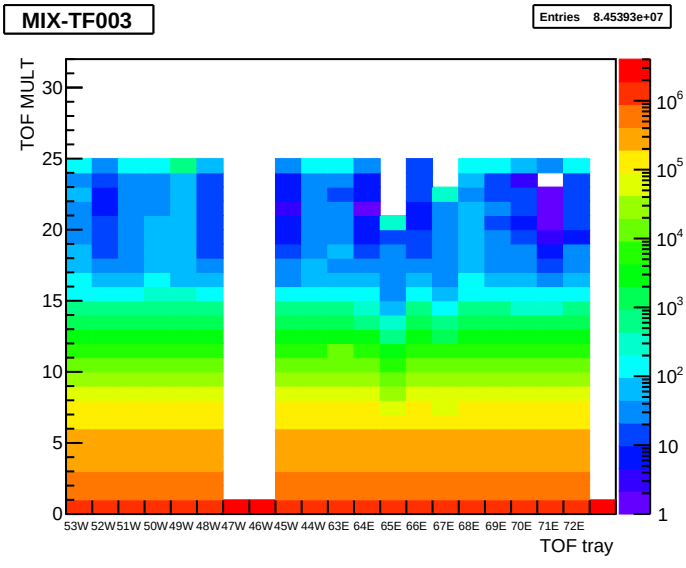

BunchId7Bit (BHT2)
Entries 59642

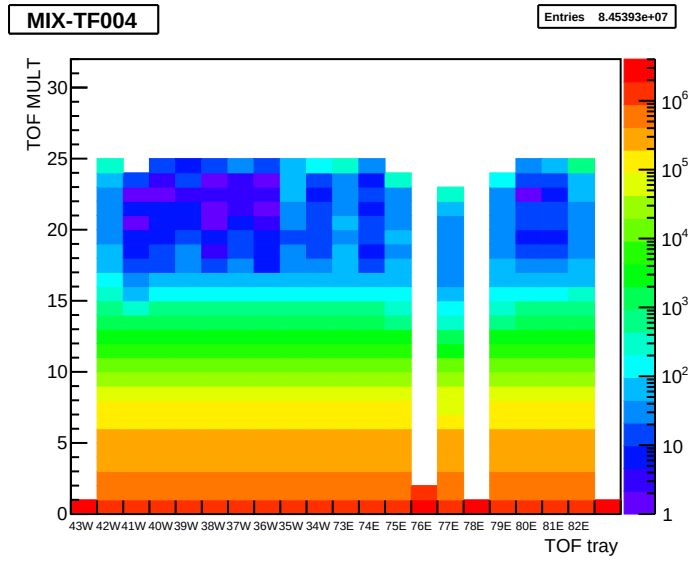

BunchId7Bit (EPD-TAC)
Entries3961466

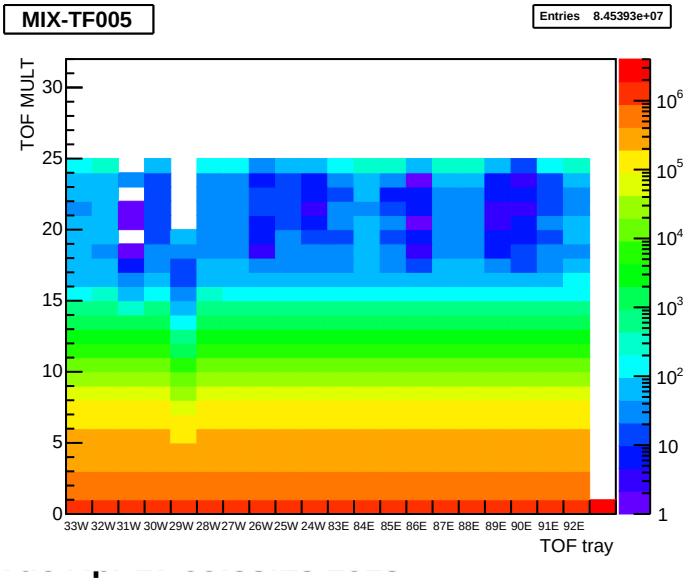

BunchId7Bit (HT.TP)
Entries3436308

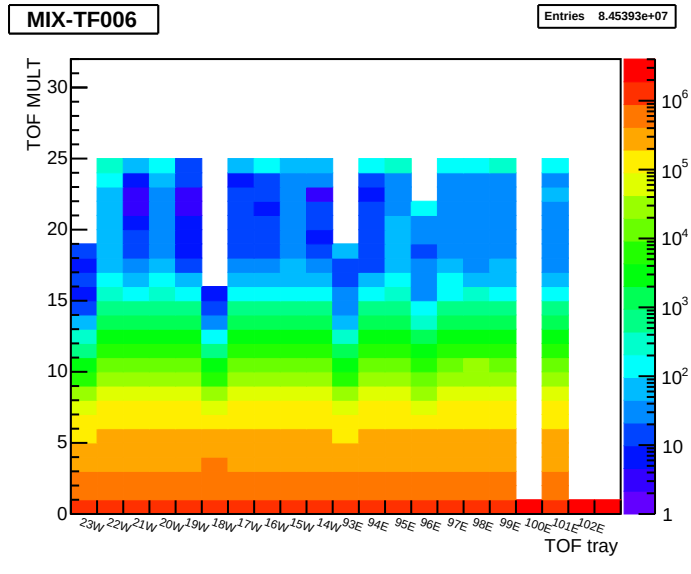

MIX-TF001
Entries 8.45393e+07
TOF MULT
TOF tray

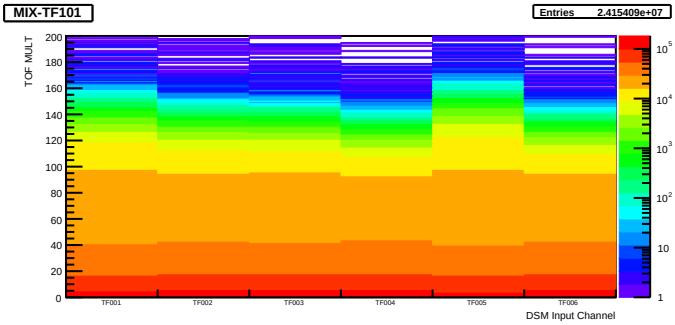

MIX-TF002
Entries 8.45393e+07
TOF MULT
TOF tray

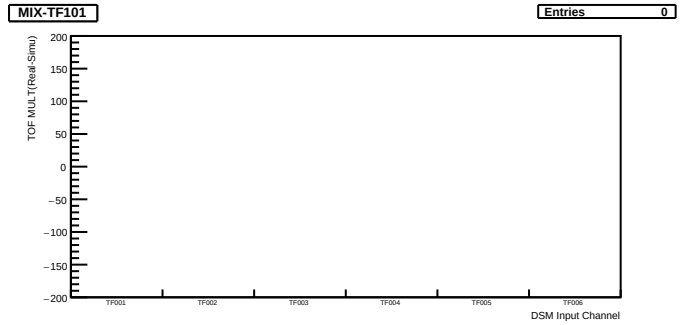

MIX-TF003
Entries 8.45393e+07
TOF MULT
TOF tray

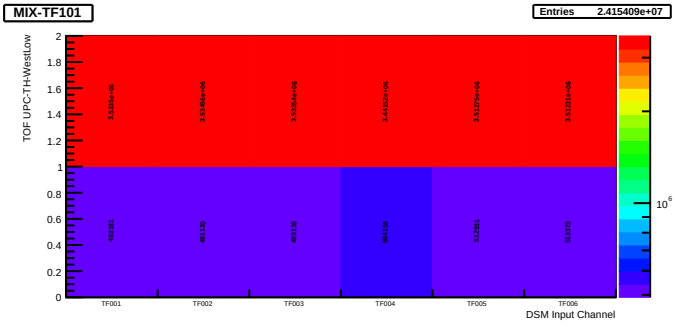

MIX-TF004
Entries 8.45393e+07
TOF MULT
TOF tray

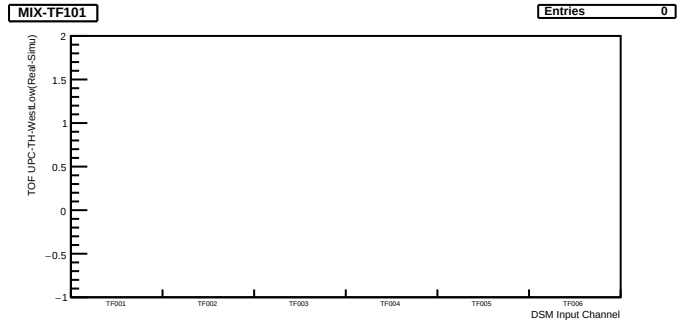

MIX-TF005
Entries 8.45393e+07
TOF MULT
TOF tray

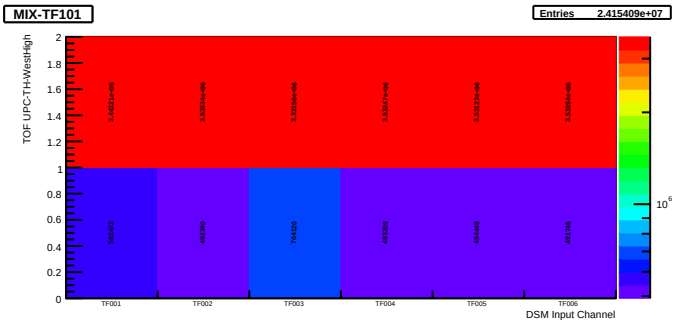

MIX-TF006
Entries 8.45393e+07
TOF MULT
TOF tray

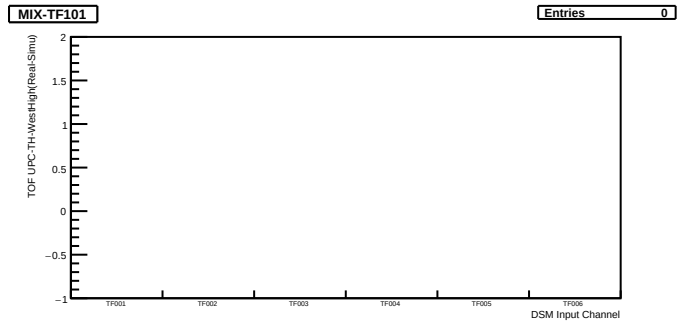

MIX-TF101
Entries 2.415409e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

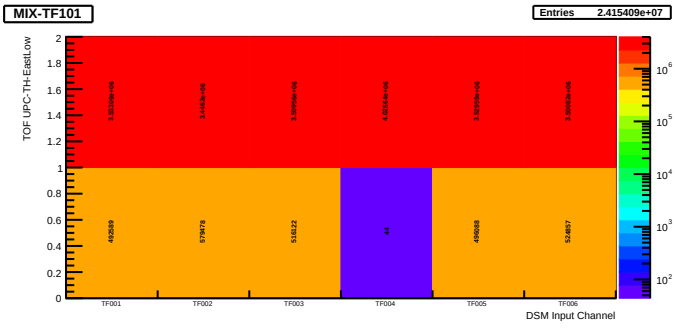

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

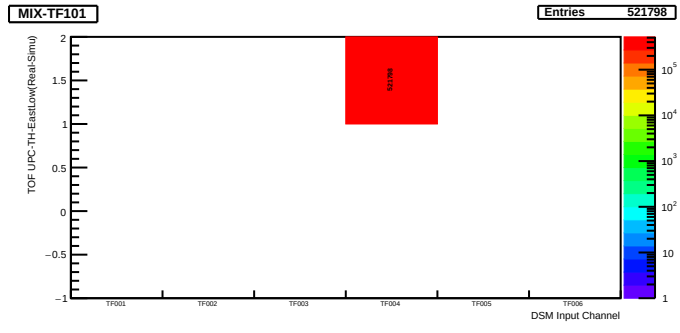

MIX-TF101
Entries 2.415409e+07
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

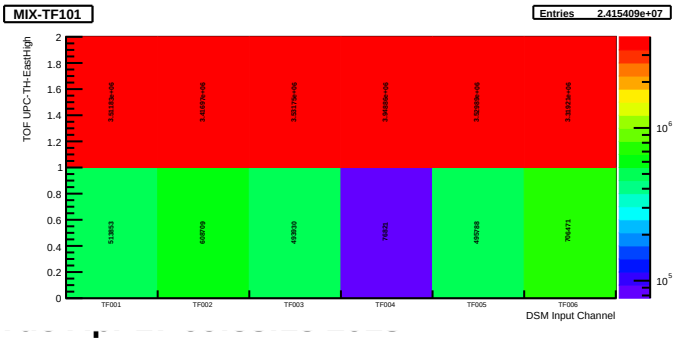

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

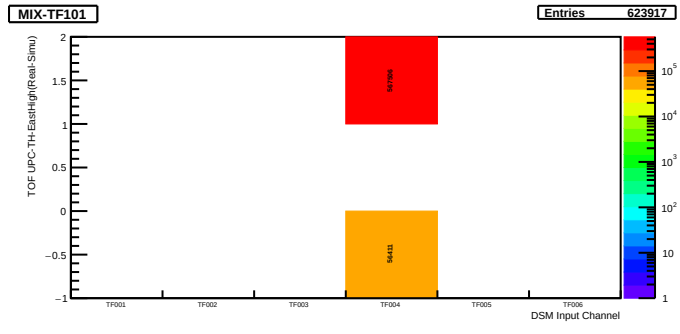

MIX-TF101
Entries 2.415409e+07
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

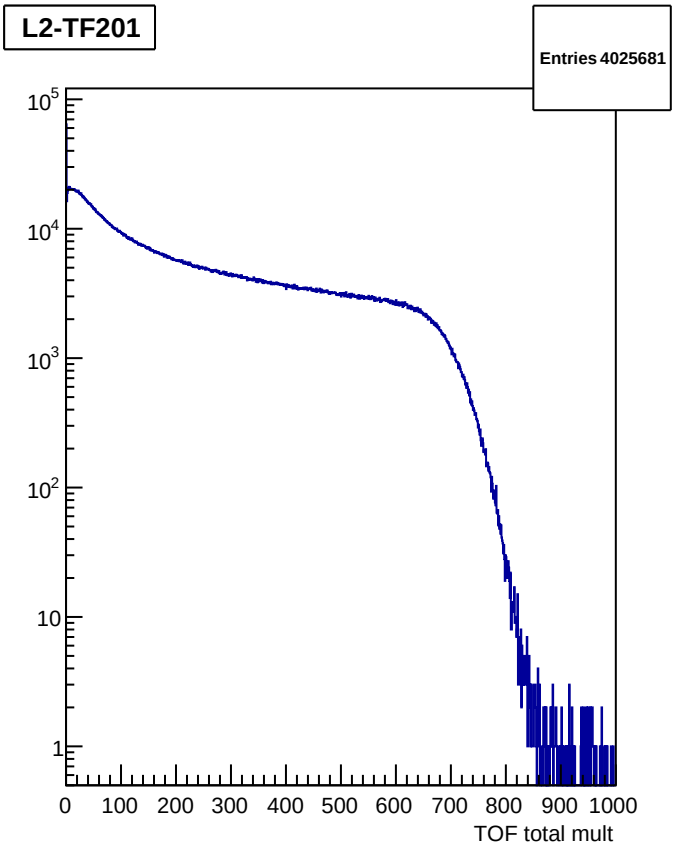

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

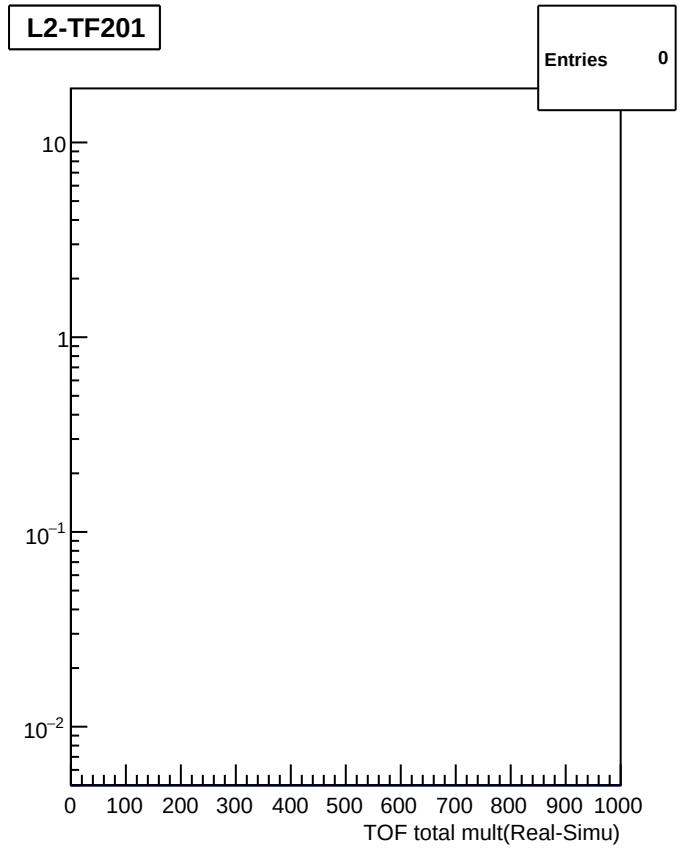

MIX-TF101
Entries 2.415409e+07
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

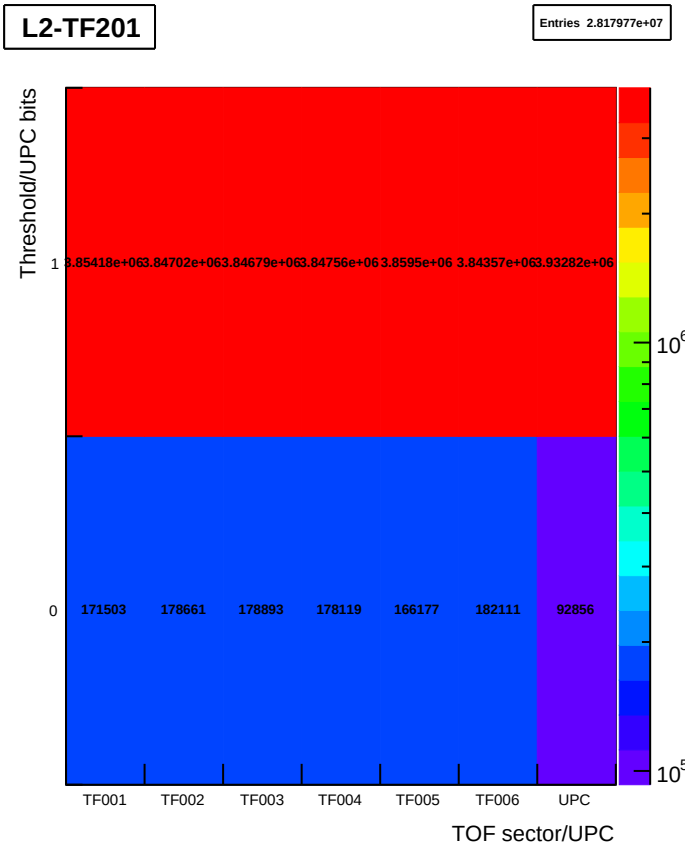

MIX-TF101
Entries 521798
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

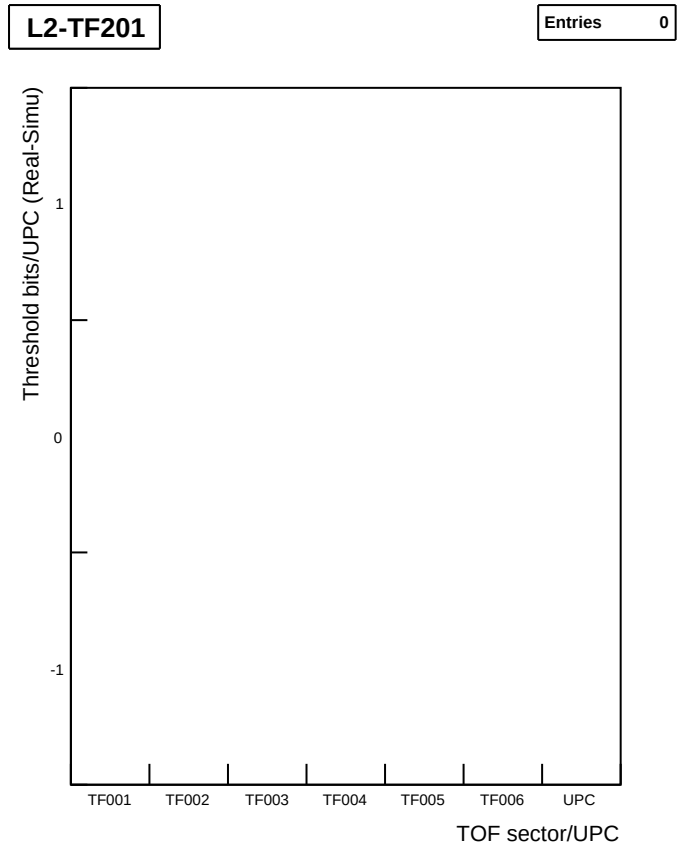

MIX-TF101
Entries 2.415409e+07
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

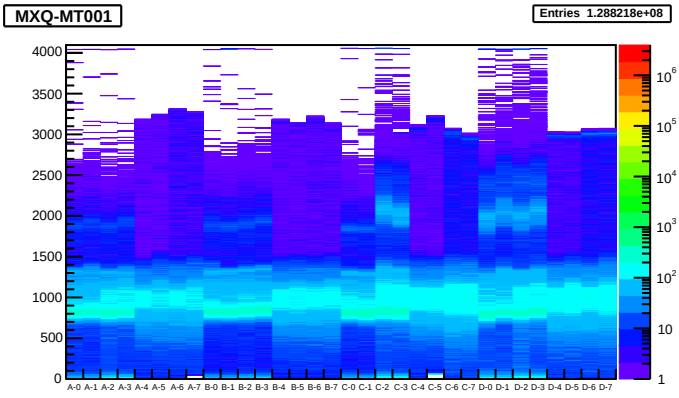

MIX-TF101
Entries 623917
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

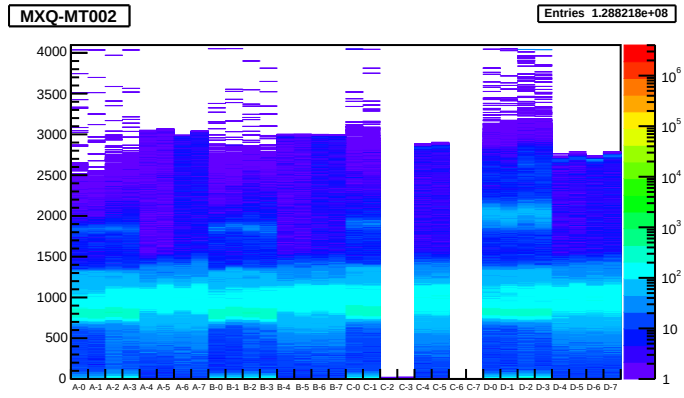

L2-TF201
Entries 4025681
TOF total mult


L2-TF201
Entries 0
TOF total mult(Real-Simu)


L2-TF201
Entries 2.817977e+07
Threshold/UPC bits
3.85418e+06 3.84702e+06 3.84679e+06 3.84756e+06 3.8595e+06 3.84357e+06 3.93282e+06
171503 178661 178893 178119 166177 182111 92856
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC


L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC


MXQ-MT001
Entries 1.288218e+08
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1


MXQ-MT002
Entries 1.288218e+08
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2500
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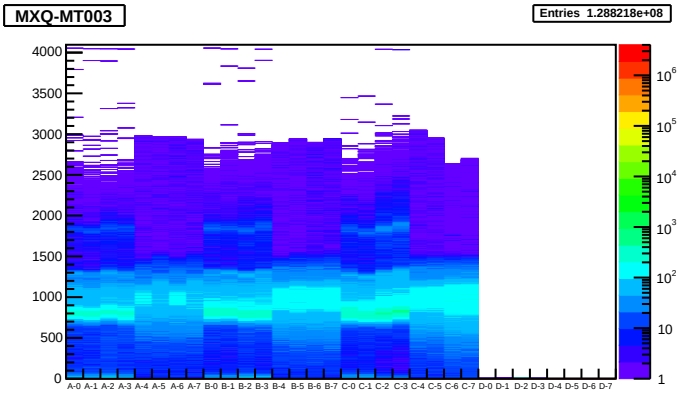
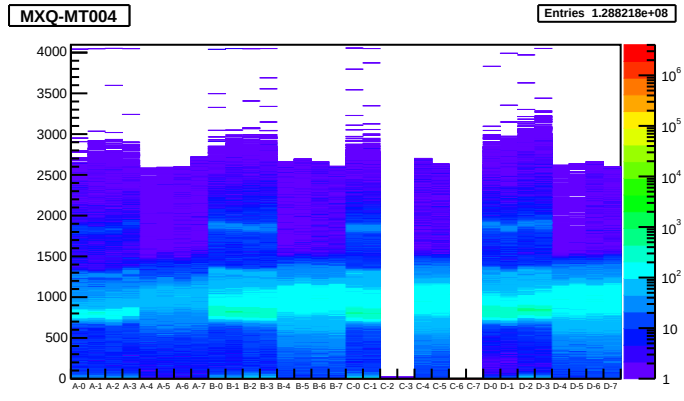
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1


MXQ-MT003
Entries 1.288218e+08
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1


MXQ-MT004
Entries 1.288218e+08
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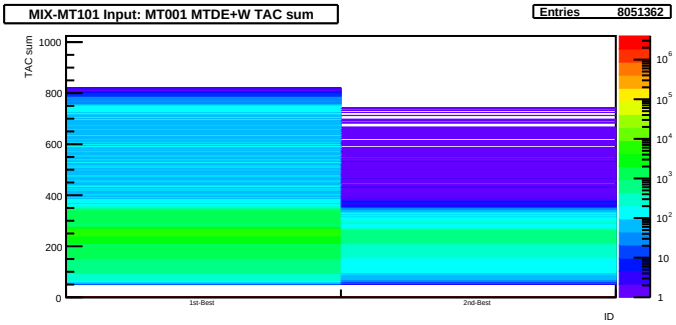
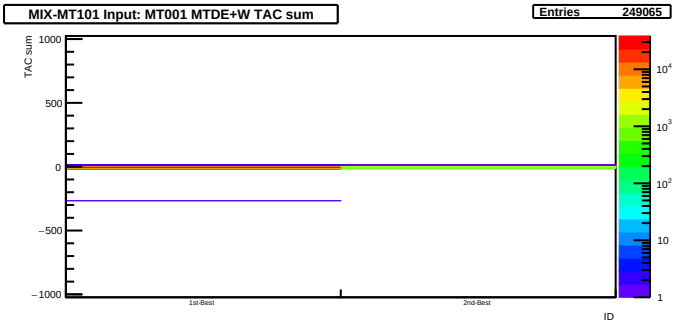
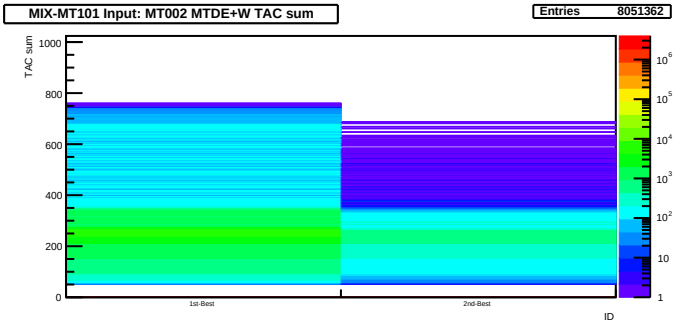
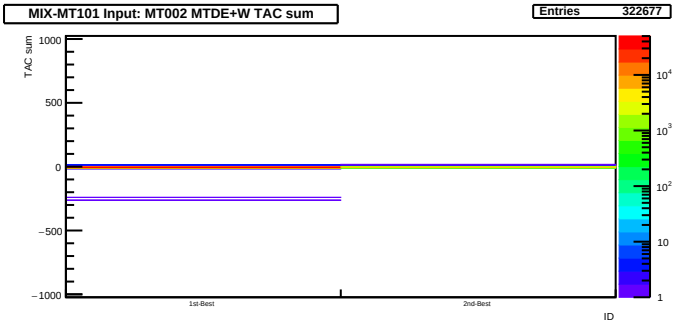
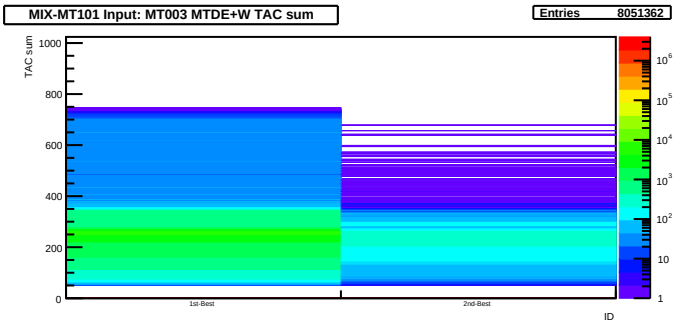
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^6
10^5
10^4
10^3
10^2
10
1

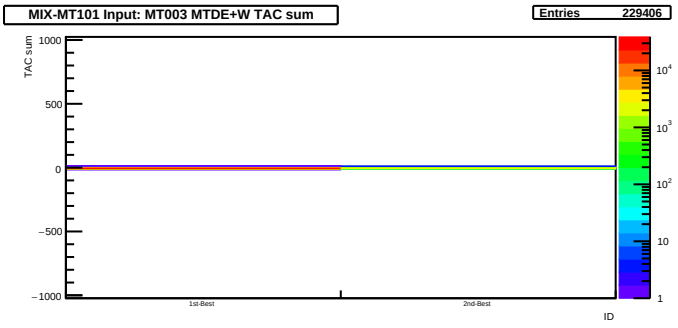

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 8051362
TAC sum
1st-Best
2nd-Best
ID

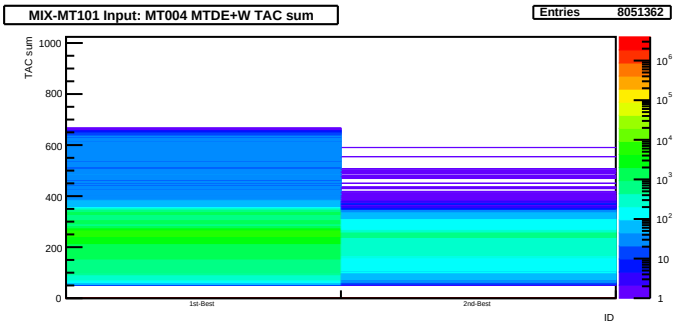

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 249065
TAC sum
1st-Best
2nd-Best
ID

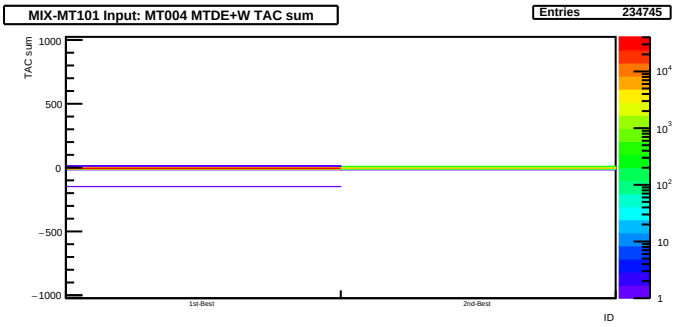

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 8051362
TAC sum
1st-Best
2nd-Best
ID

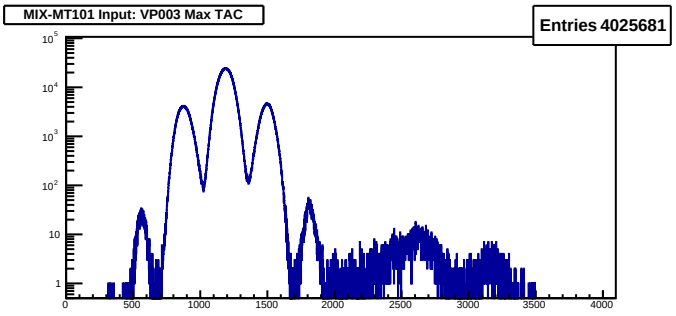

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 322677
TAC sum
1st-Best
2nd-Best
ID

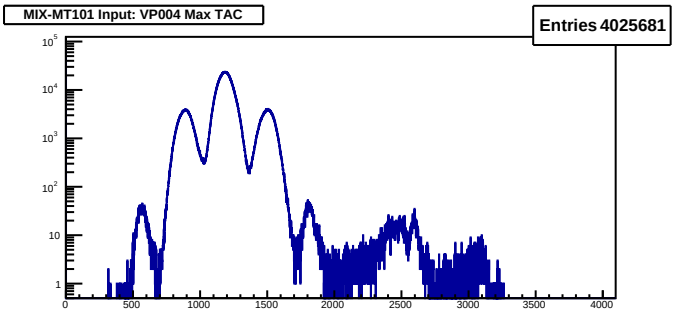

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 8051362
TAC sum
1st-Best
2nd-Best
ID

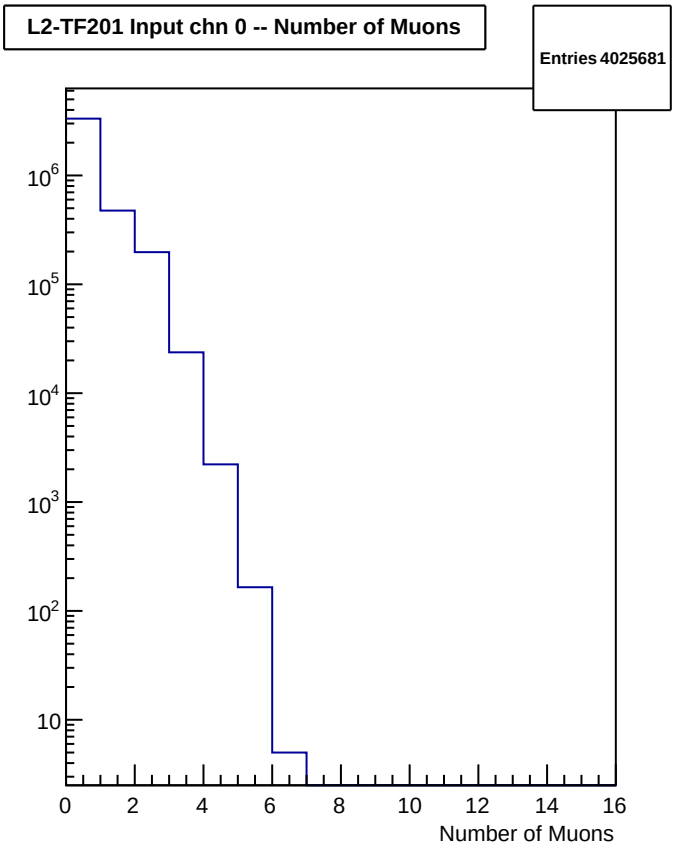

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 229406
TAC sum
1st-Best
2nd-Best
ID

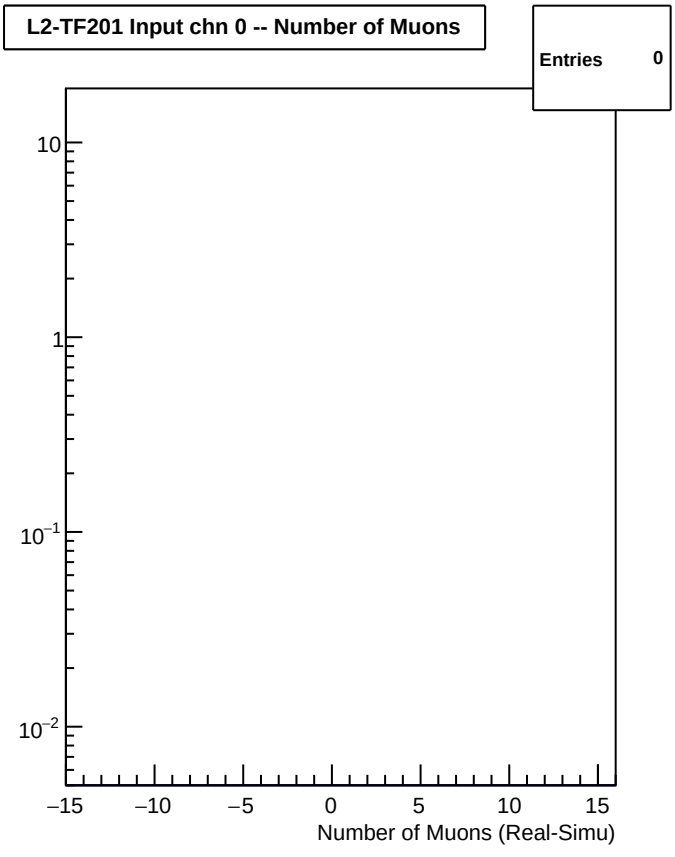

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 8051362
TAC sum
1st-Best
2nd-Best
ID

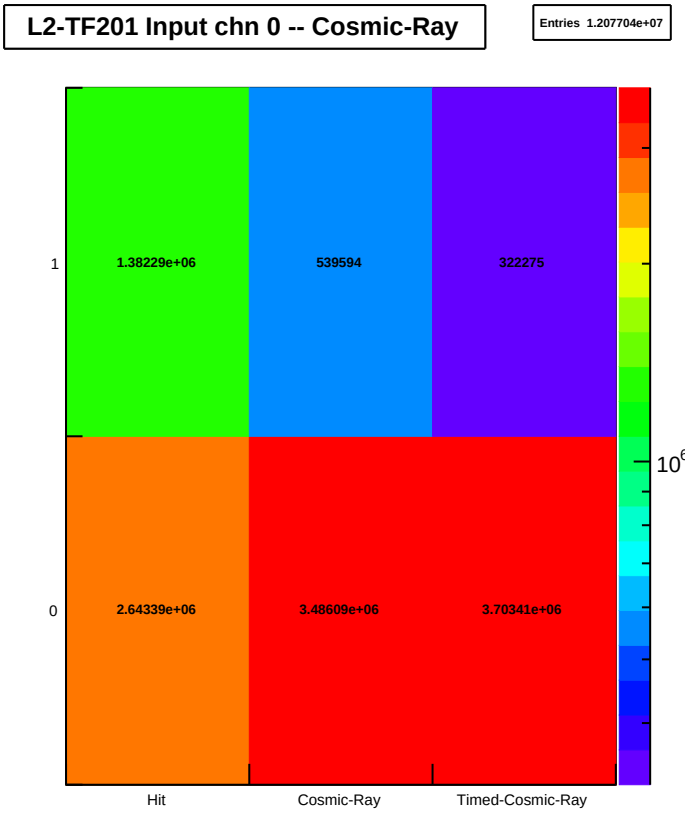

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 234745
TAC sum
1st-Best
2nd-Best
ID

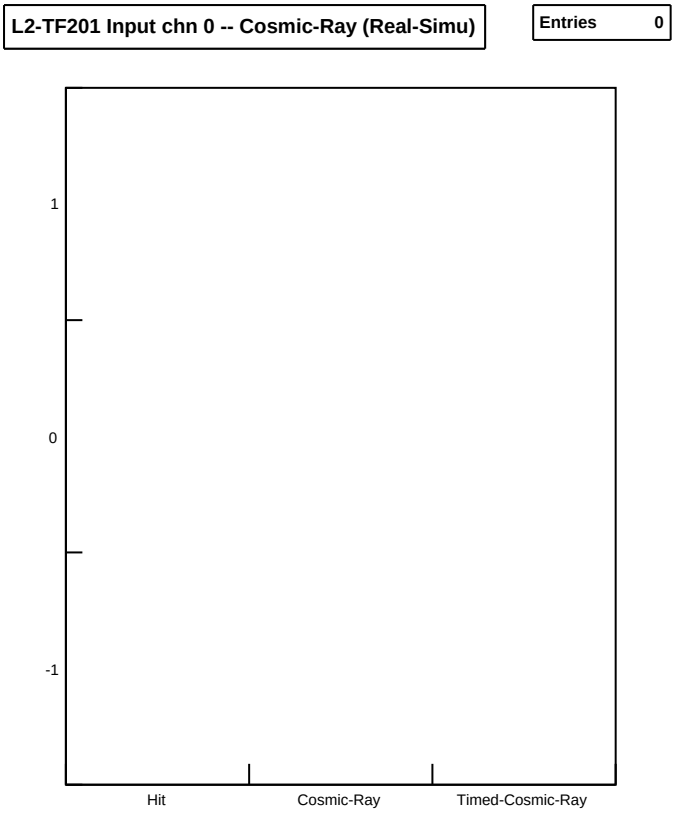

MIX-MT101 Input: VP003 Max TAC
Entries 4025681

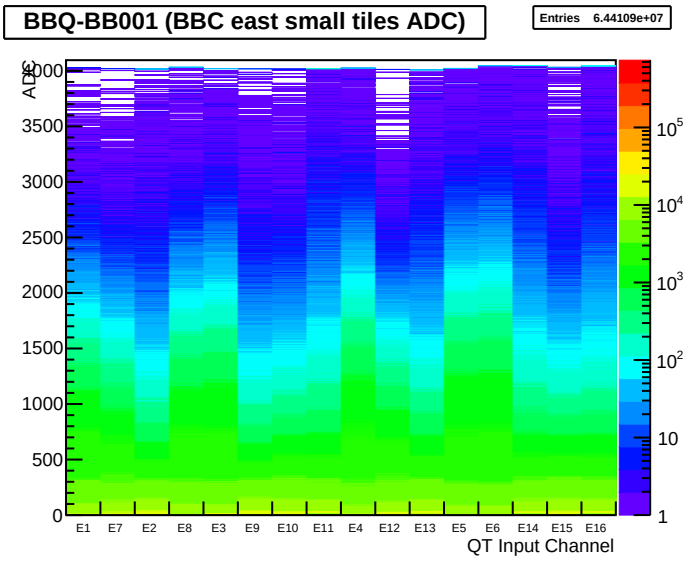

MIX-MT101 Input: VP004 Max TAC
Entries 4025681

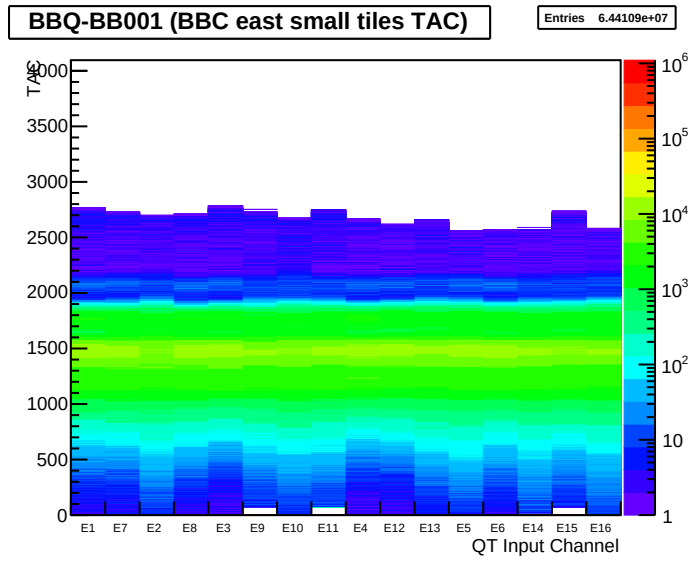

L2-TF201 Input chn 0 -- Number of Muons
Entries 4025681
Number of Muons

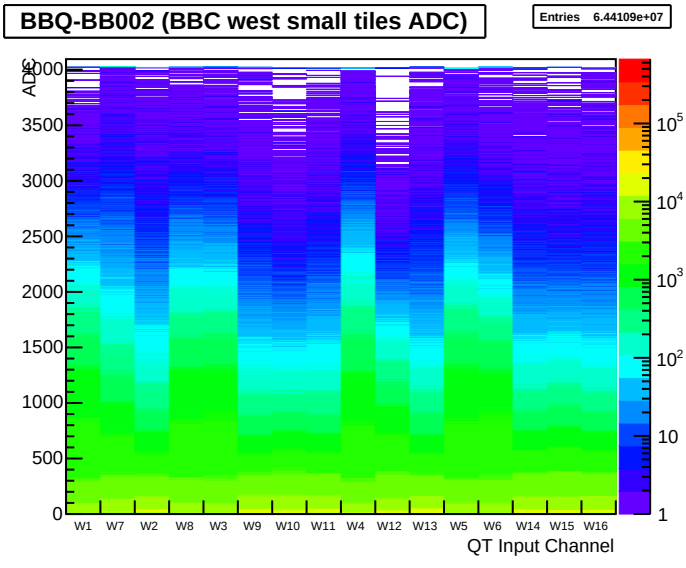

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

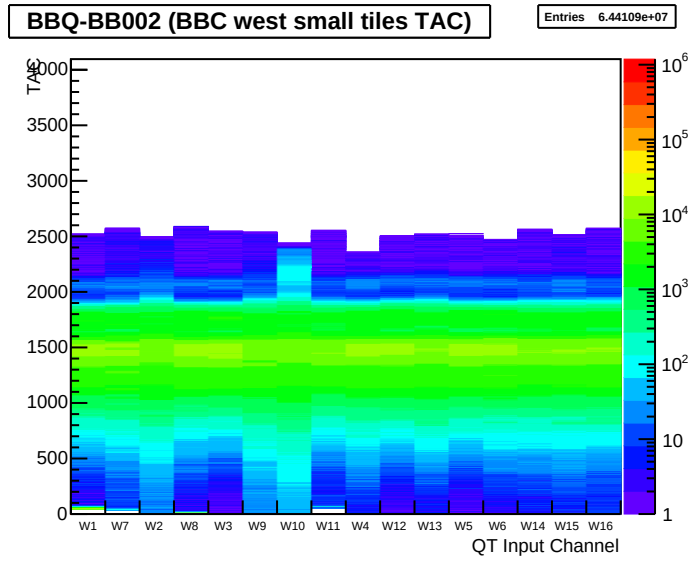

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 1.207704e+07
1.38229e+06
539594
322275
2.64339e+06
3.48609e+06
3.70341e+06
Hit
Cosmic-Ray
Timed-Cosmic-Ray

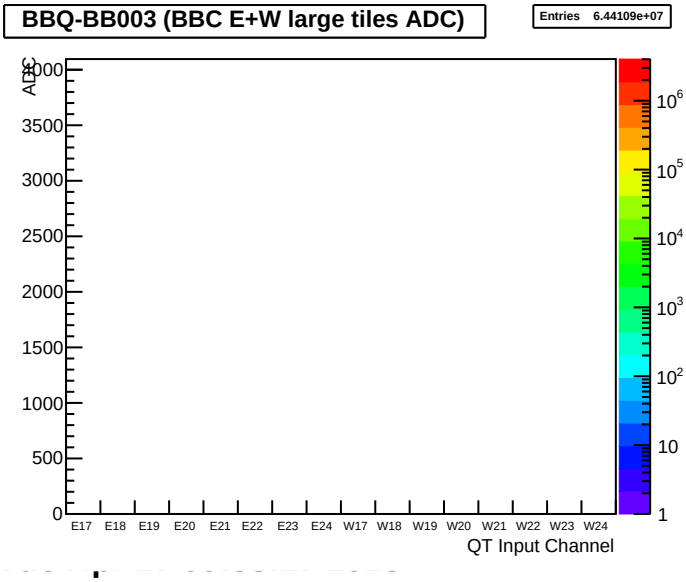

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

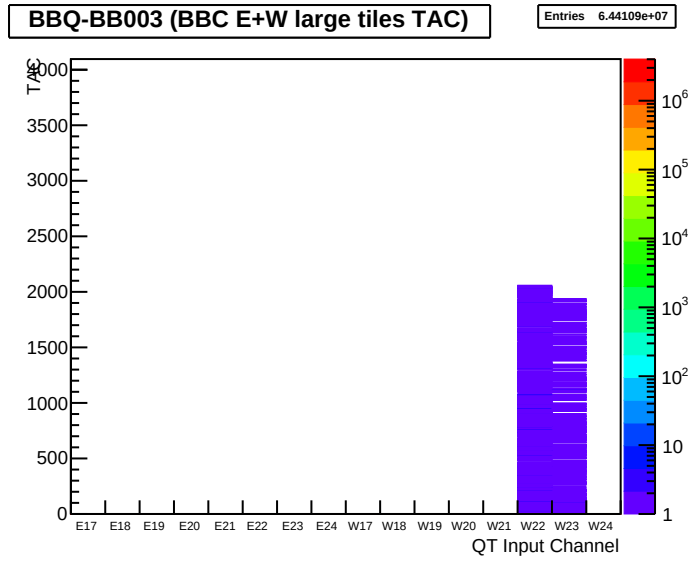

BBQ-BB001 (BBC east small tiles ADC)
Entries 6.44109e+07
ADC
QT Input Channel

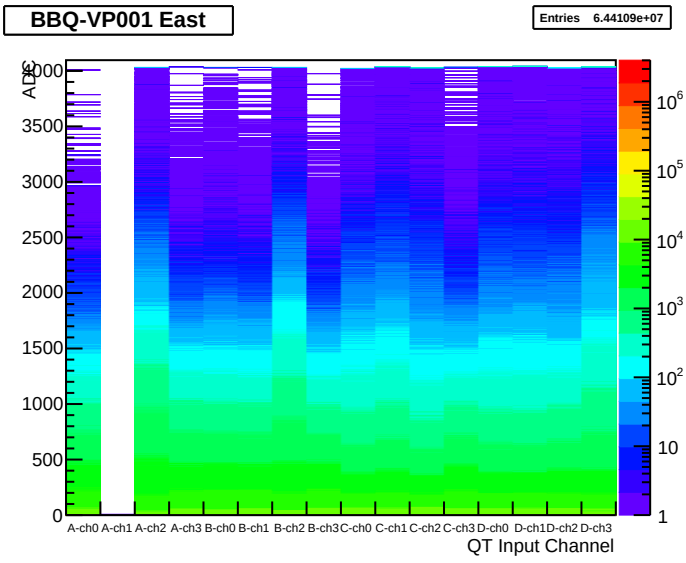

BBQ-BB001 (BBC east small tiles TAC)
Entries 6.44109e+07
TAC
QT Input Channel

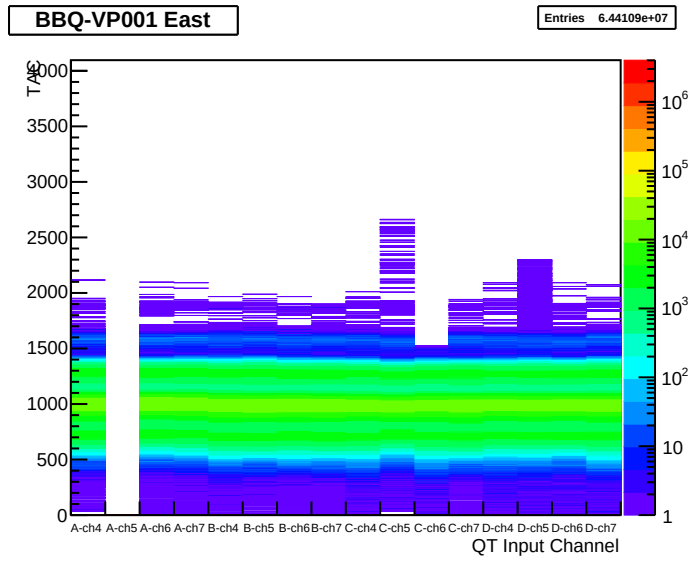

BBQ-BB002 (BBC west small tiles ADC)
Entries 6.44109e+07
ADC
QT Input Channel

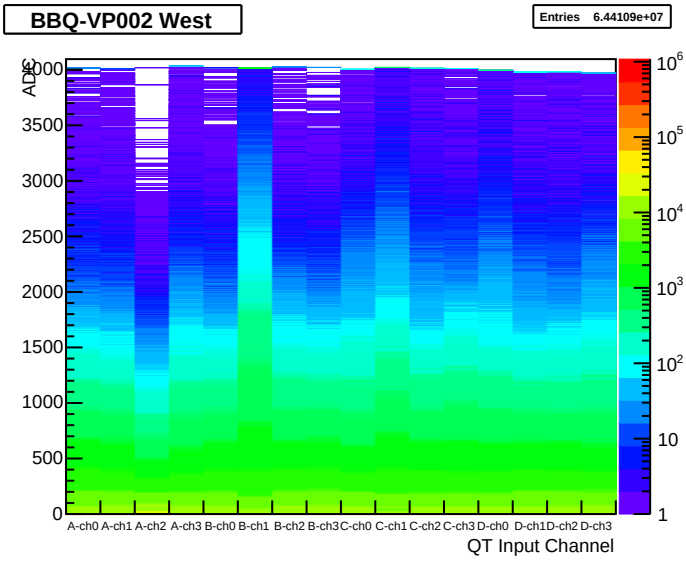

BBQ-BB002 (BBC west small tiles TAC)
Entries 6.44109e+07
TAC
QT Input Channel

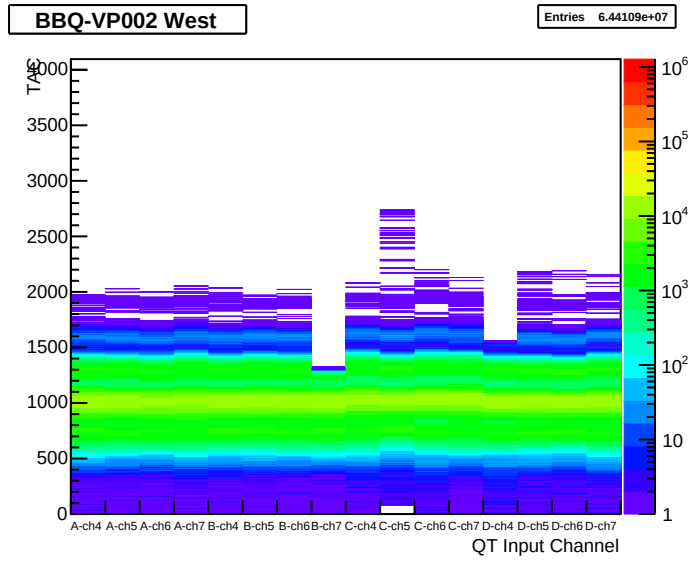

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6.44109e+07
ADC
QT Input Channel

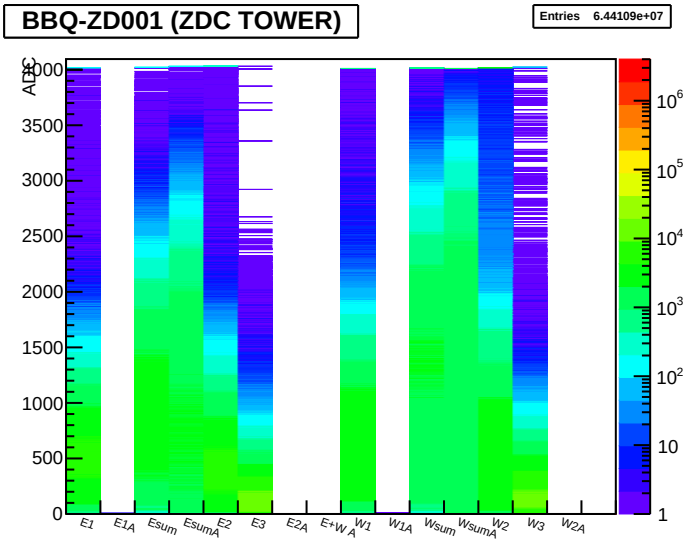

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6.44109e+07
TAC
QT Input Channel

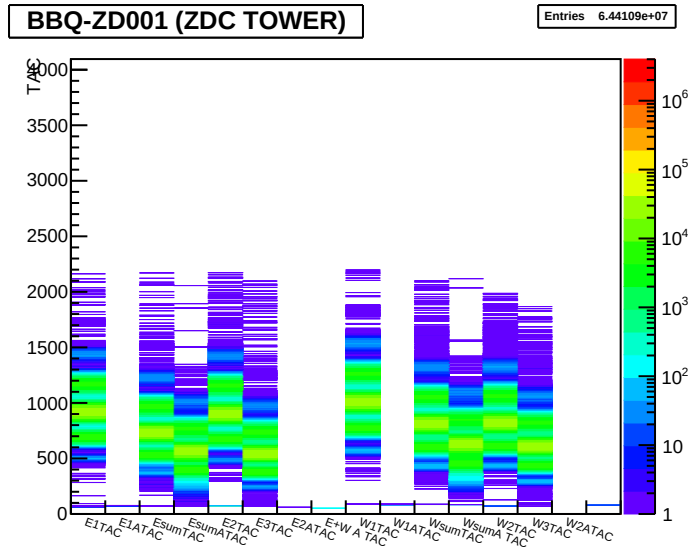

BBQ-VP001 East
Entries 6.44109e+07
ADC
QT Input Channel

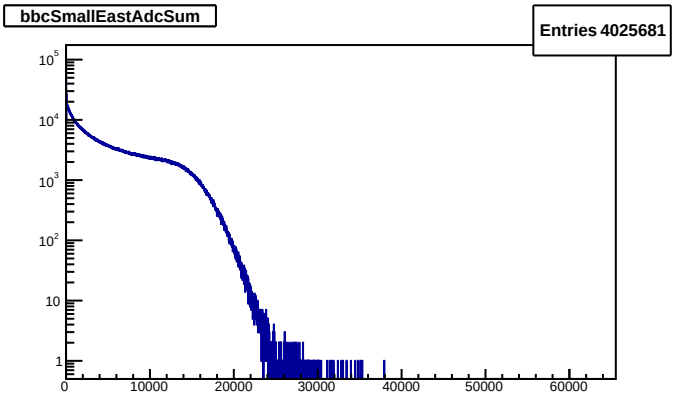

BBQ-VP001 East
Entries 6.44109e+07
TAC
QT Input Channel

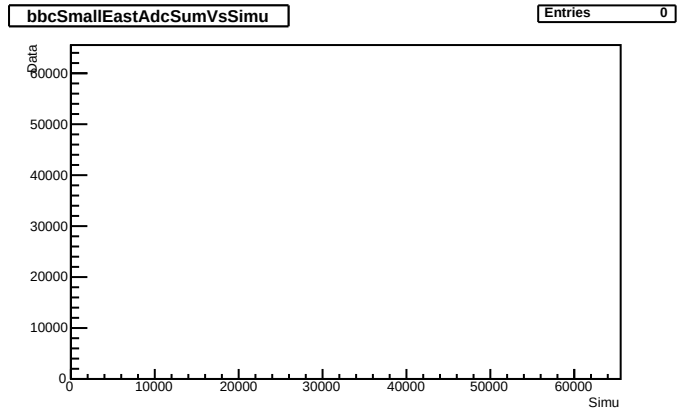

BBQ-VP002 West
Entries 6.44109e+07
ADC
QT Input Channel

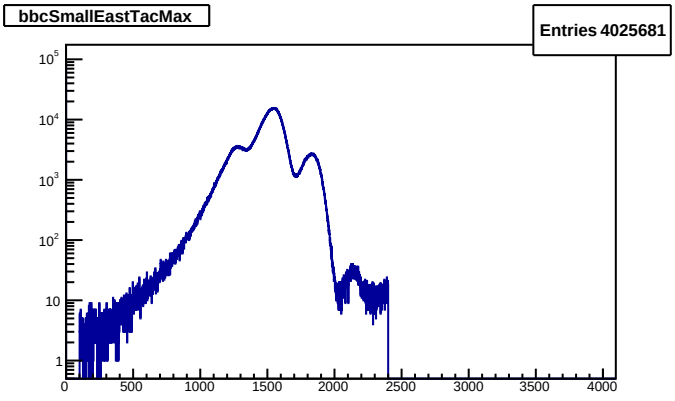

BBQ-VP002 West
Entries 6.44109e+07
TAC
QT Input Channel

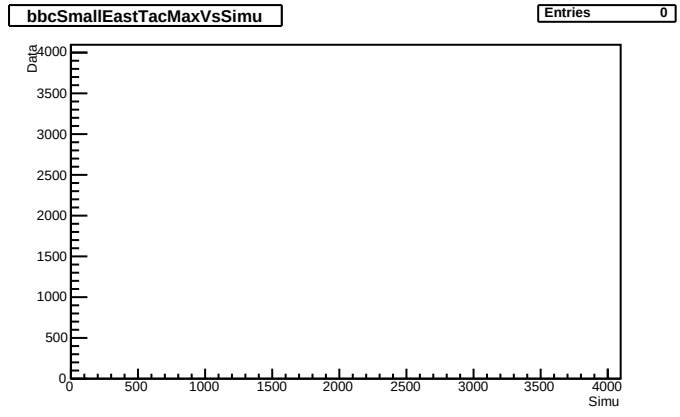

BBQ-ZD001 (ZDC TOWER)
Entries 6.44109e+07
ADC

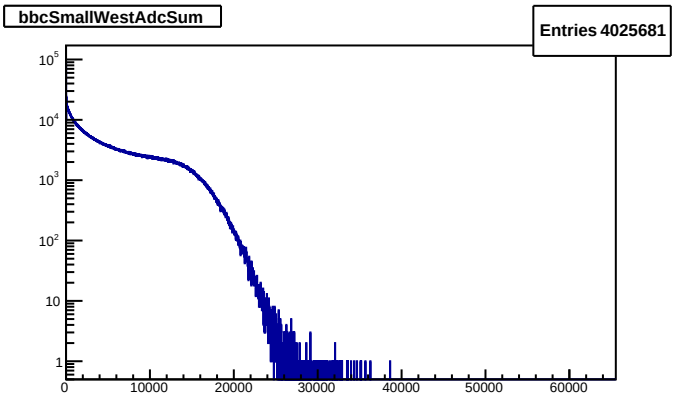

BBQ-ZD001 (ZDC TOWER)
Entries 6.44109e+07
TAC

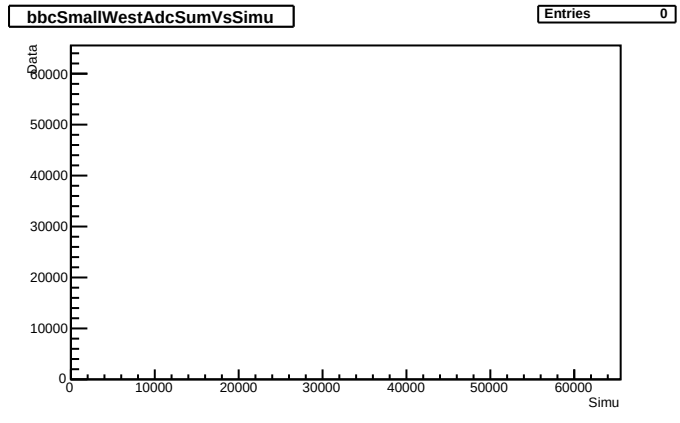

bbcSmallEastAdcSum
Entries 4025681

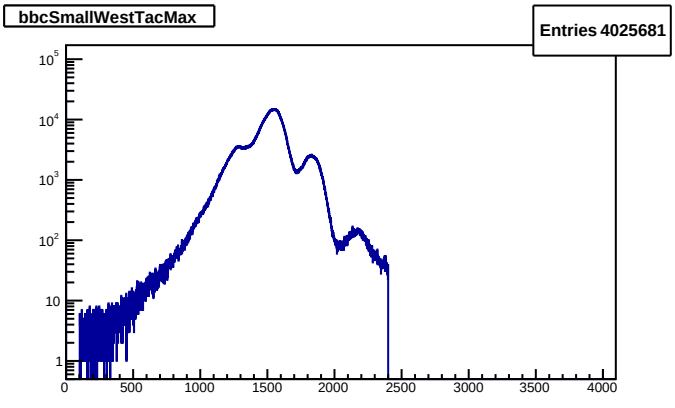

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

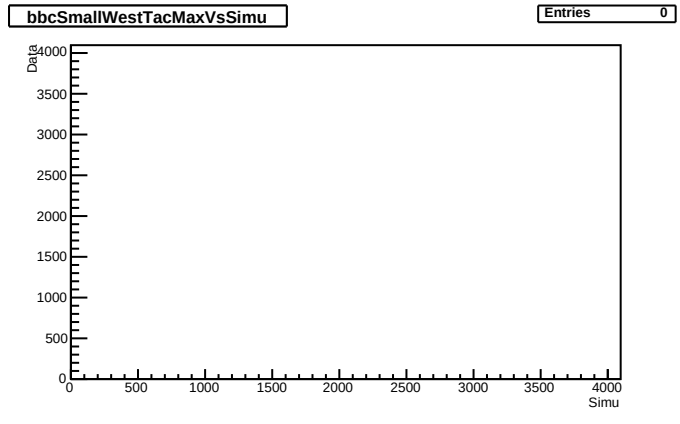

bbcSmallEastTacMax
Entries 4025681

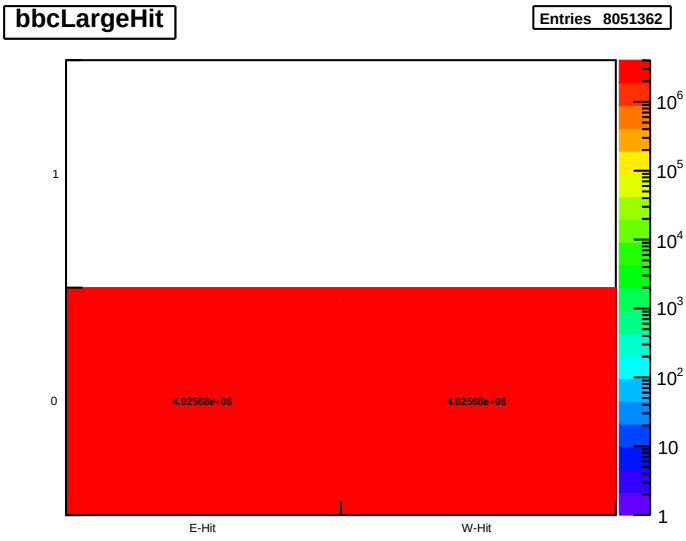

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

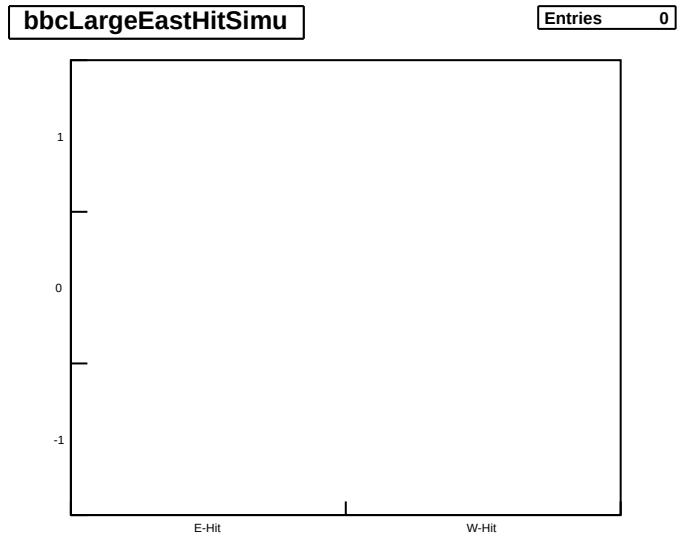

bbcSmallWestAdcSum
Entries 4025681

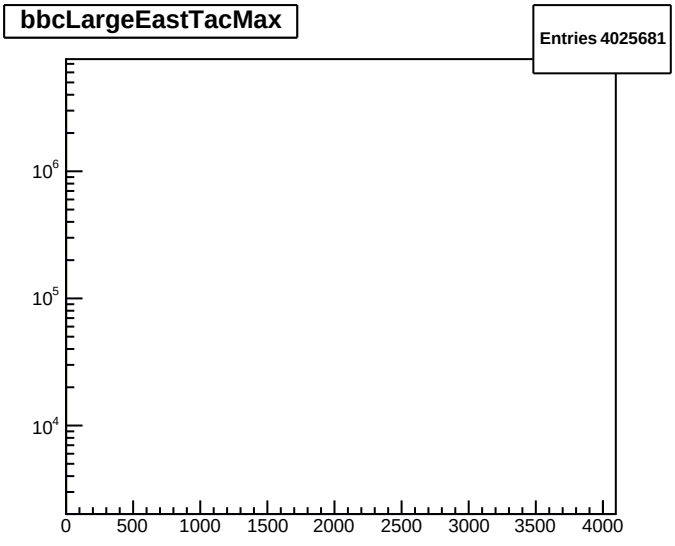

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

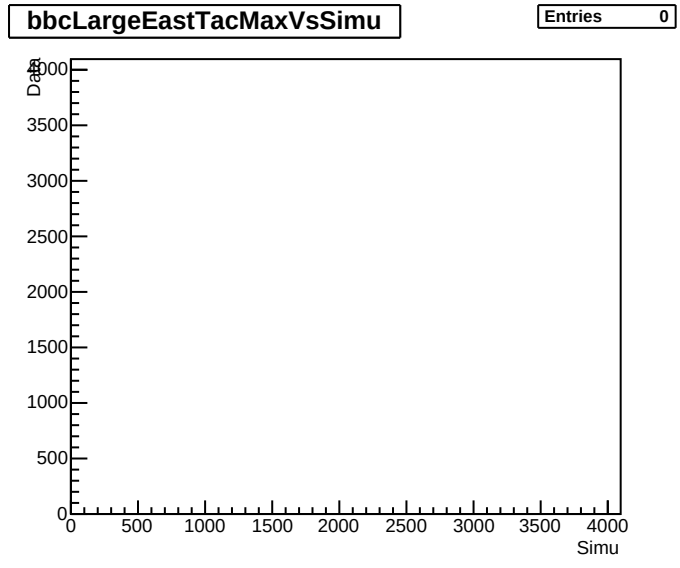

bbcSmallWestTacMax
Entries 4025681

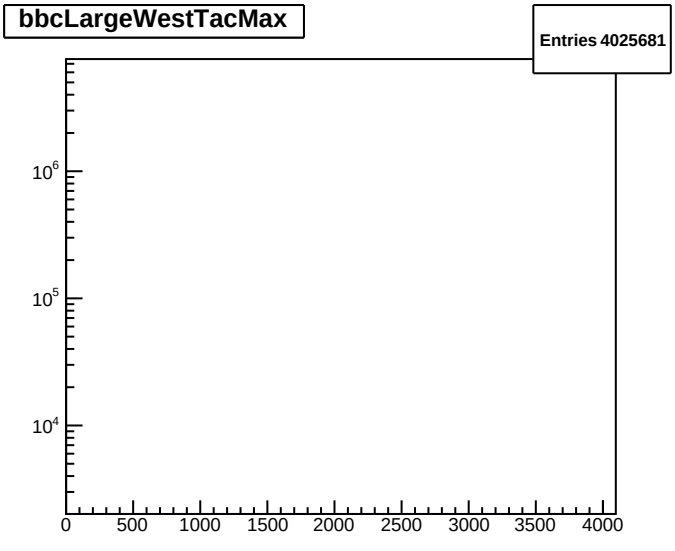

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

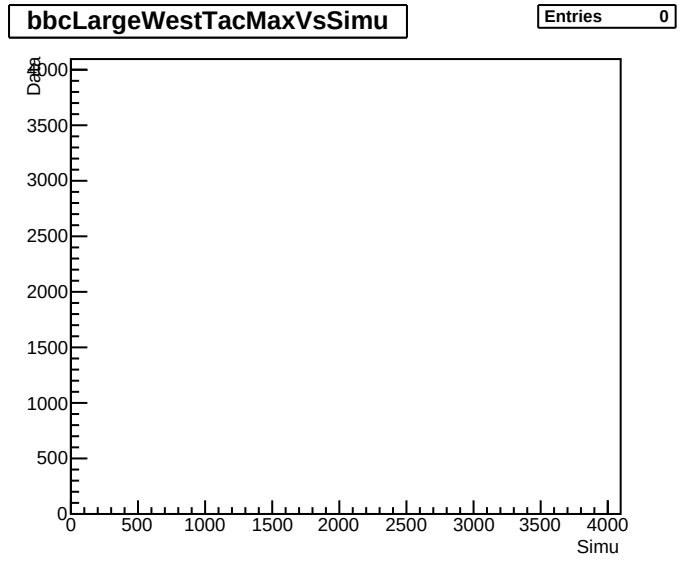

bbcLargeHit
Entries 8051362
1
0
4.02568e+06
4.02568e+06
E-Hit
W-Hit
10^6
10^5
10^4
10^3
10^2
10
1

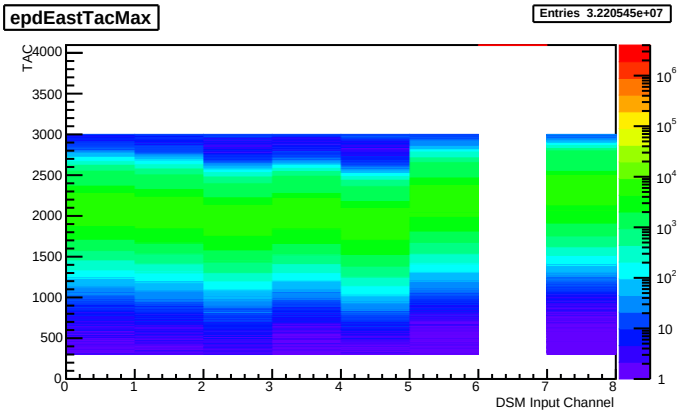

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

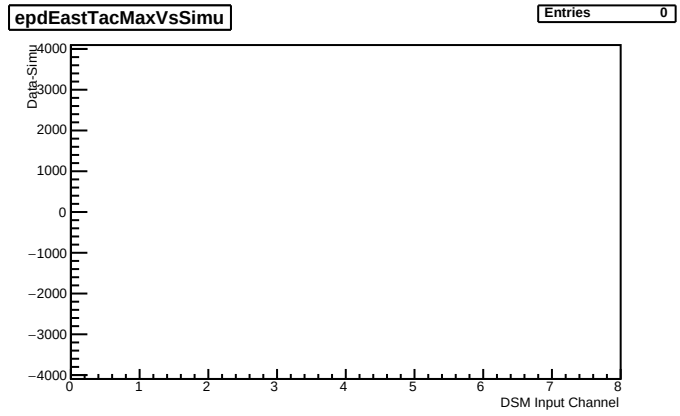

bbcLargeEastTacMax
Entries 4025681
10^6
10^5
10^4
0
500
1000
1500
2000
2500
3000
3500
4000

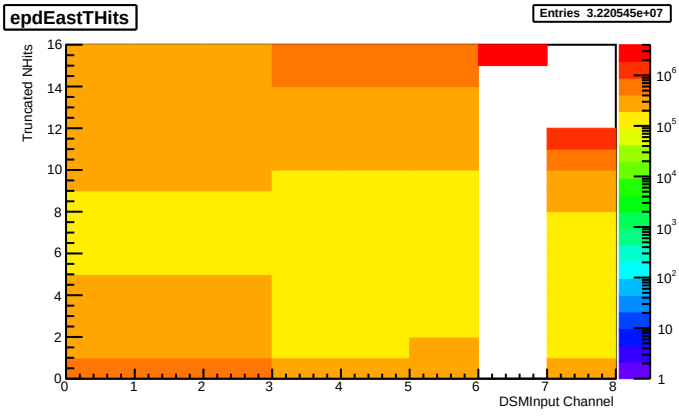

bbcLargeEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

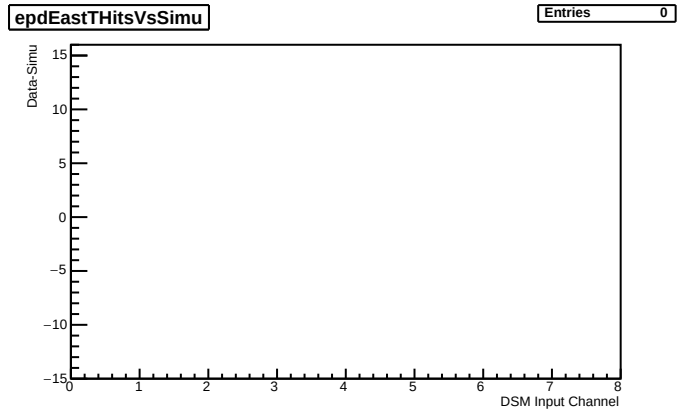

bbcLargeWestTacMax
Entries 4025681
10^6
10^5
10^4
0
500
1000
1500
2000
2500
3000
3500
4000

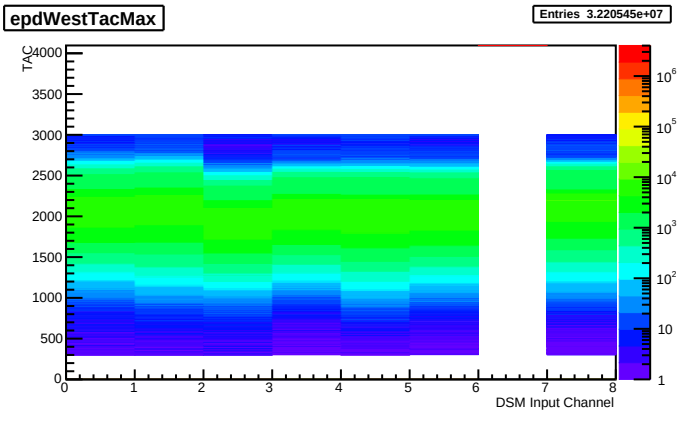

bbcLargeWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

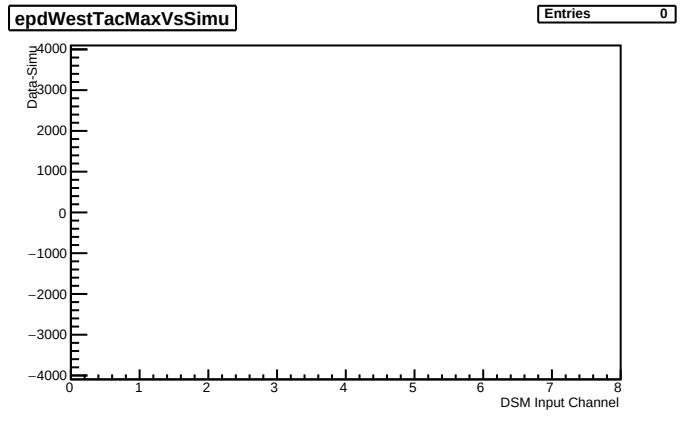

epdEastTacMax
Entries 3.220545e+07
TAC
DSM Input Channel

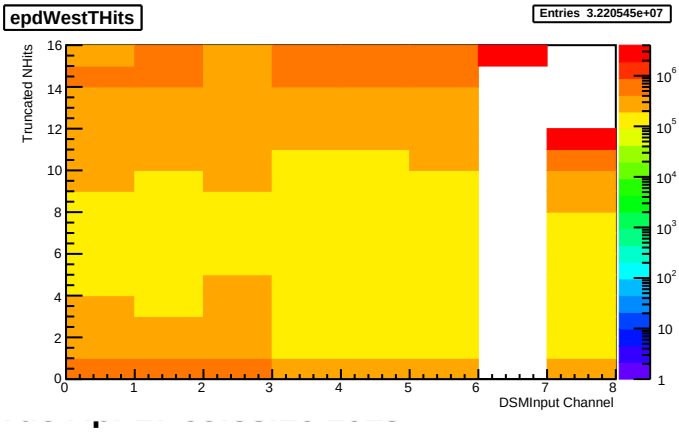

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

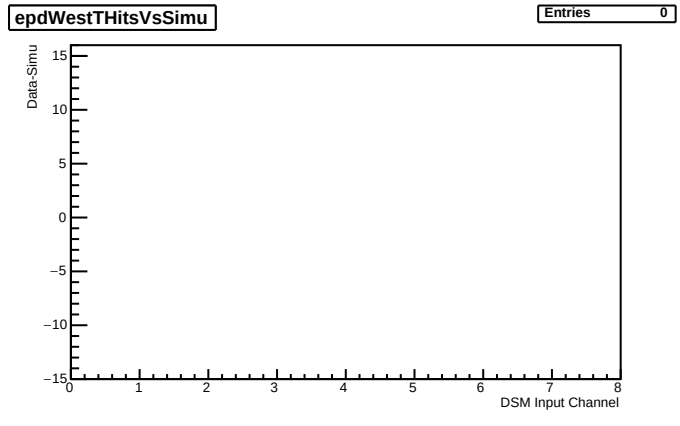

epdEastTHits
Entries 3.220545e+07
Truncated NHits
DSMInput Channel

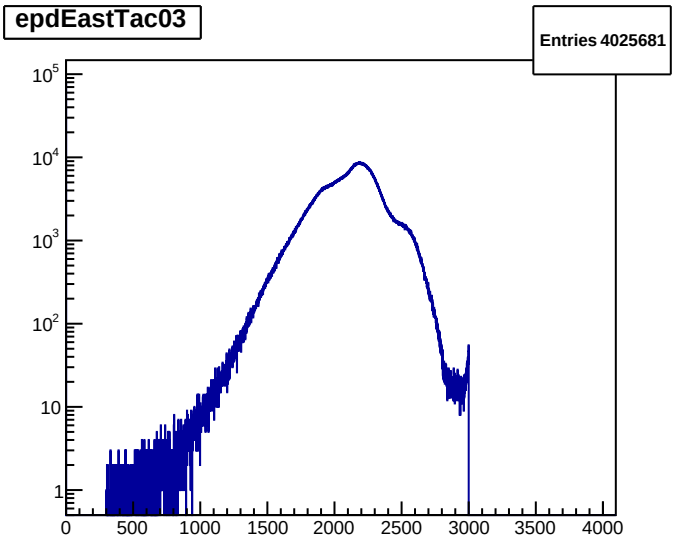

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

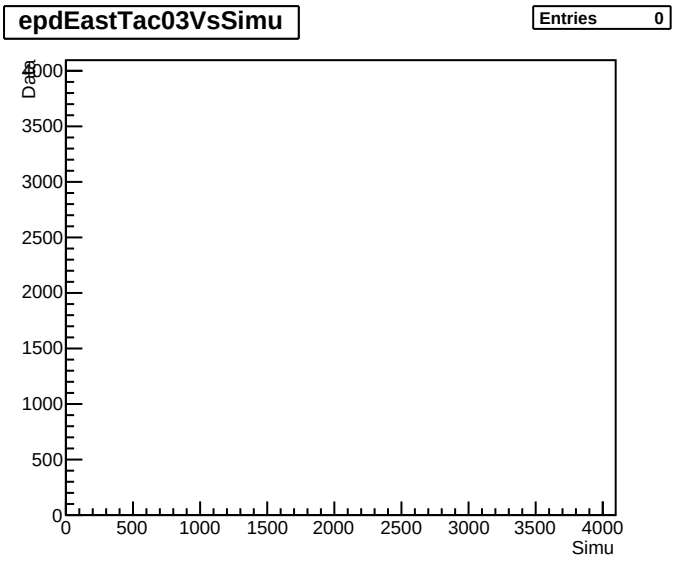

epdWestTacMax
Entries 3.220545e+07
TAC
DSM Input Channel

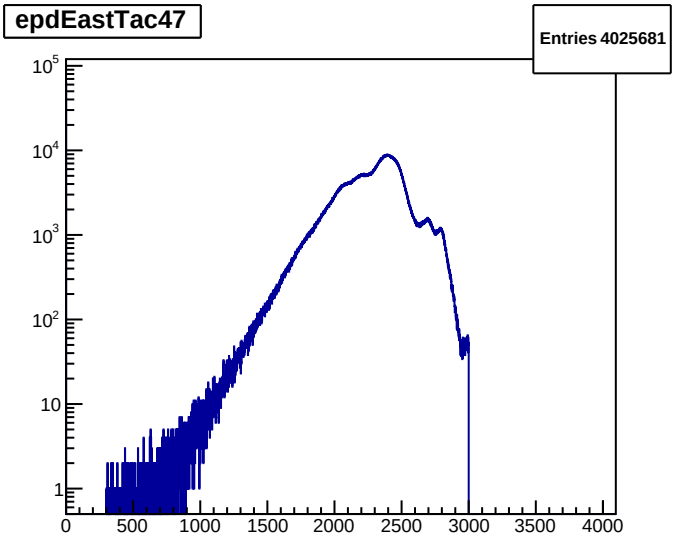

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

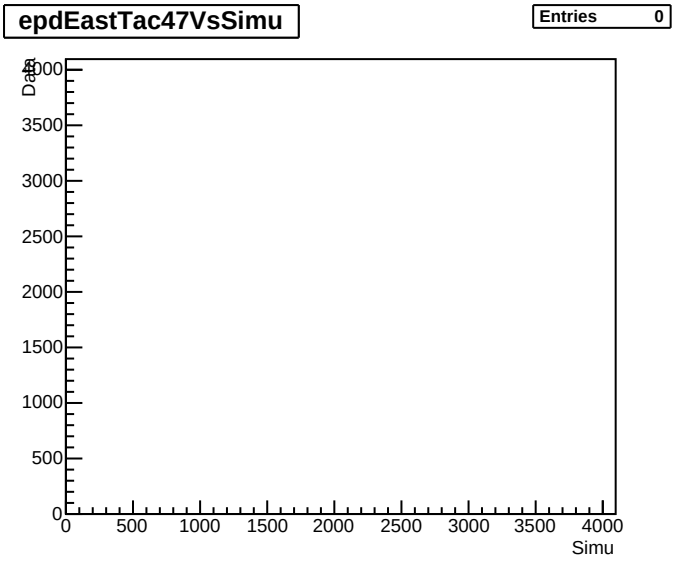

epdWestTHits
Entries 3.220545e+07
Truncated NHits
DSMInput Channel

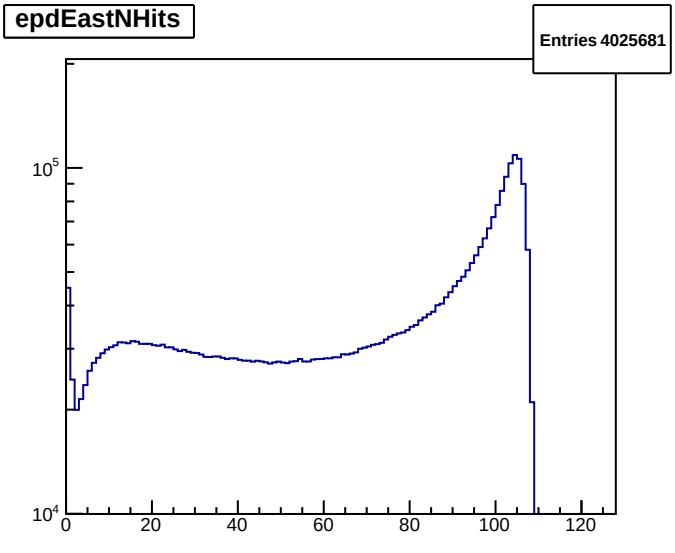

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

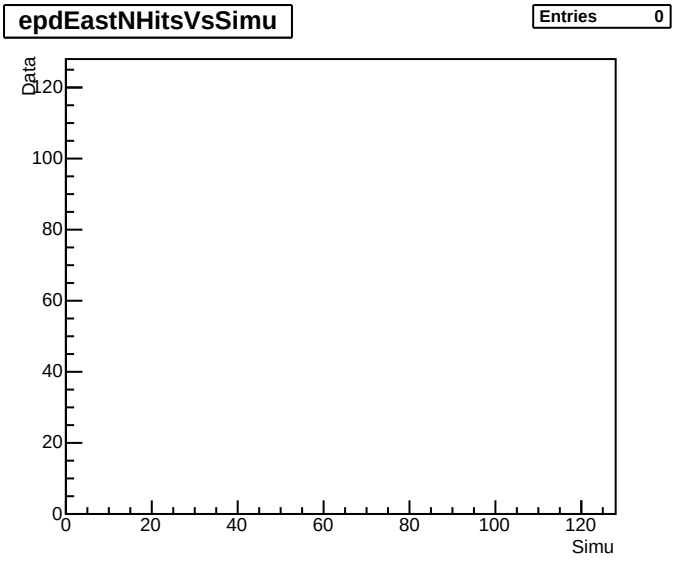

epdEastTac03
Entries 4025681

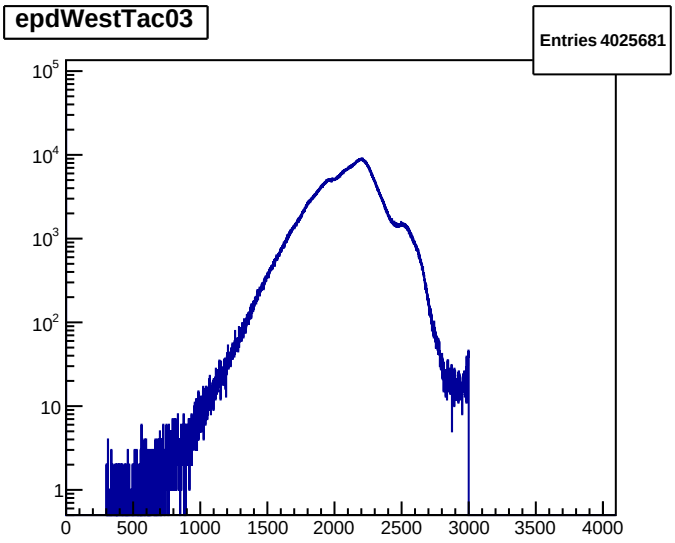

epdEastTac03VsSimu
Entries 0
Data
Simu

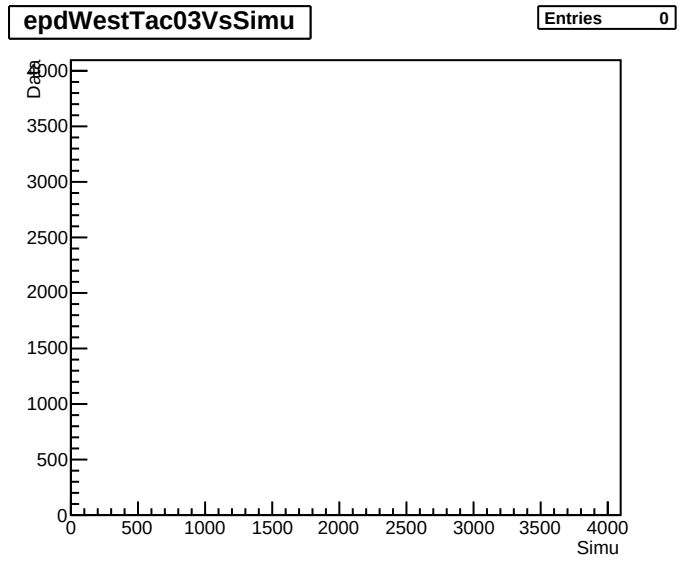

epdEastTac47
Entries 4025681

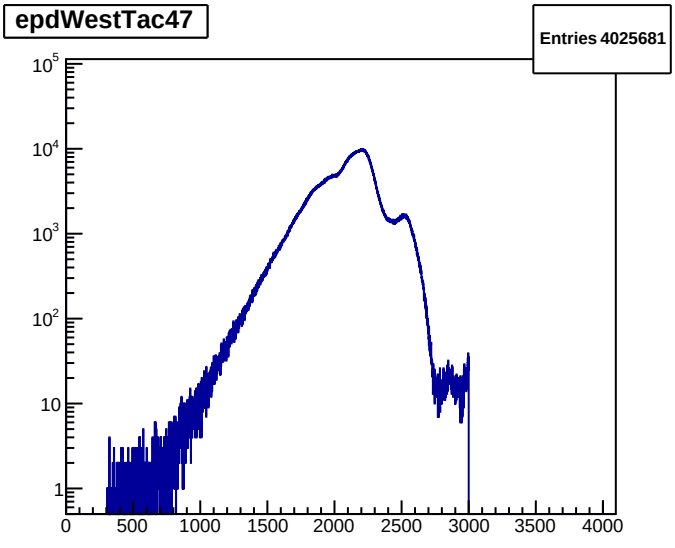

epdEastTac47VsSimu
Entries 0
Data
Simu

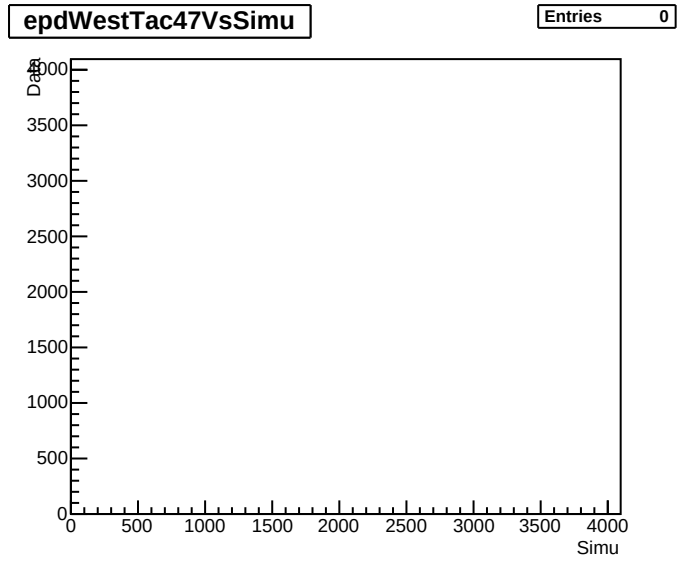

epdEastNHits
Entries 4025681

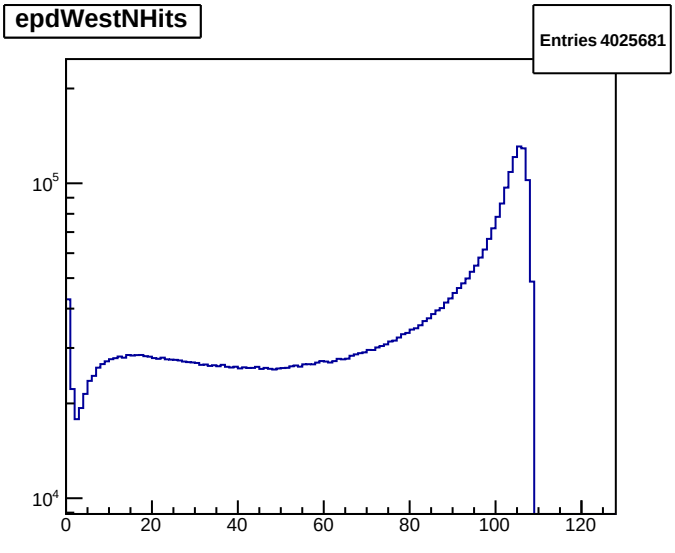

epdEastNHitsVsSimu
Entries 0
Data
Simu

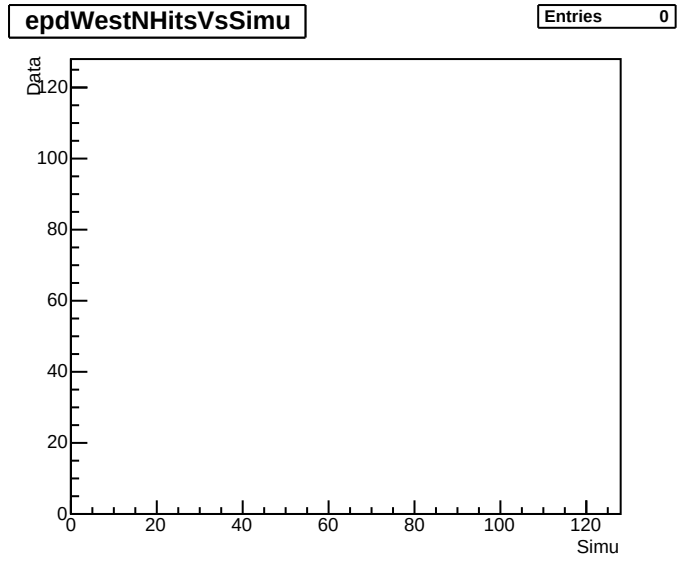

epdWestTac03
Entries 4025681

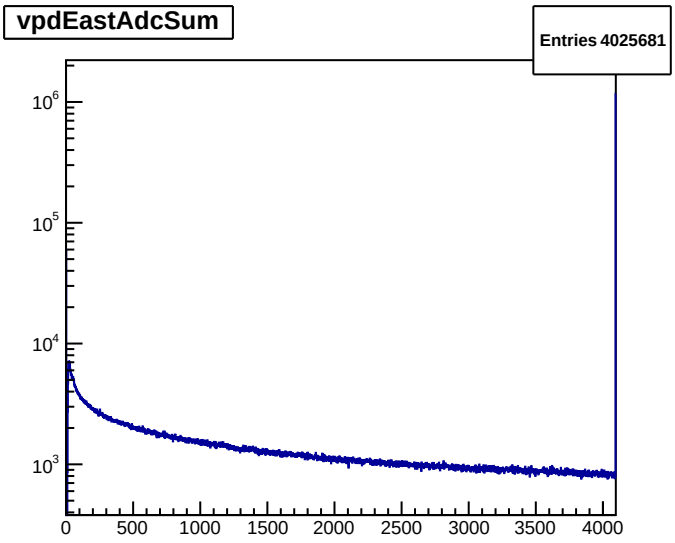

epdWestTac03VsSimu
Entries 0
Data
Simu

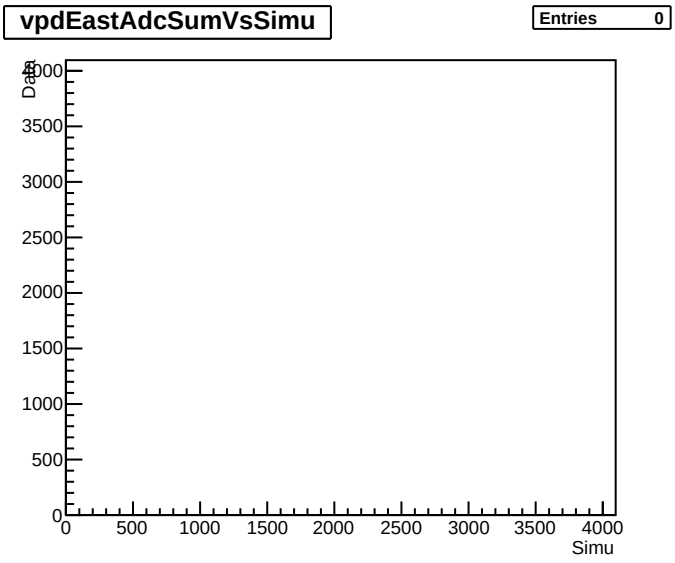

epdWestTac47
Entries 4025681

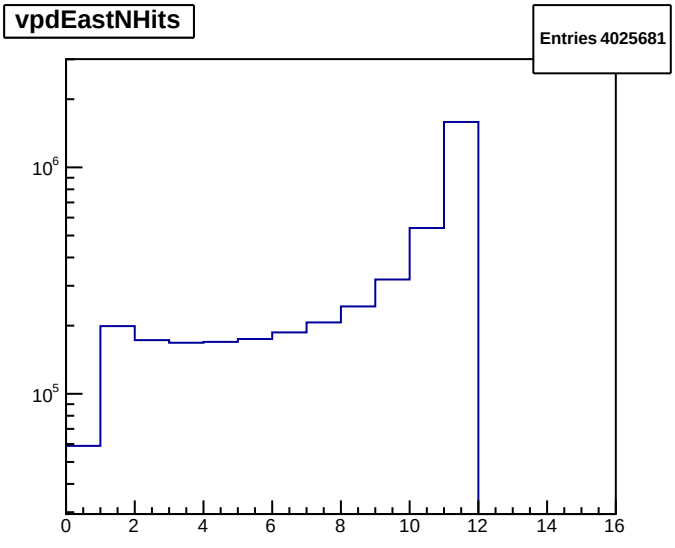

epdWestTac47VsSimu
Entries 0
Data
Simu

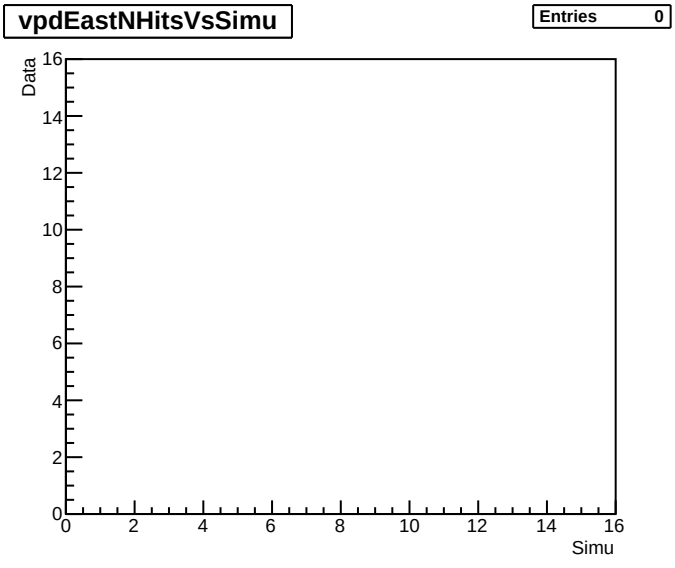

epdWestNHits
Entries 4025681

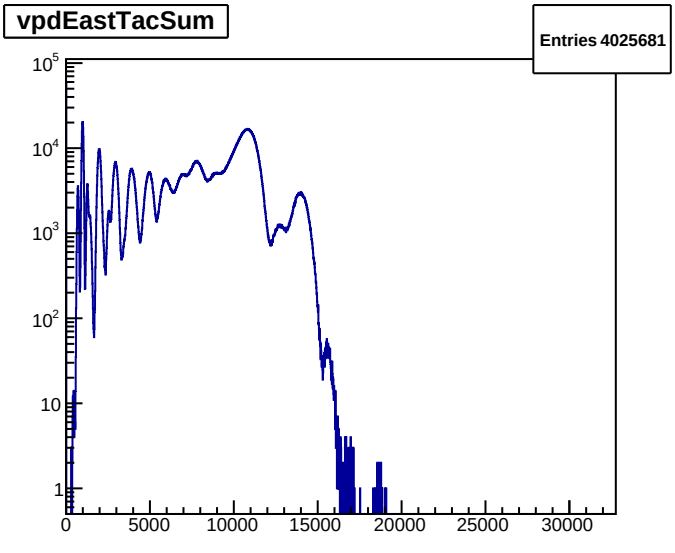

epdWestNHitsVsSimu
Entries 0
Data
Simu

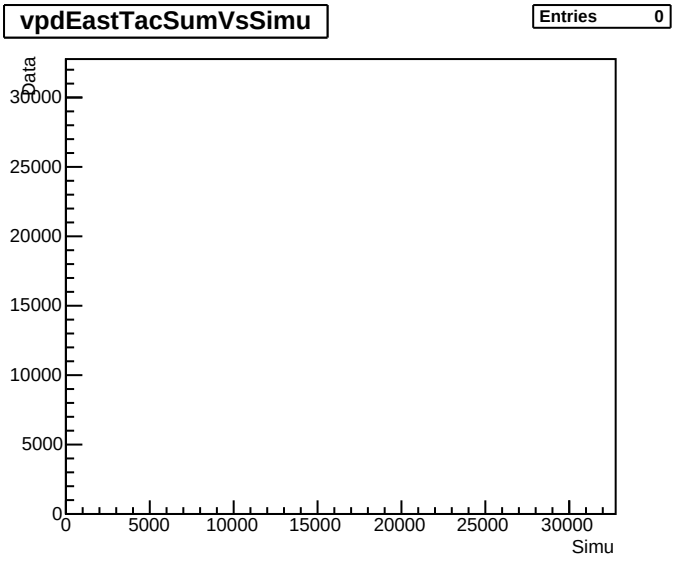

vpdEastAdcSum
Entries 4025681
10^6
10^5
10^4
10^3
0 500 1000 1500 2000 2500 3000 3500 4000

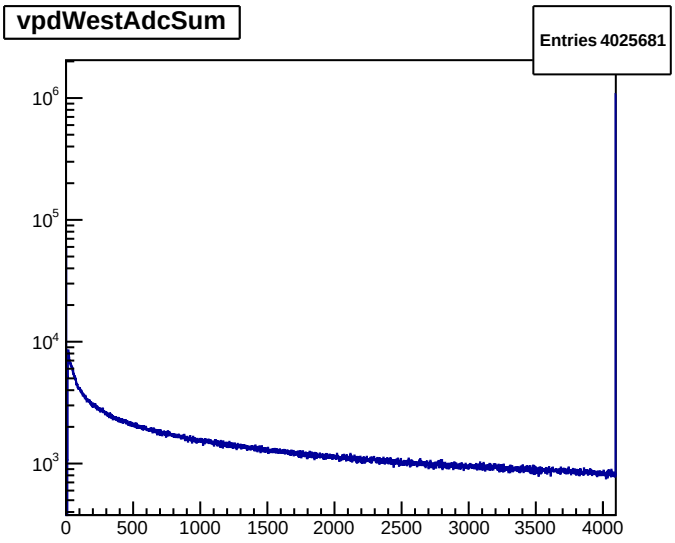

vpdEastAdcSumVsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

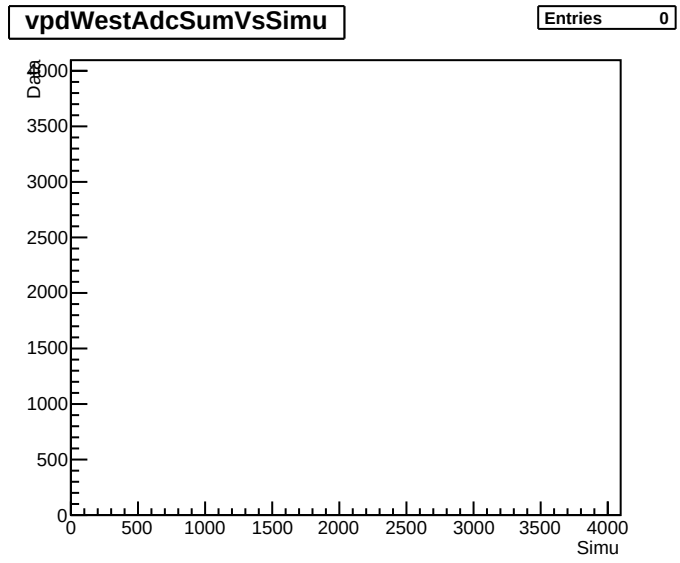

vpdEastNHits
Entries 4025681
10^6
10^5
0 2 4 6 8 10 12 14 16

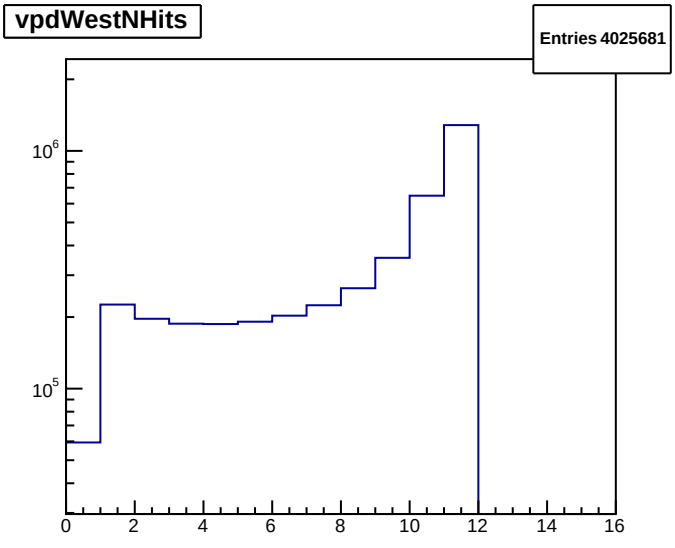

vpdEastNHitsVsSimu
Entries 0
Data
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16
Simu

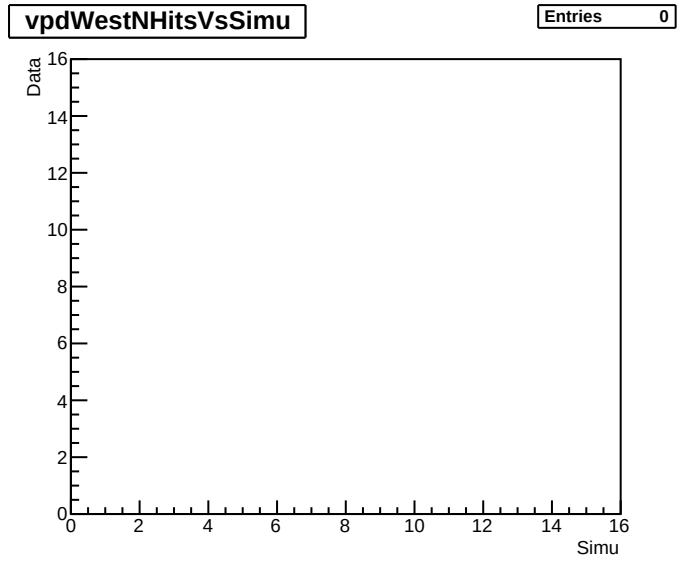

vpdEastTacSum
Entries 4025681
10^5
10^4
10^3
10^2
10
1
0 5000 10000 15000 20000 25000 30000

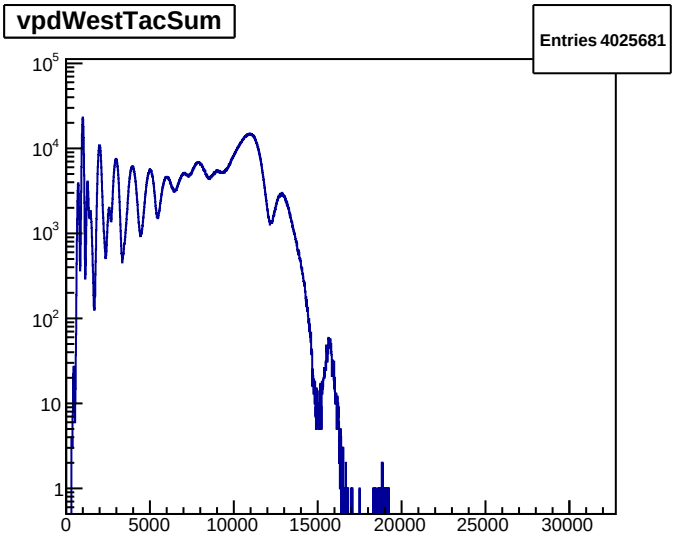

vpdEastTacSumVsSimu
Entries 0
Data
30000 25000 20000 15000 10000 5000 0
0 5000 10000 15000 20000 25000 30000
Simu

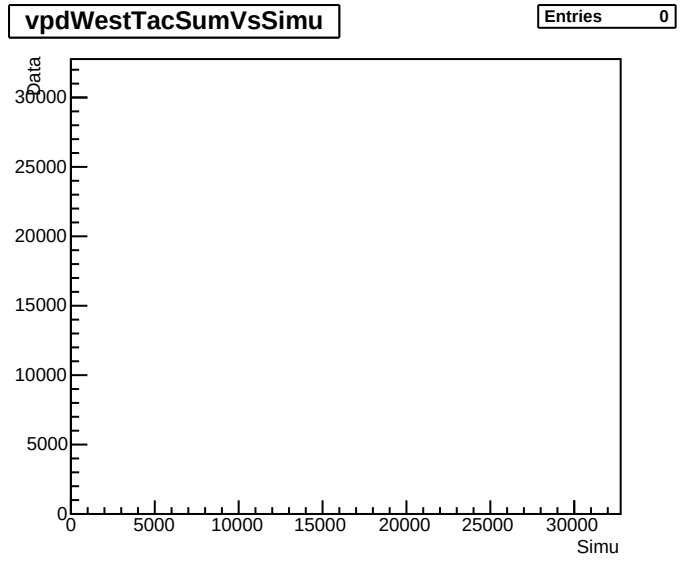

vpdWestAdcSum
Entries 4025681
10^6
10^5
10^4
10^3
0 500 1000 1500 2000 2500 3000 3500 4000

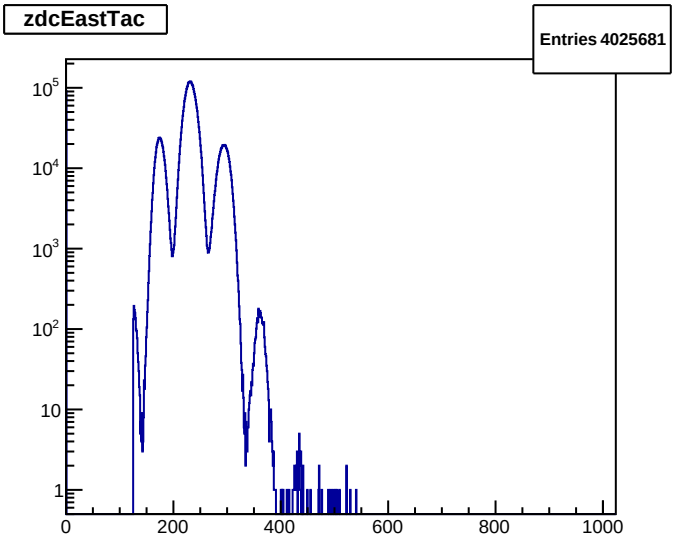

vpdWestAdcSumVsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

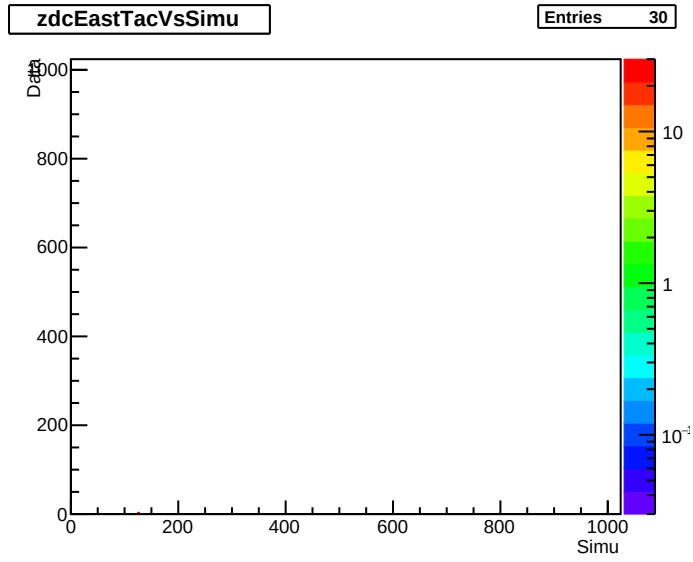

vpdWestNHits
Entries 4025681
10^6
10^5
0 2 4 6 8 10 12 14 16

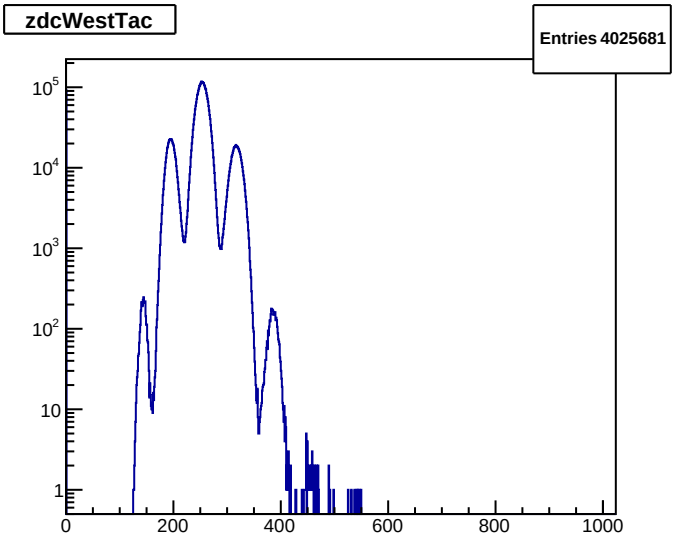

vpdWestNHitsVsSimu
Entries 0
Data
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16
Simu

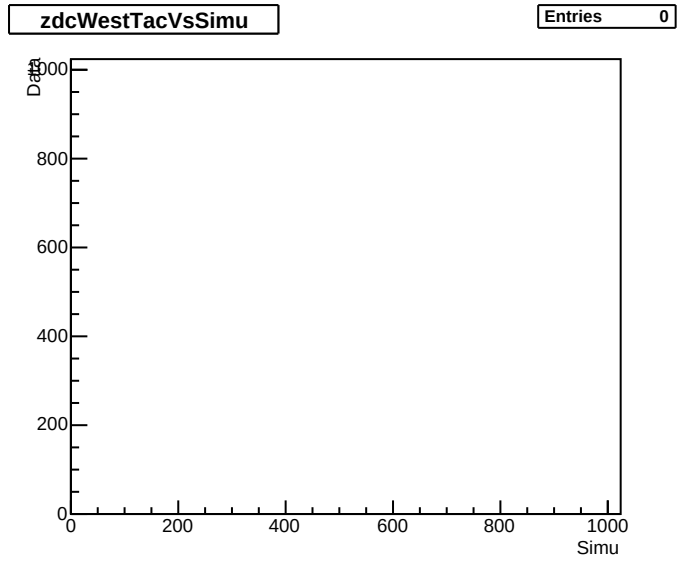

vpdWestTacSum
Entries 4025681
10^5
10^4
10^3
10^2
10
1
0 5000 10000 15000 20000 25000 30000

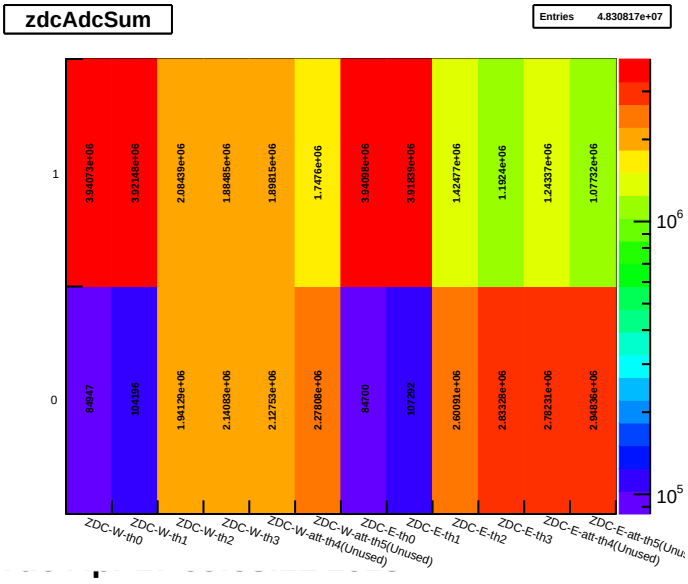

vpdWestTacSumVsSimu
Entries 0
Data
30000 25000 20000 15000 10000 5000 0
0 5000 10000 15000 20000 25000 30000
Simu

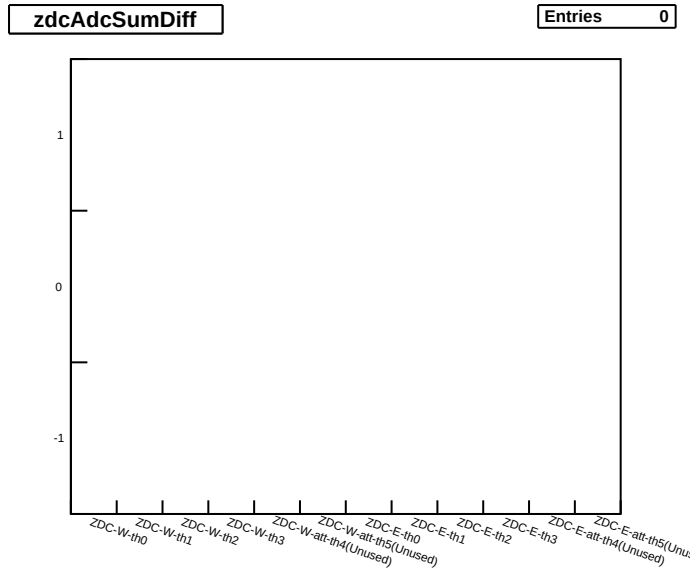

zdcEastTac
Entries 4025681

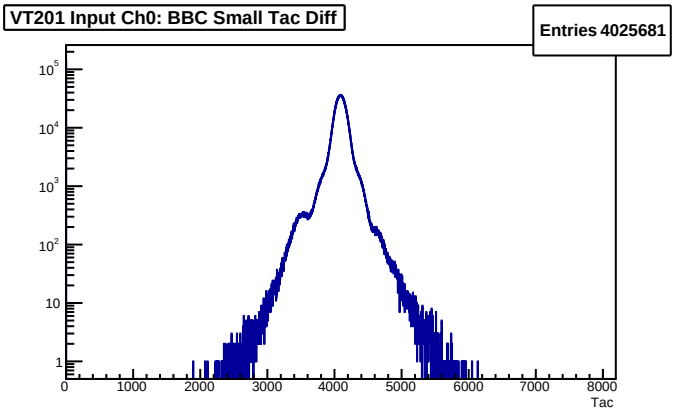

zdcEastTacVsSimu
Entries 30
Data
Simu

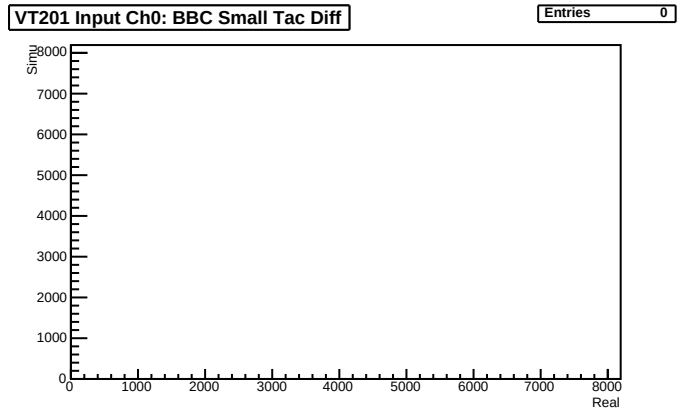

zdcWestTac
Entries 4025681

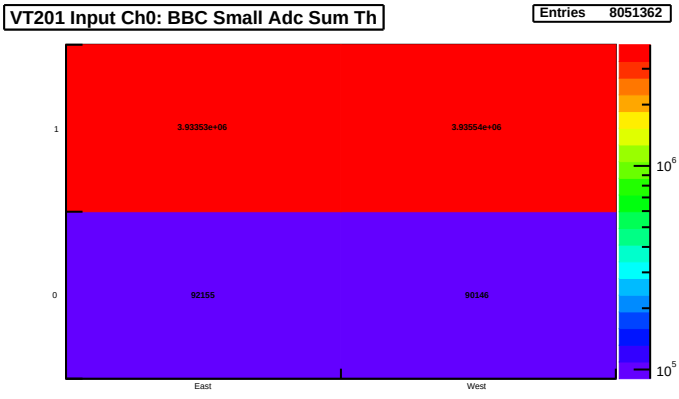

zdcWestTacVsSimu
Entries 0
Data
Simu

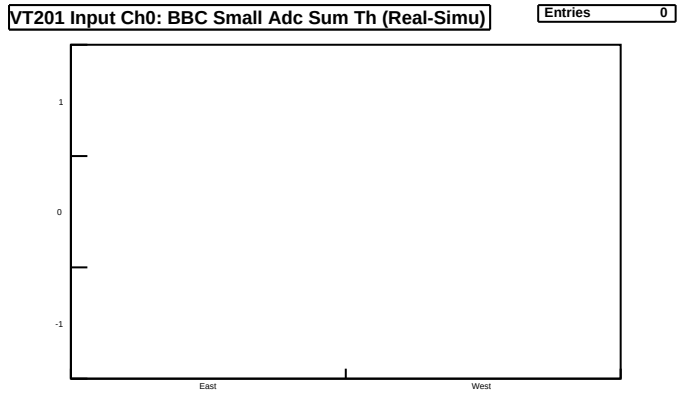

zdcAdcSum
Entries 4.830817e+07
3.94073e+06
3.92148e+06
2.08439e+06
1.88485e+06
1.89815e+06
1.7476e+06
3.94098e+06
3.91839e+06
1.42477e+06
1.1924e+06
1.24337e+06
1.07732e+06
84947
104196
1.94129e+06
2.14083e+06
2.12753e+06
2.27808e+06
84700
107292
2.60091e+06
2.83328e+06
2.78231e+06
2.94836e+06
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

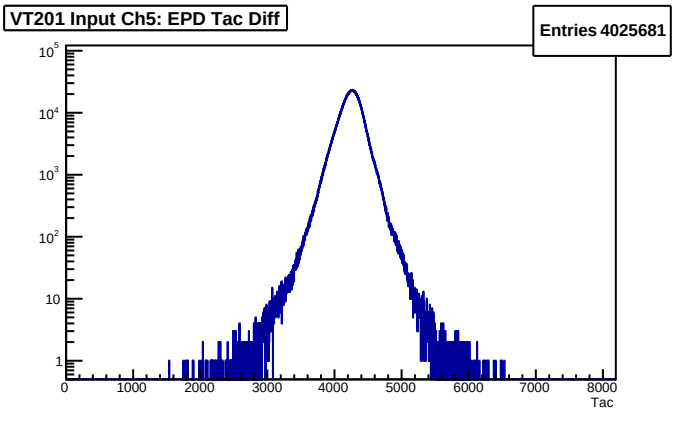

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

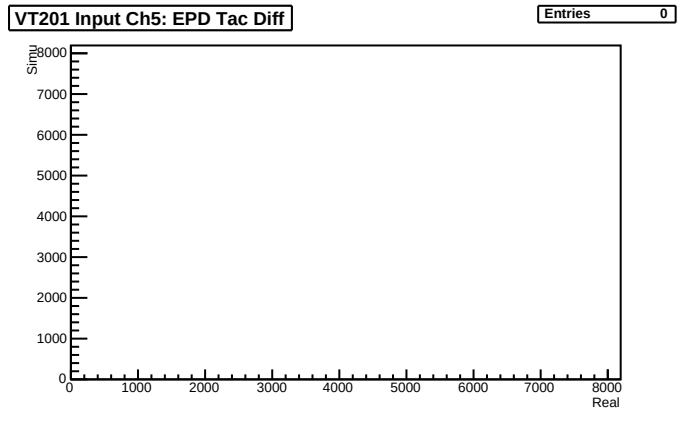

VT201 Input Ch0: BBC Small Tac Diff
Entries 4025681
Tac

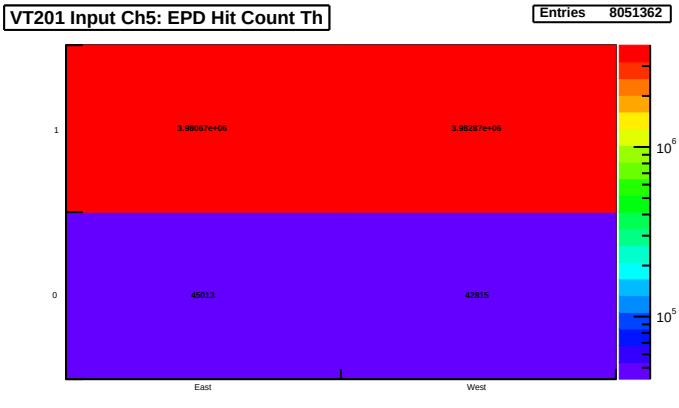

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

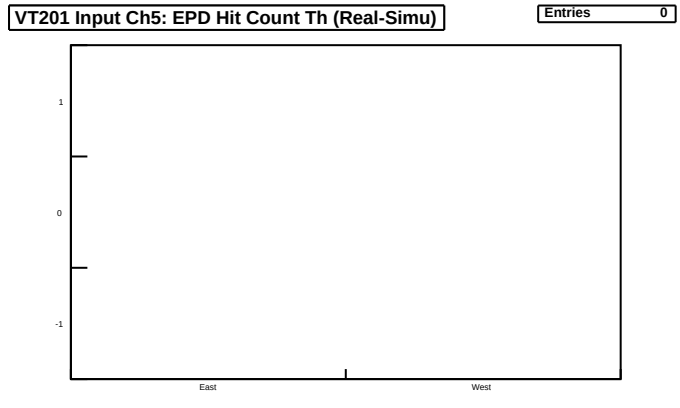

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 8051362
3.93353e+06
3.93554e+06
92155
90146
East
West

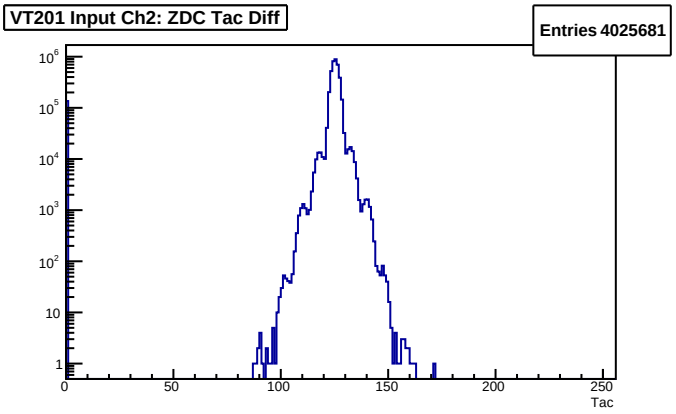

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

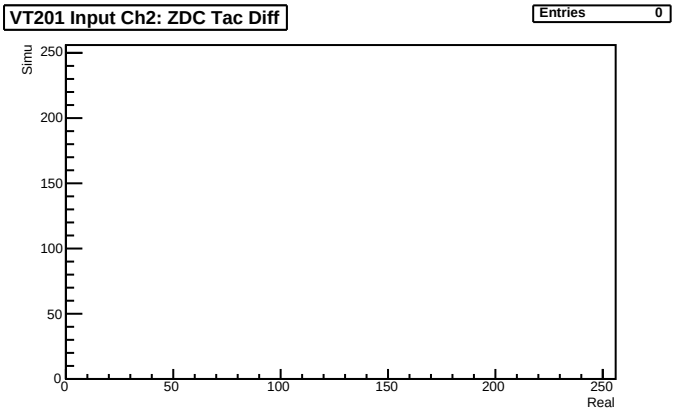

VT201 Input Ch5: EPD Tac Diff
Entries 4025681
Tac

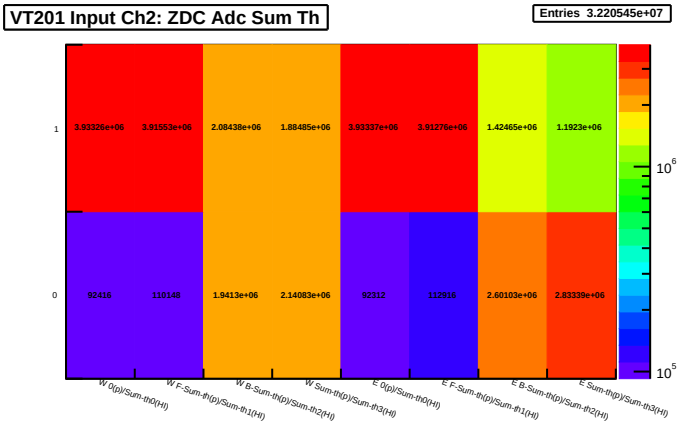

VT201 Input Ch5: EPD Tac Diff
Entries 0
Simu
Real

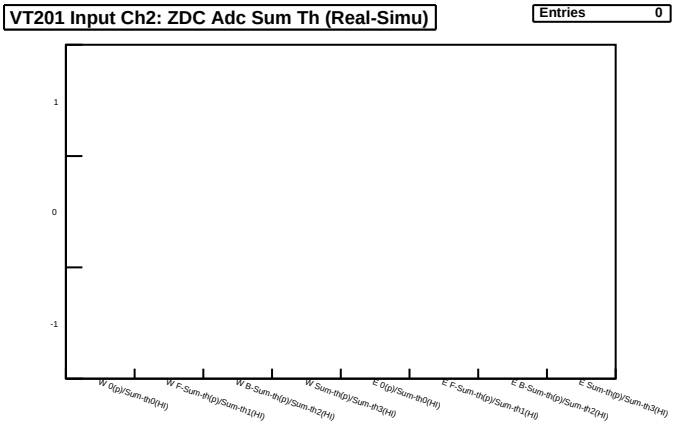

VT201 Input Ch5: EPD Hit Count Th
Entries 8051362
3.98067e+06
3.98287e+06
45013
42815
East
West

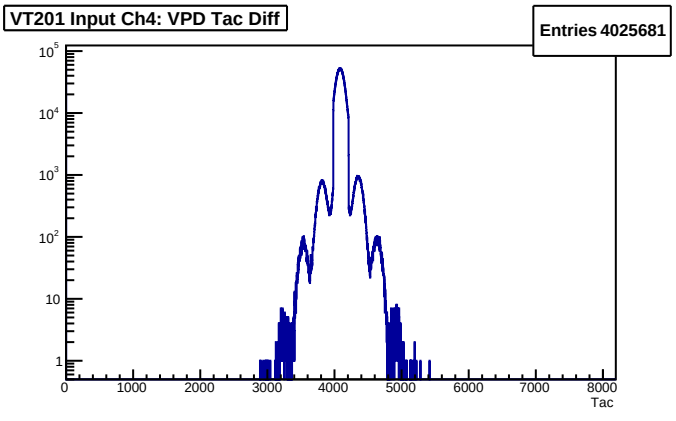

VT201 Input Ch5: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

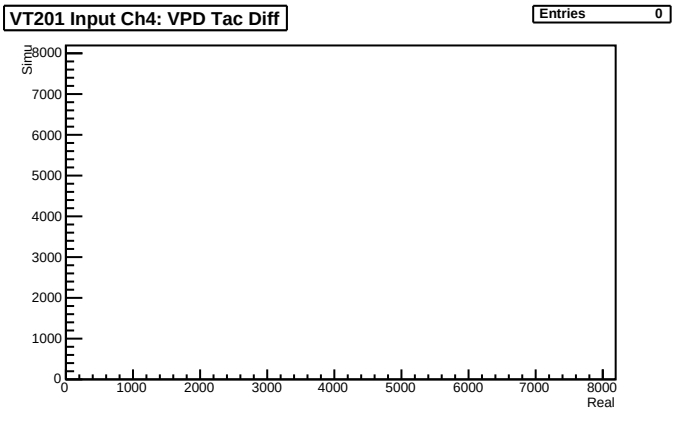

VT201 Input Ch2: ZDC Tac Diff
Entries 4025681
Tac

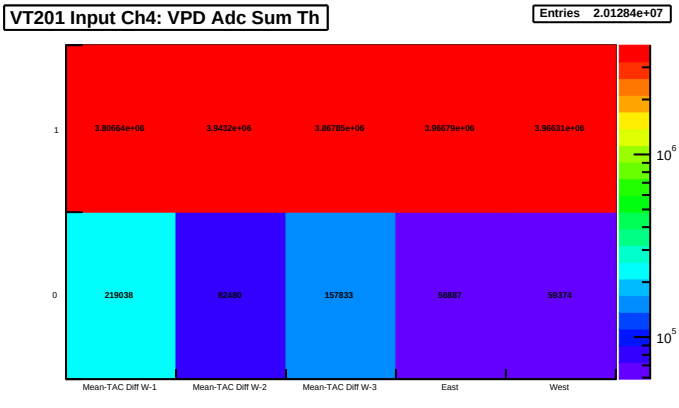

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

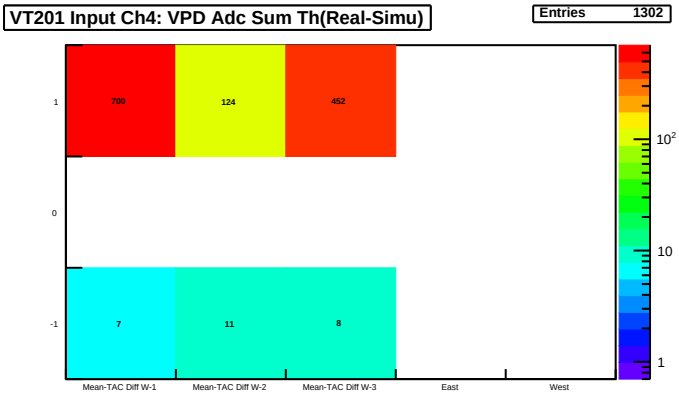

VT201 Input Ch2: ZDC Adc Sum Th
Entries 3.220545e+07
3.93326e+06
3.91553e+06
2.08438e+06
1.88485e+06
3.93337e+06
3.91276e+06
1.42465e+06
1.1923e+06
92416
110148
1.9413e+06
2.14083e+06
92312
112916
2.60103e+06
2.83339e+06
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

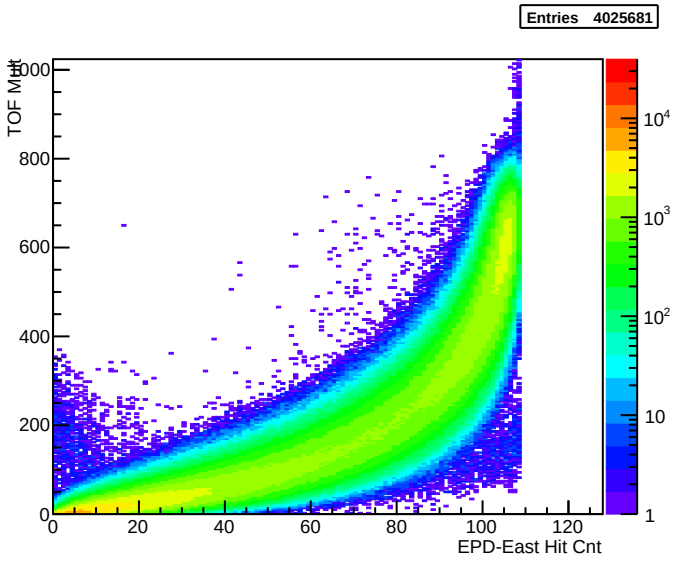

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

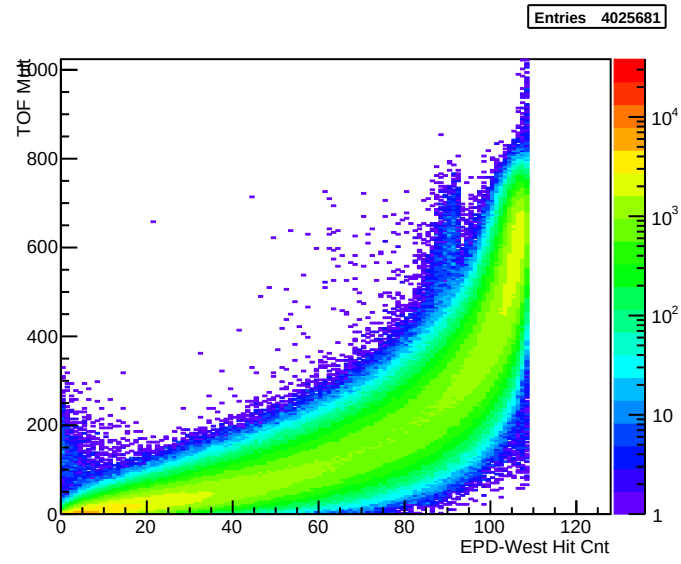

VT201 Input Ch4: VPD Tac Diff
Entries 4025681
Tac

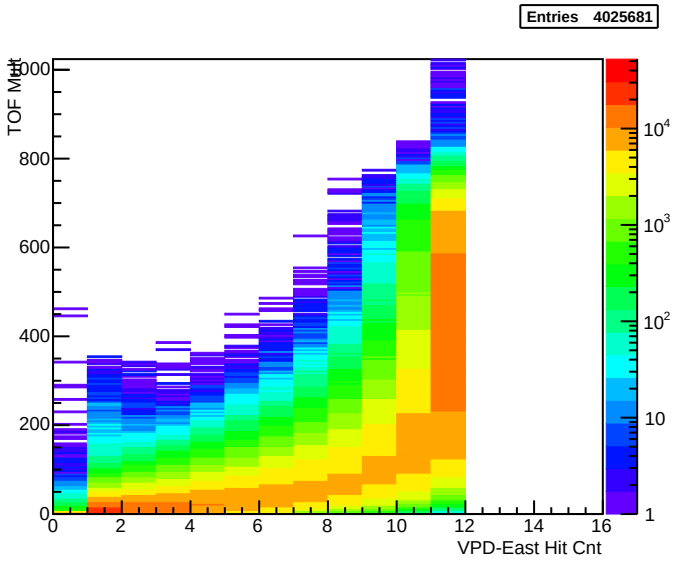

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

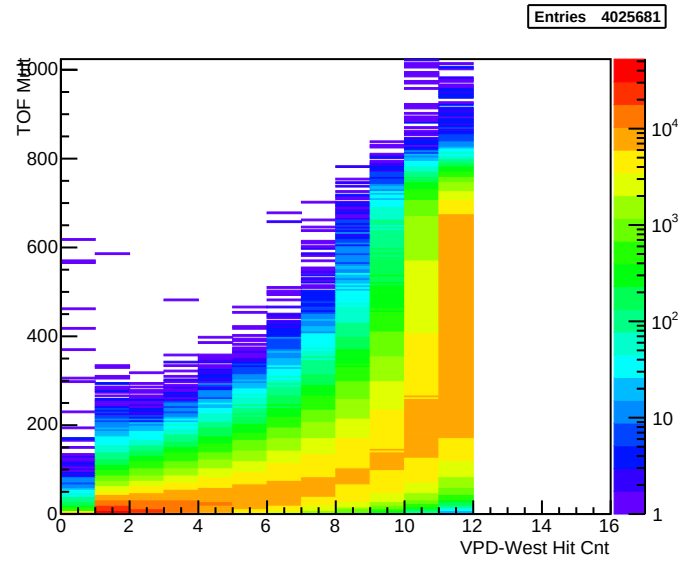

VT201 Input Ch4: VPD Adc Sum Th
Entries 2.01284e+07
3.80664e+06
3.9432e+06
3.86785e+06
3.96679e+06
3.96631e+06
219038
82480
157833
58887
59374
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

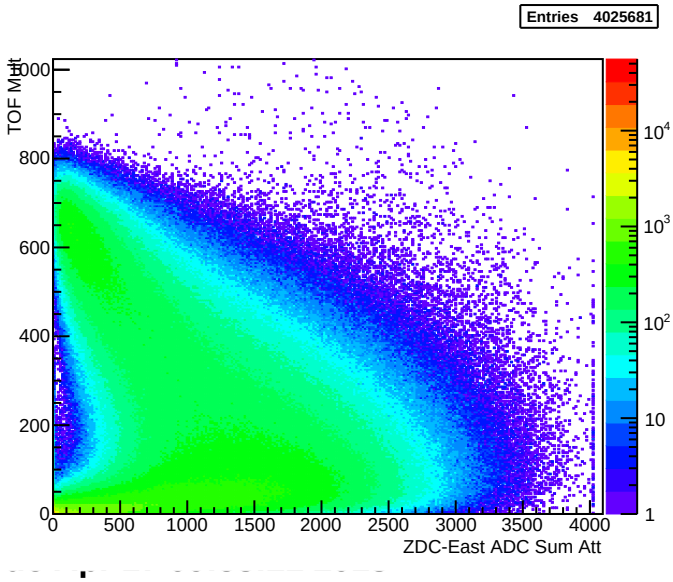

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 1302
700
124
452
7
11
8
Mean-TAC Diff W-1
Mean-TAC Diff W-2
Mean-TAC Diff W-3
East
West

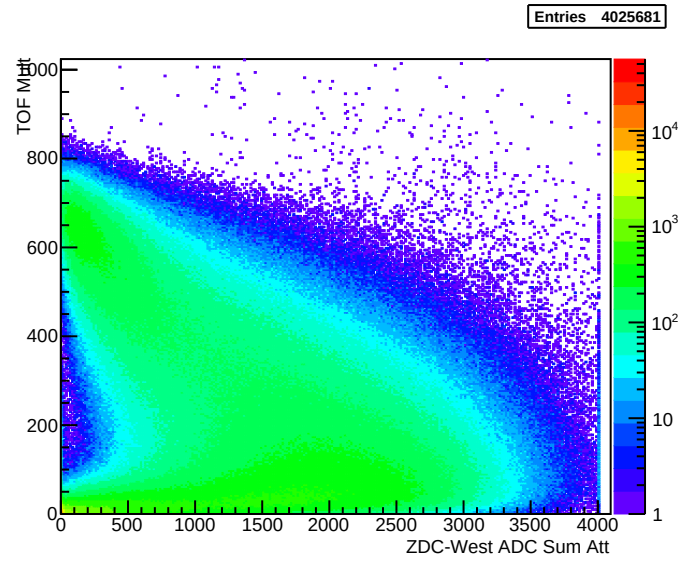

Entries 4025681
TOF Mult
EPD-East Hit Cnt

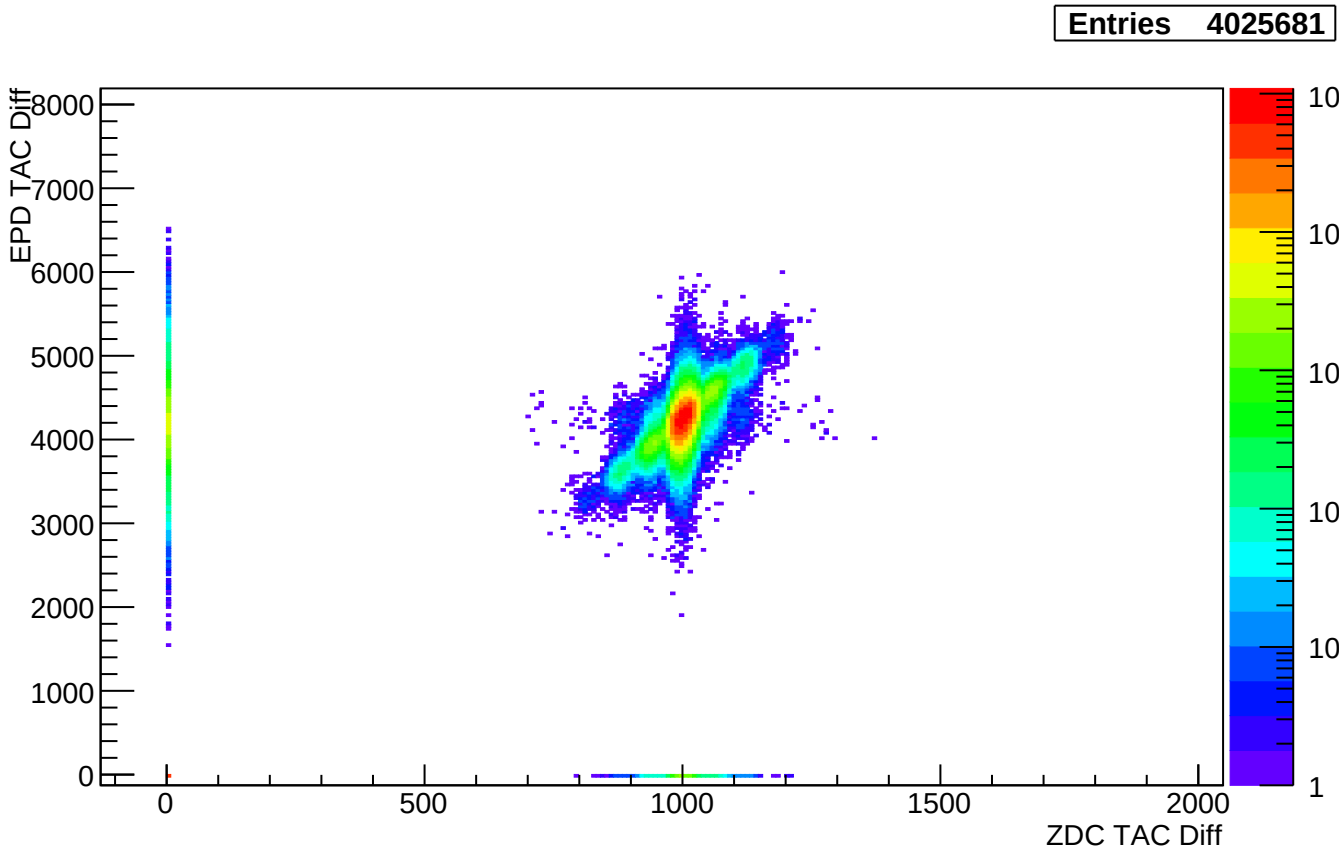

Entries 4025681
TOF Mult
EPD-West Hit Cnt

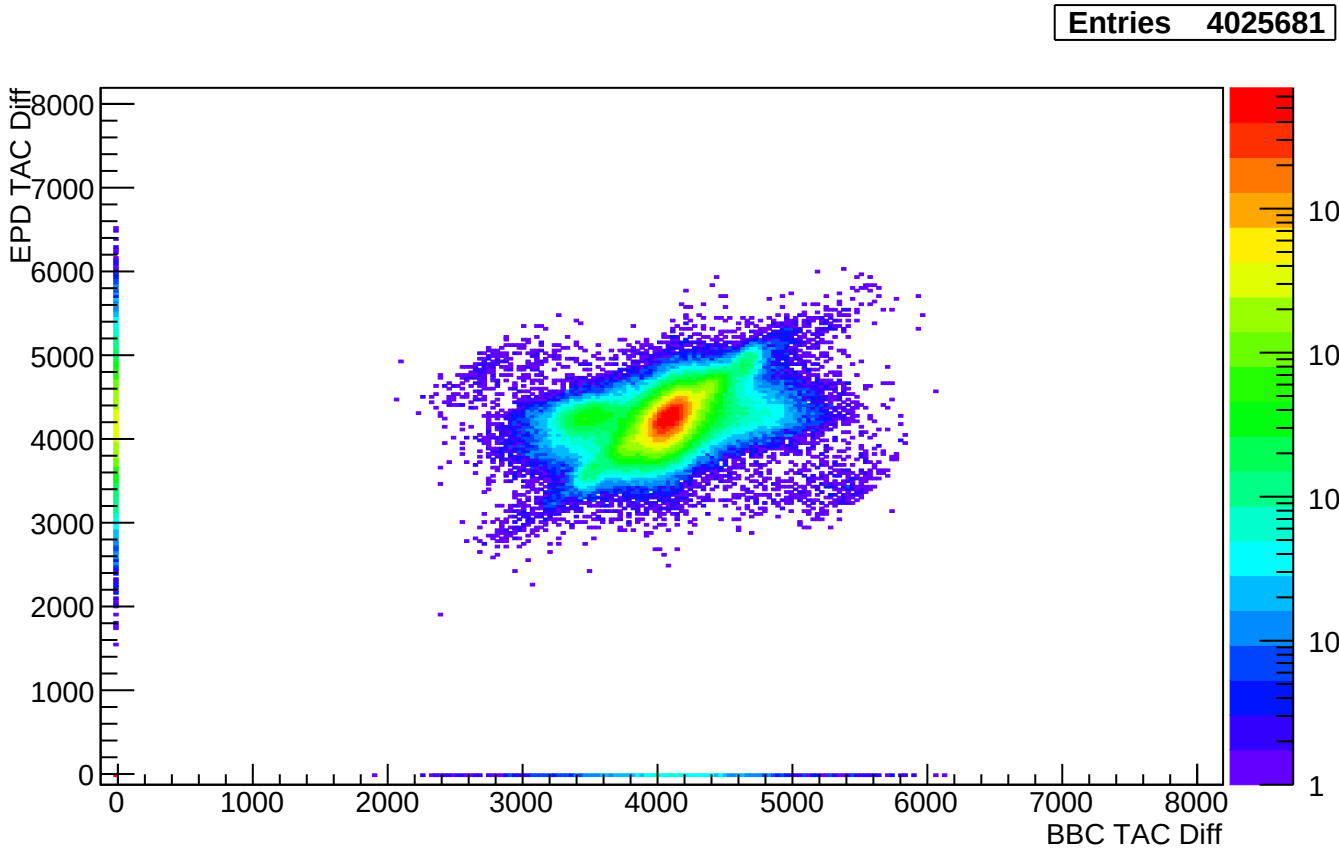

Entries 4025681
TOF Mult
VPD-East Hit Cnt

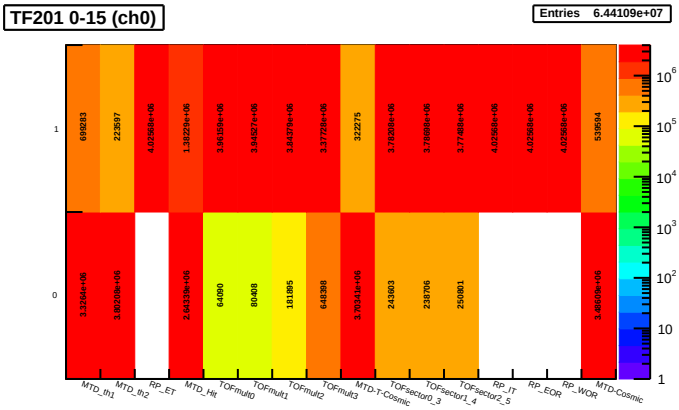

Entries 4025681
TOF Mult
VPD-West Hit Cnt

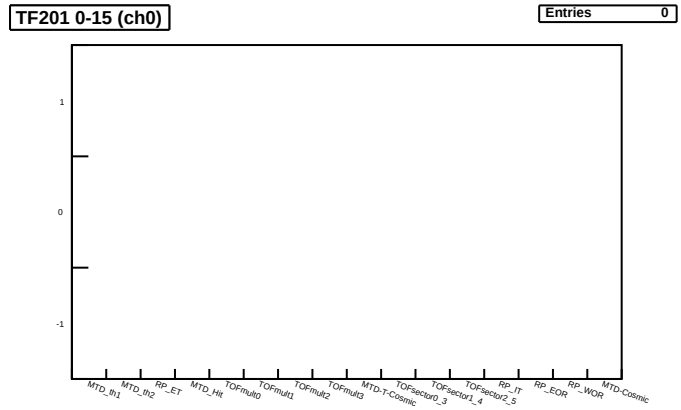

Entries 4025681
TOF Mult
ZDC-East ADC Sum Att

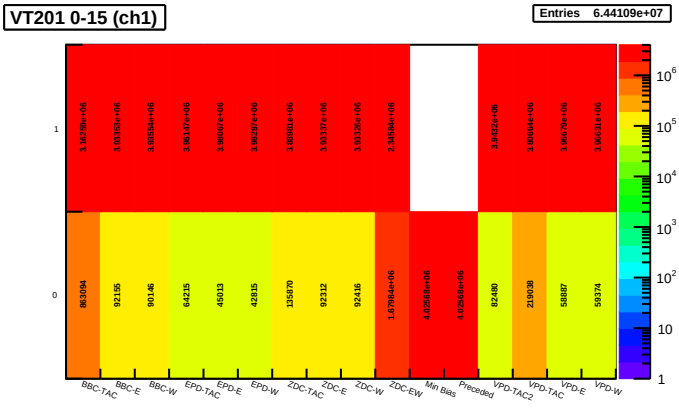

Entries 4025681
TOF Mult
ZDC-West ADC Sum Att

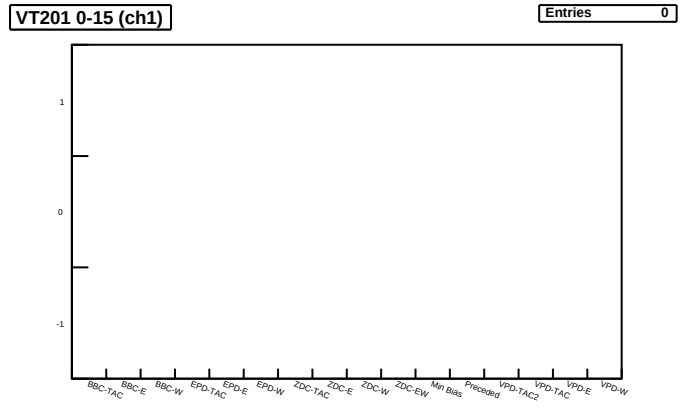

Entries 4025681
EPD TAC Diff
ZDC TAC Diff
0
500
1000
1500
2000
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2
10^3
10^4
10^5

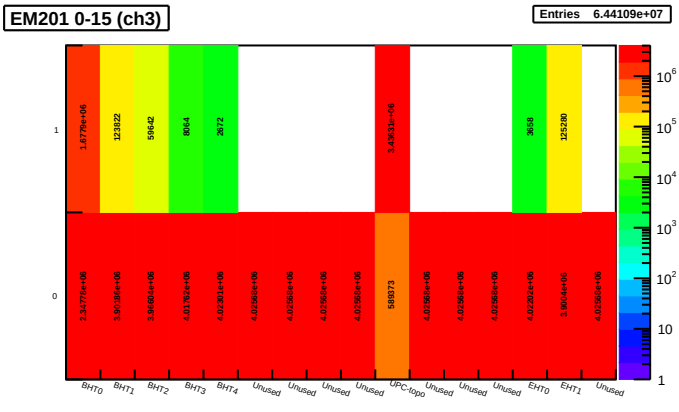

Entries 4025681
EPD TAC Diff
BBC TAC Diff
0
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10^2
10^3
10^4

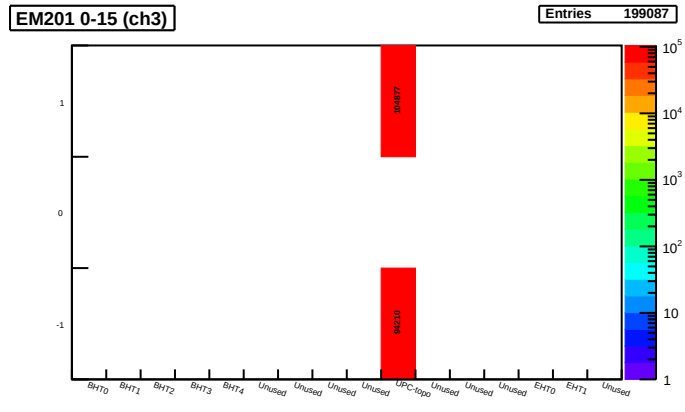

TF201 0-15 (ch0)
Entries 6.44109e+07
MTD_th1
MTD_th2
RP_ET
MTD_Hit
TOFmult0
TOFmult1
TOFmult2
TOFmult3
MTD-T-Cosmic
TOFsector0_3
TOFsector1_4
TOFsector2_5
RP_IT
RP_EOR
RP_WOR
MTD-Cosmic

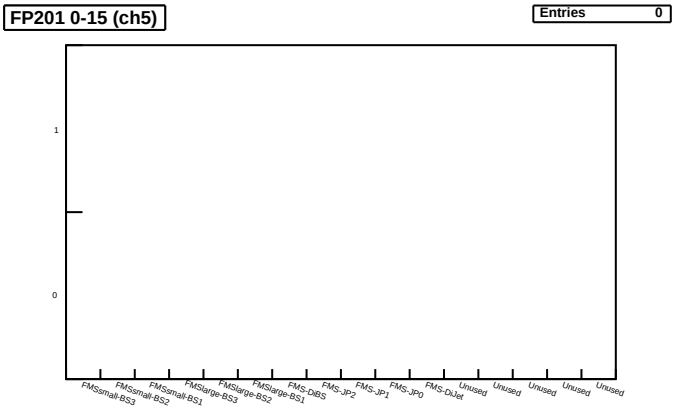

TF201 0-15 (ch0)
Entries 0
MTD_th1
MTD_th2
RP_ET
MTD_Hit
TOFmult0
TOFmult1
TOFmult2
TOFmult3
MTD-T-Cosmic
TOFsector0_3
TOFsector1_4
TOFsector2_5
RP_IT
RP_EOR
RP_WOR
MTD-Cosmic

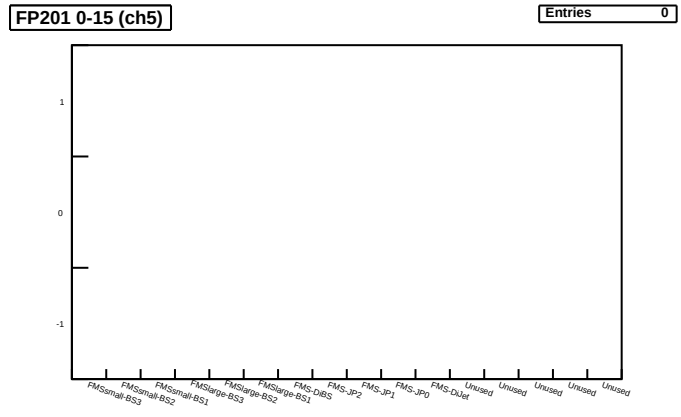

VT201 0-15 (ch1)
Entries 6.44109e+07
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-EW
Min Bias
Preceded
VPD-TAC2
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-EW
Min Bias
Preceded
VPD-TAC2
VPD-TAC
VPD-E
VPD-W


EM201 0-15 (ch3)
Entries 6.44109e+07
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1


EM201 0-15 (ch3)
Entries 199087
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-BS3
FMSsmall-BS2
FMSsmall-BS1
FMSlarge-BS3
FMSlarge-BS2
FMSlarge-BS1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-DiJet
Unused