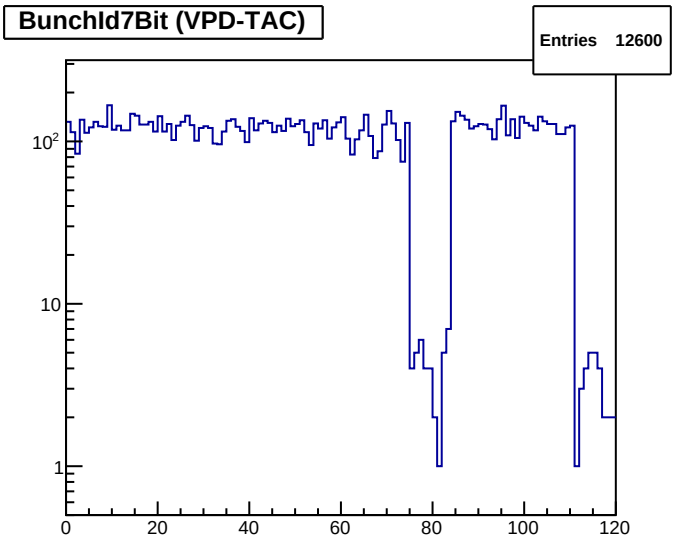
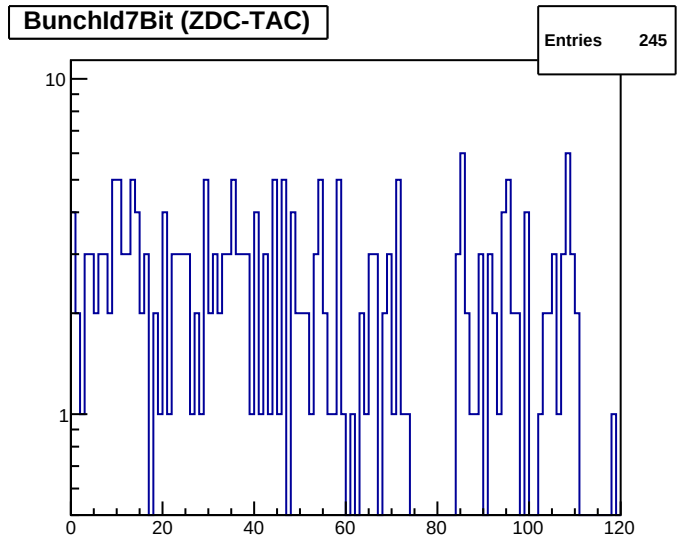
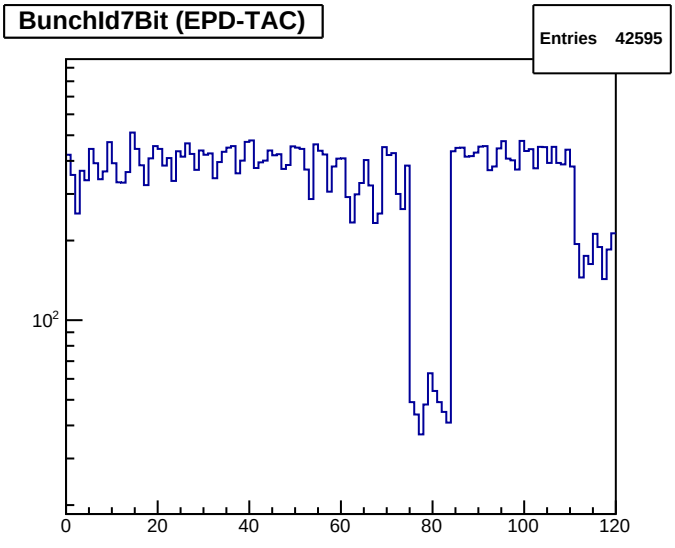
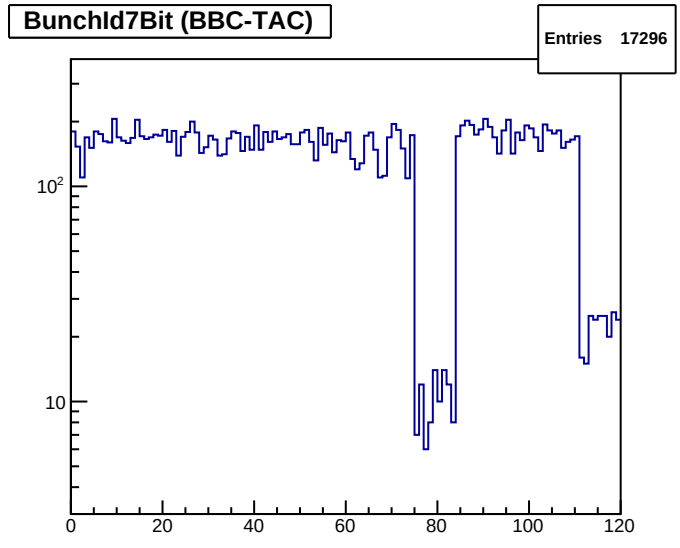
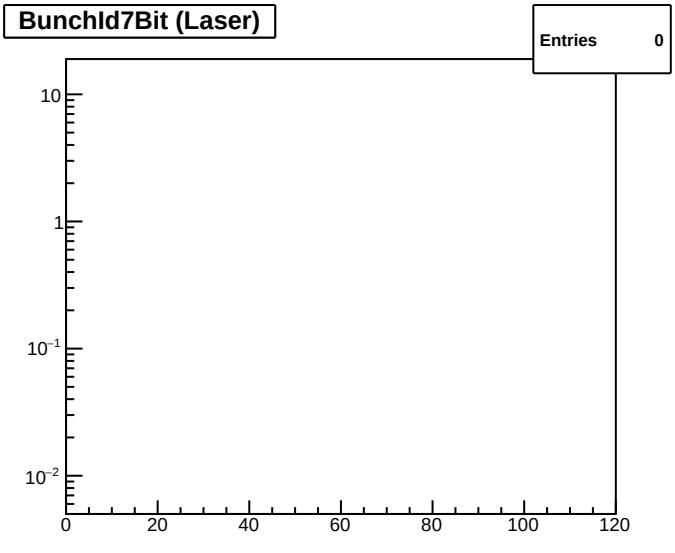
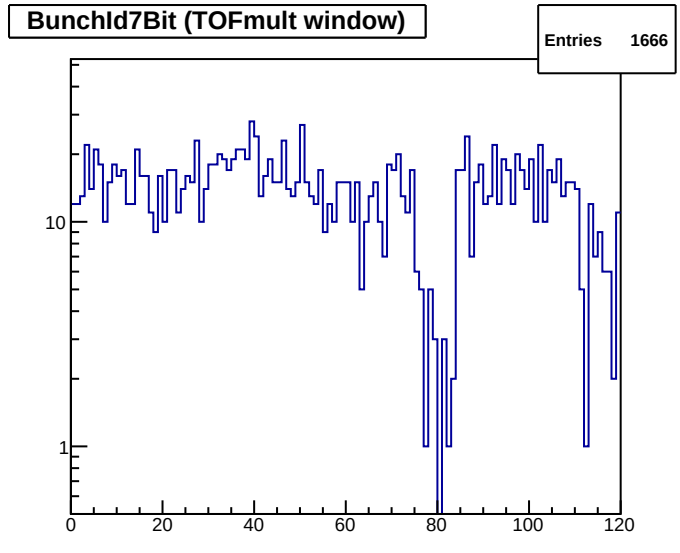

BunchId7Bit (VPD-TAC)
Entries 12600
10^2
10
1
0
20
40
60
80
100
120

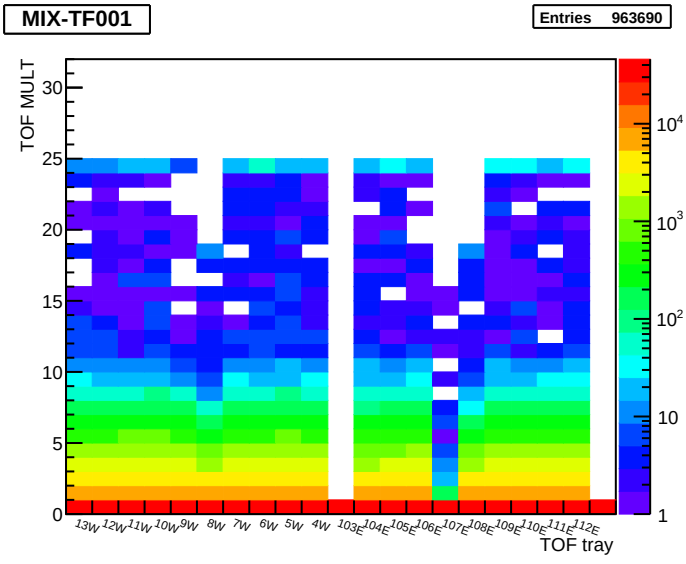

BunchId7Bit (ZDC-TAC)
Entries 245
10
1
0
20
40
60
80
100
120

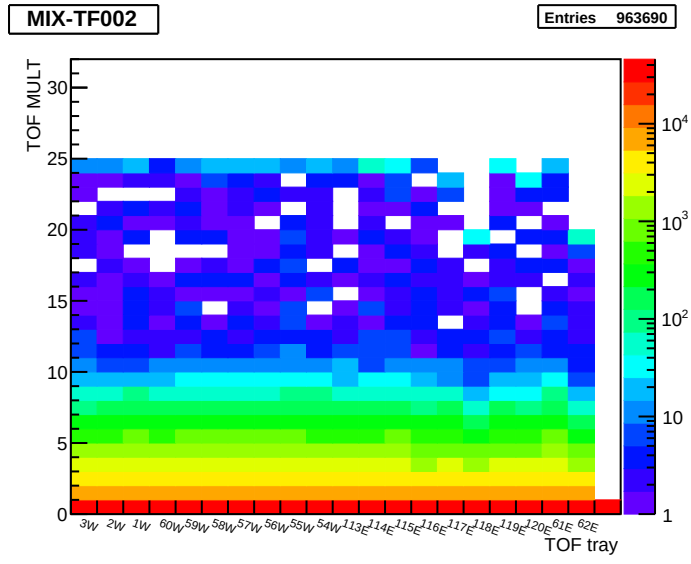

BunchId7Bit (EPD-TAC)
Entries 42595
10^2
0
20
40
60
80
100
120

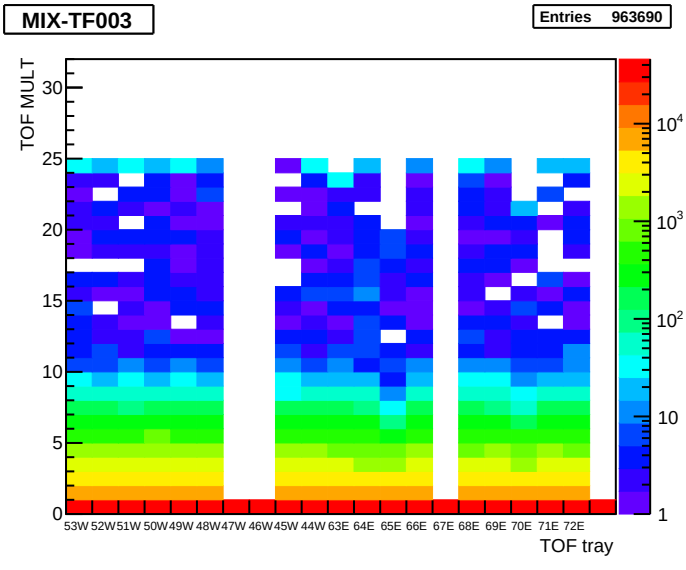

BunchId7Bit (BBC-TAC)
Entries 17296
10^2
10
0
20
40
60
80
100
120

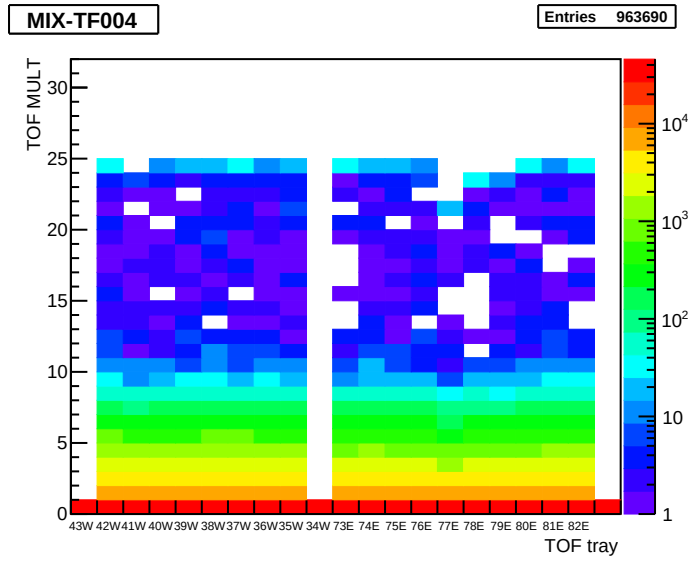

BunchId7Bit (Laser)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

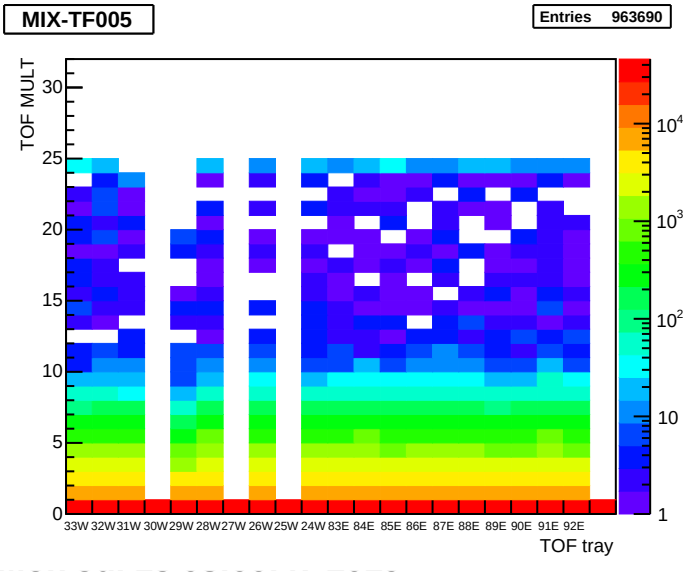

BunchId7Bit (TOFmult window)
Entries 1666
10
1
0
20
40
60
80
100
120

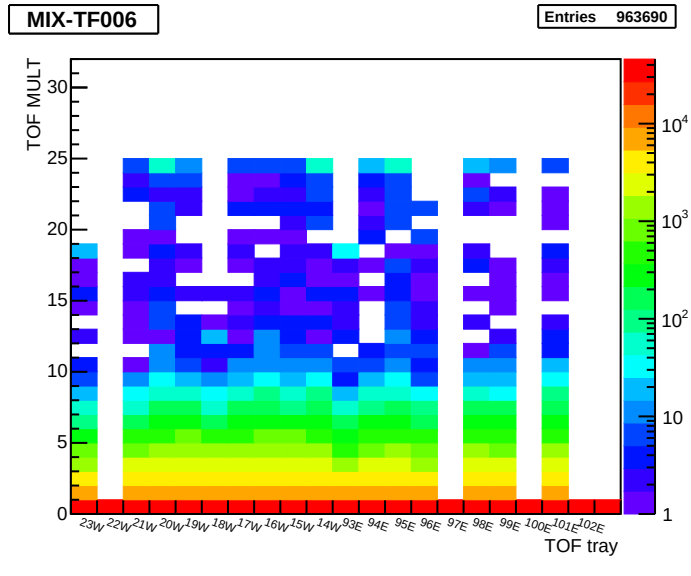

MIX-TF001
Entries 963690
TOF MULT
TOF tray

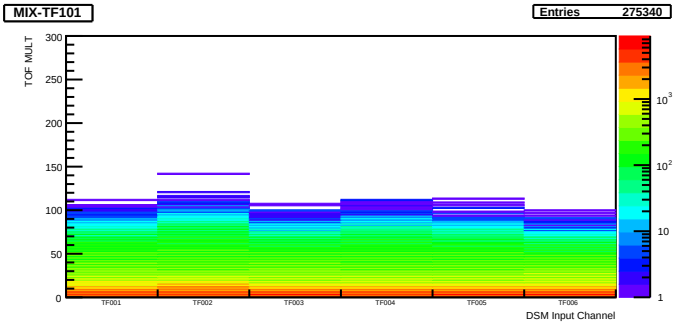

MIX-TF002
Entries 963690
TOF MULT
TOF tray

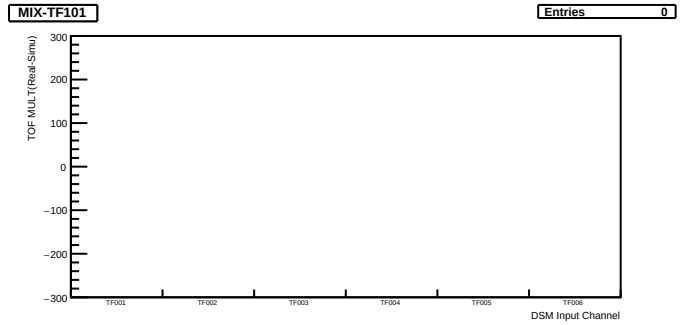

MIX-TF003
Entries 963690
TOF MULT
TOF tray

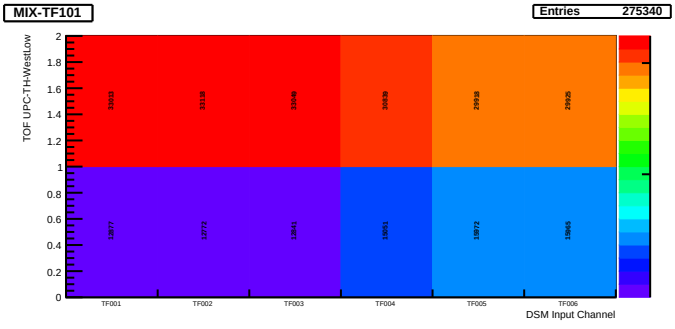

MIX-TF004
Entries 963690
TOF MULT
TOF tray

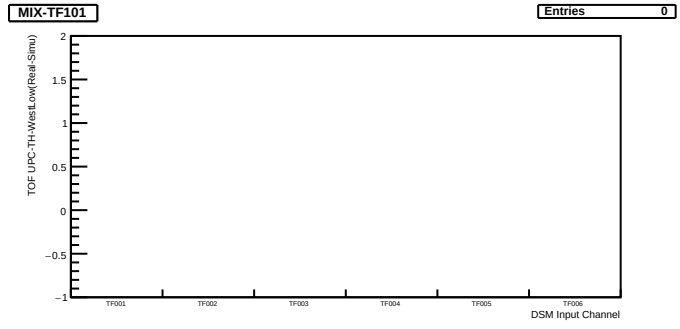

MIX-TF005
Entries 963690
TOF MULT
TOF tray

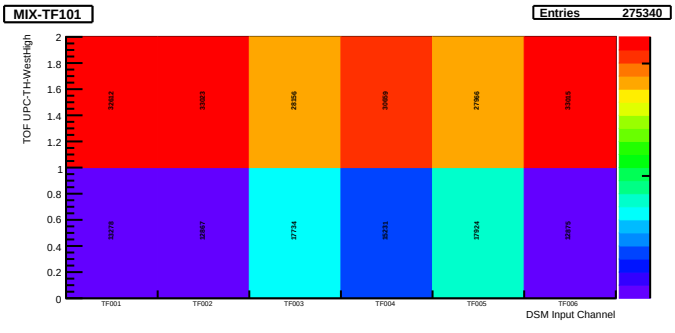

MIX-TF006
Entries 963690
TOF MULT
TOF tray

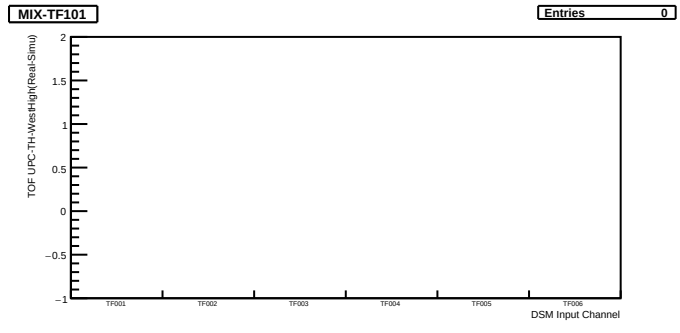

MIX-TF101
Entries 275340
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

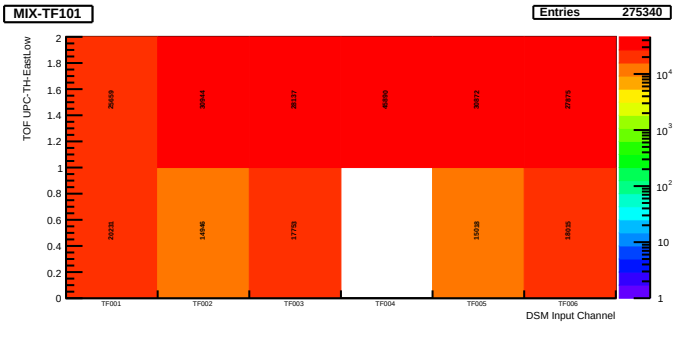

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

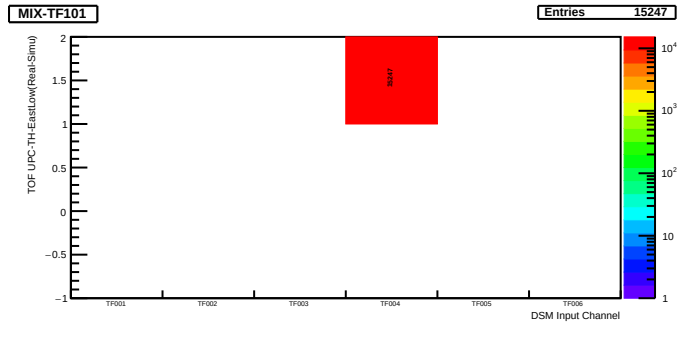

MIX-TF101
Entries 275340
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

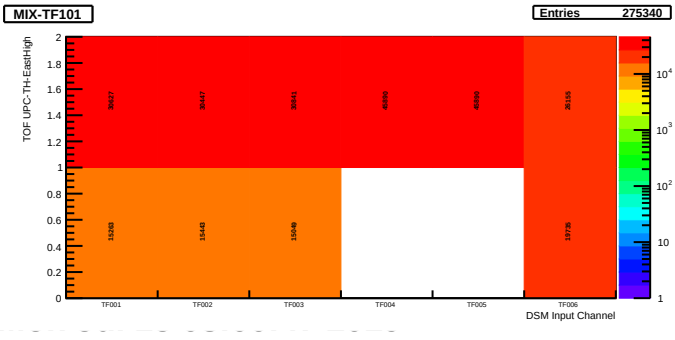

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

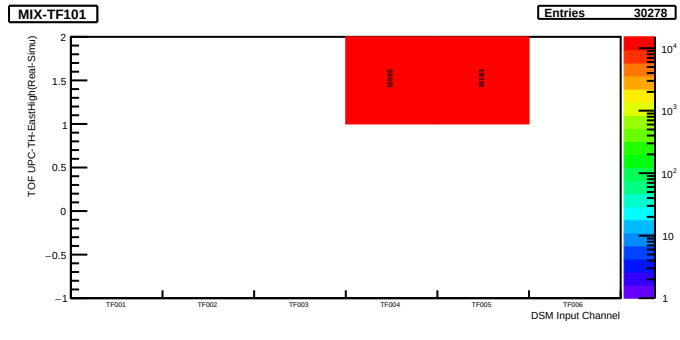

MIX-TF101
Entries 275340
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

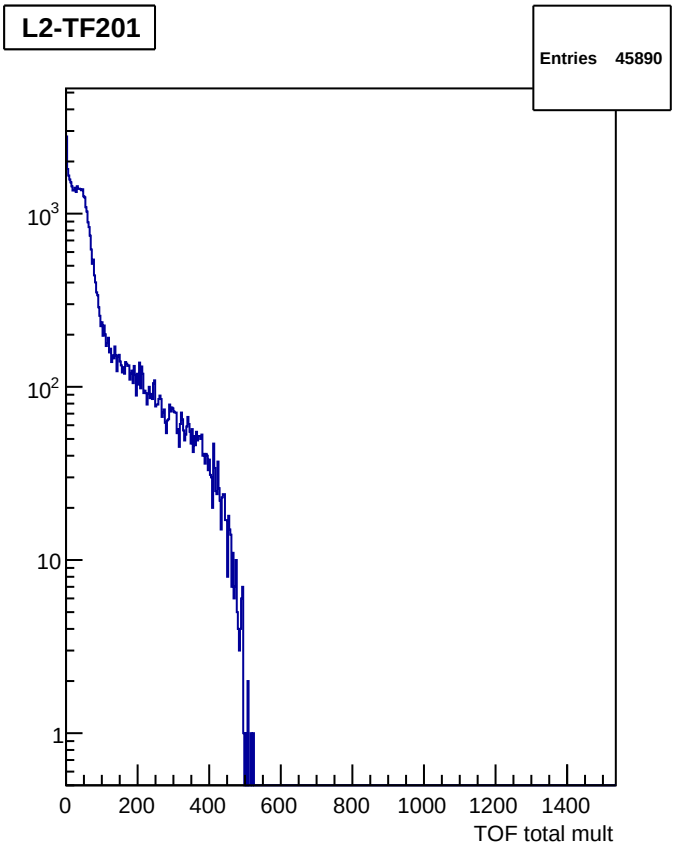

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

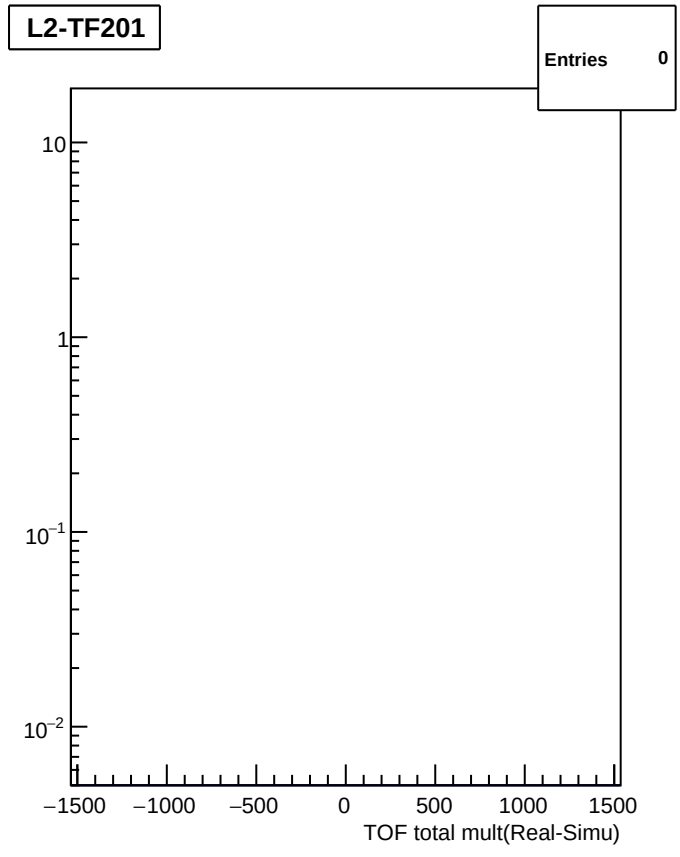

MIX-TF101
Entries 275340
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

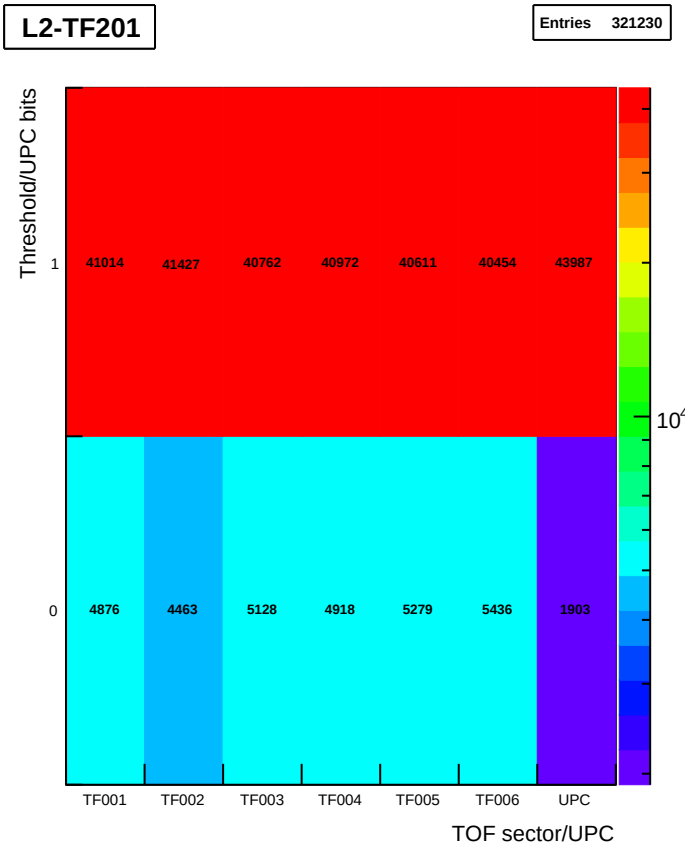

MIX-TF101
Entries 15247
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

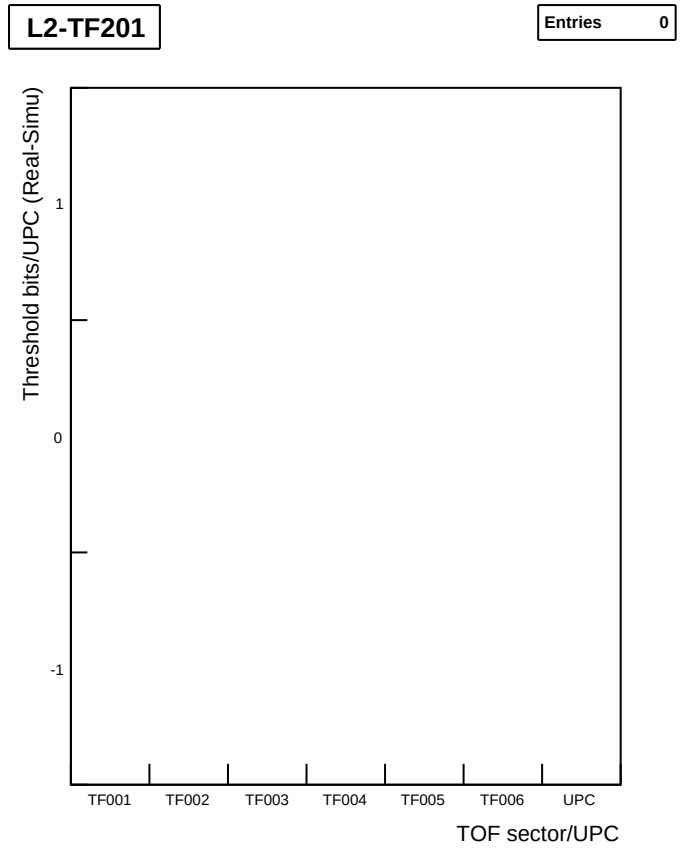

MIX-TF101
Entries 275340
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

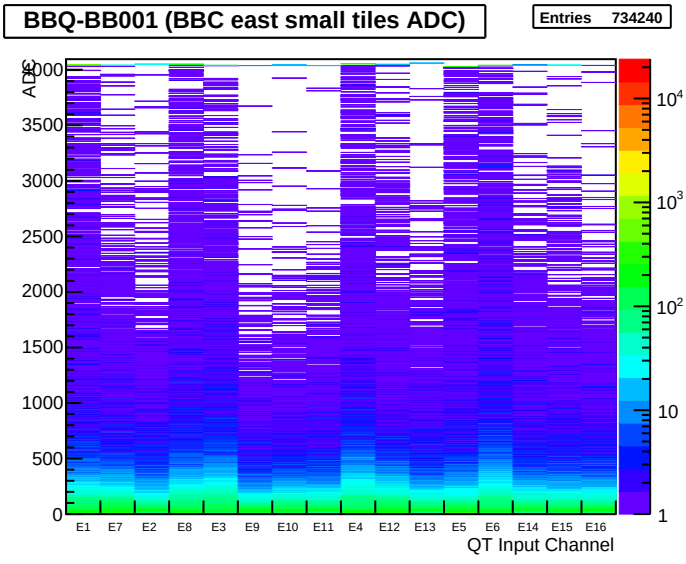

MIX-TF101
Entries 30278
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

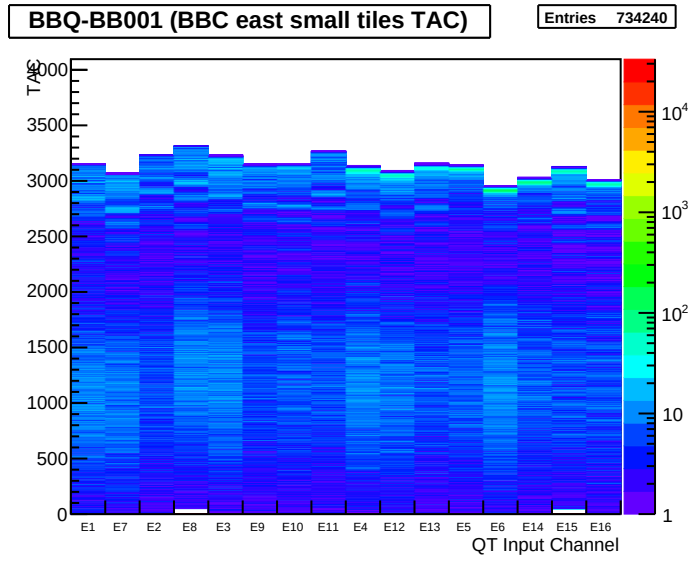

L2-TF201
Entries 45890
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

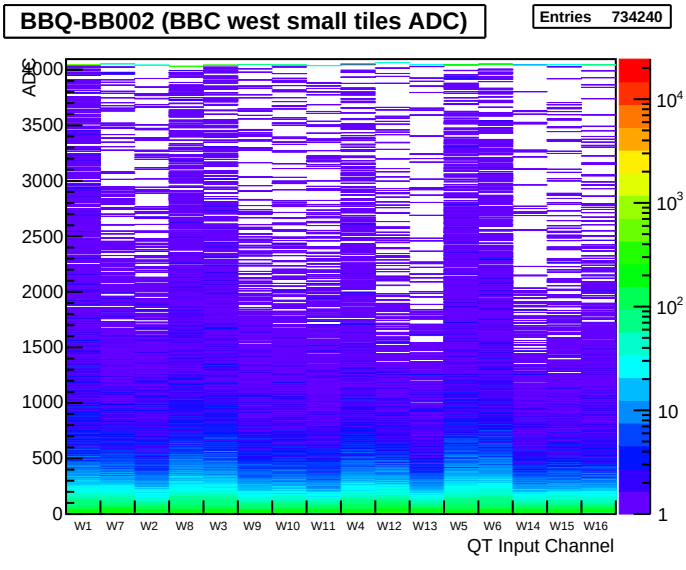

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

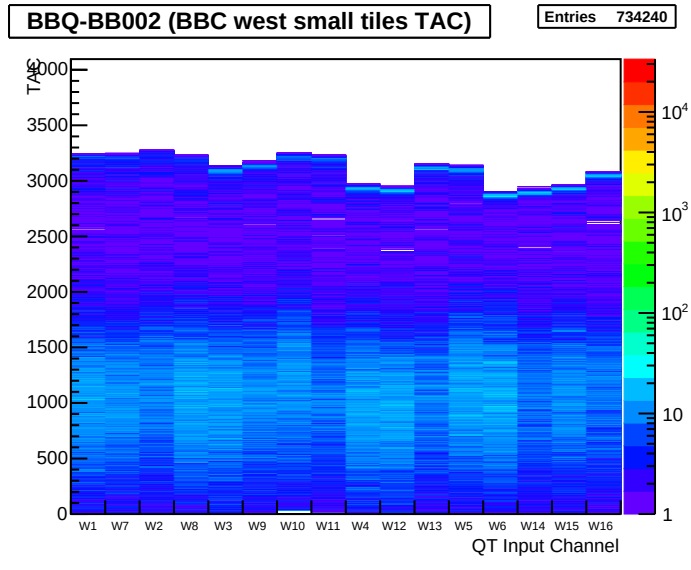

L2-TF201
Entries 321230
Threshold/UPC bits
1
0
41014
41427
40762
40972
40611
40454
43987
4876
4463
5128
4918
5279
5436
1903
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

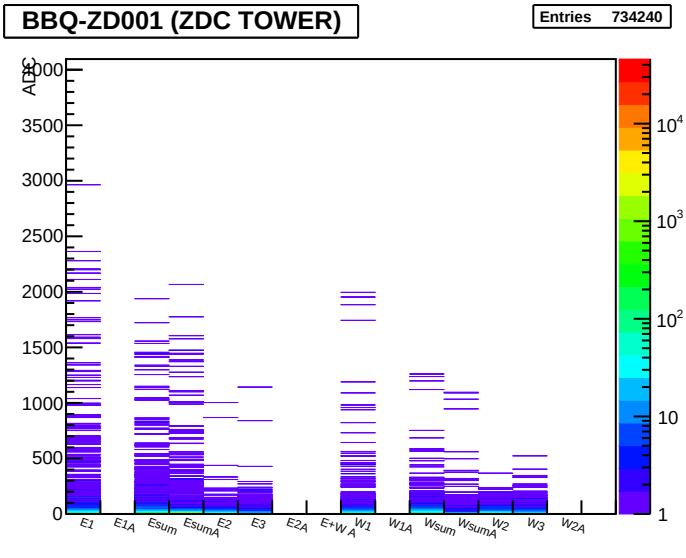

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

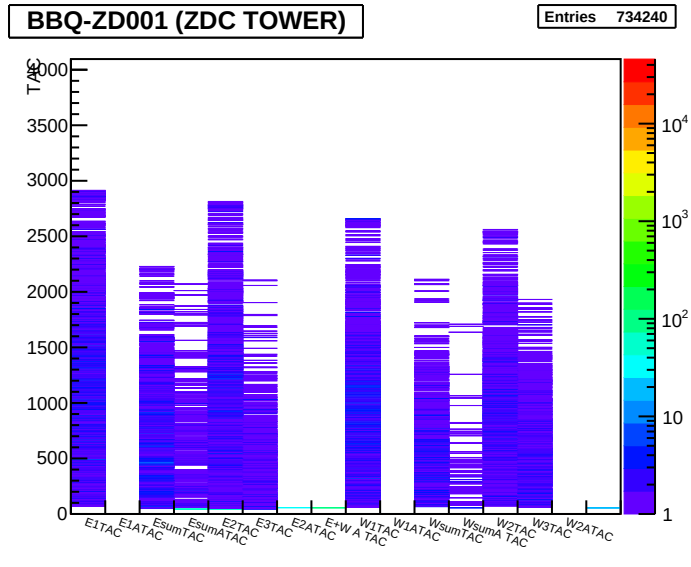

BBQ-BB001 (BBC east small tiles ADC)
Entries 734240
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

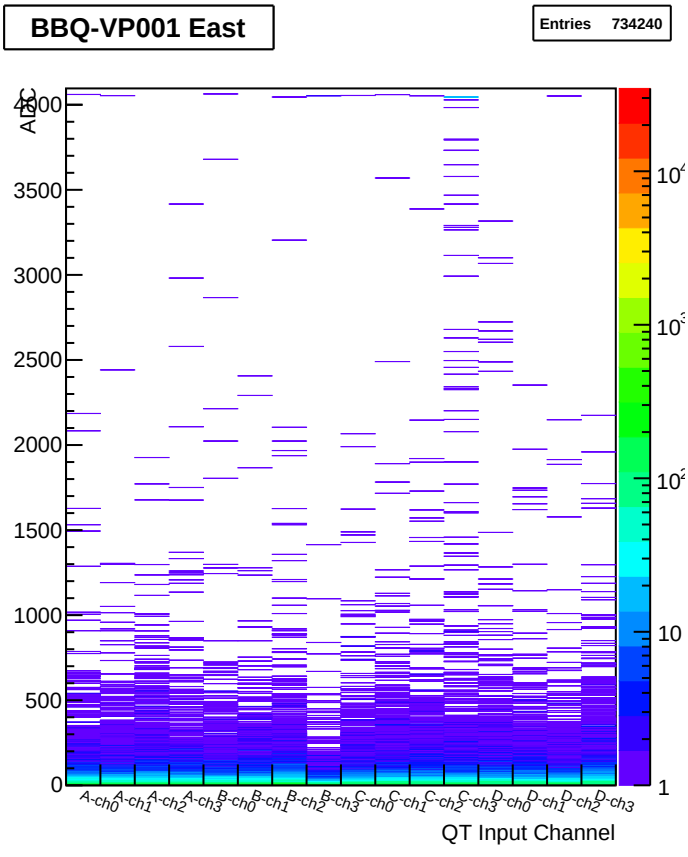

BBQ-BB001 (BBC east small tiles TAC)
Entries 734240
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

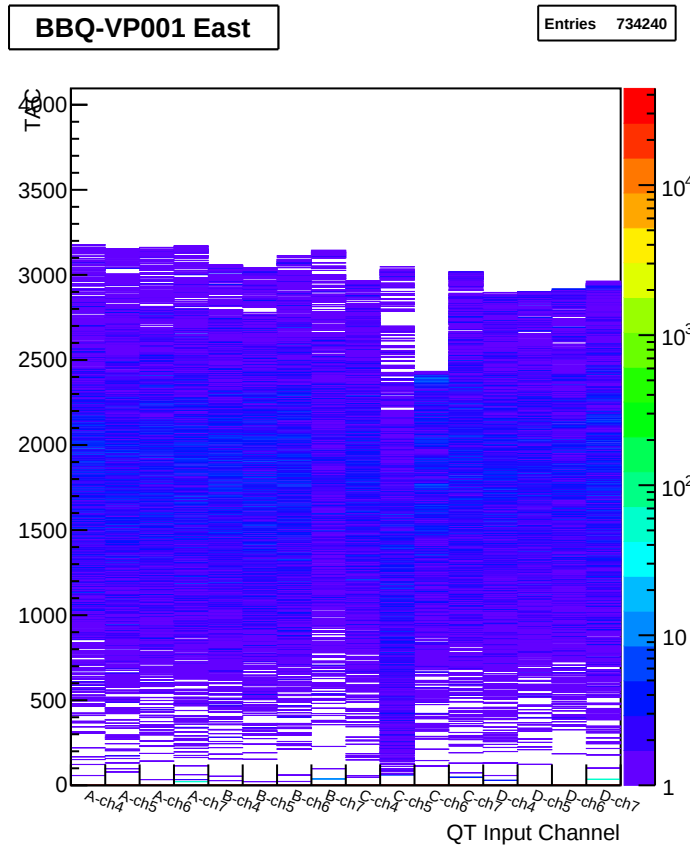

BBQ-BB002 (BBC west small tiles ADC)
Entries 734240
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

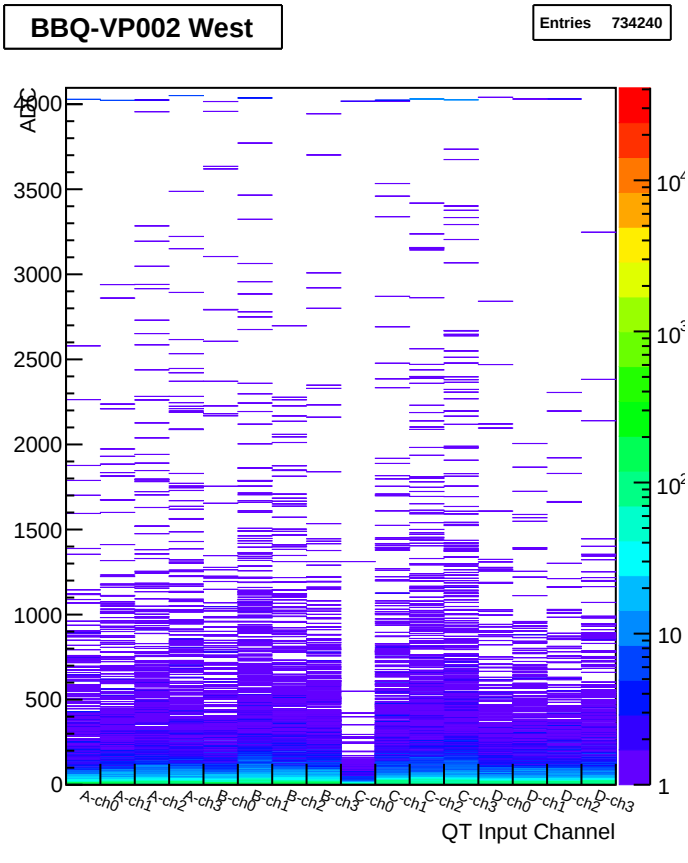

BBQ-BB002 (BBC west small tiles TAC)
Entries 734240
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

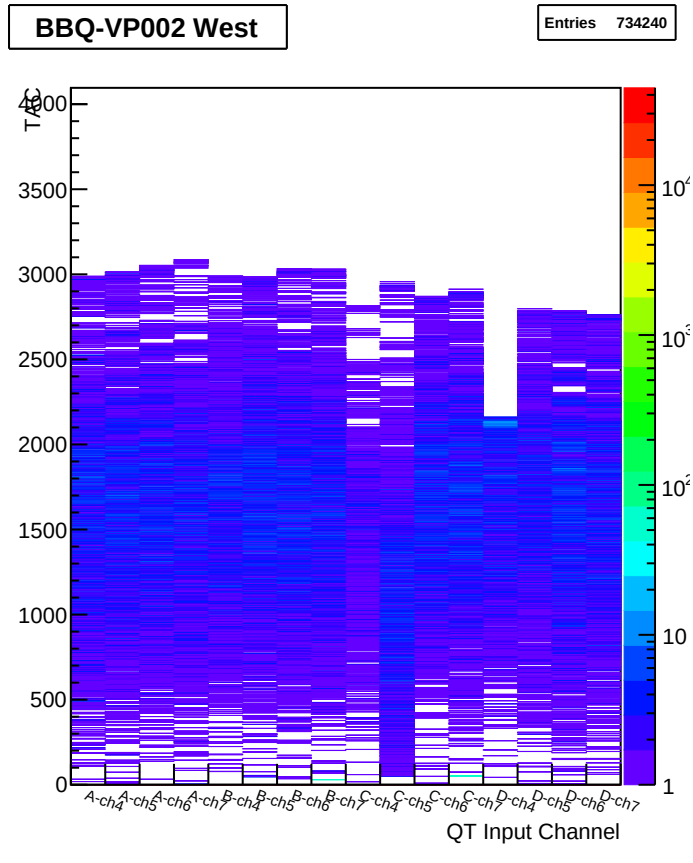

BBQ-ZD001 (ZDC TOWER)
Entries 734240
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

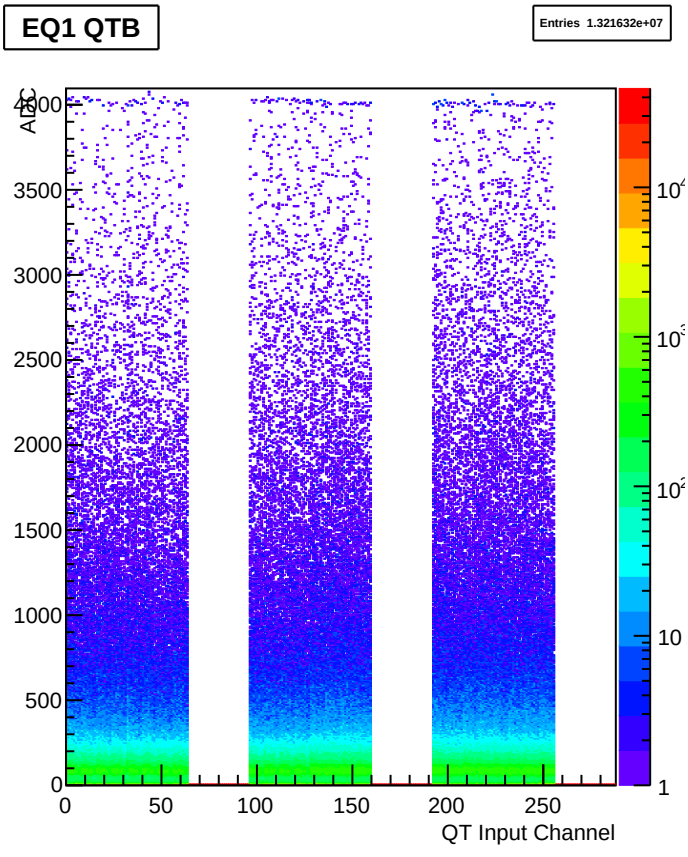

BBQ-ZD001 (ZDC TOWER)
Entries 734240
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

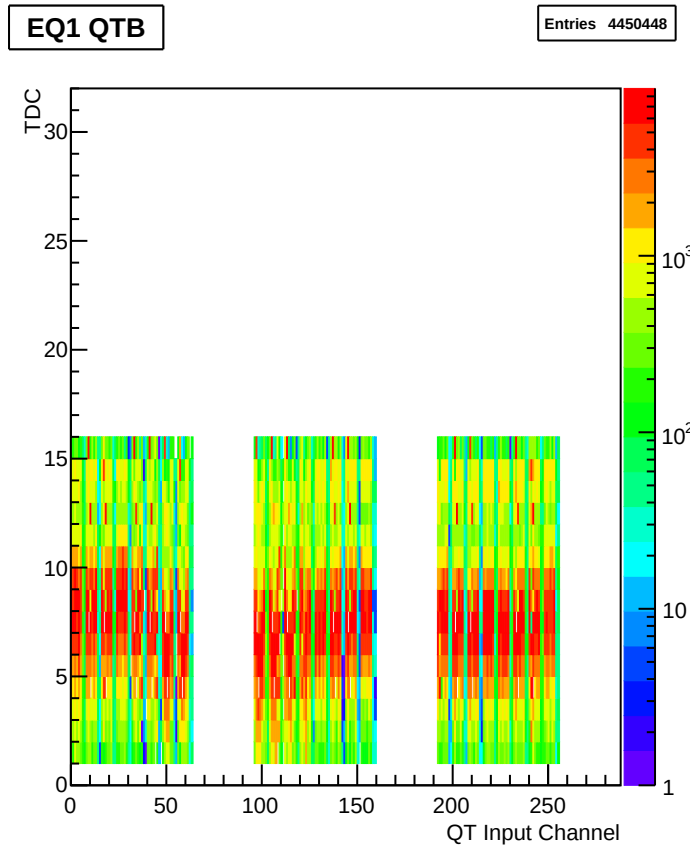

BBQ-VP001 East
Entries 734240
ADC
QT Input Channel

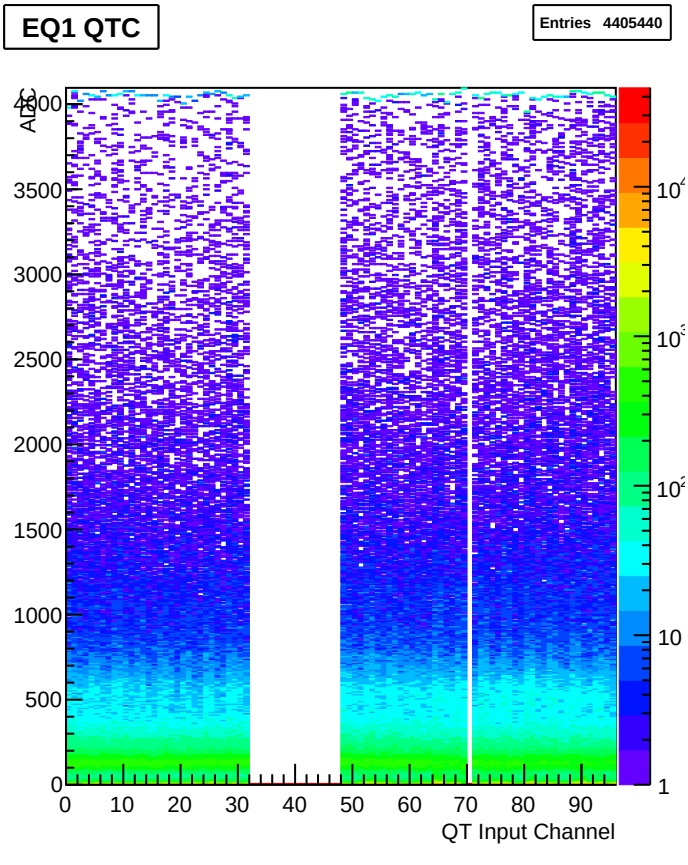

BBQ-VP001 East
Entries 734240
TAC
QT Input Channel

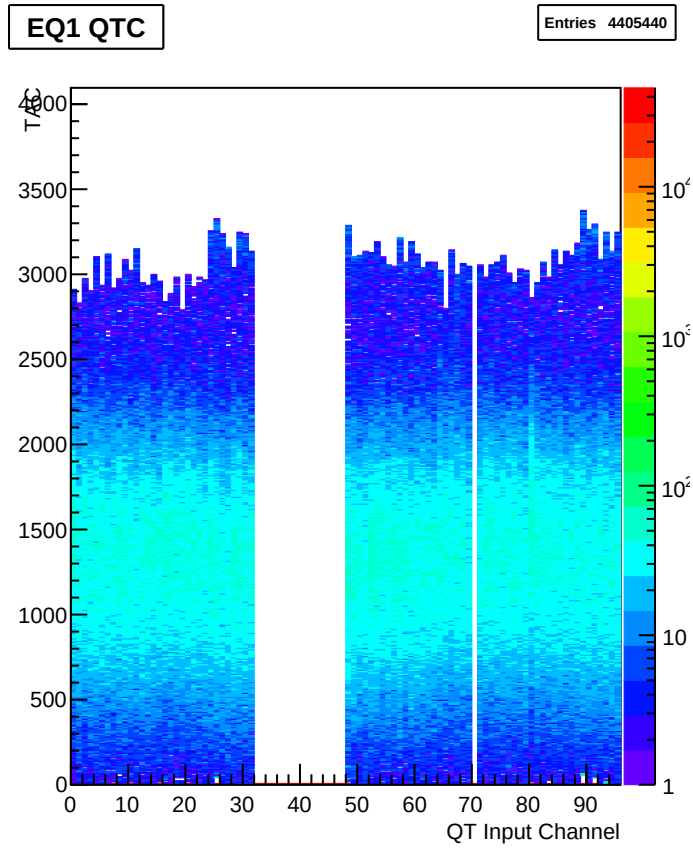

BBQ-VP002 West
Entries 734240
ADC
QT Input Channel

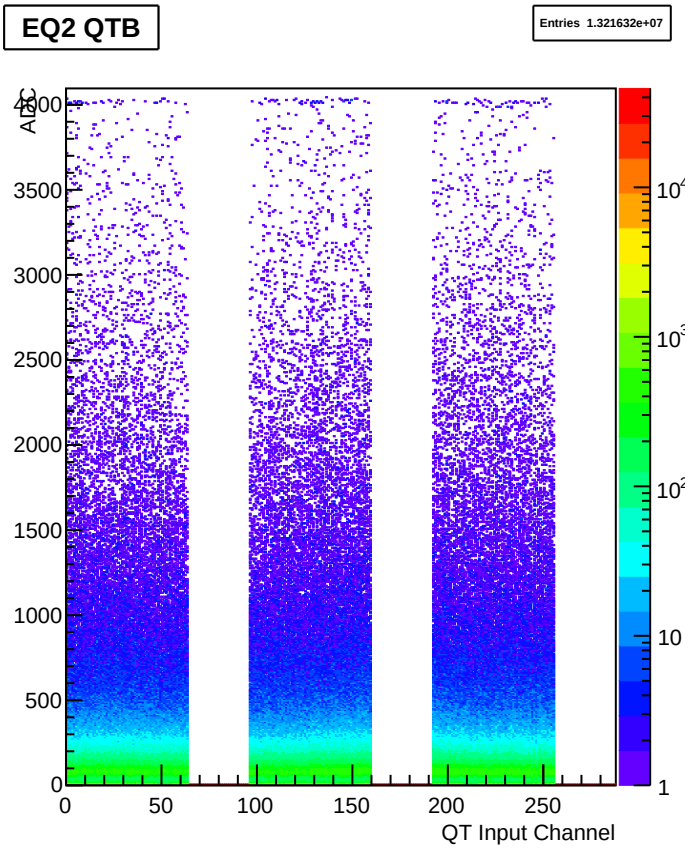

BBQ-VP002 West
Entries 734240
TAC
QT Input Channel

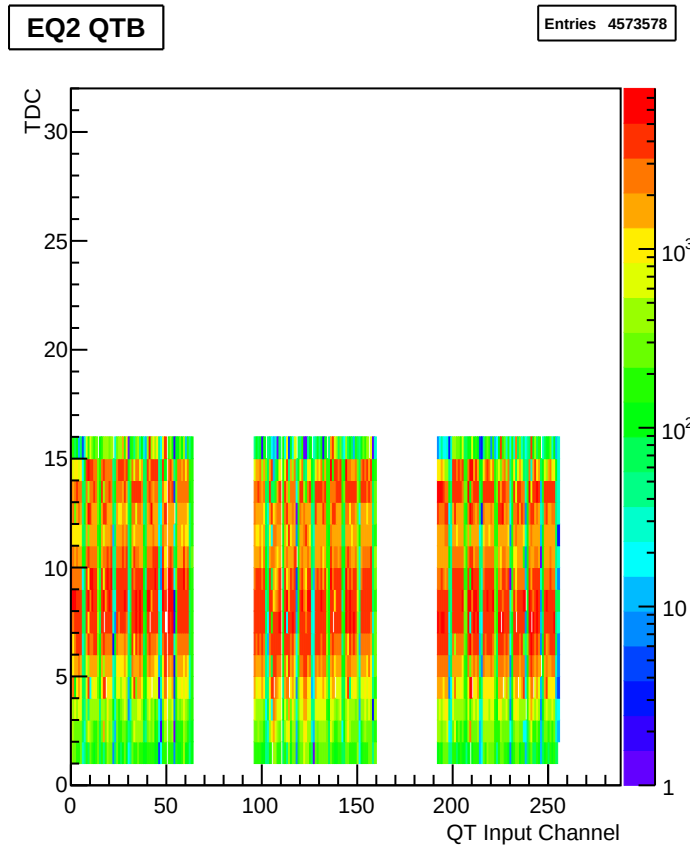

EQ1 QTB
Entries 1.321632e+07
ADC
QT Input Channel

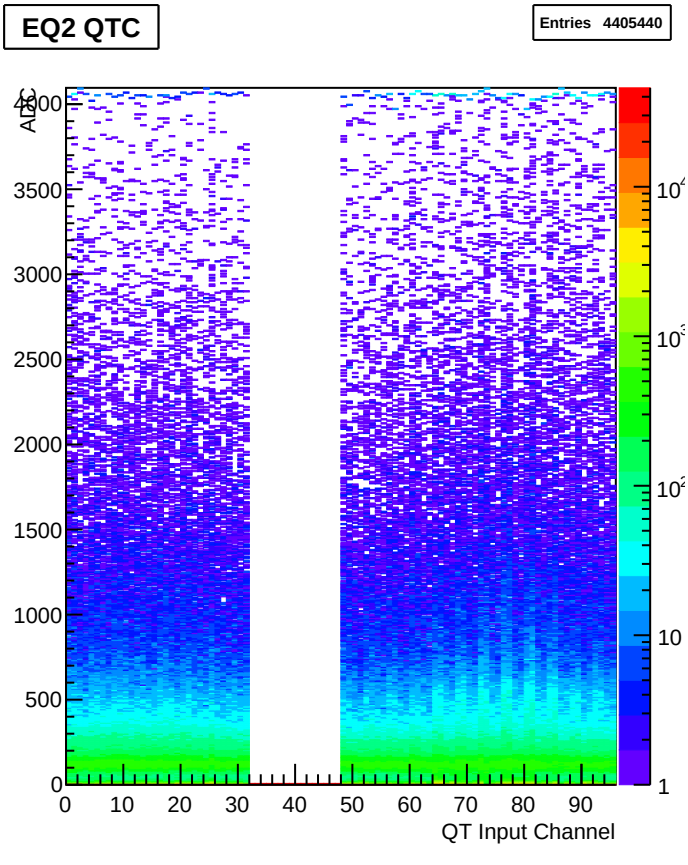

EQ1 QTB
Entries 4450448
TDC
QT Input Channel

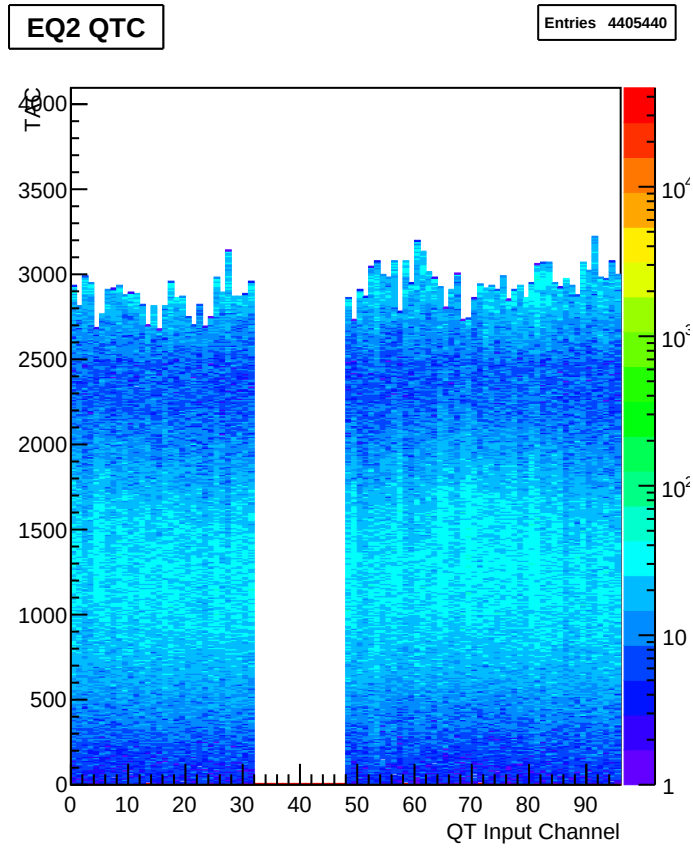

EQ1 QTC
Entries 4405440
ADC
QT Input Channel

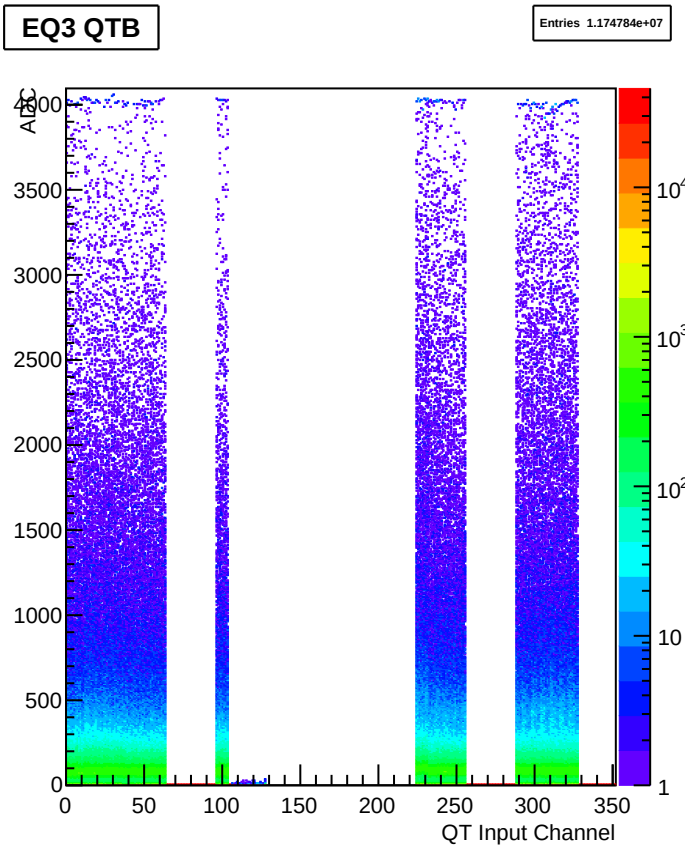

EQ1 QTC
Entries 4405440
TADC
QT Input Channel

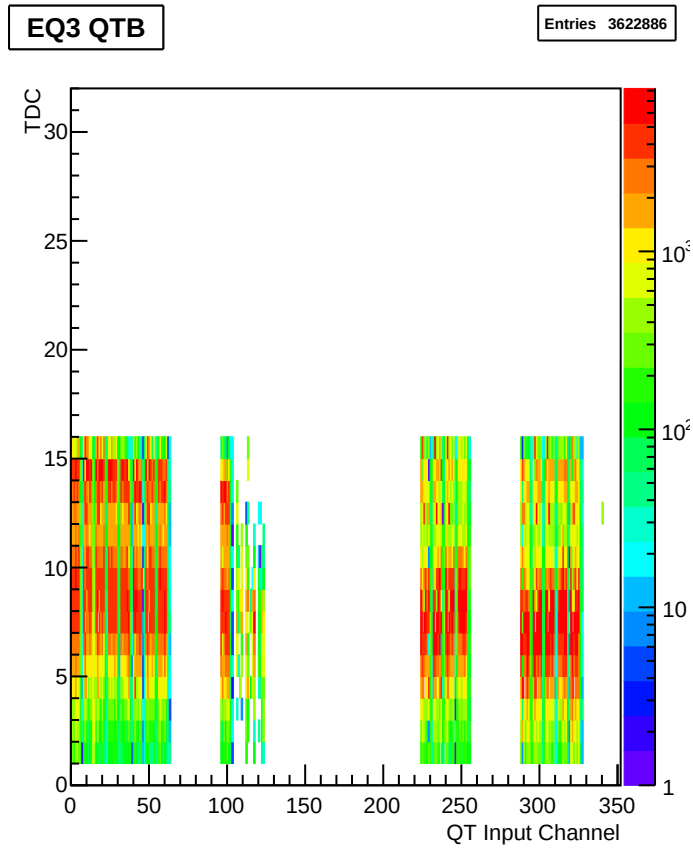

EQ2 QTB
Entries 1.321632e+07
ADC
QT Input Channel

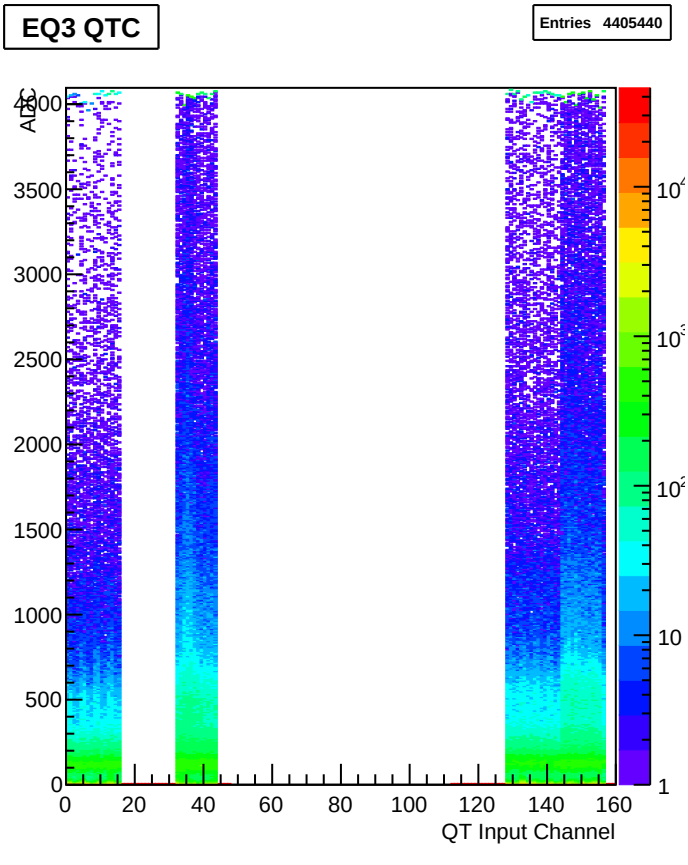

EQ2 QTB
Entries 4573578
TDC
QT Input Channel

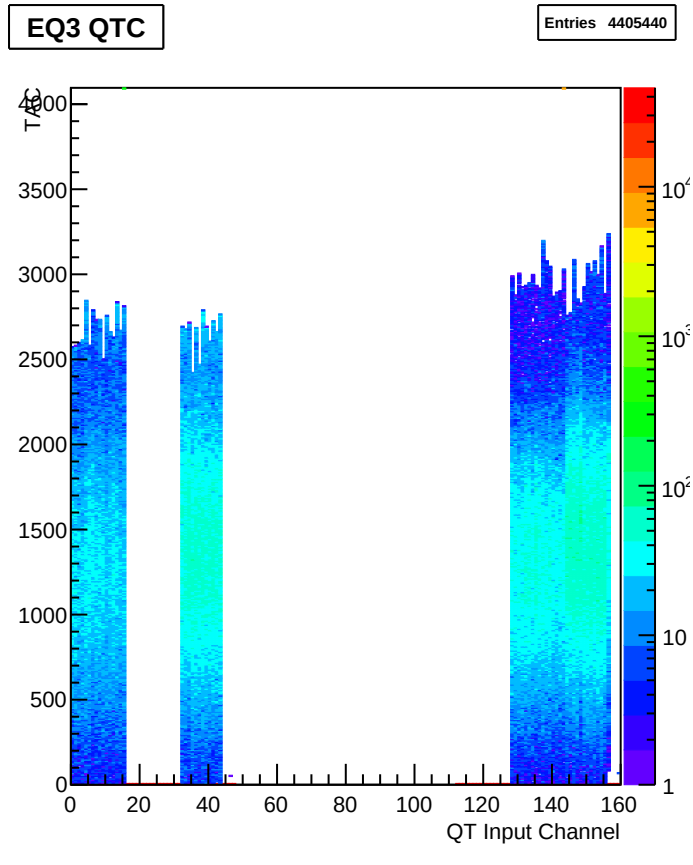

EQ2 QTC
Entries 4405440
ADC
QT Input Channel

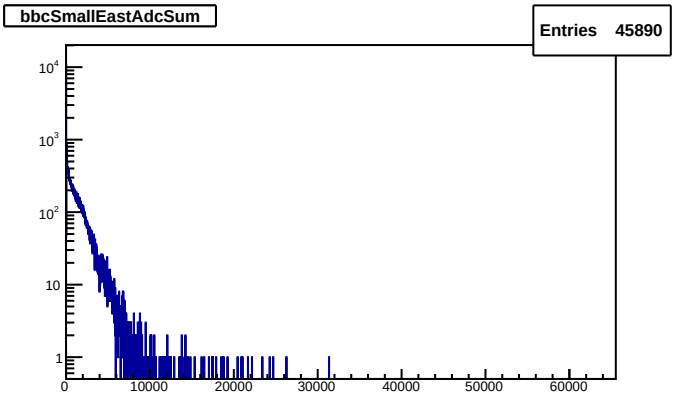

EQ2 QTC
Entries 4405440
TADC
QT Input Channel

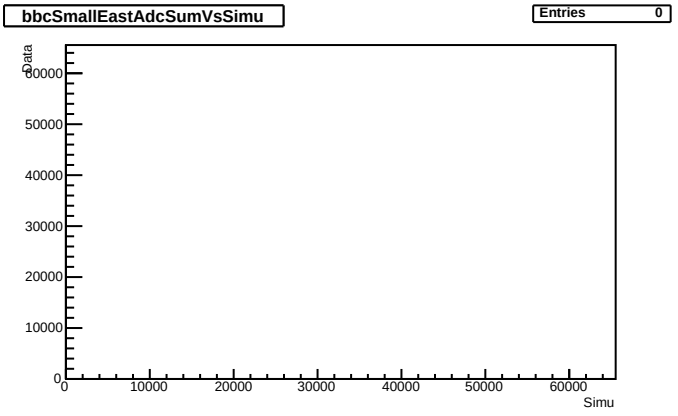

EQ3 QTB
Entries 1.174784e+07
ADC
QT Input Channel

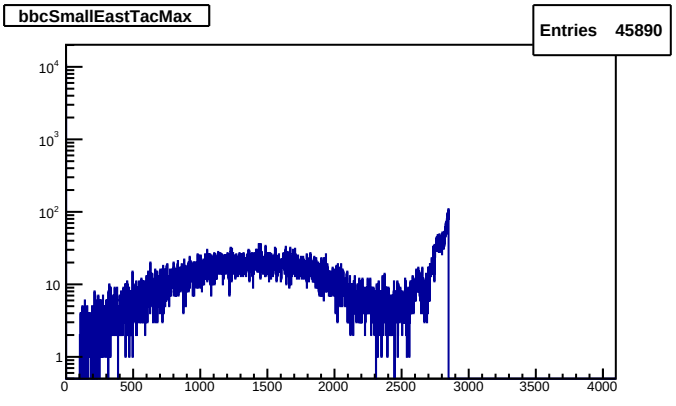

EQ3 QTB
Entries 3622886
TDC
QT Input Channel

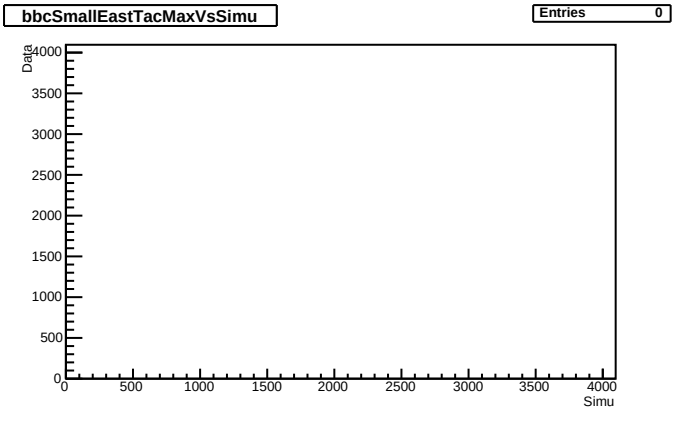

EQ3 QTC
Entries 4405440
ADC
QT Input Channel

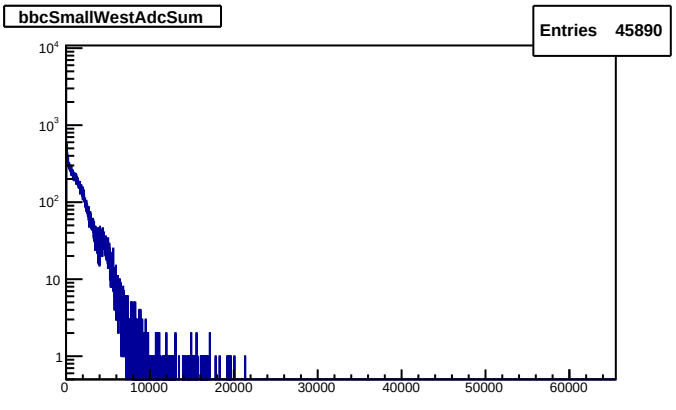

EQ3 QTC
Entries 4405440
TADC
QT Input Channel

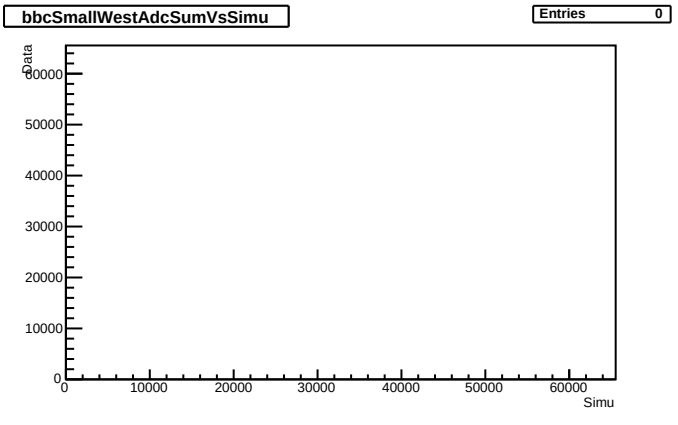

bbcSmallEastAdcSum
Entries 45890


bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu


bbcSmallEastTacMax
Entries 45890


bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu


bbcSmallWestAdcSum
Entries 45890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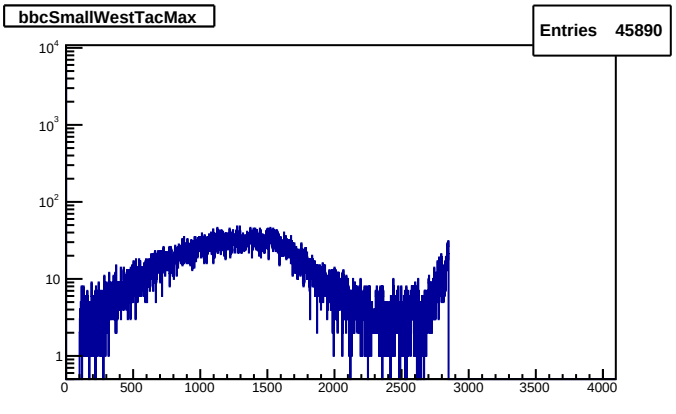
Entries 0
Data
Simu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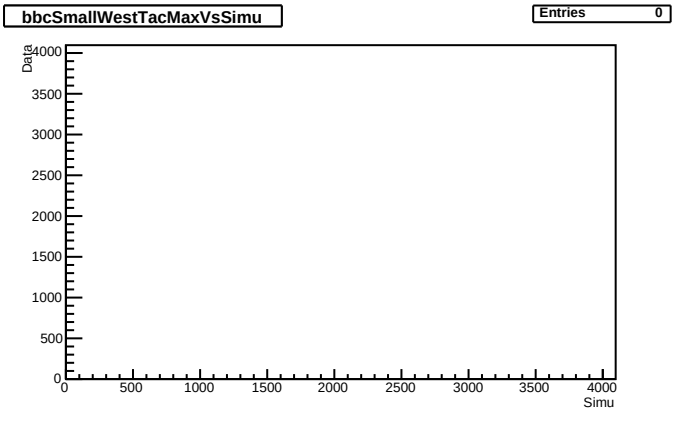
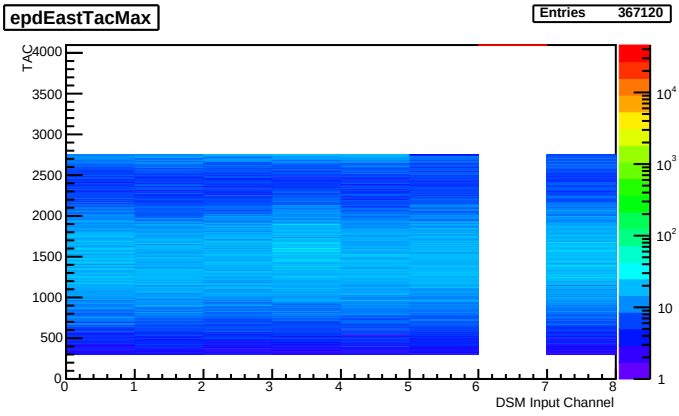
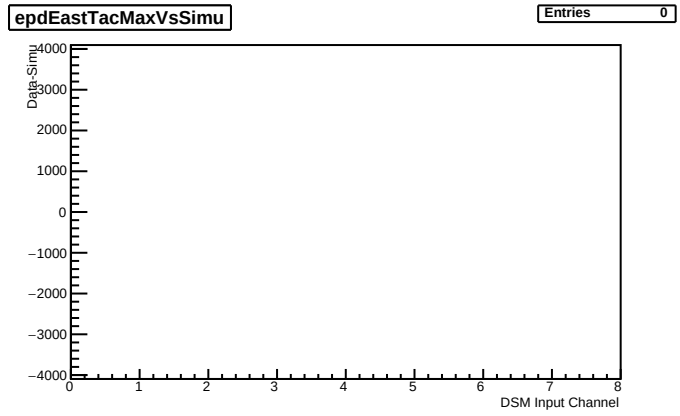
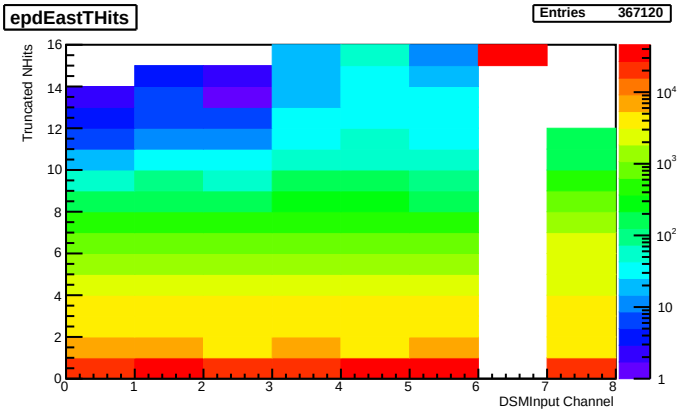
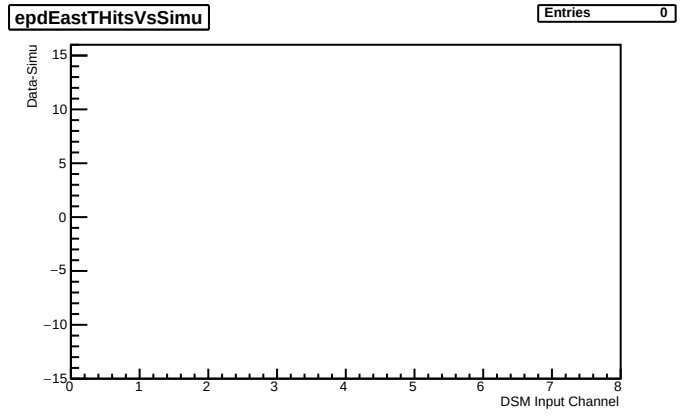
Entries 45890


bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu


epdEastTacMax
Entries 367120
TAC
DSM Input Channel


epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel


epdEastTHits
Entries 367120
Truncated NHits
DSMInput Channel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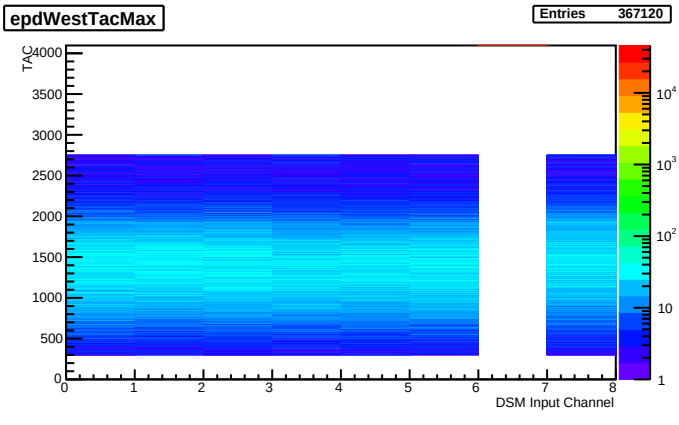
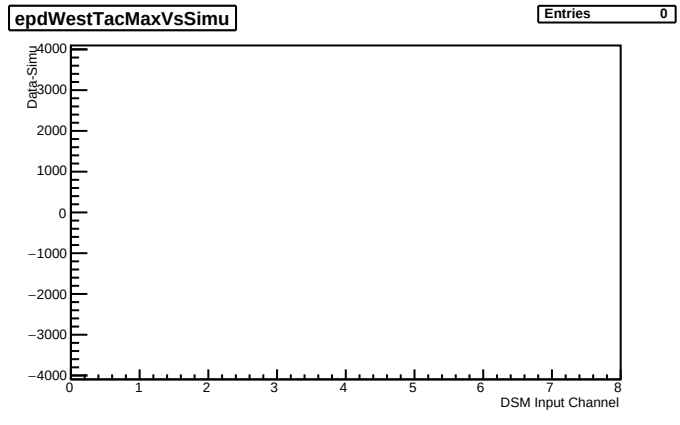
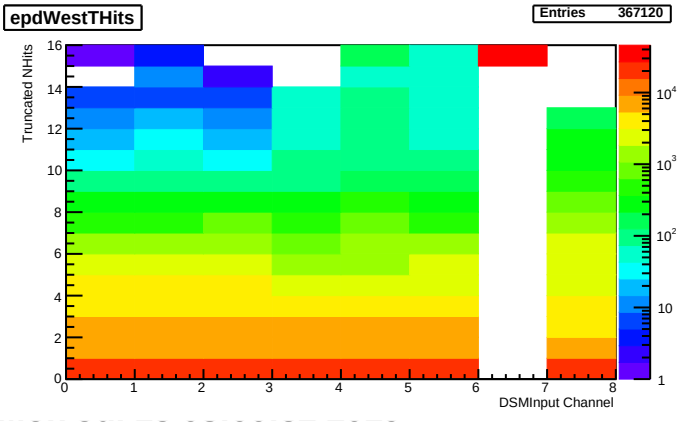
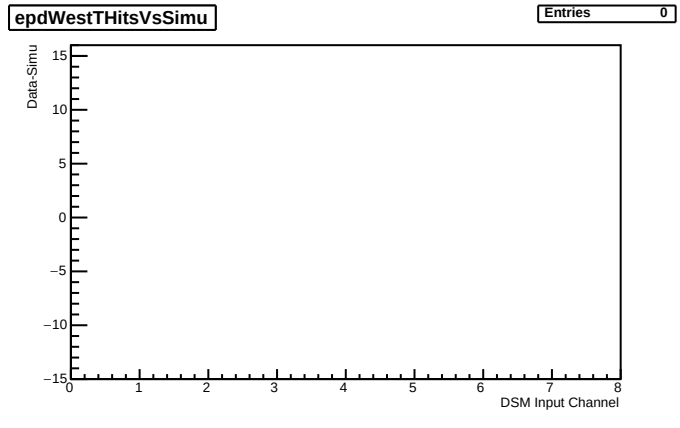
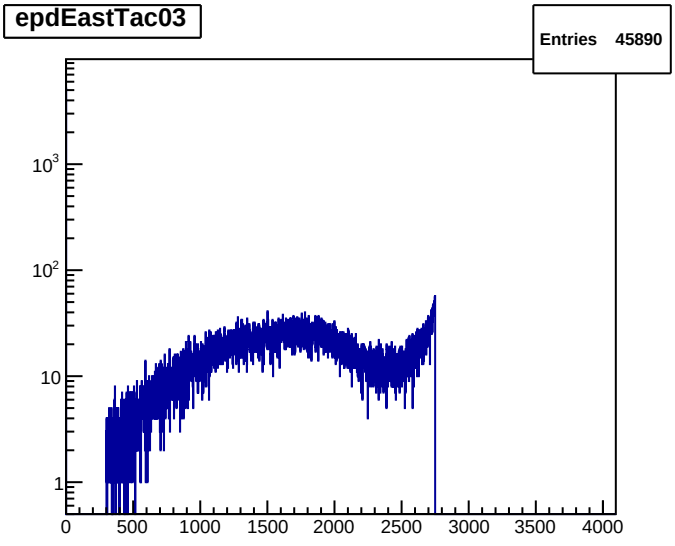
Entries 0
Data-Simu
DSM Input Channel

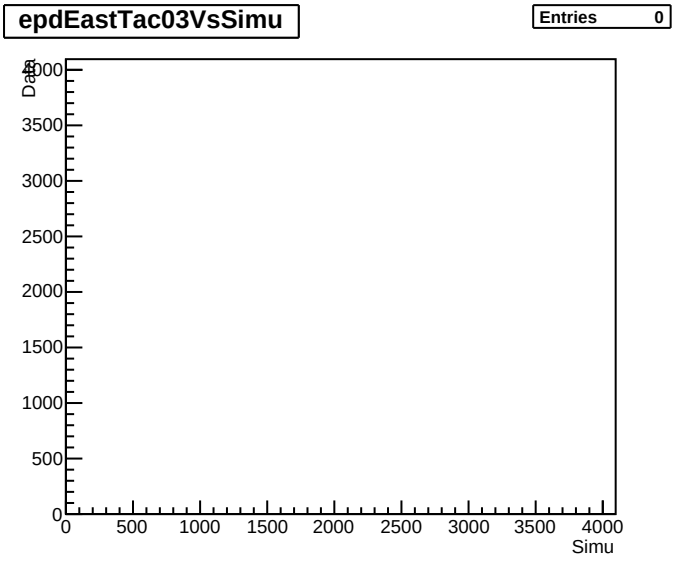

epdWestTacMax
Entries 367120
TAC
DSM Input Channel

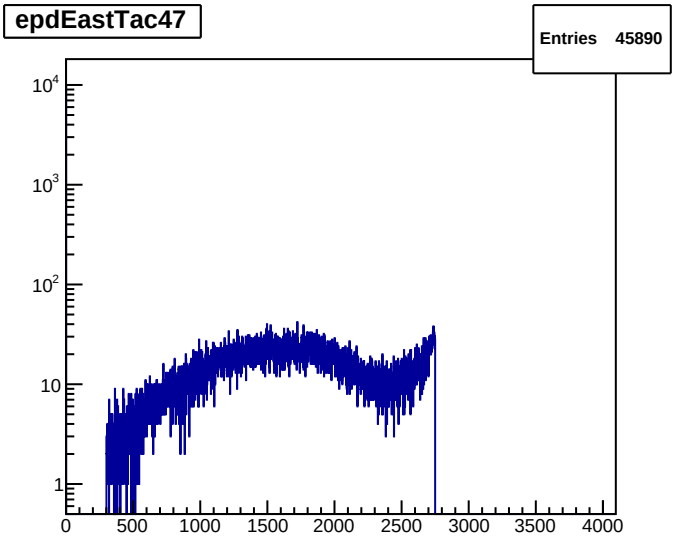

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

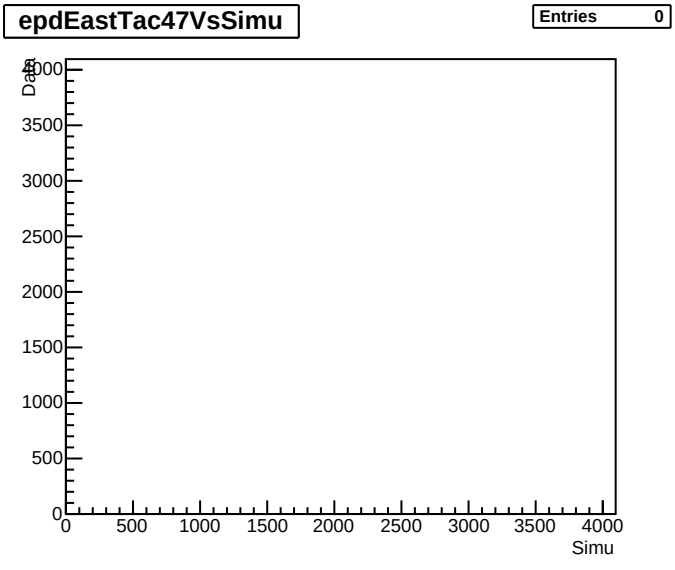

epdWestTHits
Entries 367120
Truncated NHits
DSMInput Channel


epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel


epdEastTac03
Entries 45890
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000


epdEastTac03VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu


epdEastTac47
Entries 45890
10^4
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000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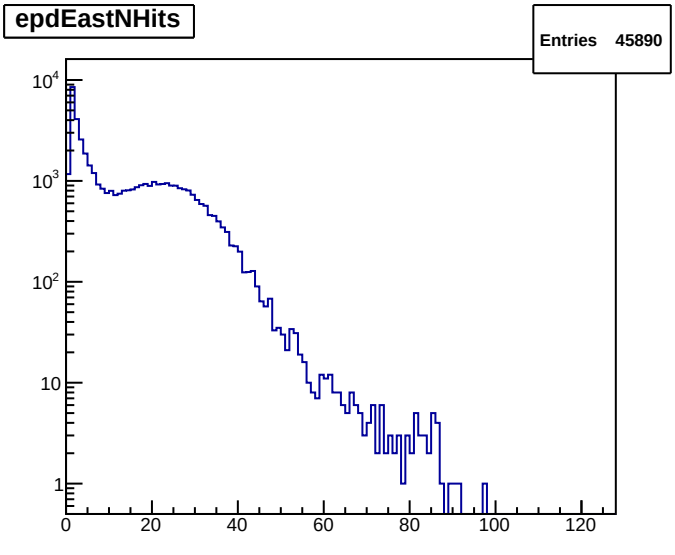
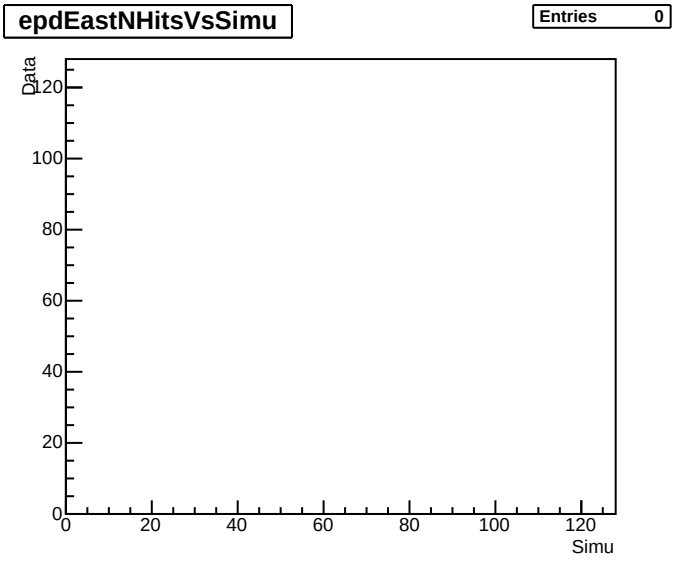
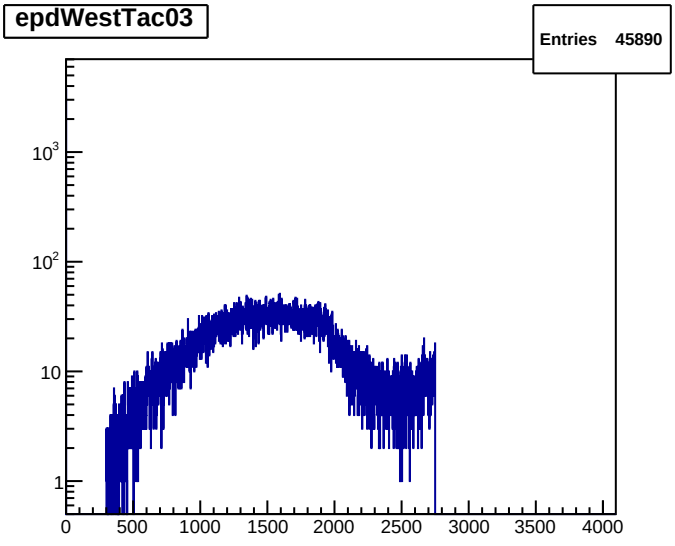
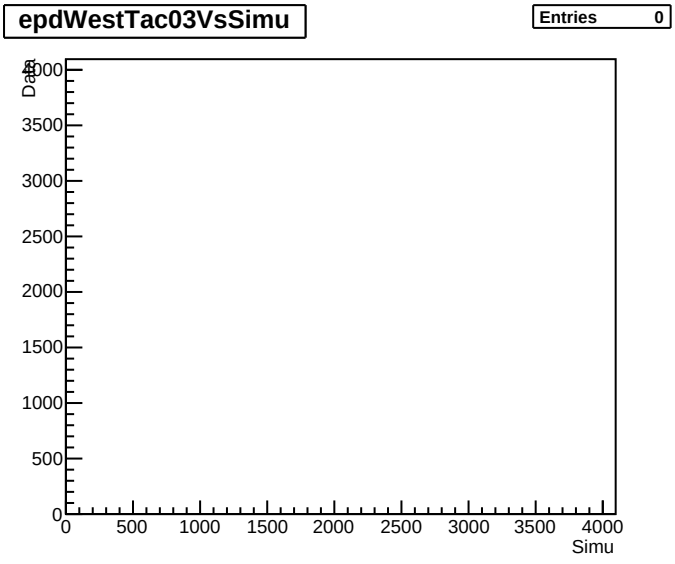
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu


epdEastNHits
Entries 45890
10^4
10^3
10^2
10
1
0 20 40 60 80 100 120


epdEastNHitsVsSimu
Entries 0
Data
120 100 80 60 40 20 0
0 20 40 60 80 100 120
Simu


epdWestTac03
Entries 45890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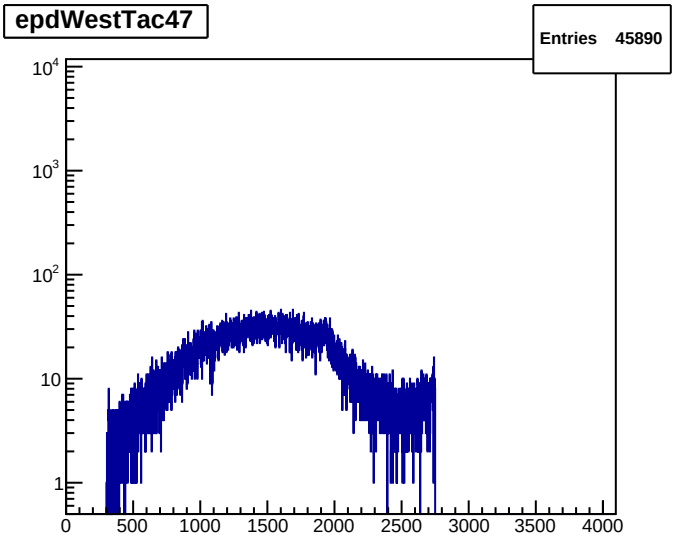
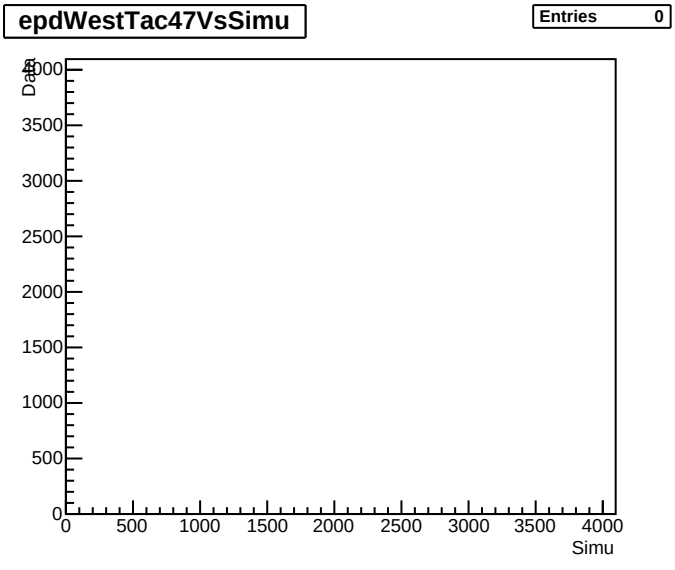
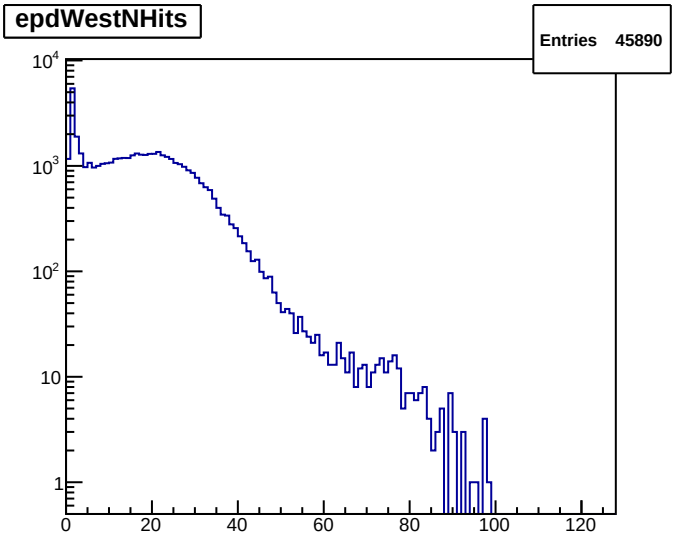
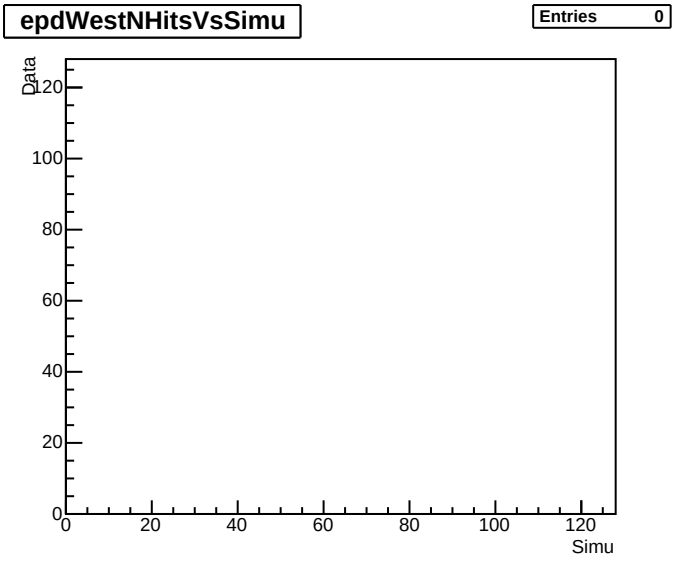
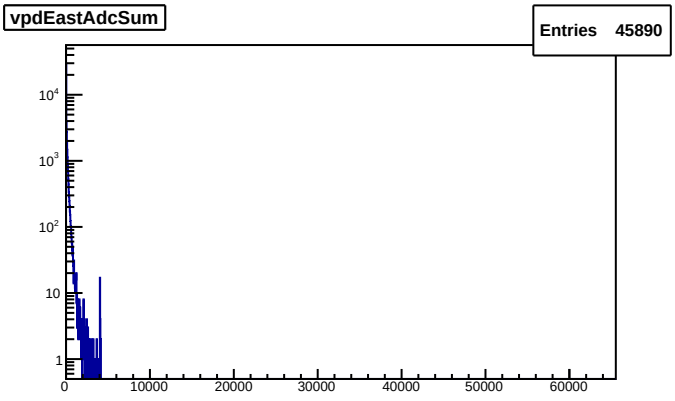
Entries 0
Data
Simu

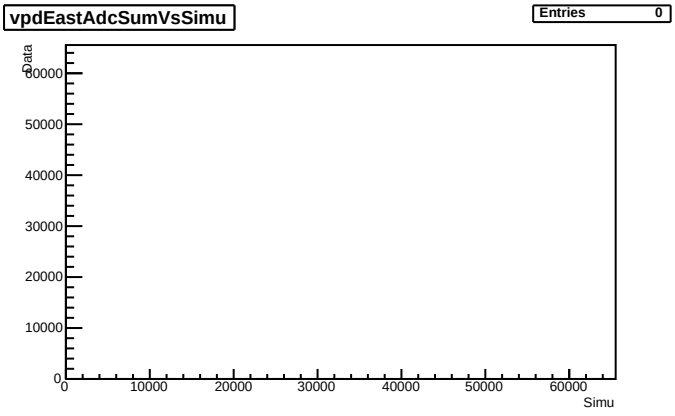

epdWestTac47
Entries 45890

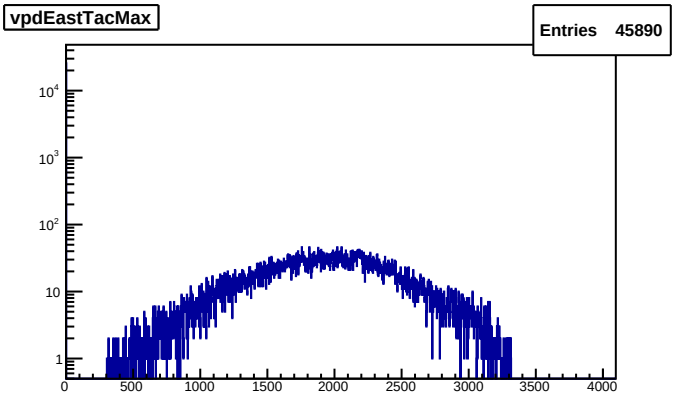

epdWestTac47VsSimu
Entries 0
Data
Simu

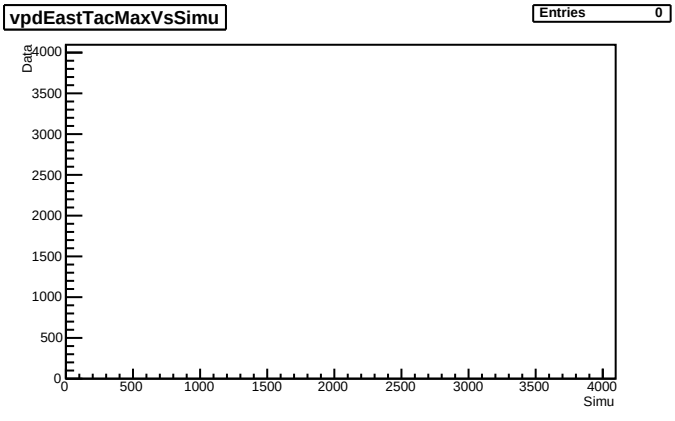

epdWestNHits
Entries 45890


epdWestNHitsVsSimu
Entries 0
Data
Simu


vpdEastAdcSum
Entries 45890


vpdEastAdcSumVsSimu
Entries 0
Data
Simu


vpdEastTacMax
Entries 45890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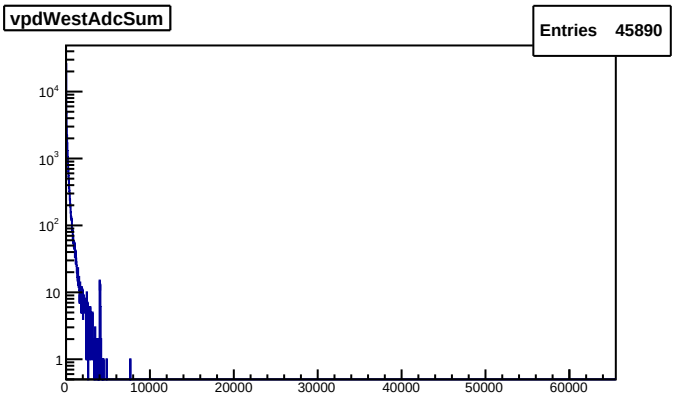
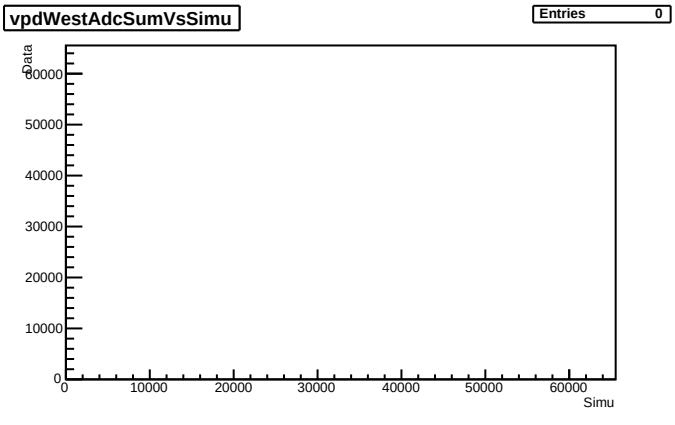
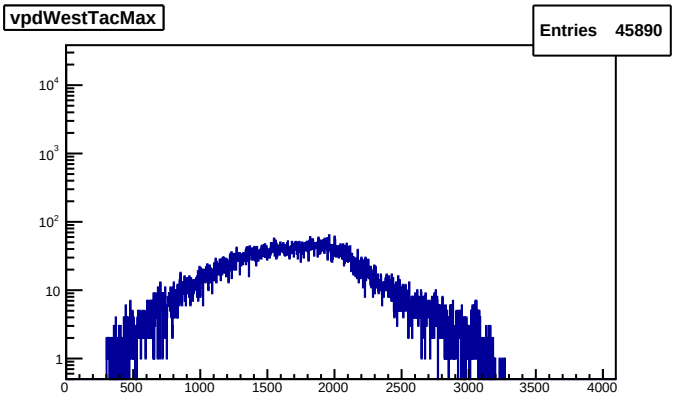
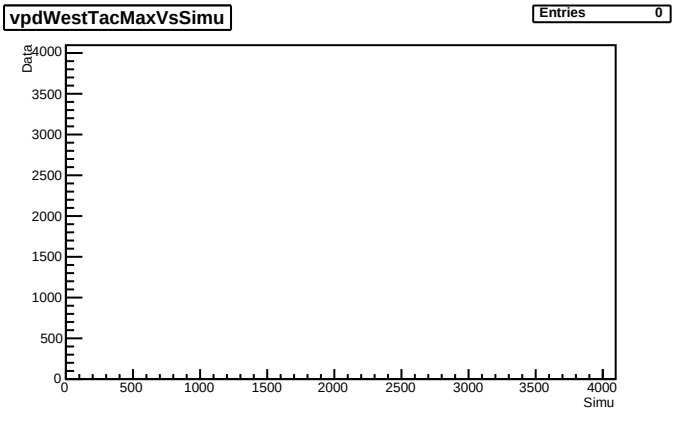
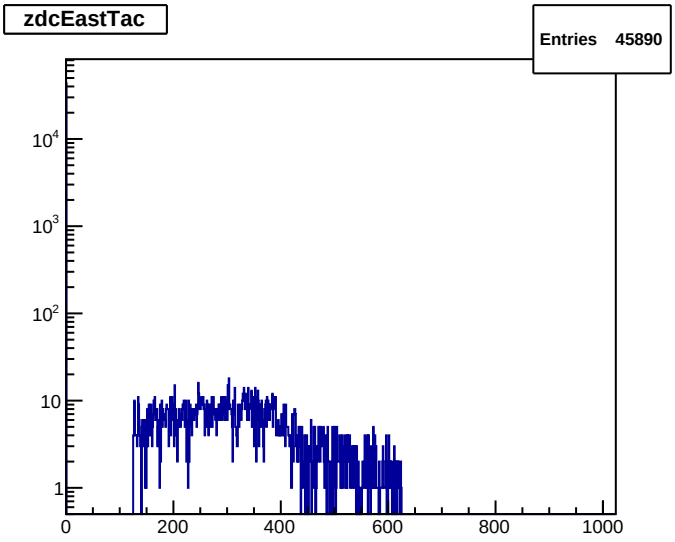
Entries 0
Data
Simu

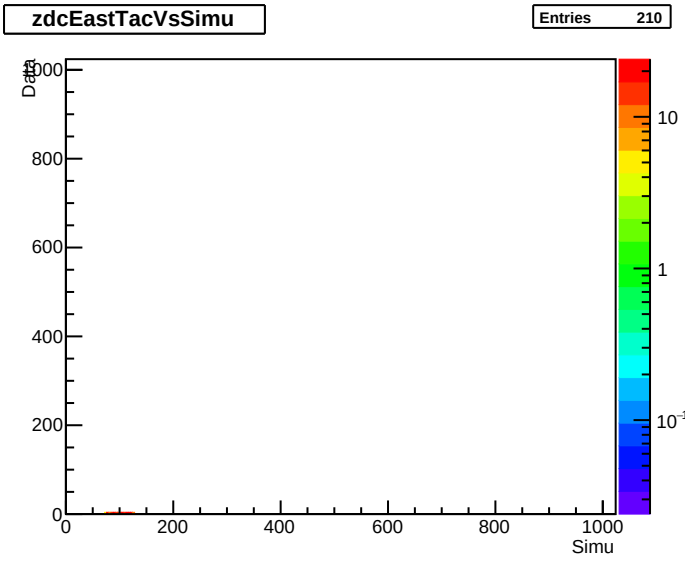

vpdWestAdcSum
Entries 45890

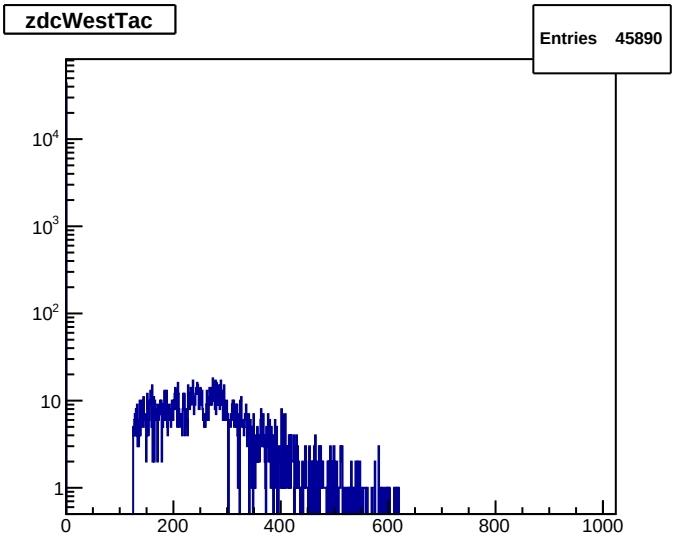

vpdWestAdcSumVsSimu
Entries 0
Data
Simu

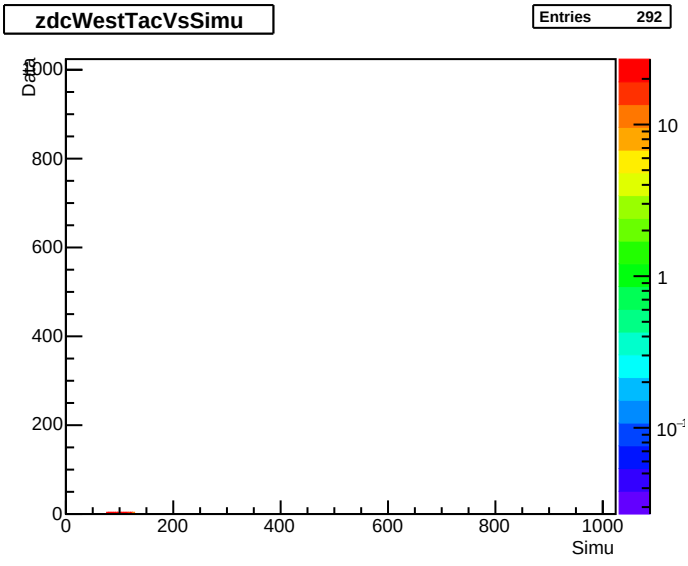

vpdWestTacMax
Entries 45890

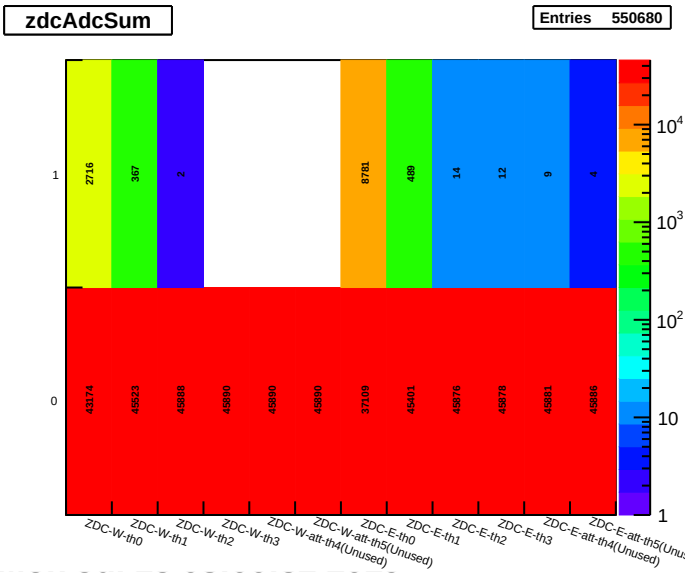

vpdWestTacMaxVsSimu
Entries 0
Data
Simu

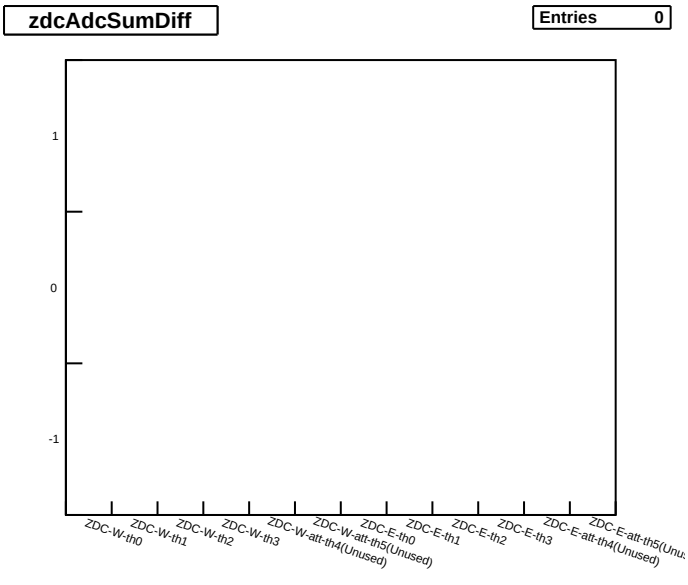

zdcEastTac
Entries 45890

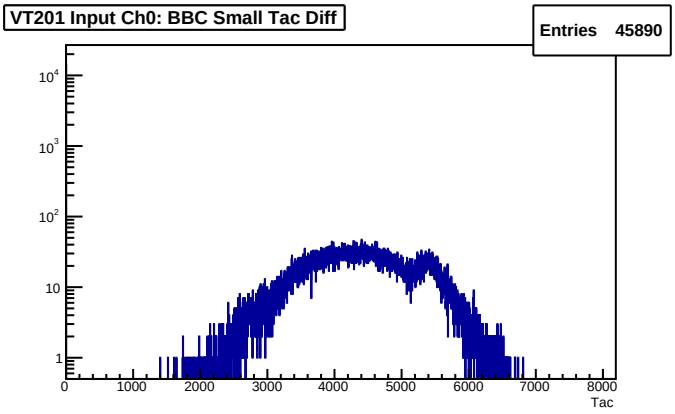

zdcEastTacVsSimu
Entries 210
Data
Simu

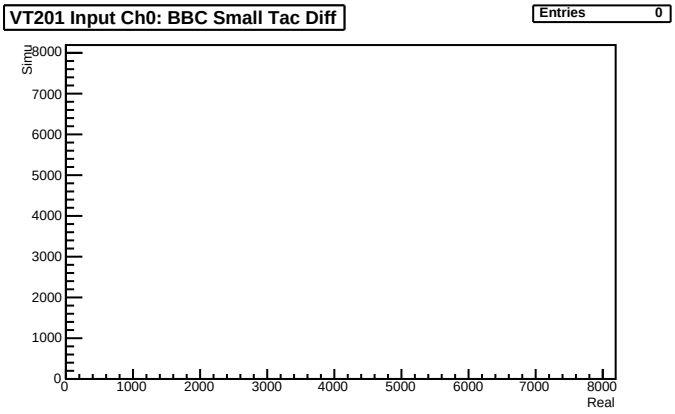

zdcWestTac
Entries 45890

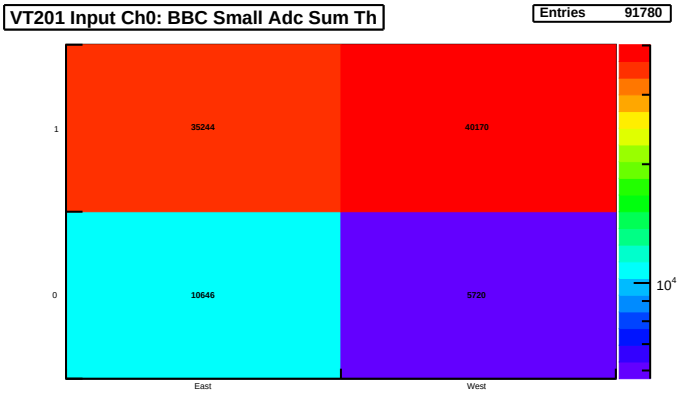

zdcWestTacVsSimu
Entries 292
Data
Simu

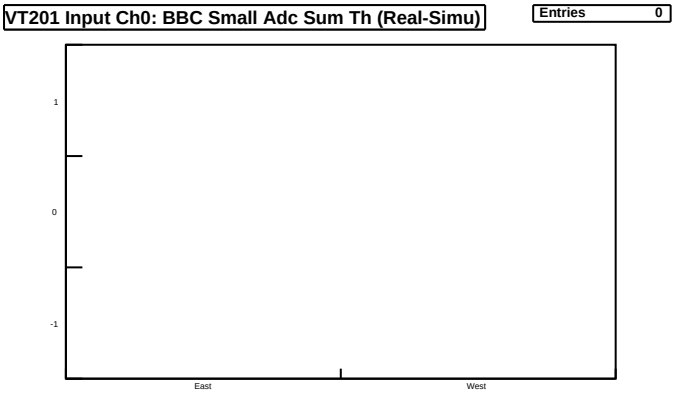

zdcAdcSum
Entries 550680
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

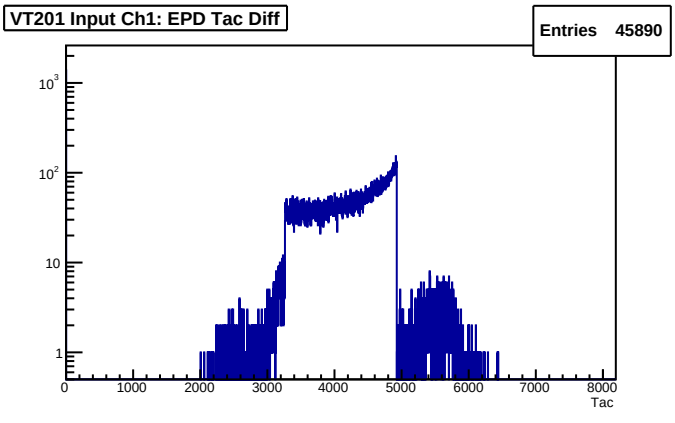

zdcAdcSumDiff
Entries 0
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

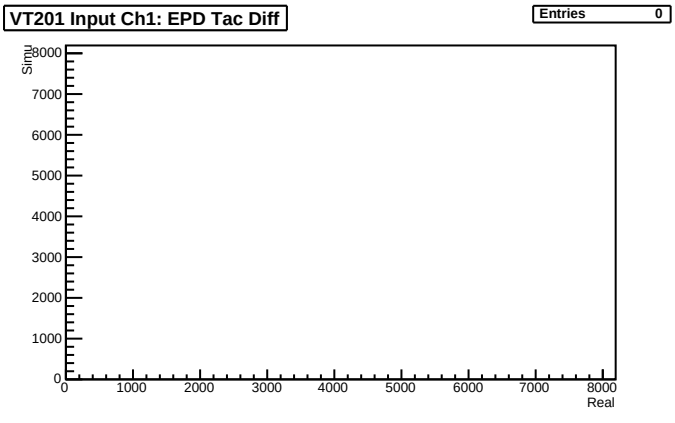

VT201 Input Ch0: BBC Small Tac Diff
Entries 45890
Tac

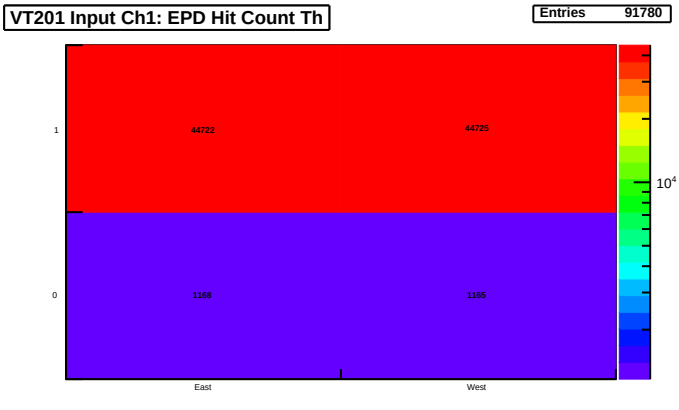

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

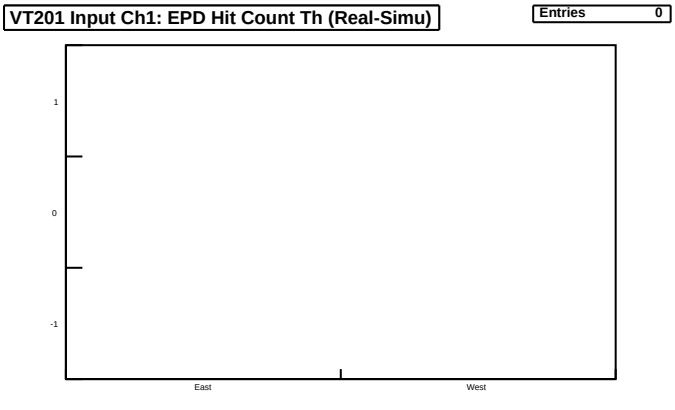

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 91780
35244
40170
10646
5720
East
West

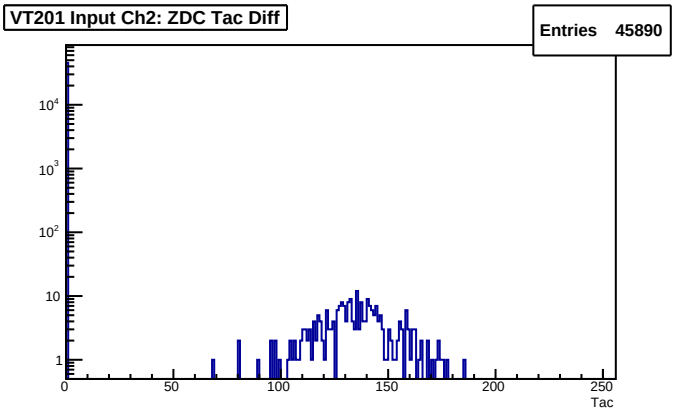

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

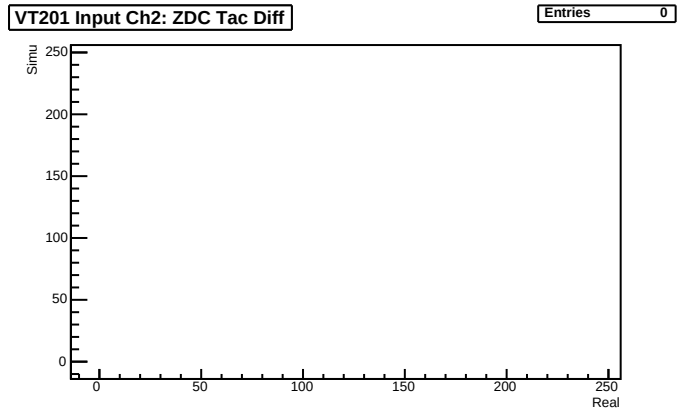

VT201 Input Ch1: EPD Tac Diff
Entries 45890
Tac

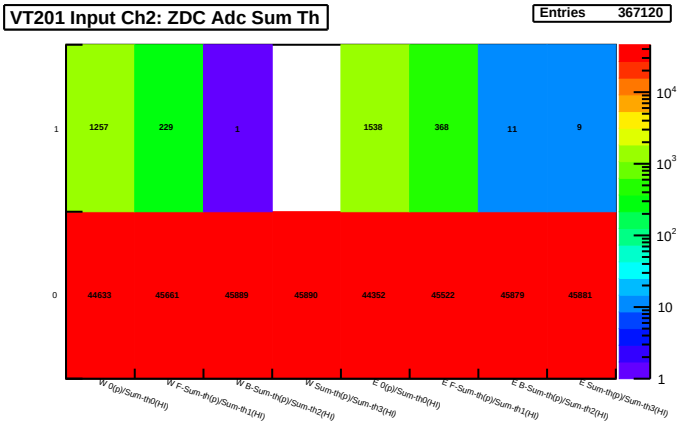

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

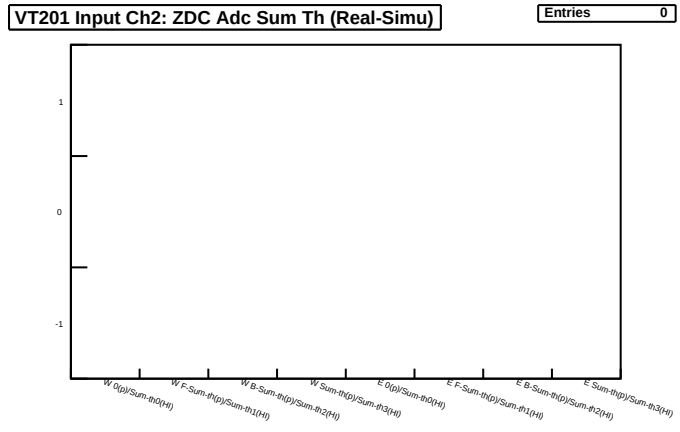

VT201 Input Ch1: EPD Hit Count Th
Entries 91780
44722
44725
1168
1165
East
West

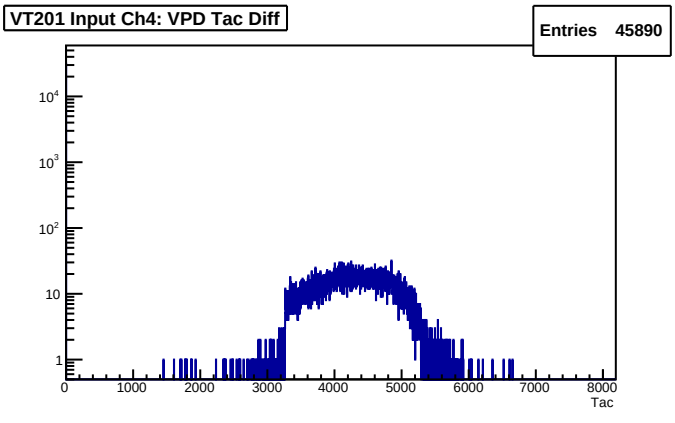

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

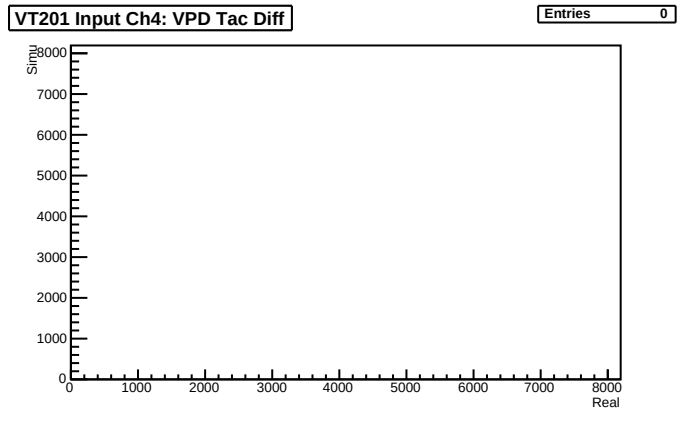

VT201 Input Ch2: ZDC Tac Diff
Entries 45890
Tac

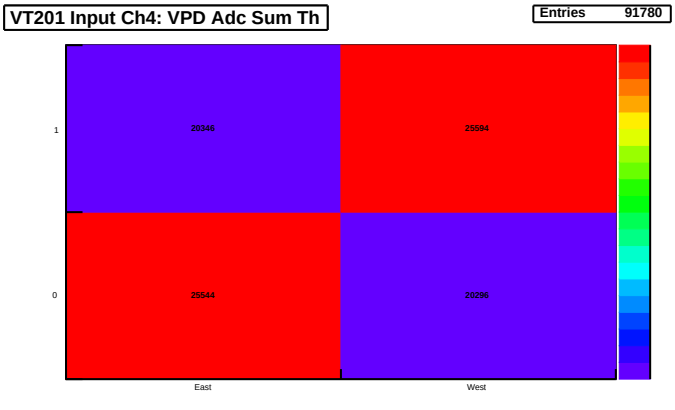

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

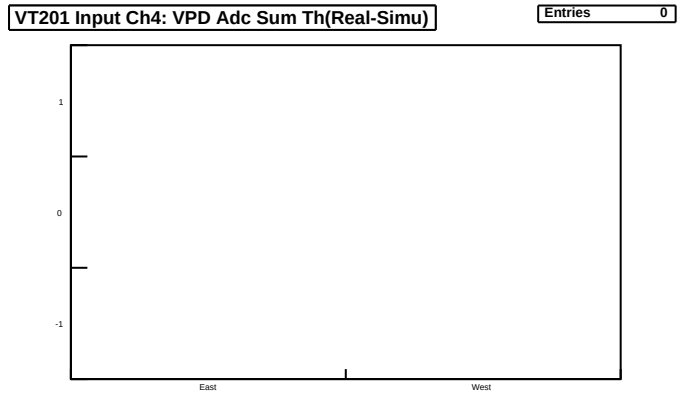

VT201 Input Ch2: ZDC Adc Sum Th
Entries 367120
1257
229
1
1538
368
11
9
44633
45661
45889
45890
44352
45522
45879
45881
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

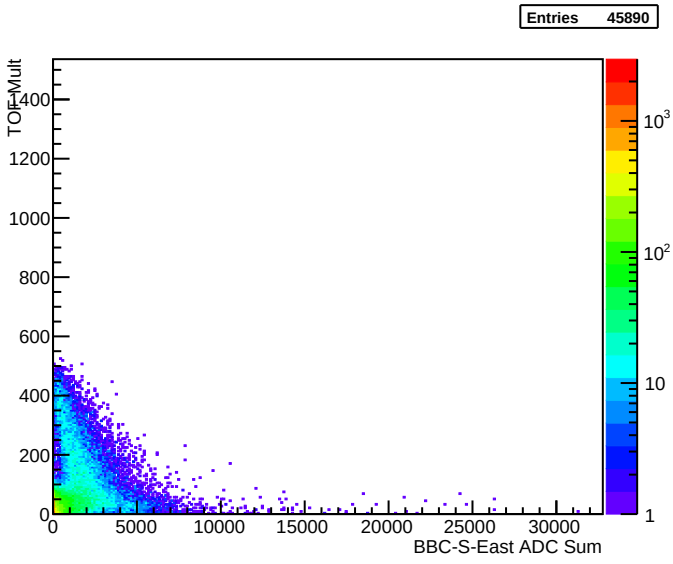

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

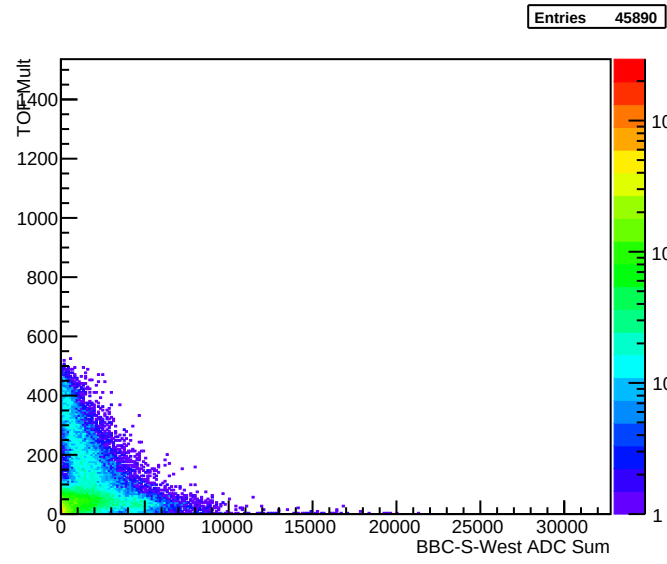

VT201 Input Ch4: VPD Tac Diff
Entries 45890
Tac

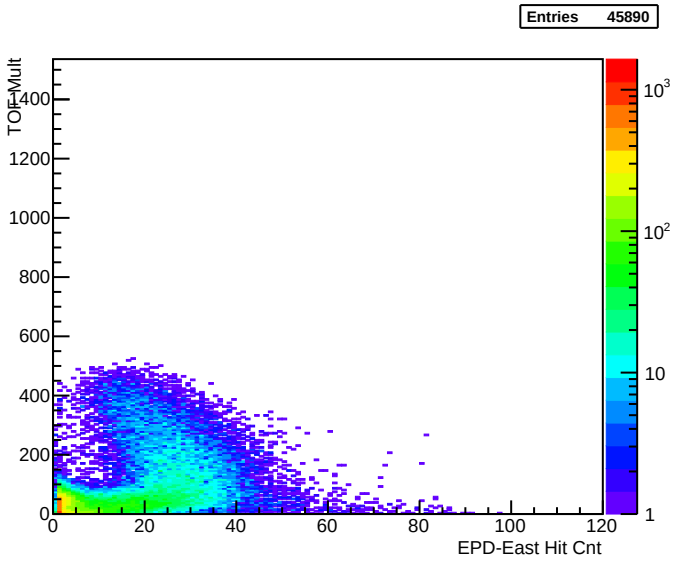

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

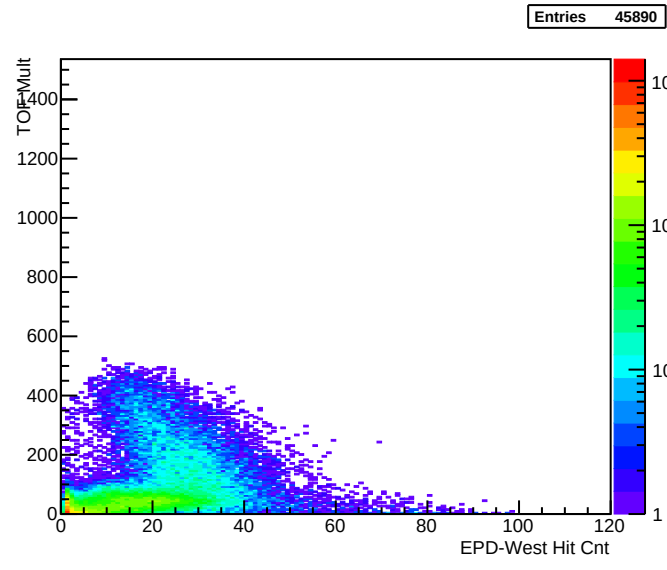

VT201 Input Ch4: VPD Adc Sum Th
Entries 91780
20346
25594
25544
20296
East
West

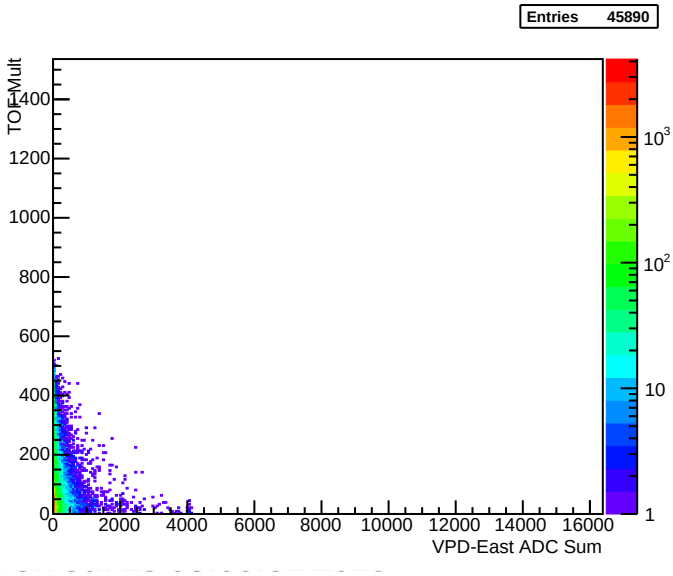

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

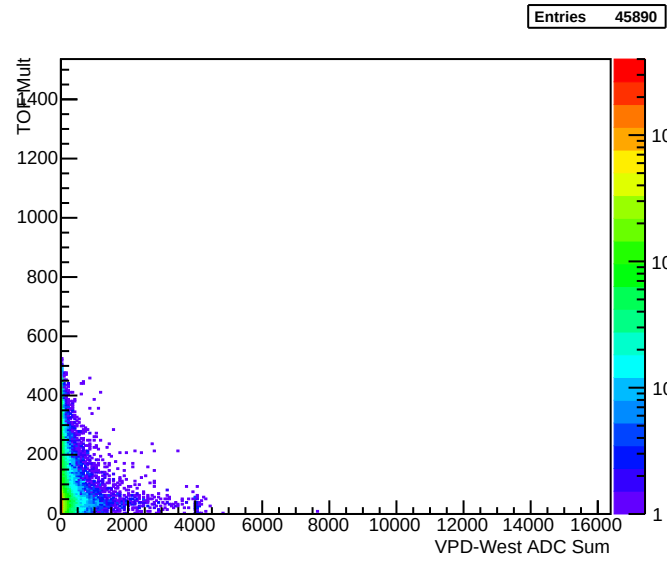

Entries 45890
TOF-Mult
BBC-S-East ADC Sum

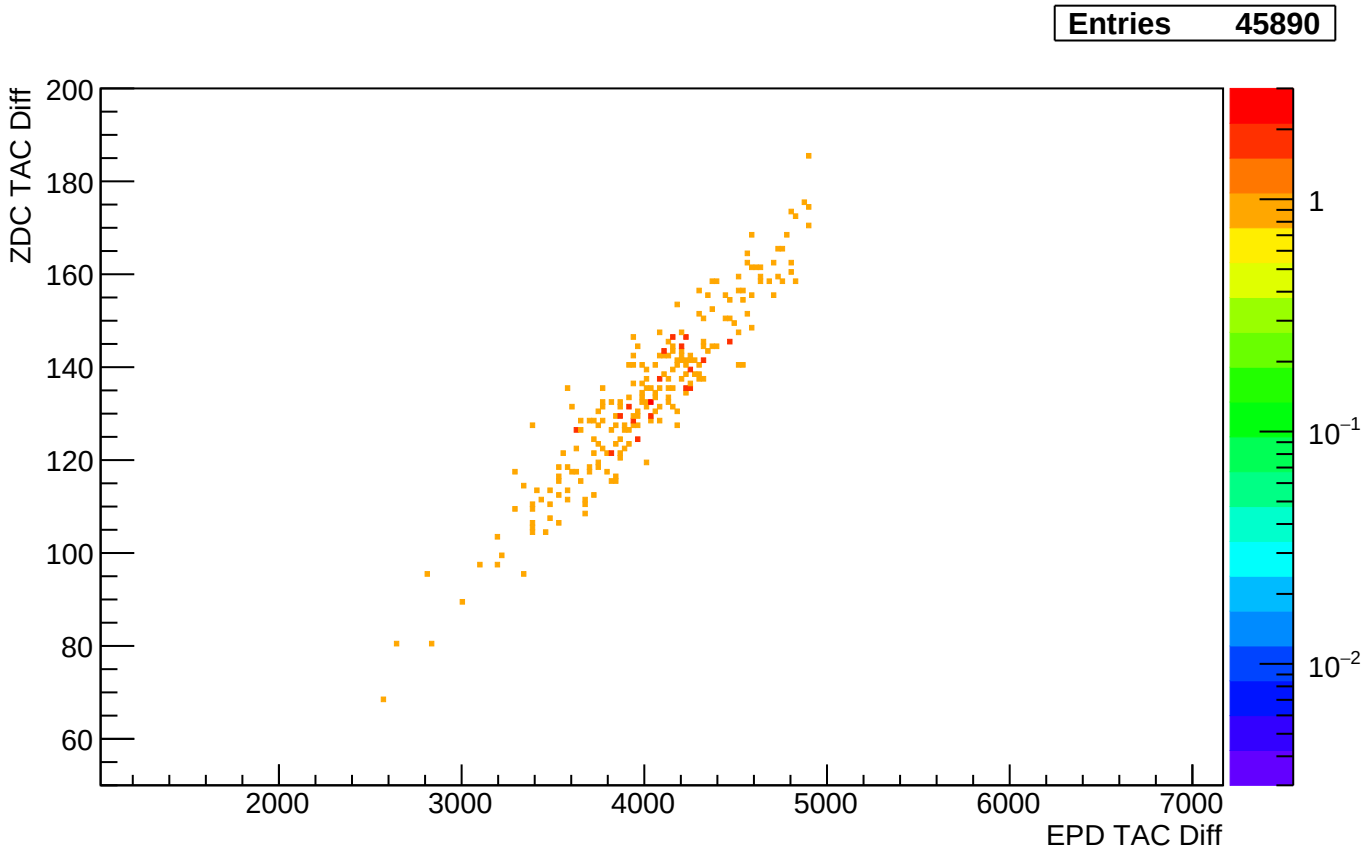

Entries 45890
TOF-Mult
BBC-S-West ADC Sum

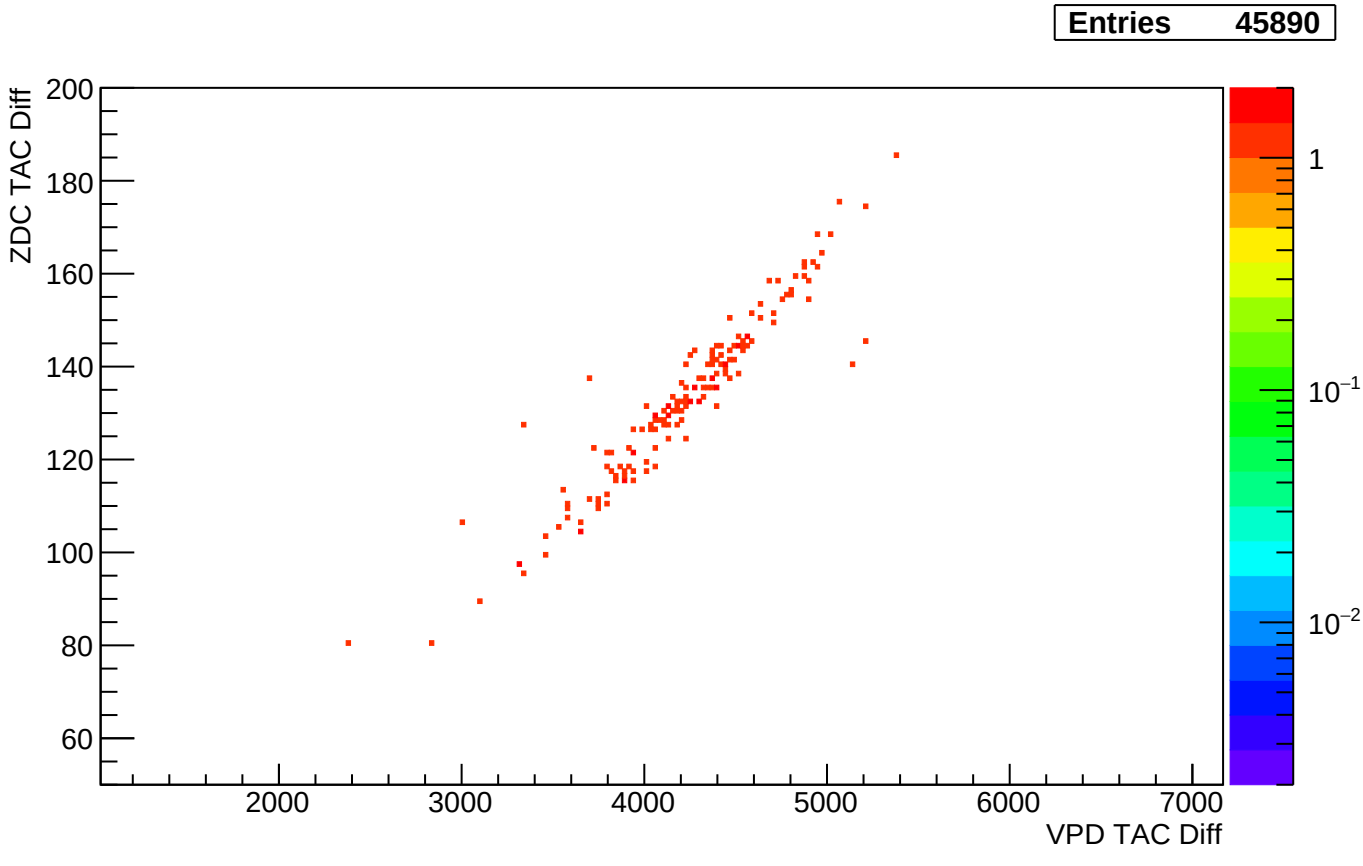

Entries 45890
TOF-Mult
EPD-East Hit Cnt

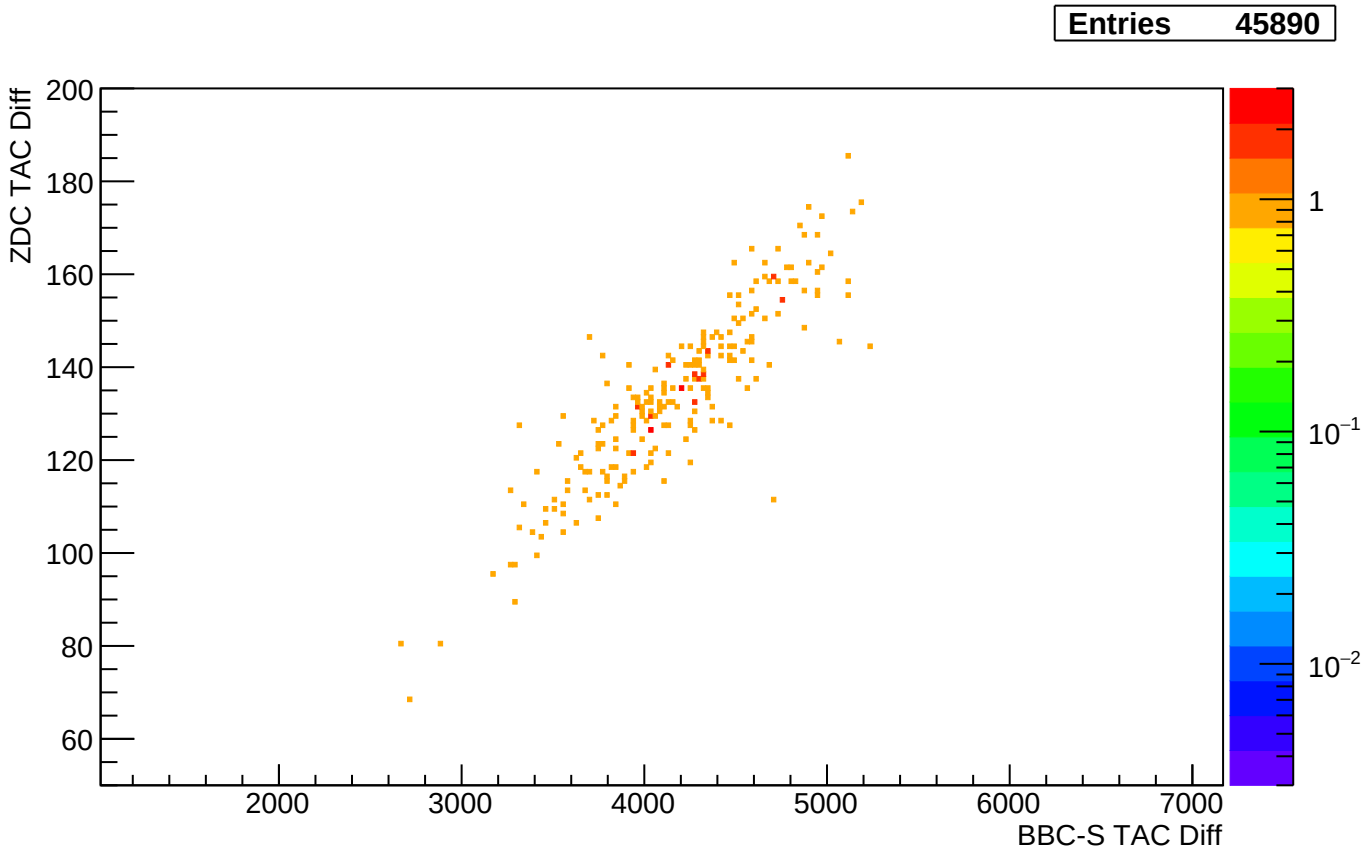

Entries 45890
TOF-Mult
EPD-West Hit Cnt

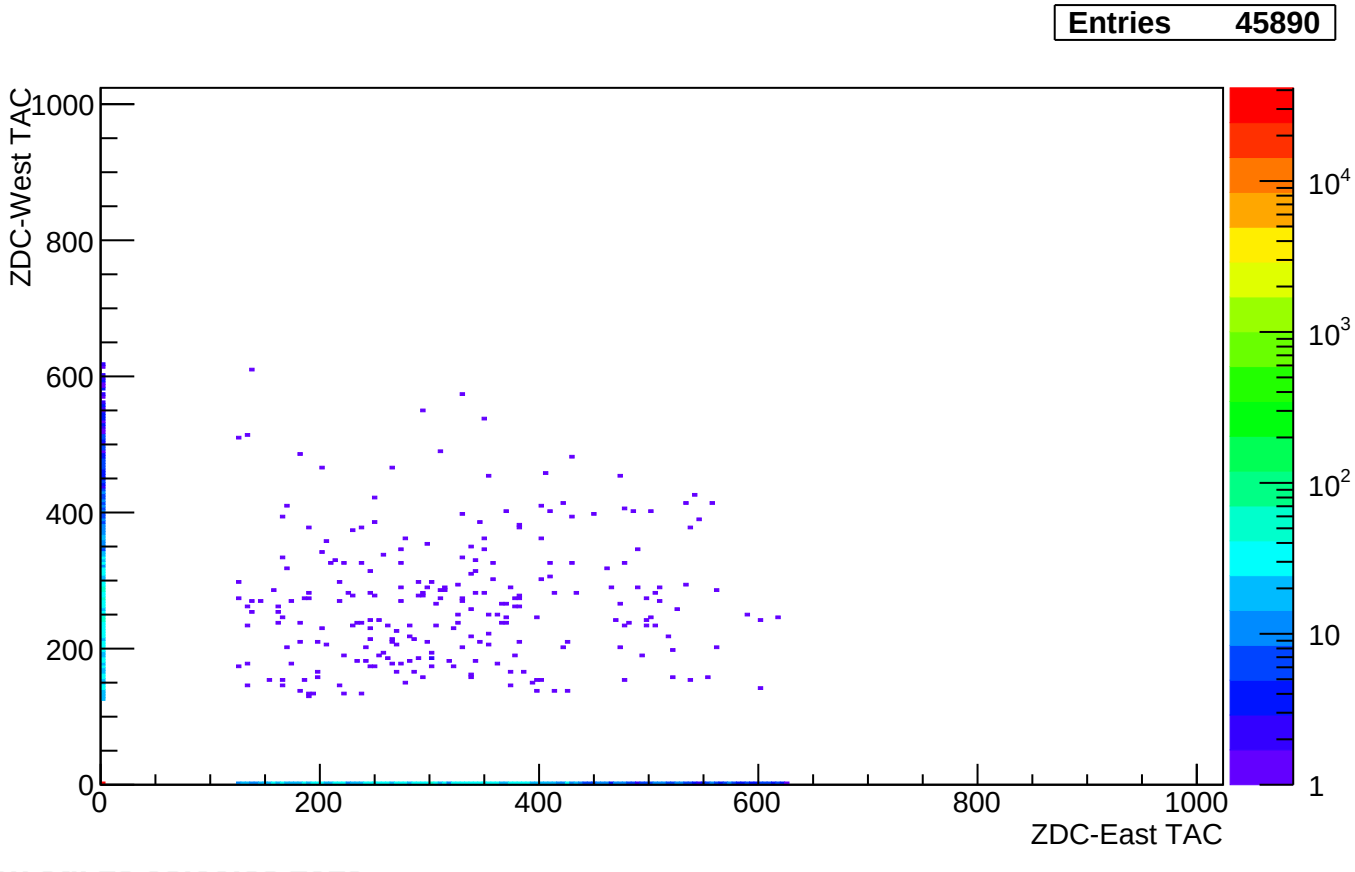

Entries 45890
TOF-Mult
VPD-East ADC Sum

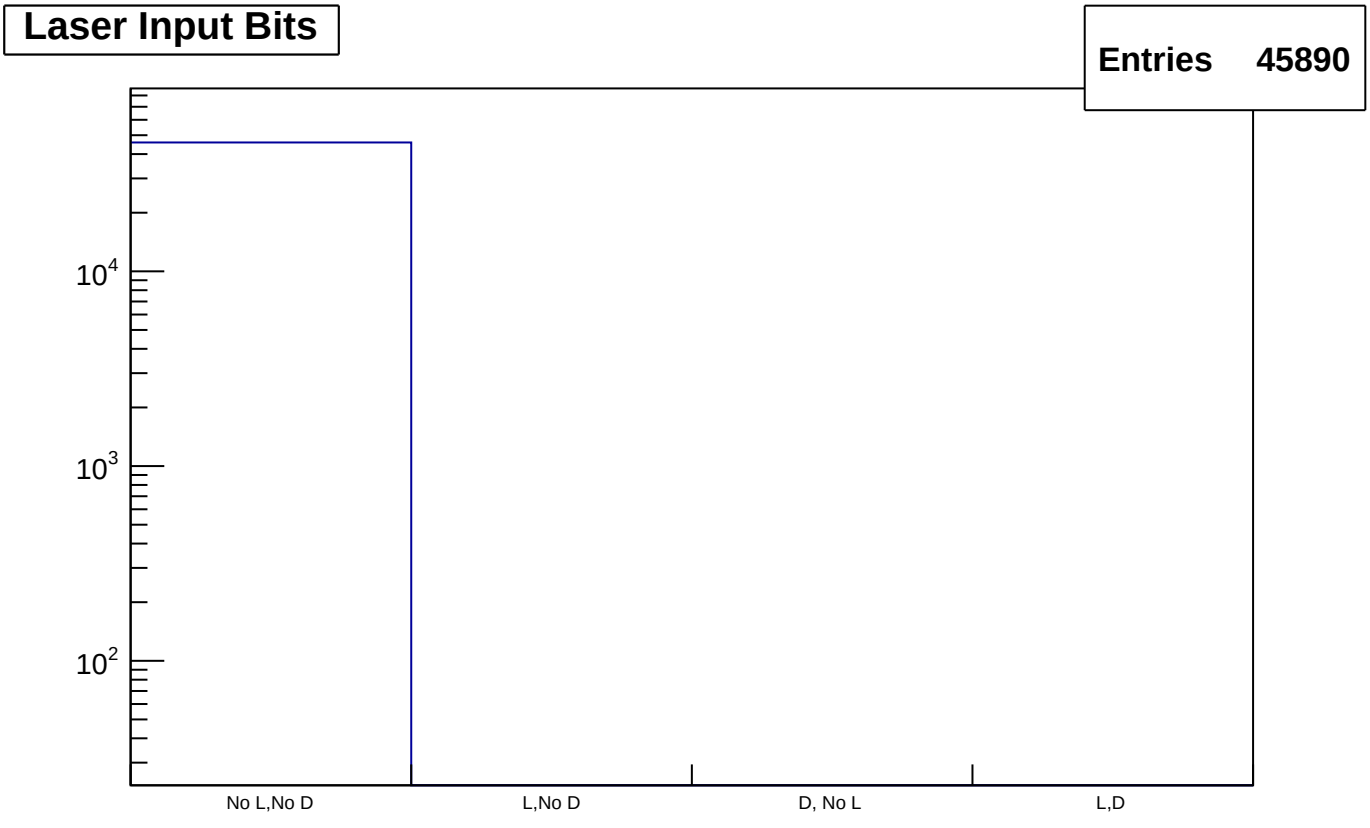

Entries 45890
TOF-Mult
VPD-West ADC Sum

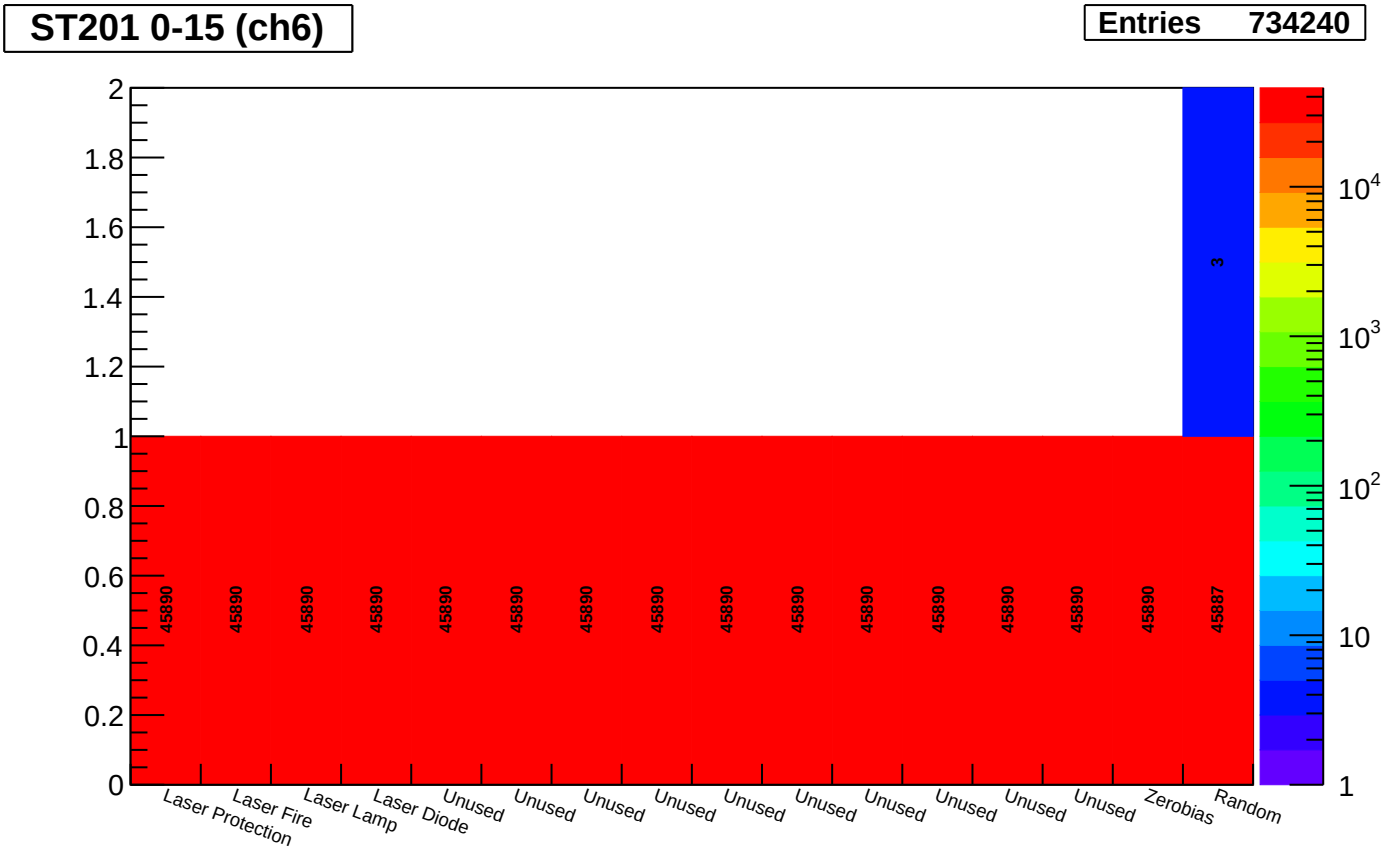

Entries 45890
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff
1
10^{-1}
10^{-2}

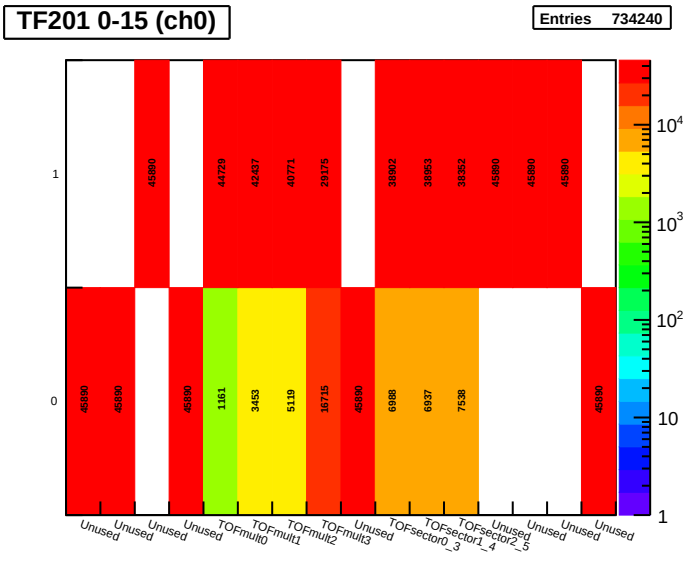

Entries 45890
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff
1
10^{-1}
10^{-2}

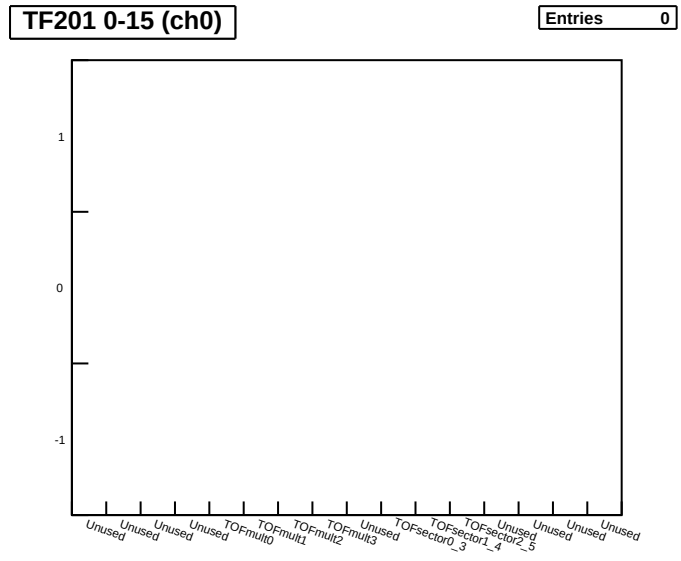

Entries 45890
ZDC TAC Diff
BBC-S TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
1
10^{-1}
10^{-2}

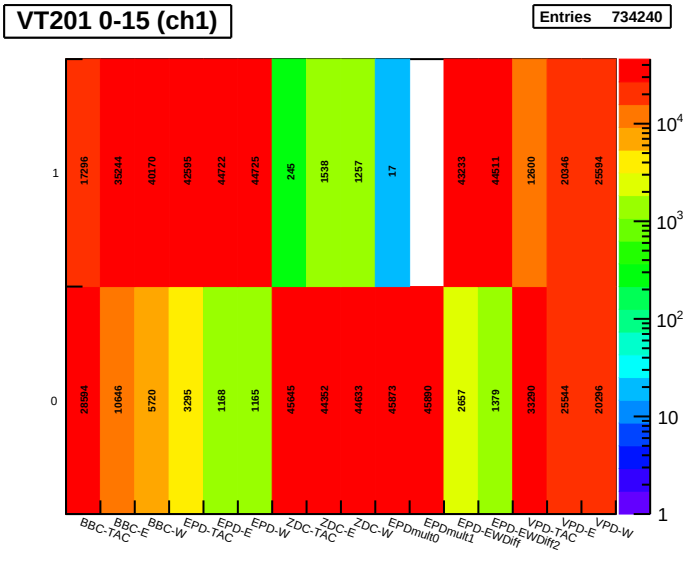

Entries 45890
ZDC-West TAC
ZDC-East TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
10^4
10^3
10^2
10
1

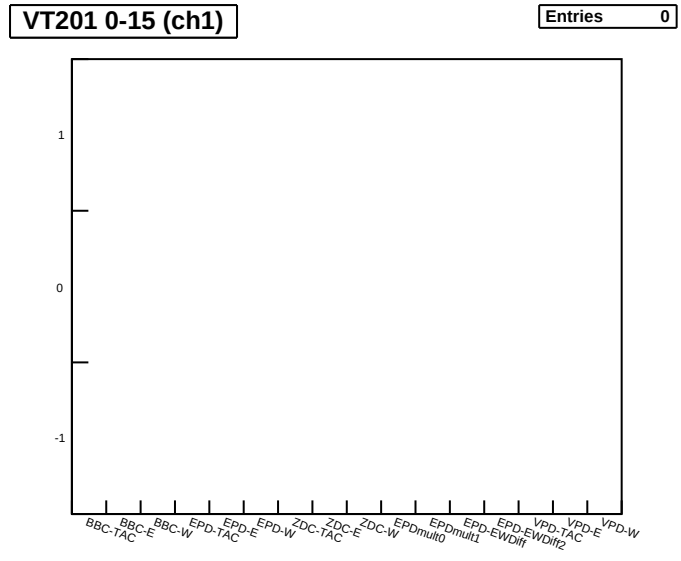

Laser Input Bits
Entries 45890
10^4
10^3
10^2
No L,No D
L,No D
D, No L
L,D

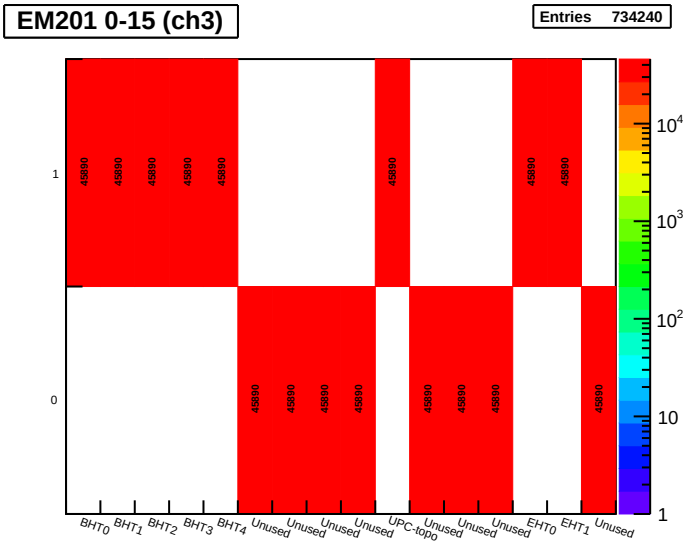

ST201 0-15 (ch6)
Entries 734240
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
45890
45887
3
10^4
10^3
10^2
10
1
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random

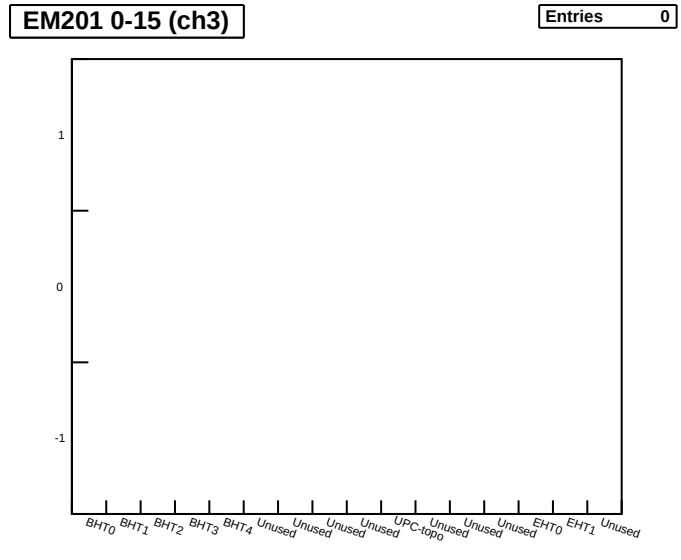

TF201 0-15 (ch0)
Entries 734240
1
0
45890 44729 42437 40771 29175 38902 38953 38352 45890 45890 45890
45890 45890 45890 1161 3453 5119 16715 45890 6988 6937 7538 45890
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused Unused
10^4
10^3
10^2
10
1


TF201 0-15 (ch0)
Entries 0
1
0
-1
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused


VT201 0-15 (ch1)
Entries 734240
1
0
17296 35244 40170 42595 44722 44725 245 1538 1257 17 42233 44611 12600 20346 25594
28594 10646 5720 3295 1168 1165 45645 44352 44633 45873 45890 2657 1379 33290 25544 20296
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W
10^4
10^3
10^2
10
1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 734240
1
0
45890 45890 45890 45890 45890 45890 45890 45890
45890 45890 45890 45890 45890 45890 45890 45890
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
10^4
10^3
10^2
10
1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused