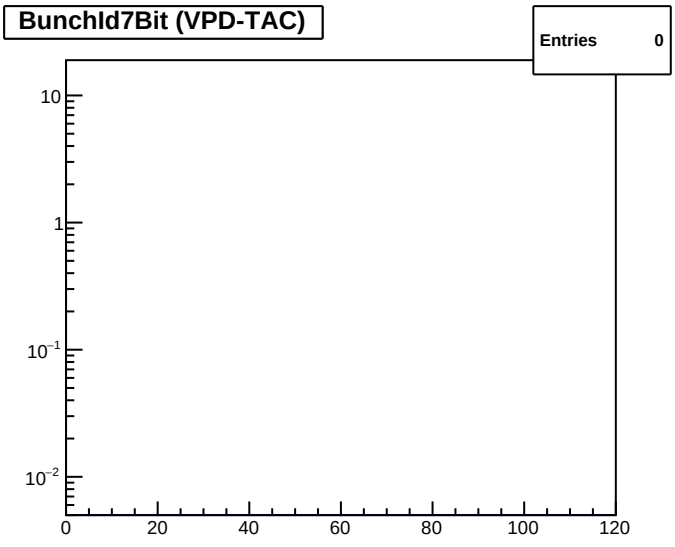
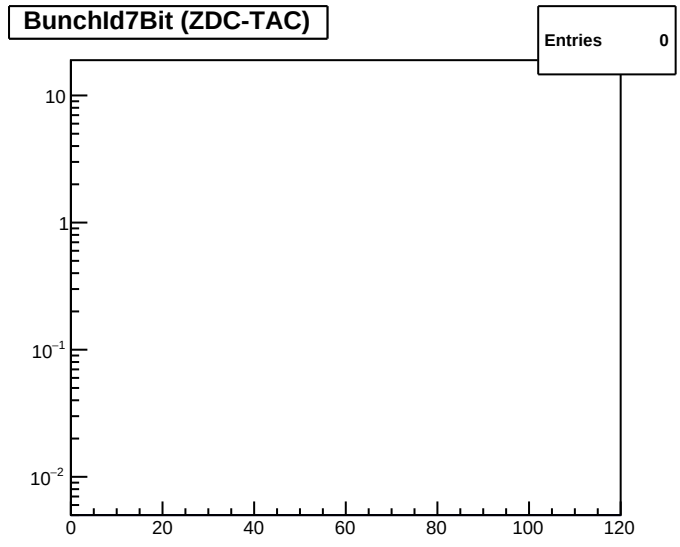
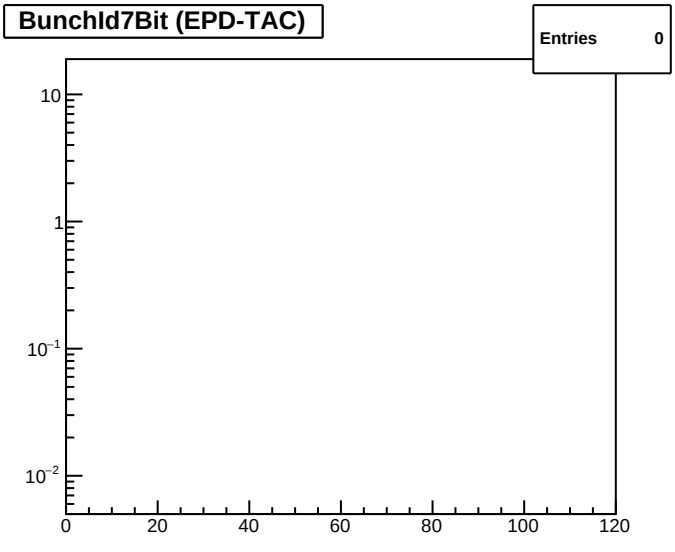
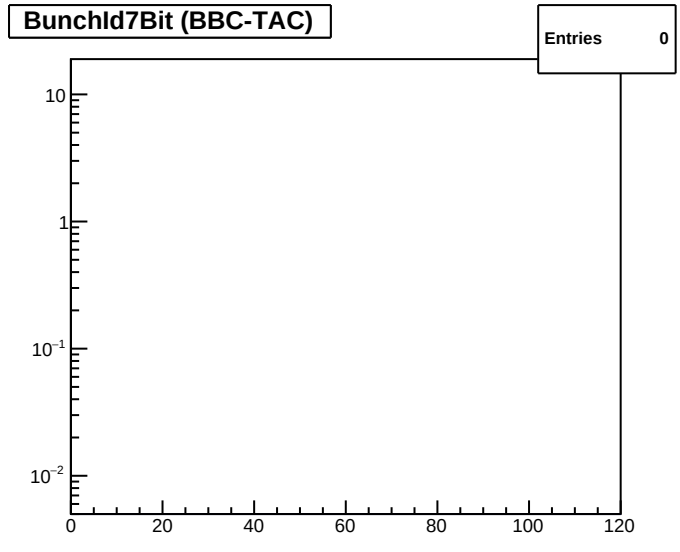
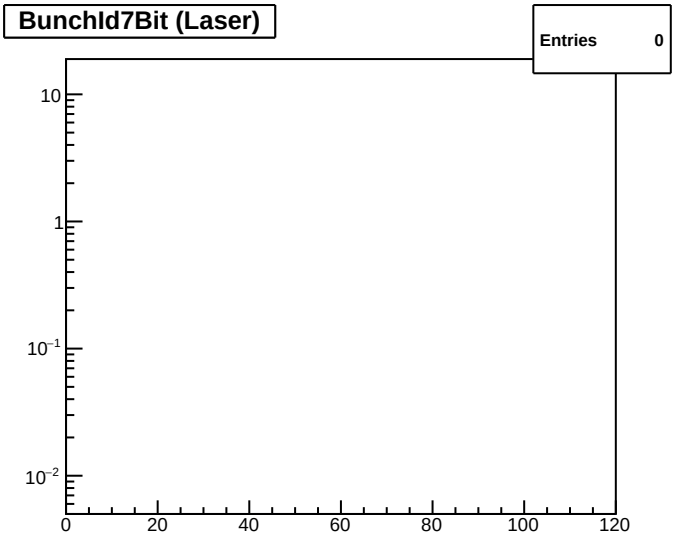
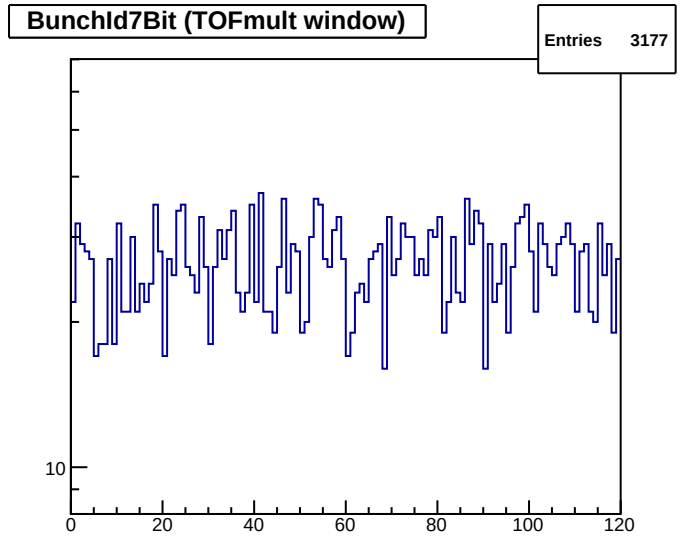

BunchId7Bit (VPD-TAC)
Entries 0
10
1
10⁻¹
10⁻²
0 20 40 60 80 100 120

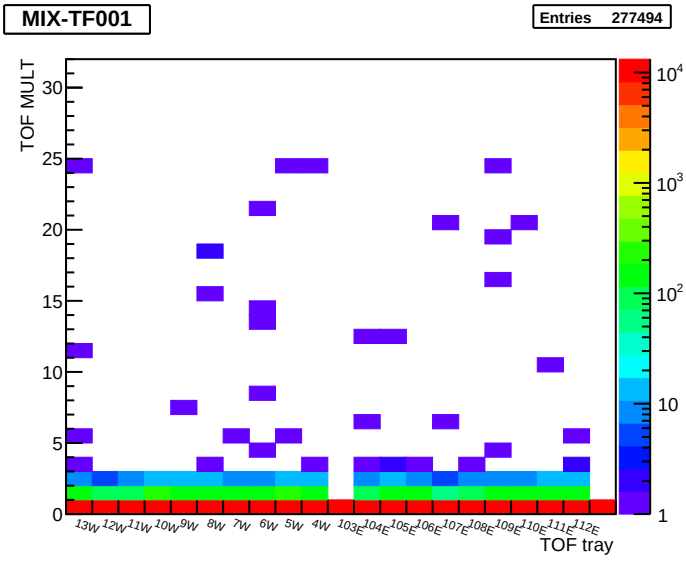

BunchId7Bit (ZDC-TAC)
Entries 0
10
1
10⁻¹
10⁻²
0 20 40 60 80 100 120

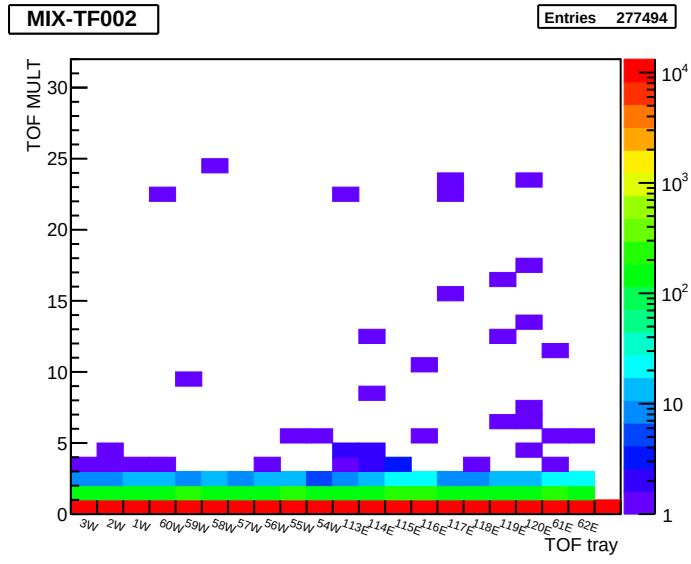

BunchId7Bit (EPD-TAC)
Entries 0
10
1
10⁻¹
10⁻²
0 20 40 60 80 100 120

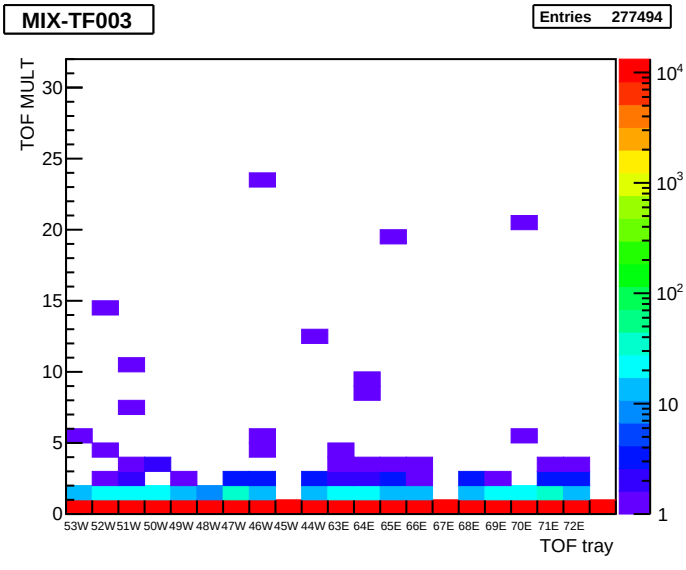

BunchId7Bit (BBC-TAC)
Entries 0
10
1
10⁻¹
10⁻²
0 20 40 60 80 100 120

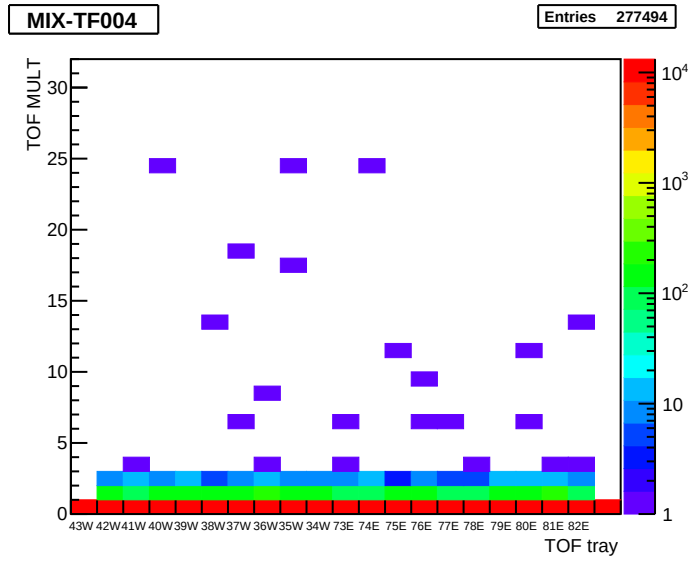

BunchId7Bit (Laser)
Entries 0
10
1
10⁻¹
10⁻²
0 20 40 60 80 100 120

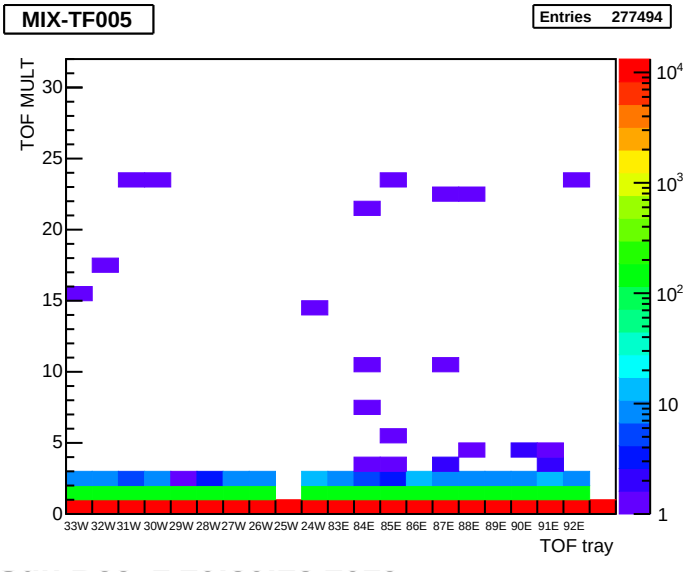

BunchId7Bit (TOFmult window)
Entries 3177
10
0 20 40 60 80 100 120

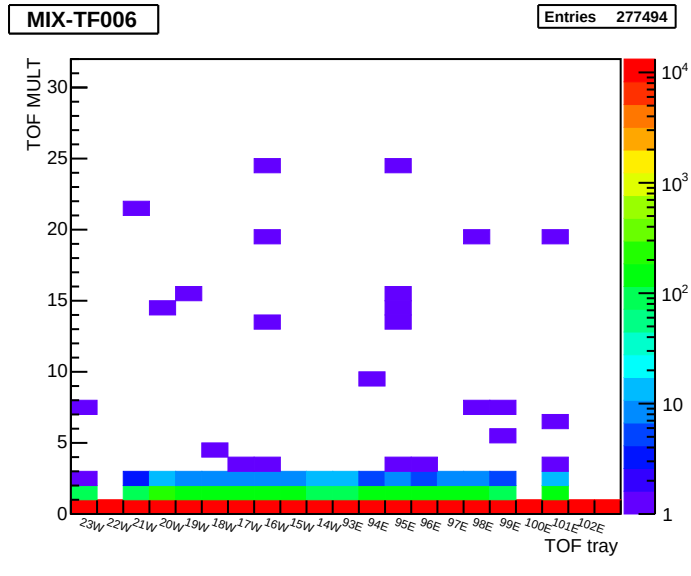

MIX-TF001
Entries 277494
TOF MULT
TOF tray

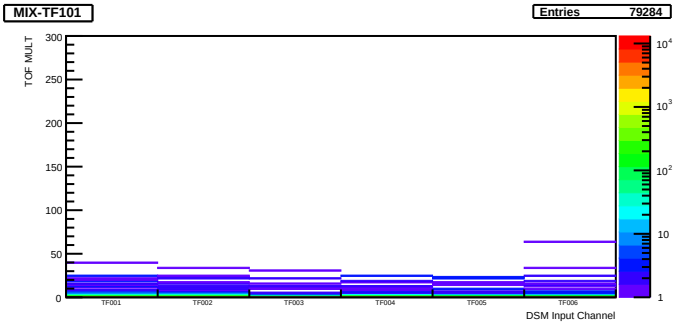

MIX-TF002
Entries 277494
TOF MULT
TOF tray

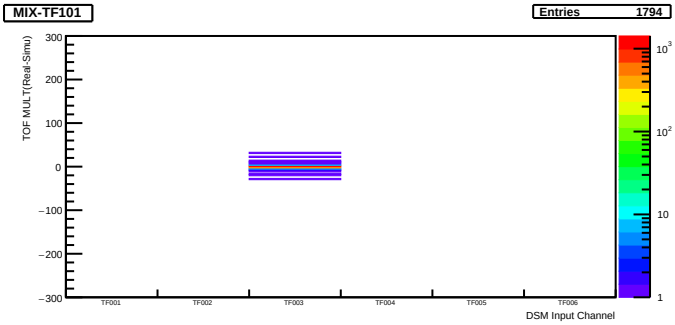

MIX-TF003
Entries 277494
TOF MULT
TOF tray

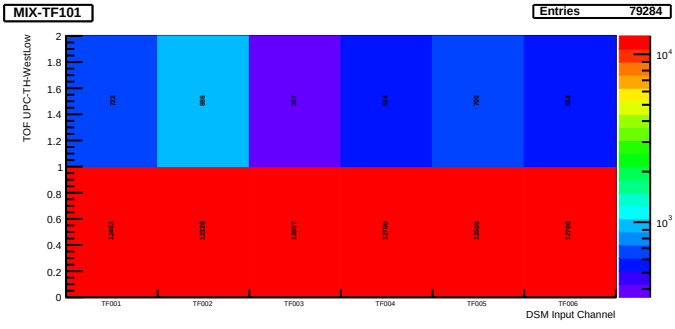

MIX-TF004
Entries 277494
TOF MULT
TOF tray

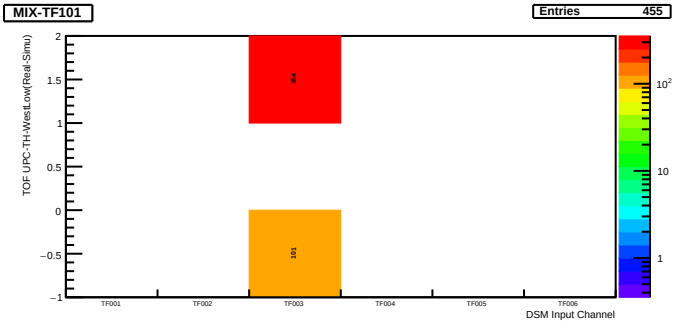

MIX-TF005
Entries 277494
TOF MULT
TOF tray

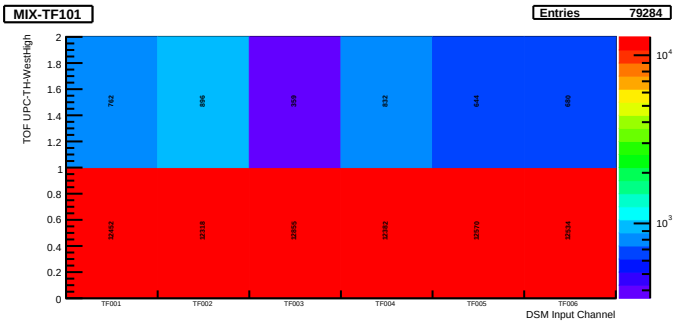

MIX-TF006
Entries 277494
TOF MULT
TOF tray

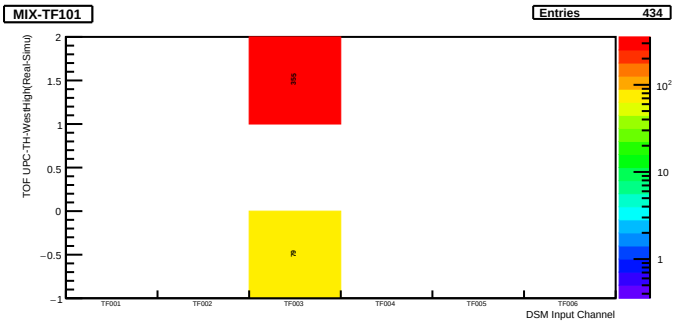

MIX-TF101
Entries 79284
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

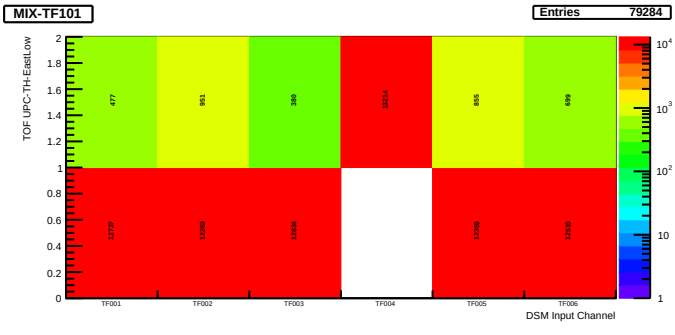

MIX-TF101
Entries 1794
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

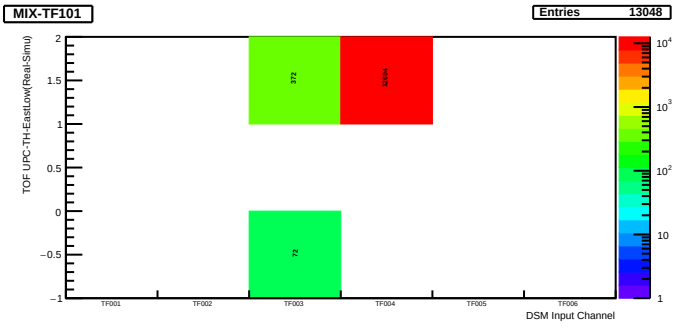

MIX-TF101
Entries 79284
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

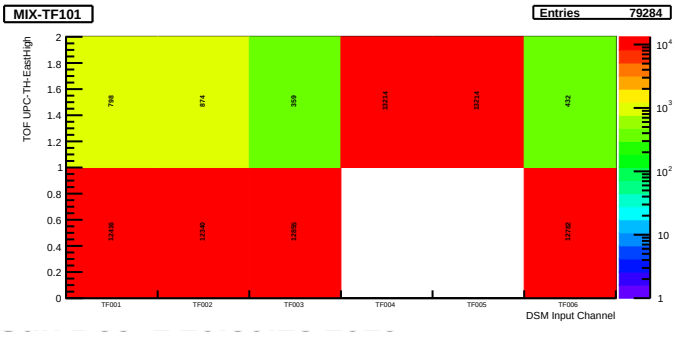

MIX-TF101
Entries 455
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

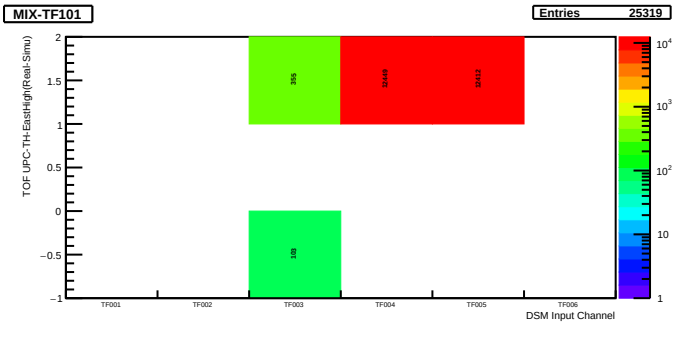

MIX-TF101
Entries 79284
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

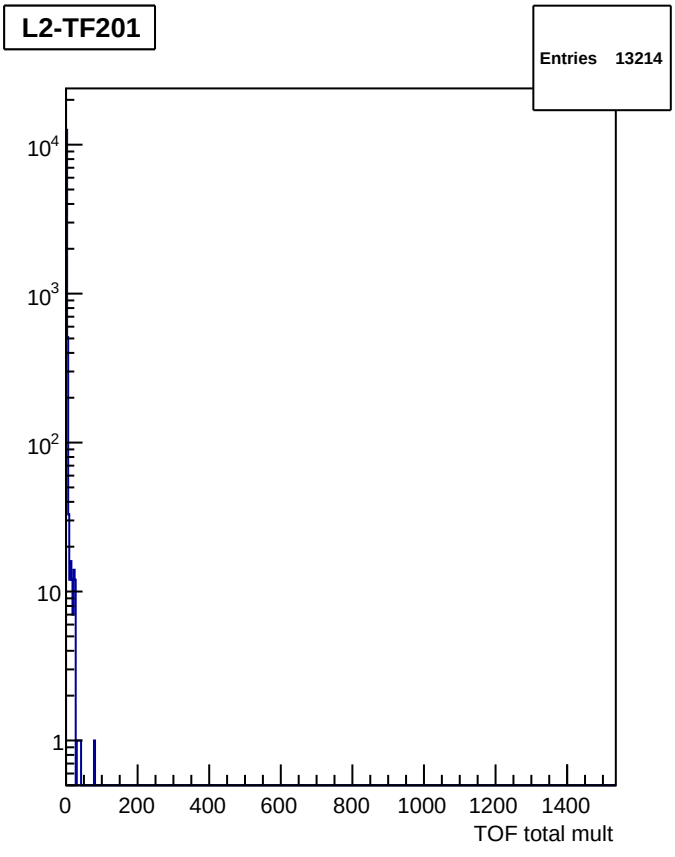

MIX-TF101
Entries 434
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

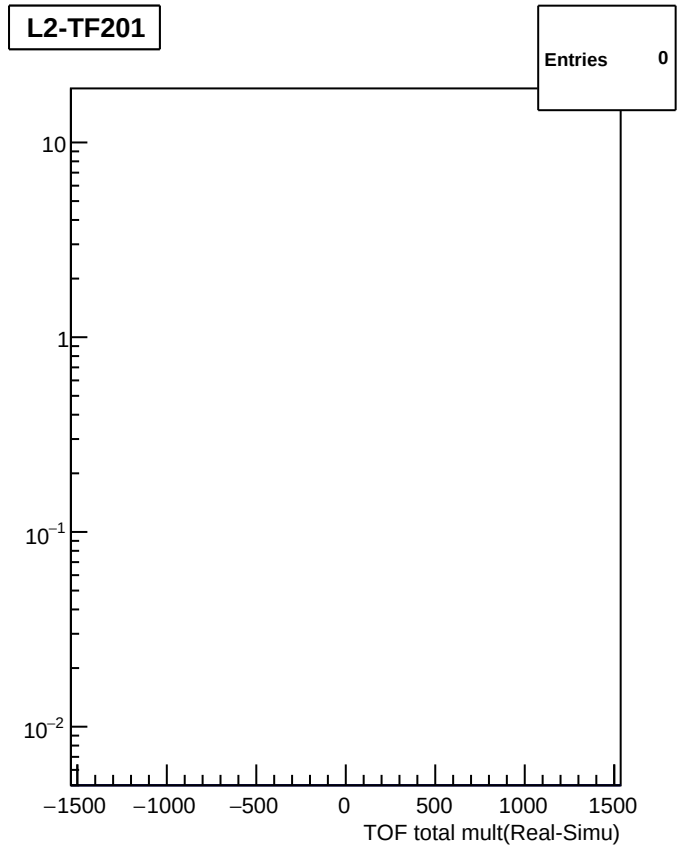

MIX-TF101
Entries 79284
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

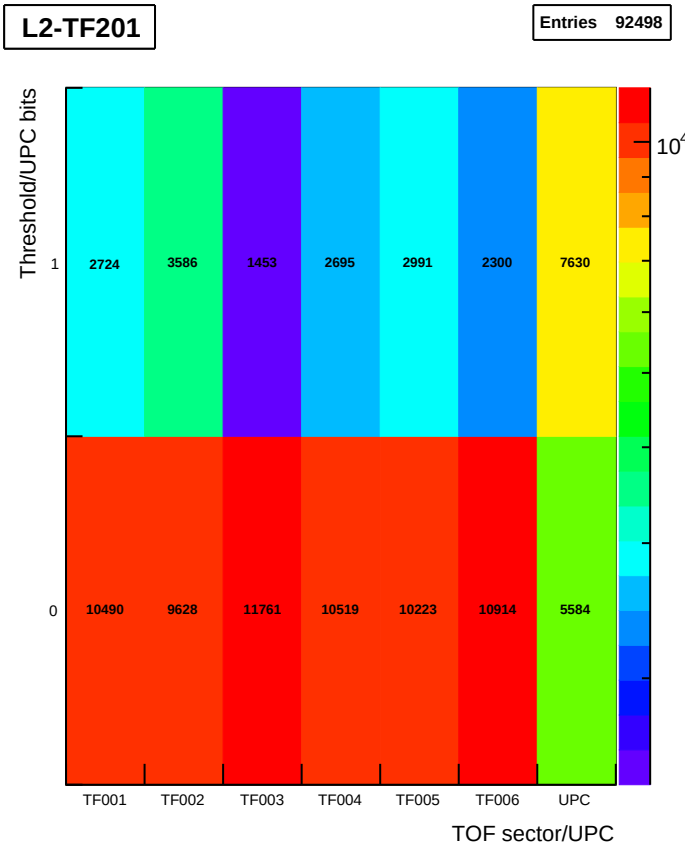

MIX-TF101
Entries 13048
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

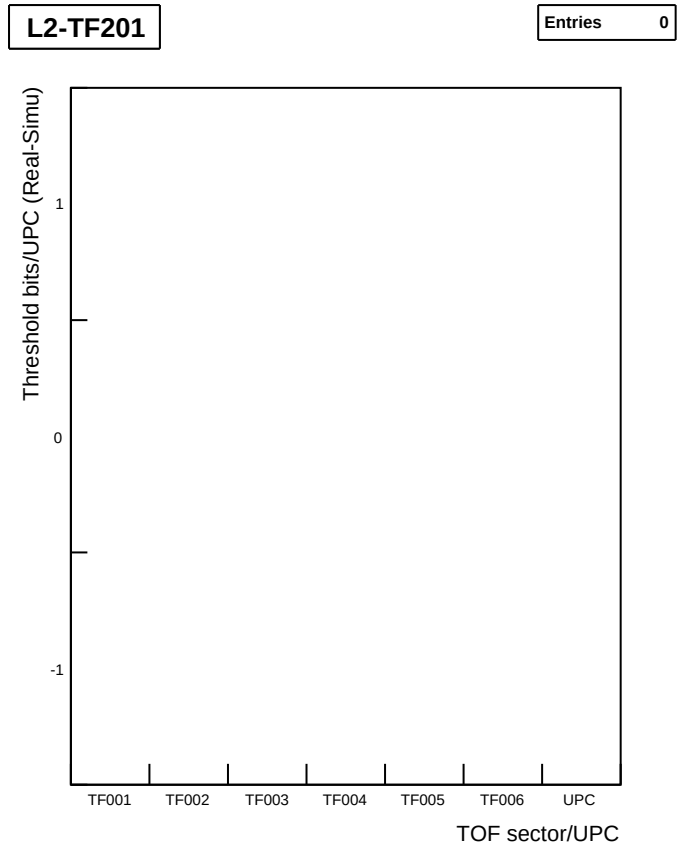

MIX-TF101
Entries 79284
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

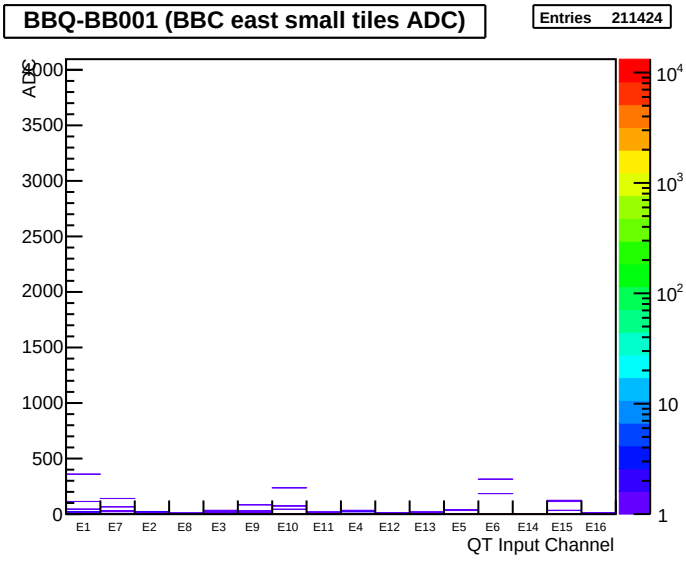

MIX-TF101
Entries 25319
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

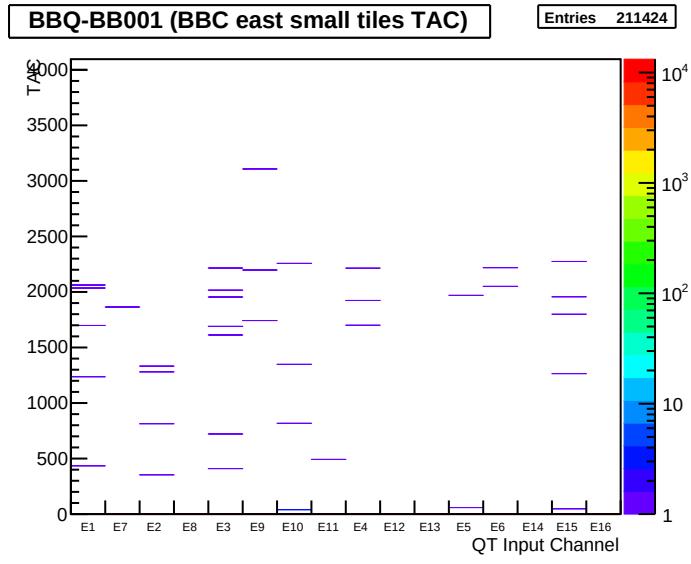

L2-TF201
Entries 13214
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

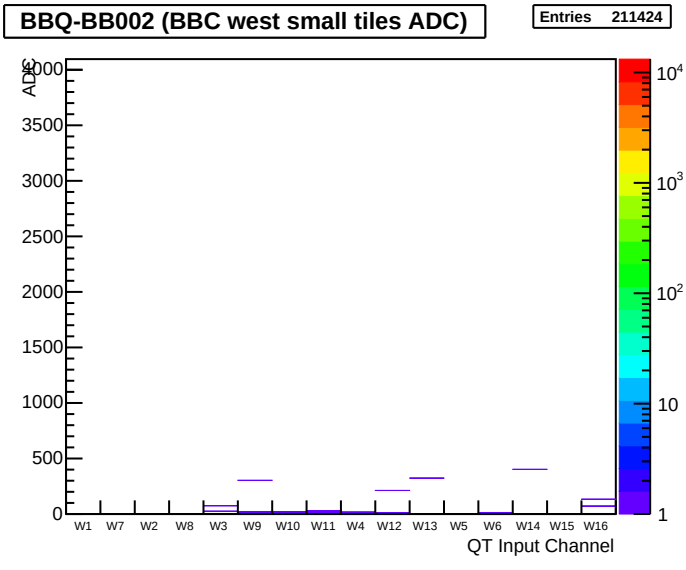

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−1500
−1000
−500
0
500
1000
1500
TOF total mult(Real-Simu)

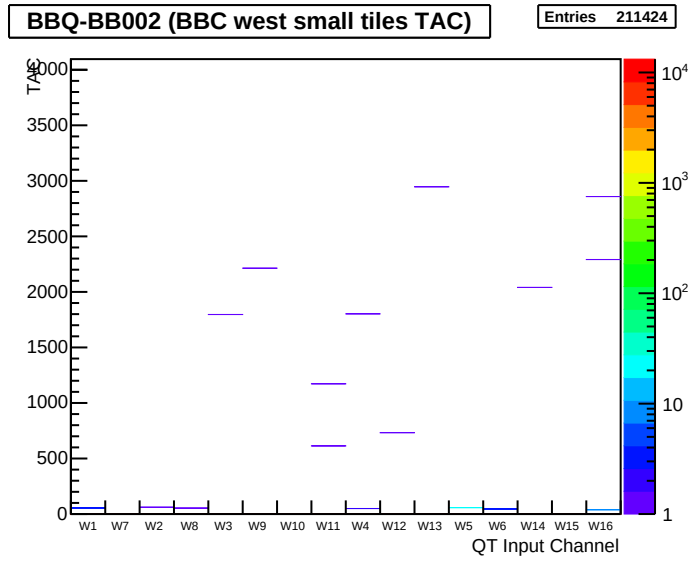

L2-TF201
Entries 92498
Threshold/UPC bits
1
0
2724
3586
1453
2695
2991
2300
7630
10490
9628
11761
10519
10223
10914
5584
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

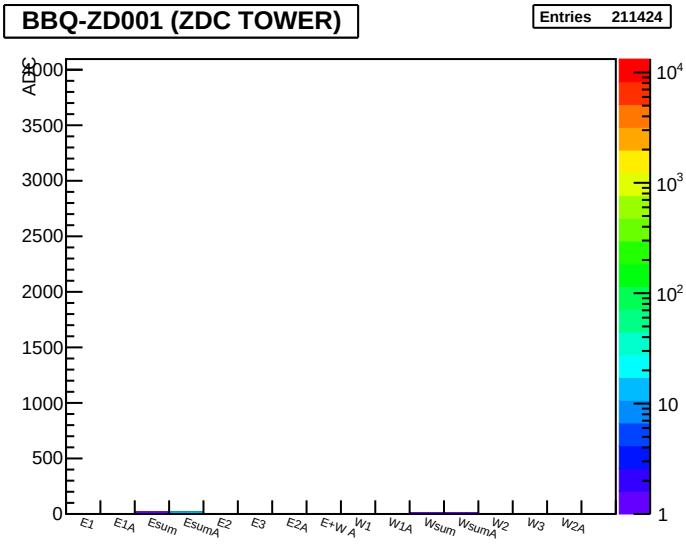

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

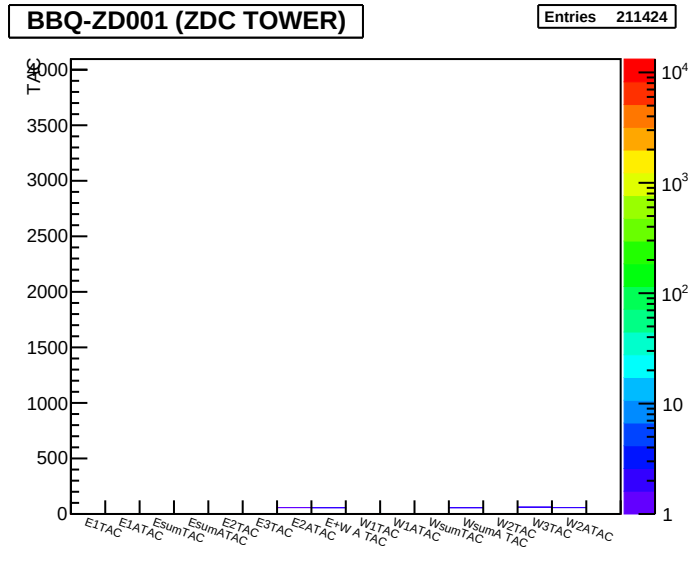

BBQ-BB001 (BBC east small tiles ADC)
Entries 211424
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

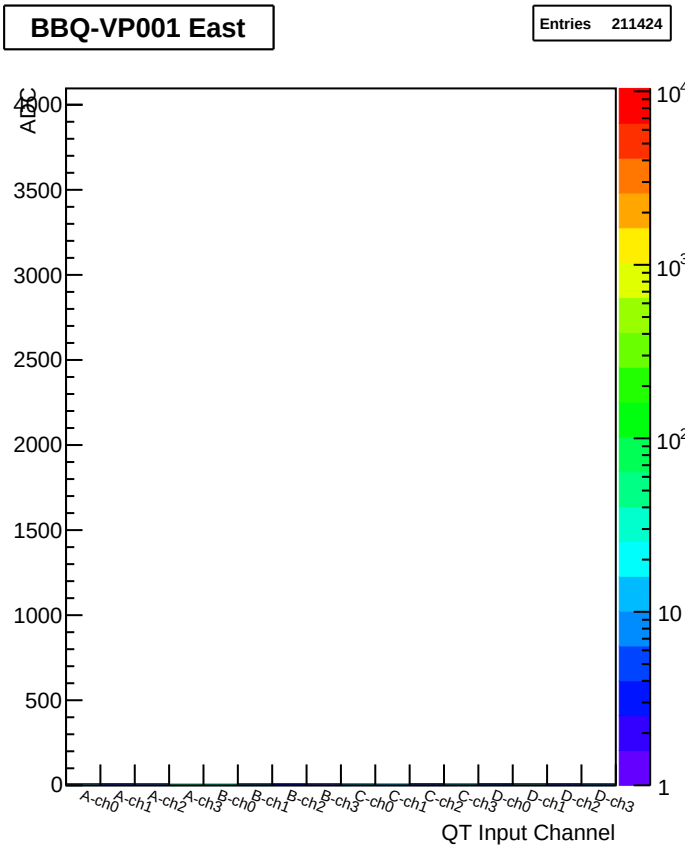

BBQ-BB001 (BBC east small tiles TAC)
Entries 211424
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

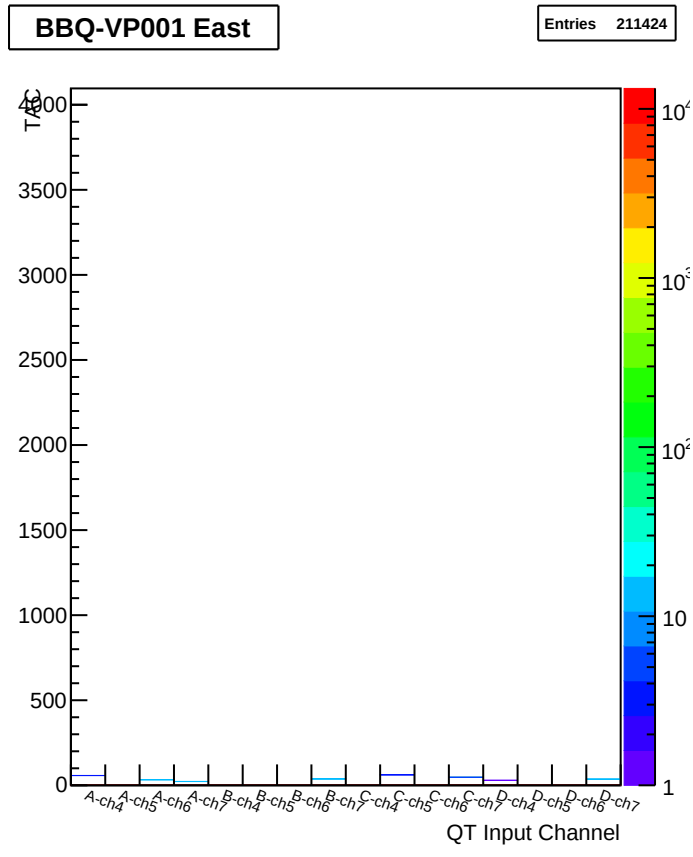

BBQ-BB002 (BBC west small tiles ADC)
Entries 211424
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

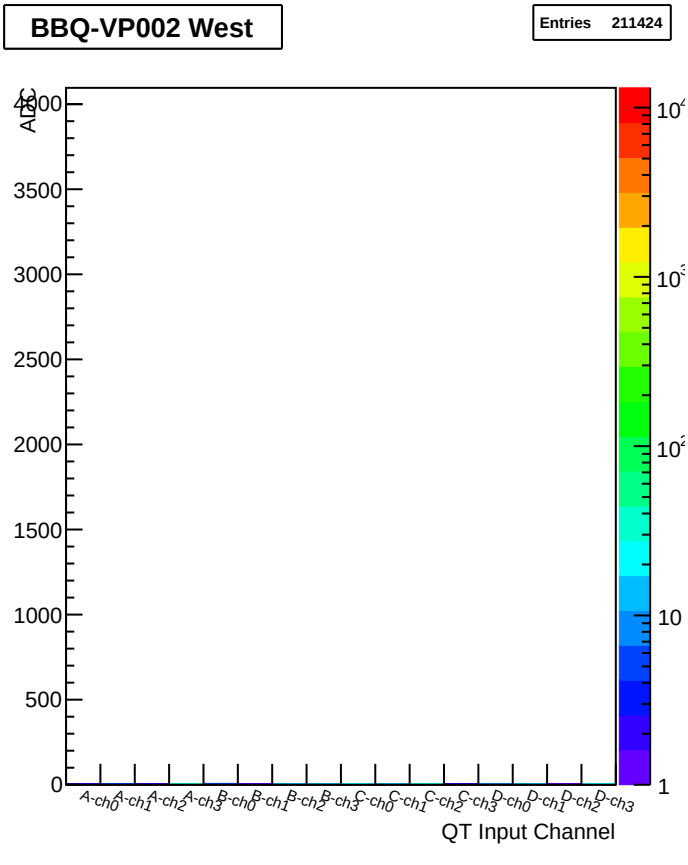

BBQ-BB002 (BBC west small tiles TAC)
Entries 211424
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

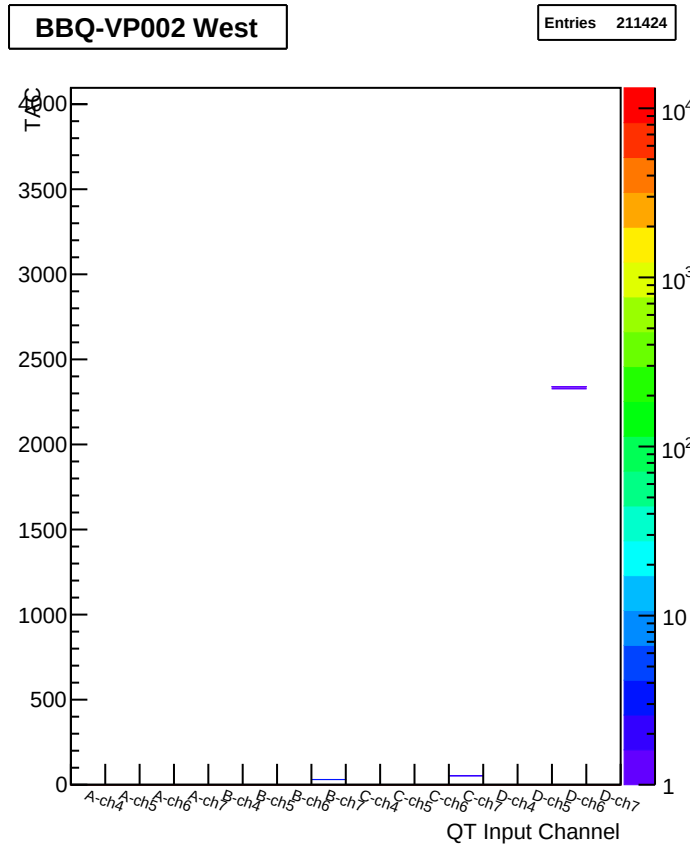

BBQ-ZD001 (ZDC TOWER)
Entries 211424
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

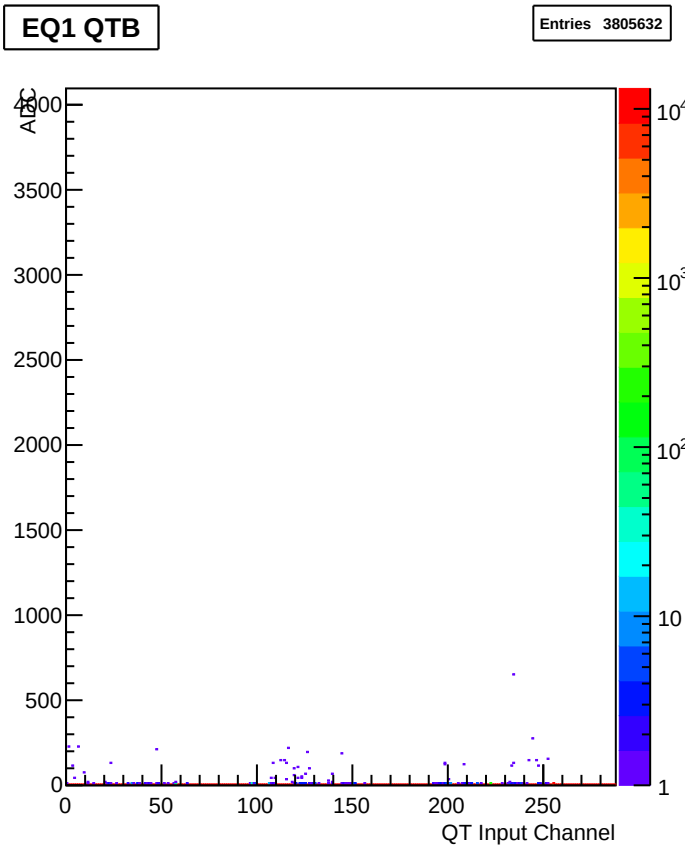

BBQ-ZD001 (ZDC TOWER)
Entries 211424
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

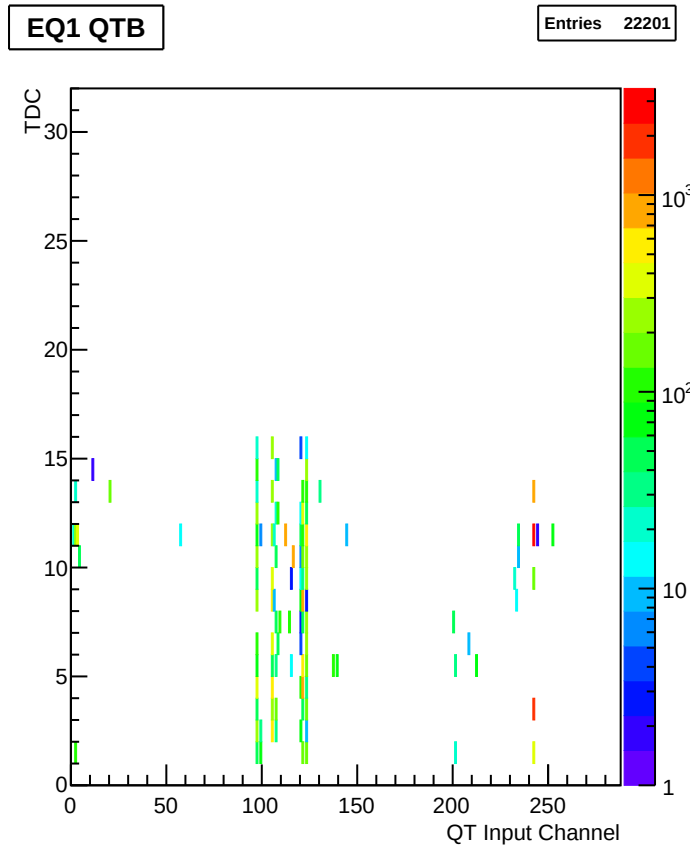

BBQ-VP001 East
Entries 211424
ADC
QT Input Channel

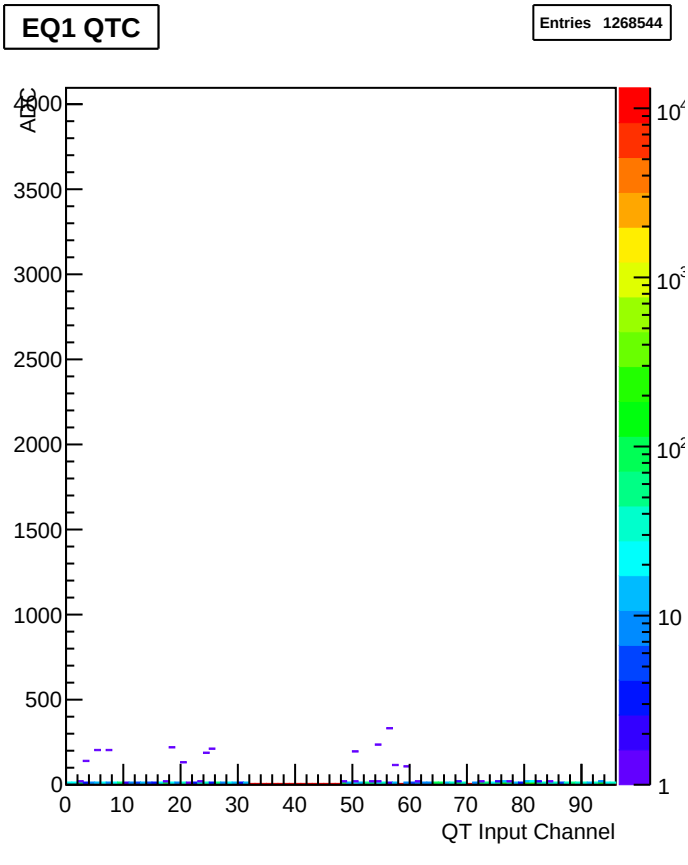

BBQ-VP001 East
Entries 211424
TADC
QT Input Channel

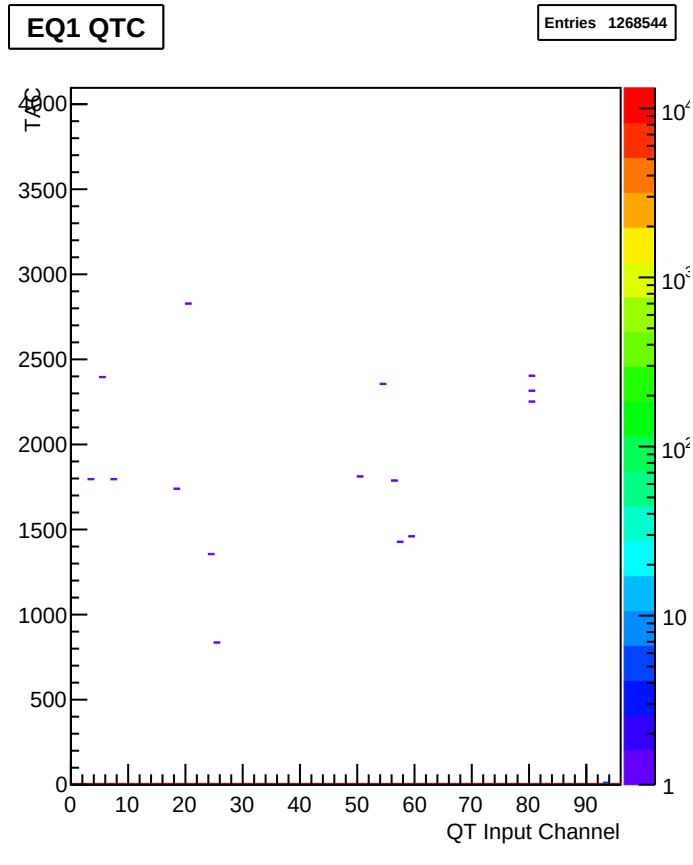

BBQ-VP002 West
Entries 211424
ADC
QT Input Channel

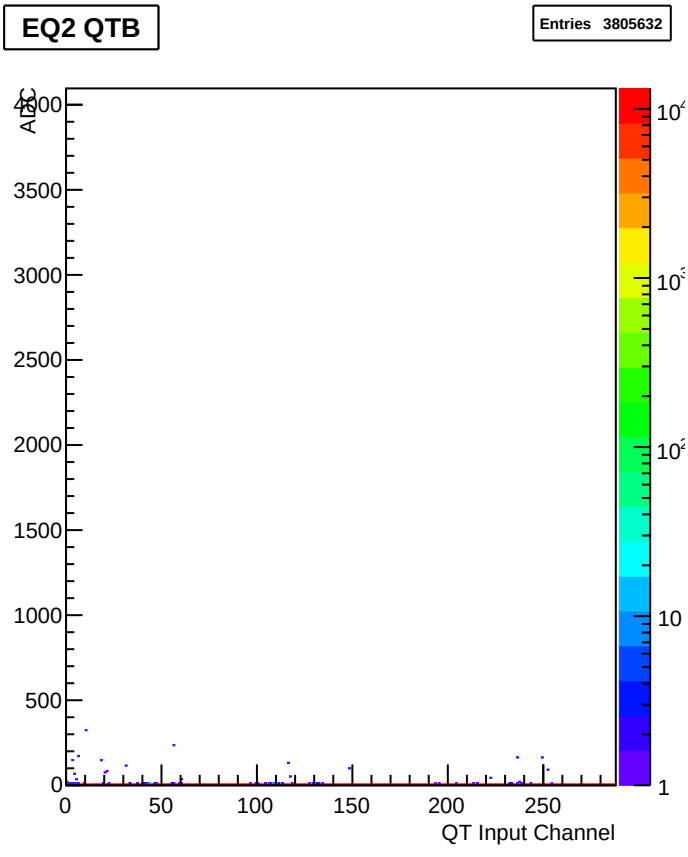

BBQ-VP002 West
Entries 211424
TADC
QT Input Channel

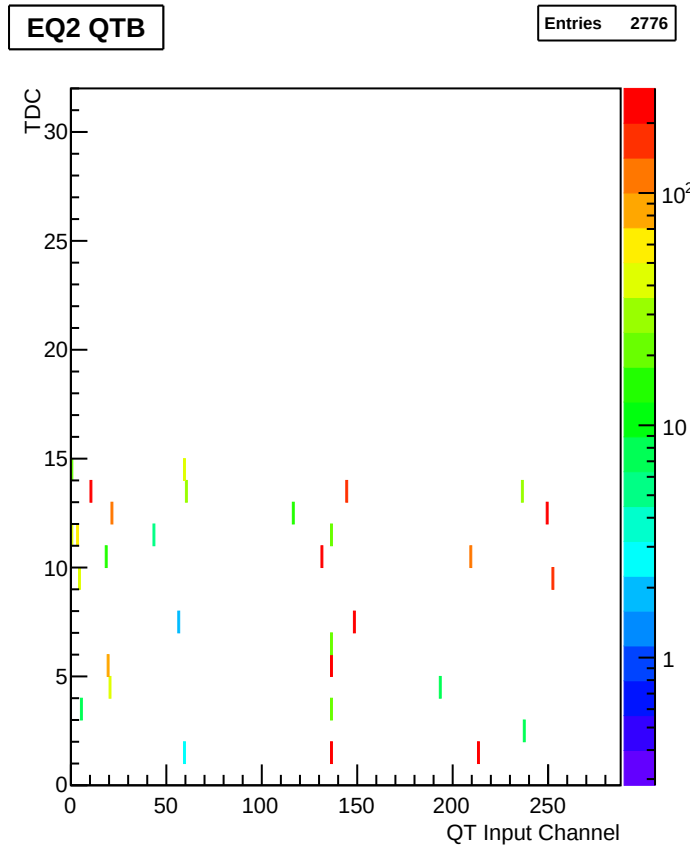

EQ1 QTB
Entries 3805632
ADC
QT Input Channel

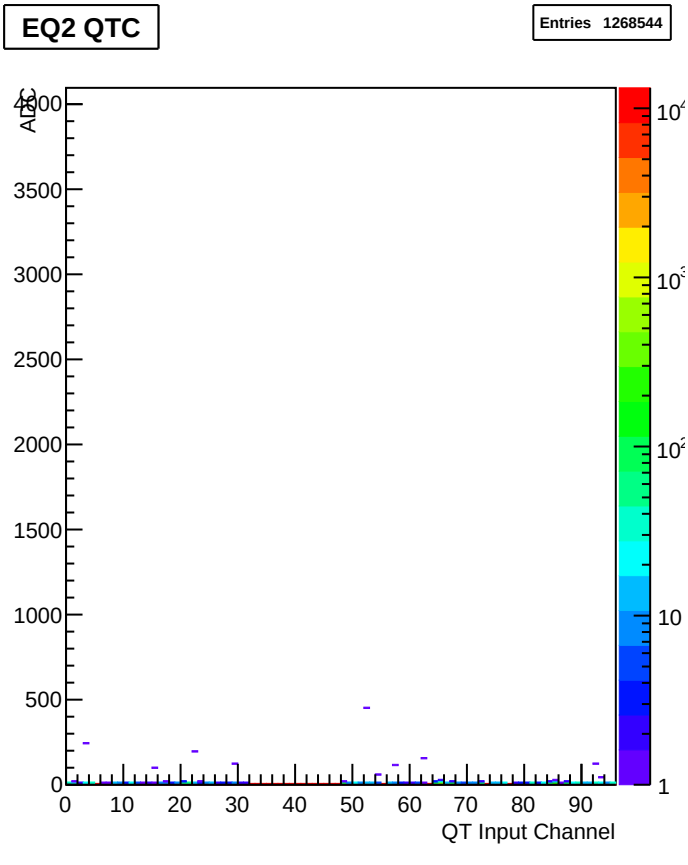

EQ1 QTB
Entries 22201
TDC
QT Input Channel

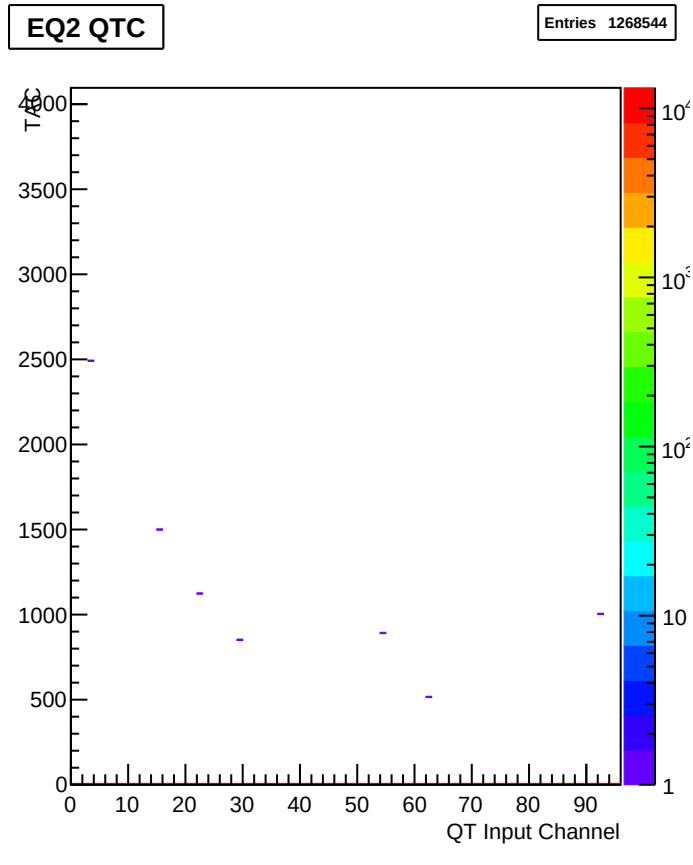

EQ1 QTC
Entries 1268544
ADC
QT Input Channel

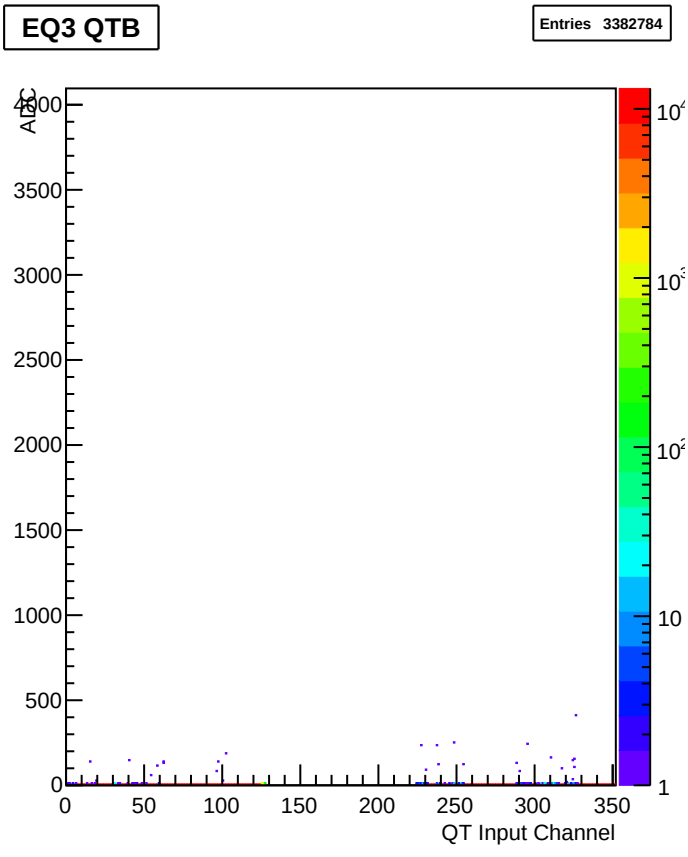

EQ1 QTC
Entries 1268544
TADC
QT Input Channel

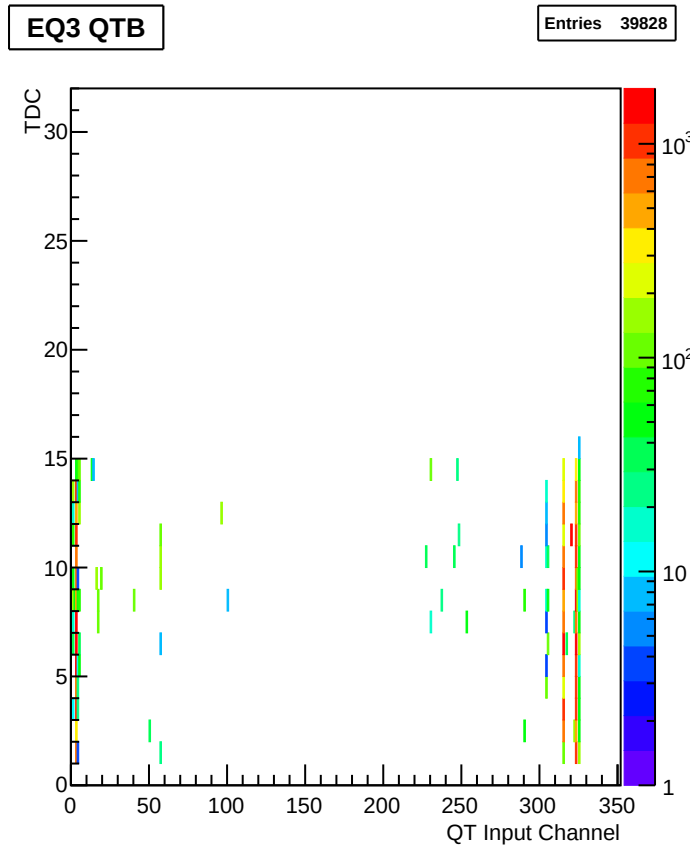

EQ2 QTB
Entries 3805632
ADC
QT Input Channel

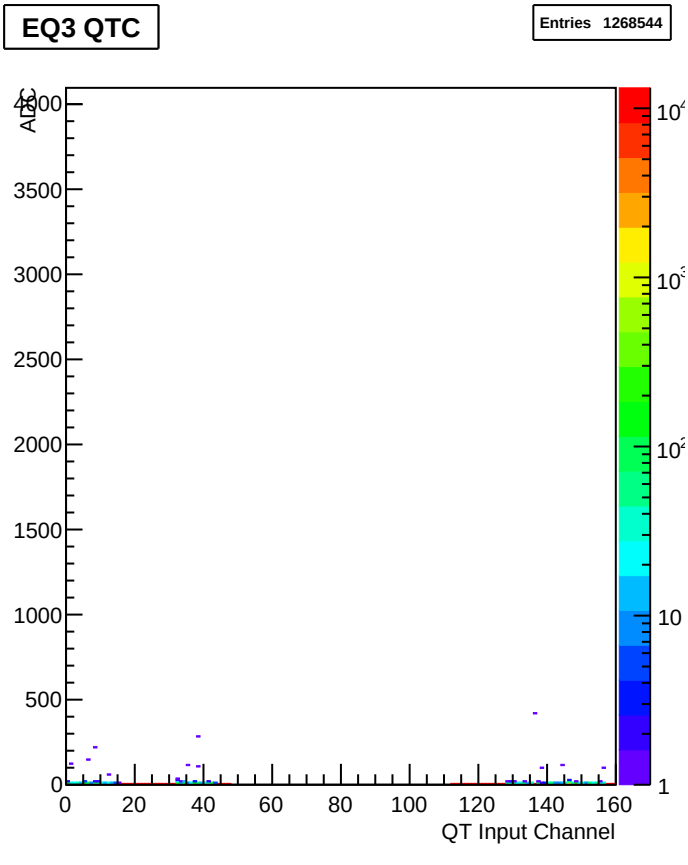

EQ2 QTB
Entries 2776
TDC
QT Input Channel

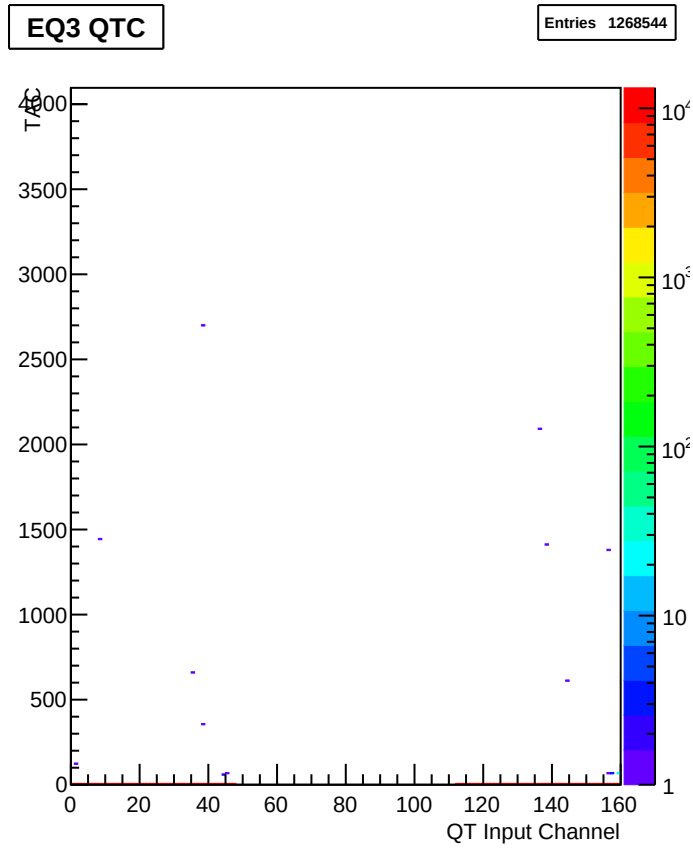

EQ2 QTC
Entries 1268544
ADC
QT Input Channel

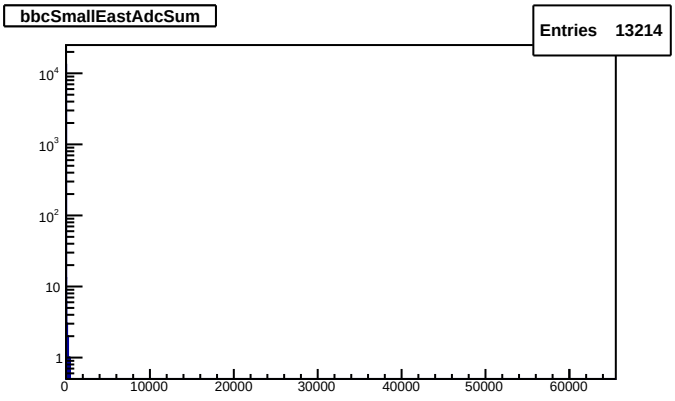

EQ2 QTC
Entries 1268544
TADC
QT Input Channel

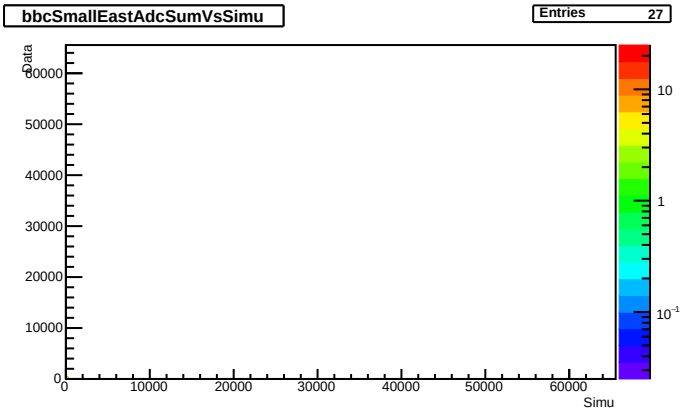

EQ3 QTB
Entries 3382784
ADC
QT Input Channel

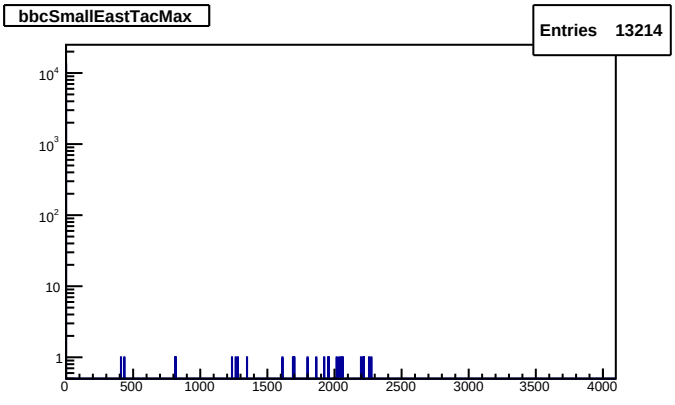

EQ3 QTB
Entries 39828
TDC
QT Input Channel

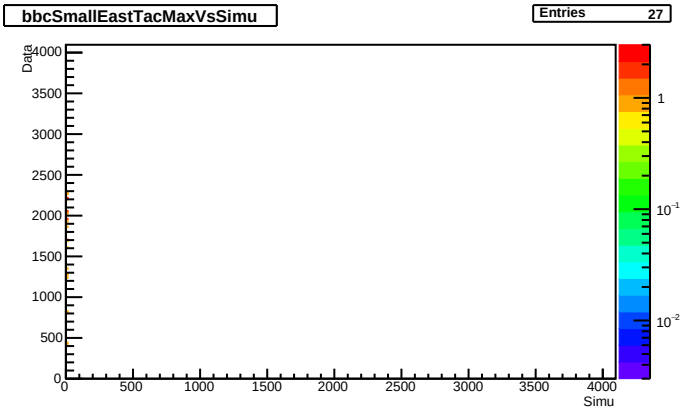

EQ3 QTC
Entries 1268544
ADC
QT Input Channel

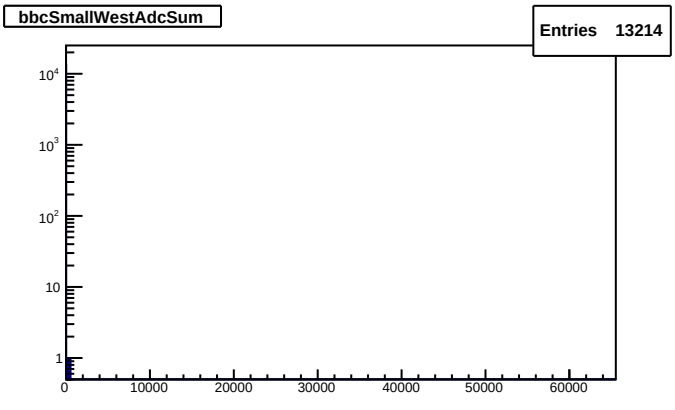

EQ3 QTC
Entries 1268544
TADC
QT Input Channel

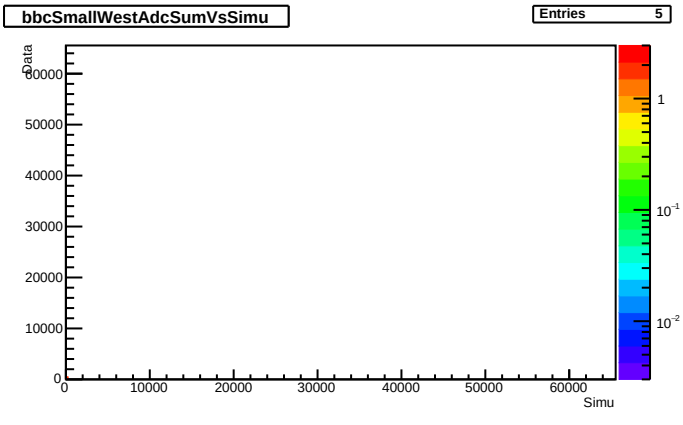

bbcSmallEastAdcSum
Entries 13214

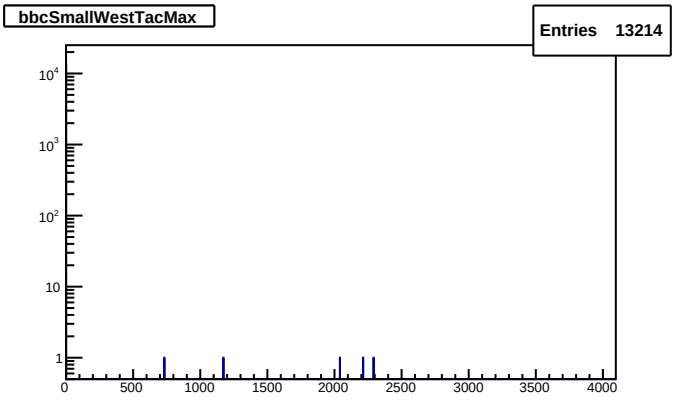

bbcSmallEastAdcSumVsSimu
Entries 27
Data
Simu

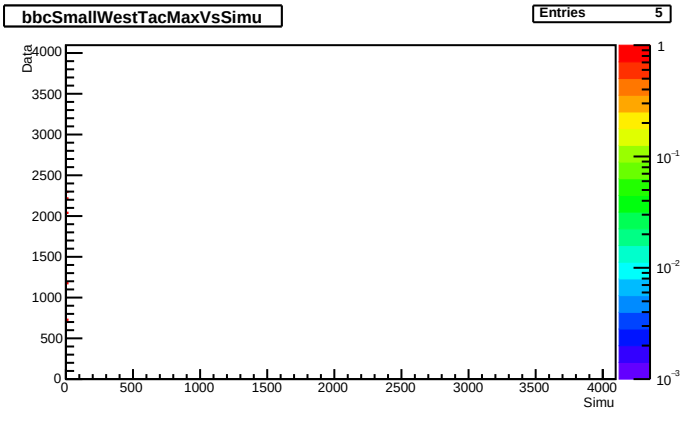

bbcSmallEastTacMax
Entries 13214

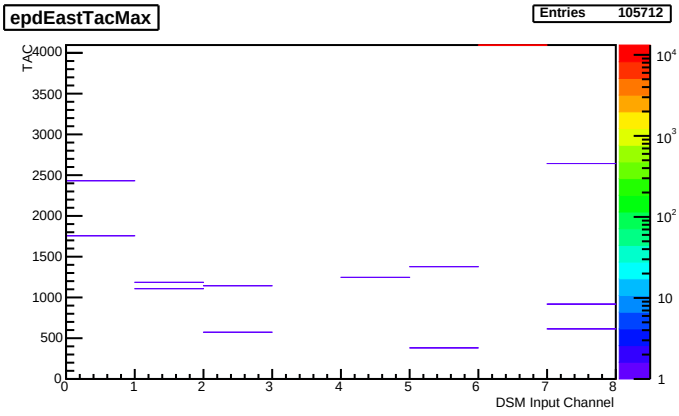

bbcSmallEastTacMaxVsSimu
Entries 27
Data
Simu

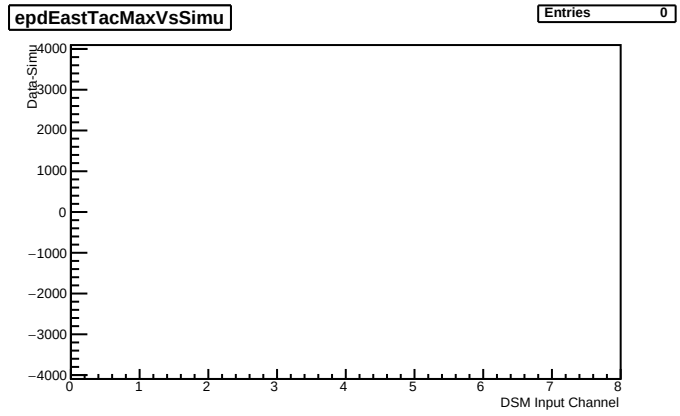

bbcSmallWestAdcSum
Entries 13214

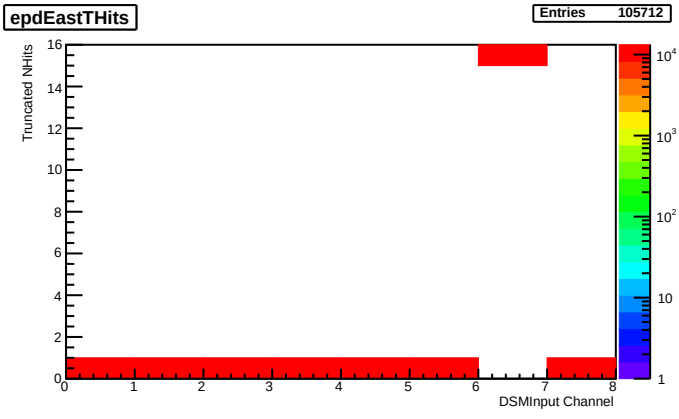

bbcSmallWestAdcSumVsSimu
Entries 5
Data
Simu

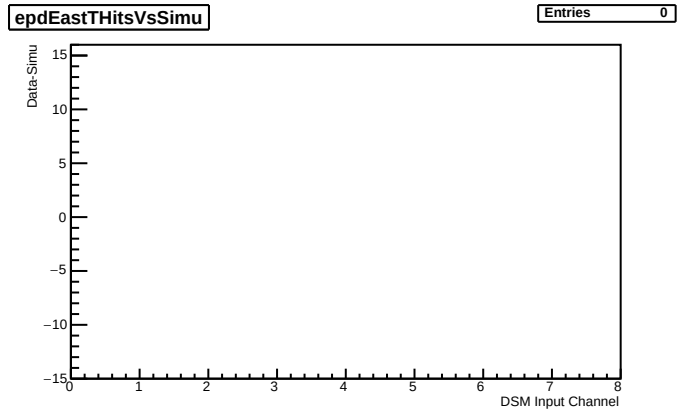

bbcSmallWestTacMax
Entries 13214

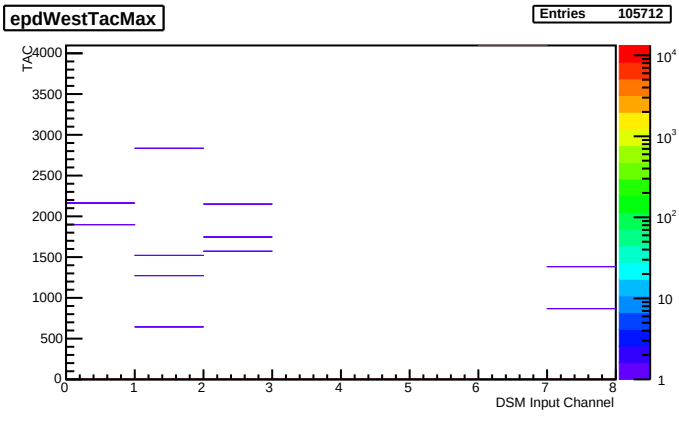

bbcSmallWestTacMaxVsSimu
Entries 5
Data
Simu

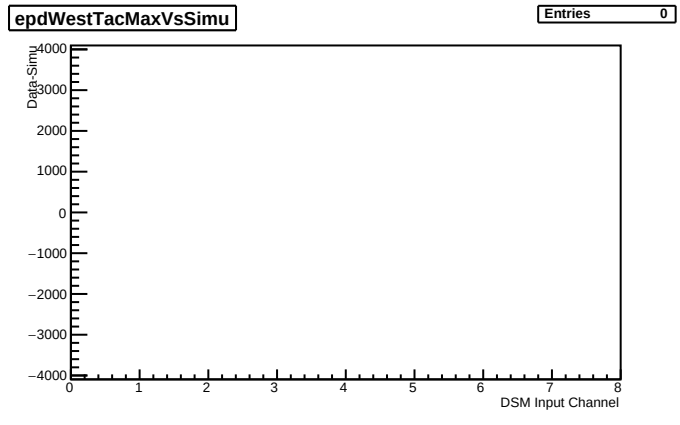

epdEastTacMax
Entries 105712
TAC
DSM Input Channel

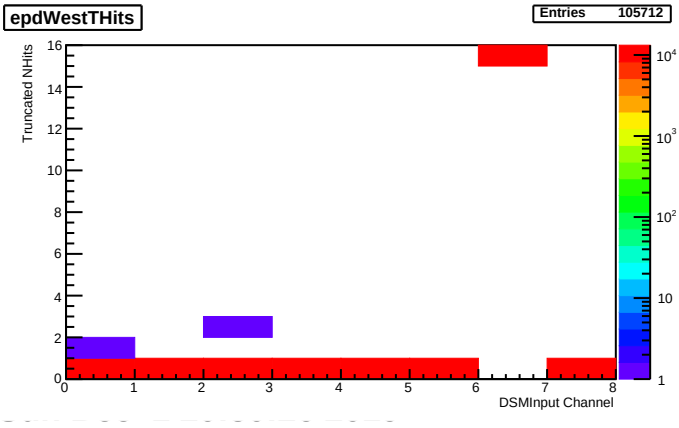

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

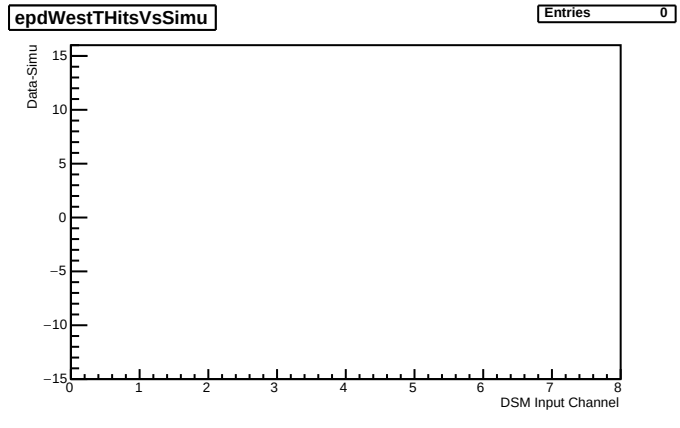

epdEastTHits
Entries 105712
Truncated NHits
DSMInput Channel

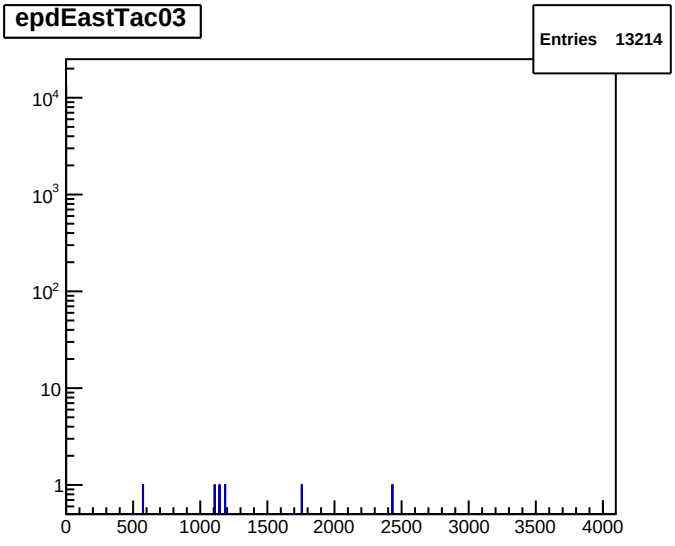

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

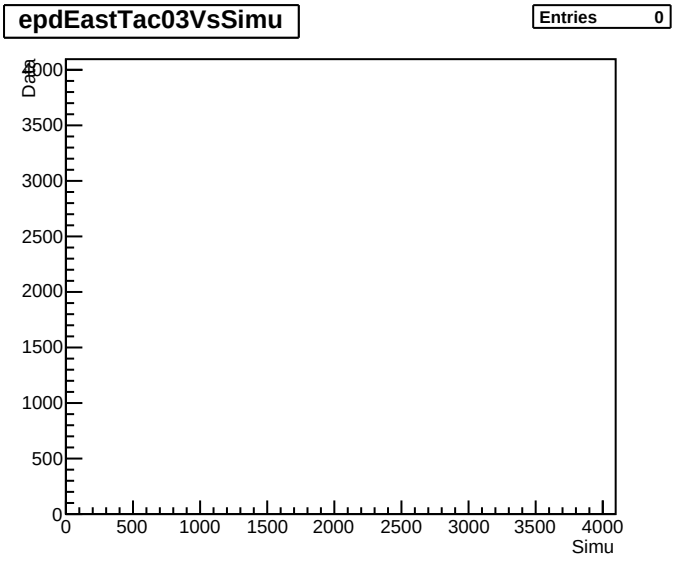

epdWestTacMax
Entries 105712
TAC
DSM Input Channel

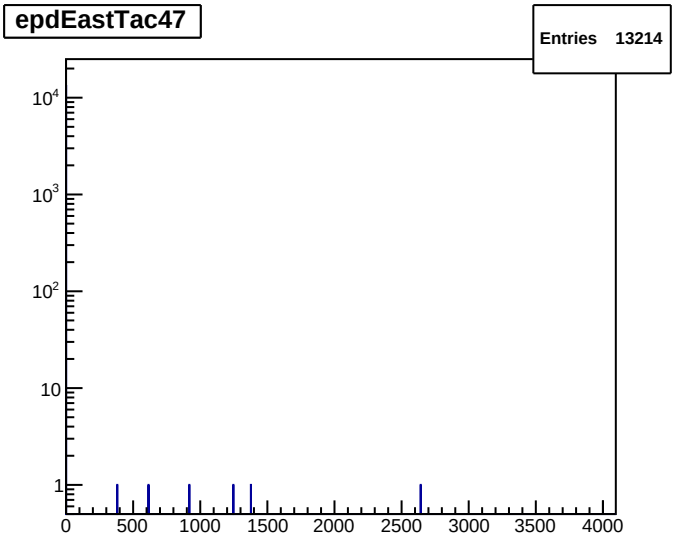

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

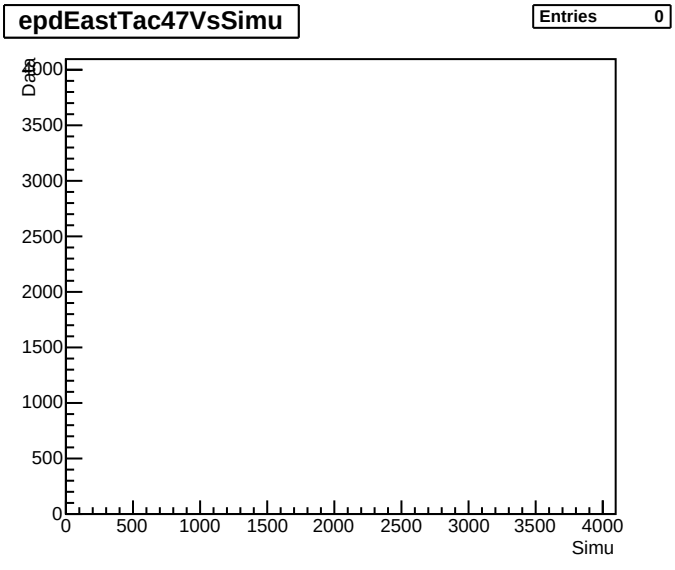

epdWestTHits
Entries 105712
Truncated NHits
DSMInput Channel

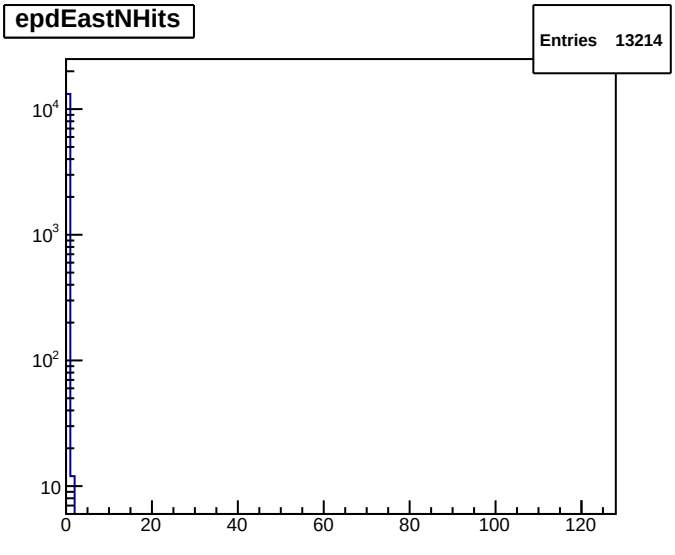

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

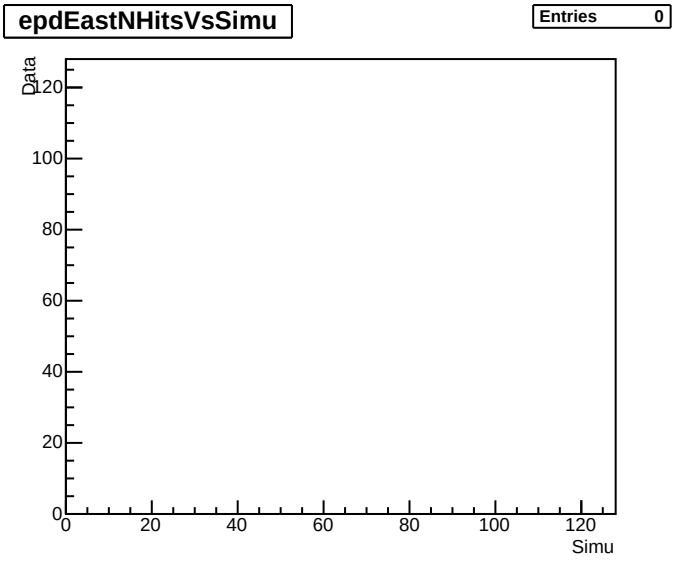

epdEastTac03
Entries 13214


epdEastTac03VsSimu
Entries 0
Data
Simu


epdEastTac47
Entries 13214


epdEastTac47VsSimu
Entries 0
Data
Simu


epdEastNHits
Entries 13214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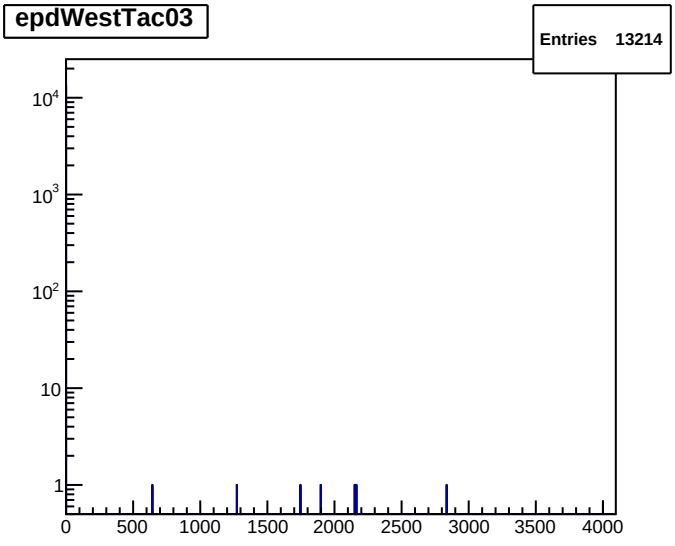
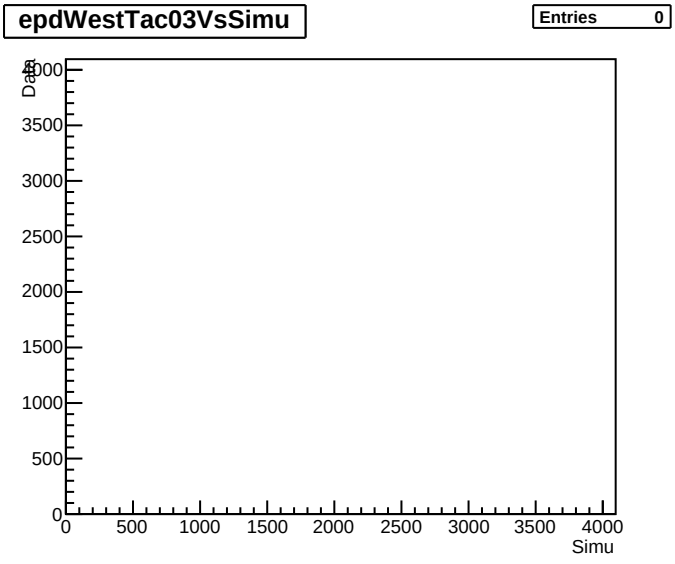
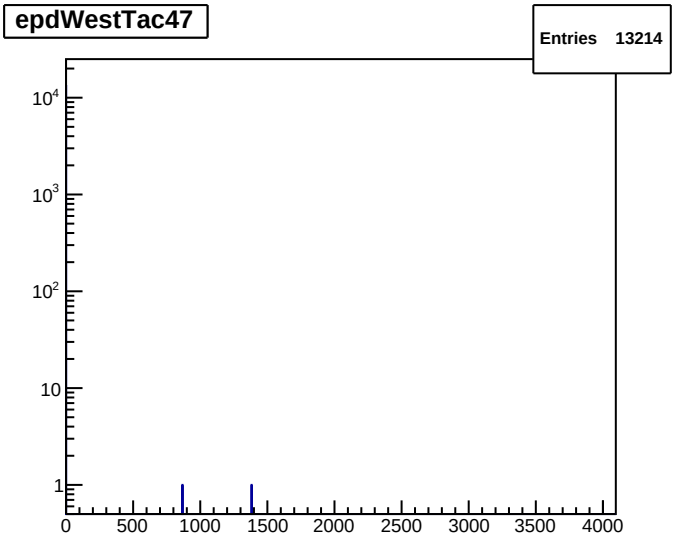
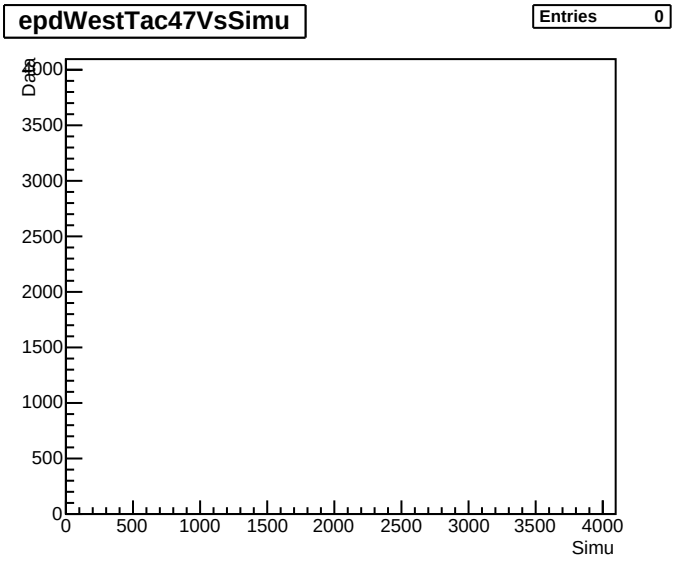
Entries 0
Data
Simu


epdWestTac03
Entries 13214


epdWestTac03VsSimu
Entries 0
Data
Simu


epdWestTac47
Entries 13214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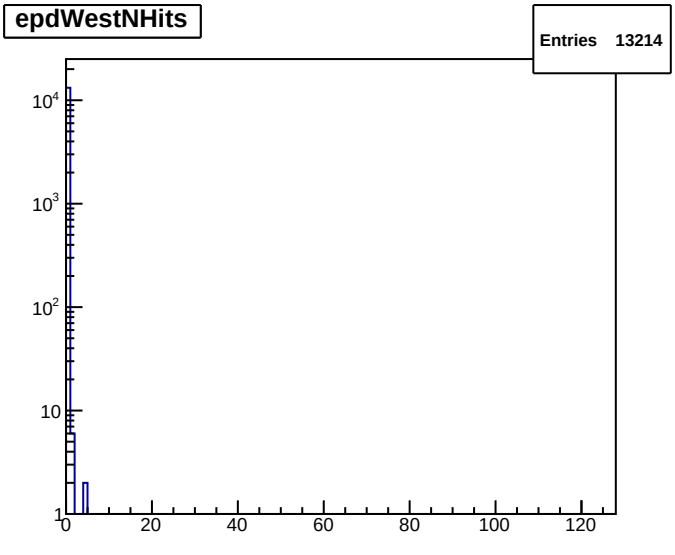
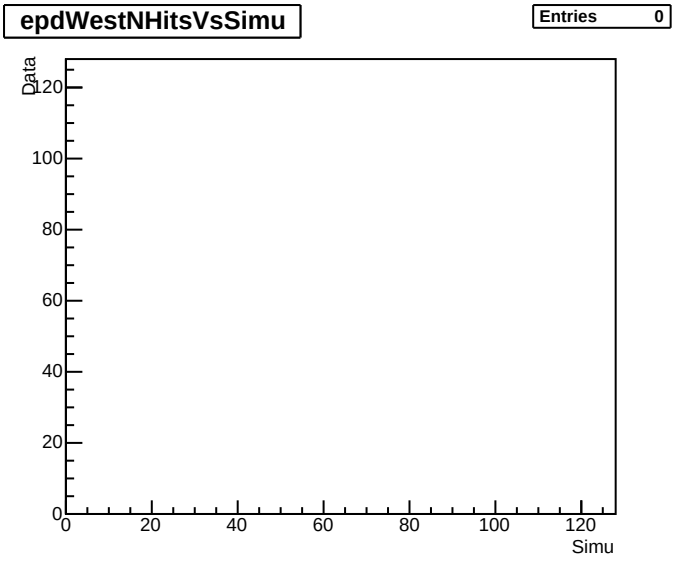
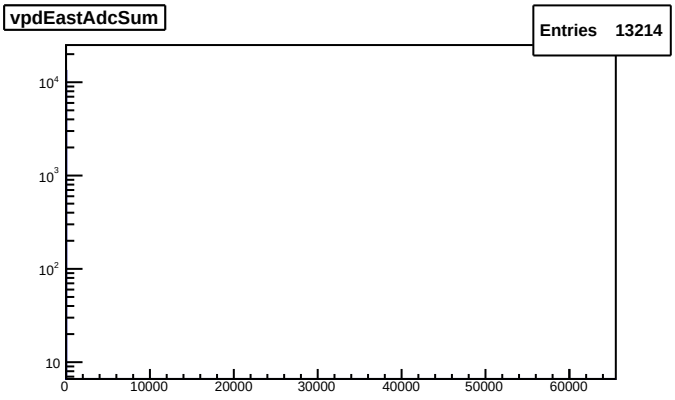
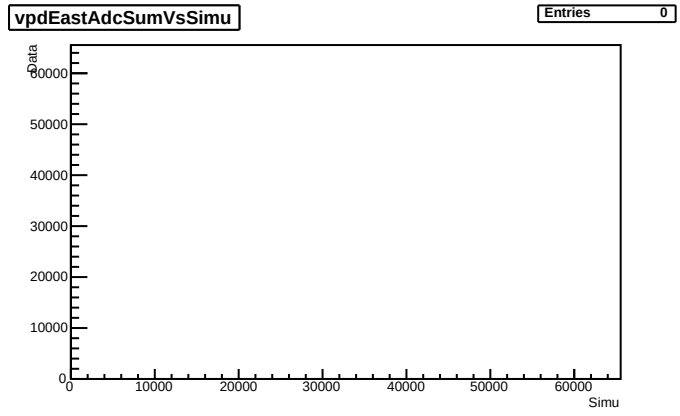
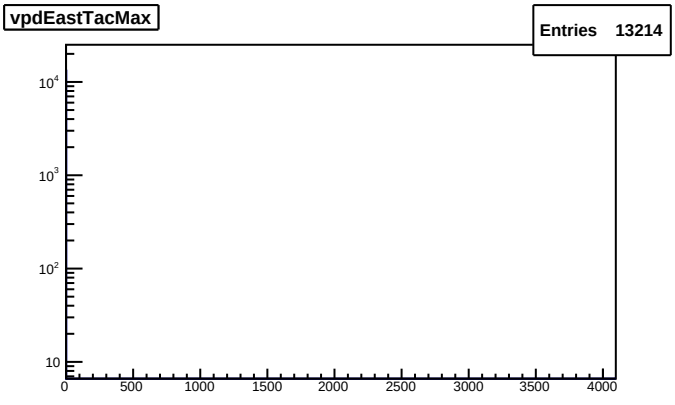
Entries 0
Data
Simu

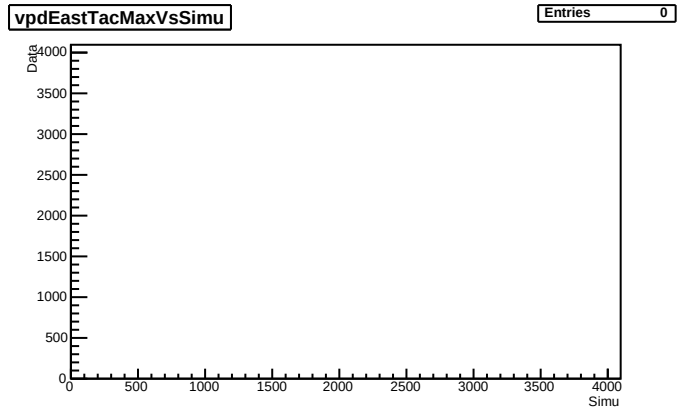

epdWestNHits
Entries 13214

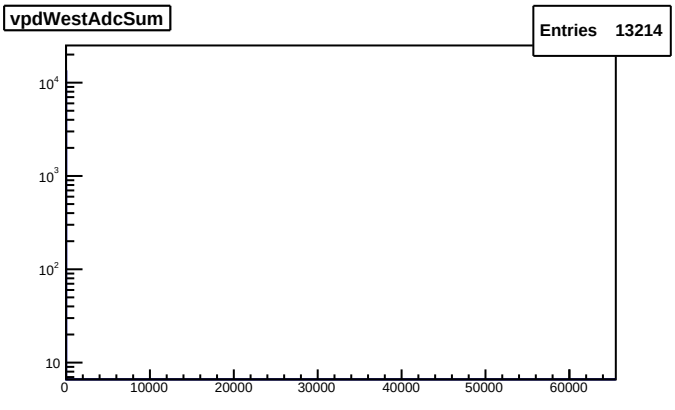

epdWestNHitsVsSimu
Entries 0
Data
Simu

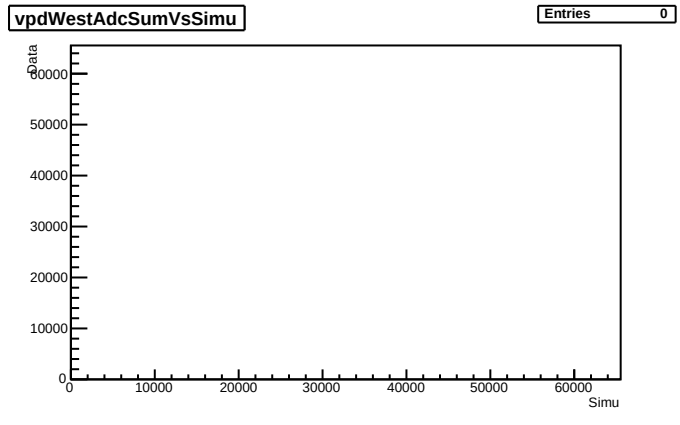

vpdEastAdcSum
Entries 13214

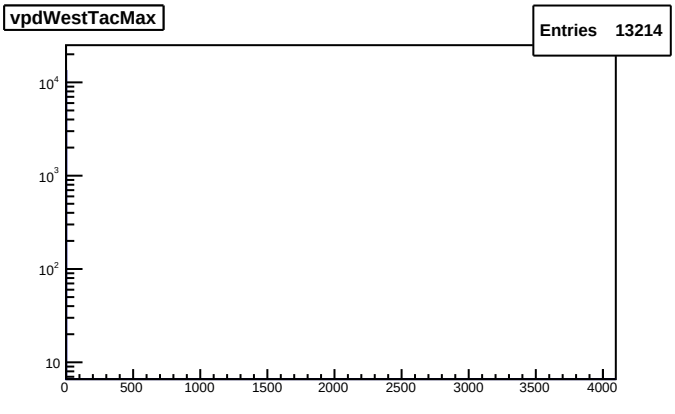

vpdEastAdcSumVsSimu
Entries 0
Data
Simu

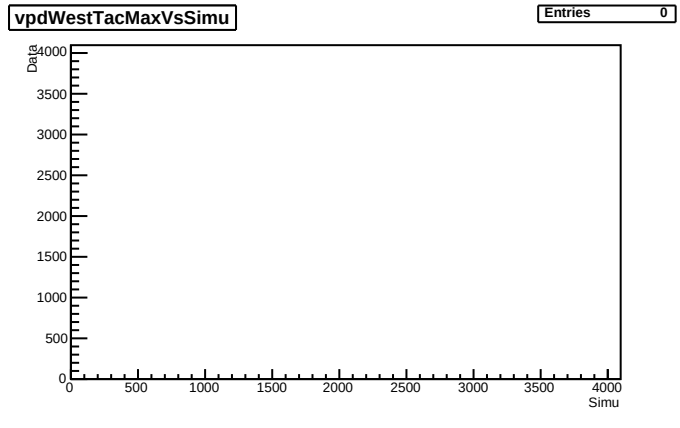

vpdEastTacMax
Entries 13214

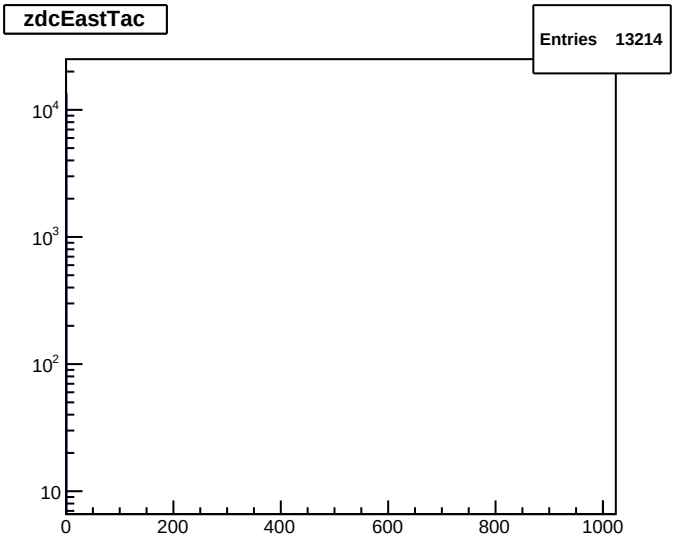

vpdEastTacMaxVsSimu
Entries 0
Data
Simu

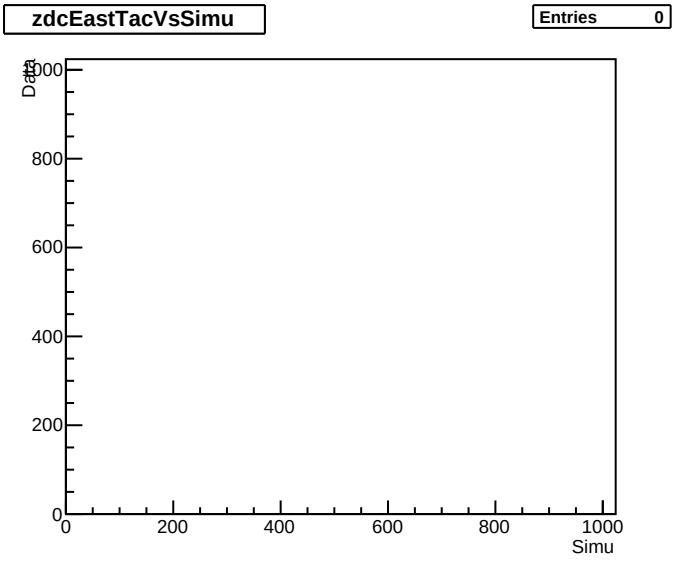

vpdWestAdcSum
Entries 13214

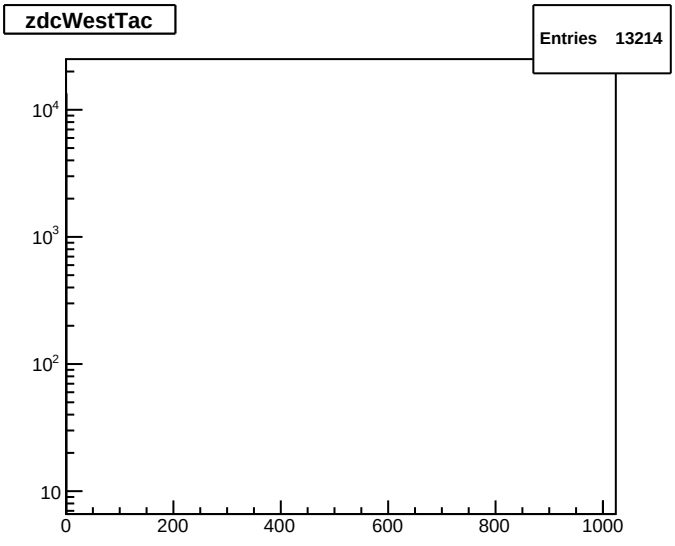

vpdWestAdcSumVsSimu
Entries 0
Data
Simu

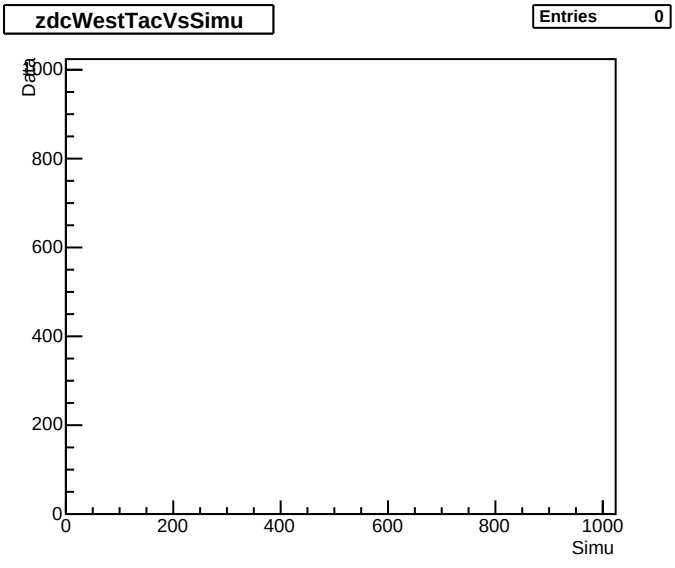

vpdWestTacMax
Entries 13214

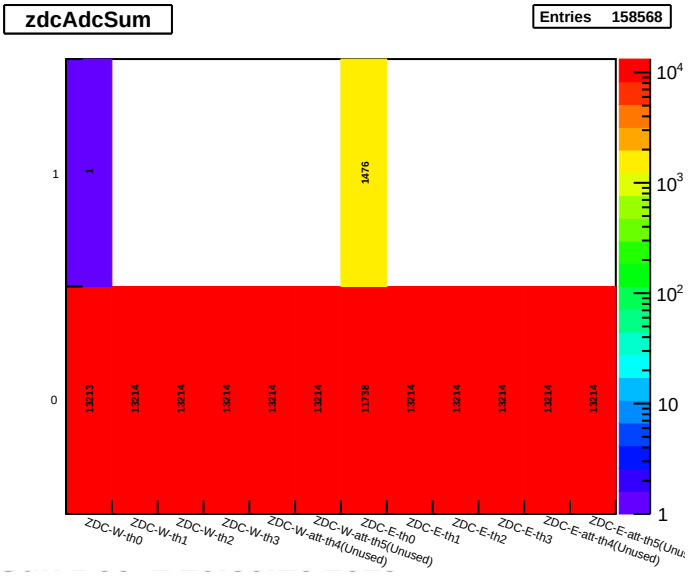

vpdWestTacMaxVsSimu
Entries 0
Data
Simu

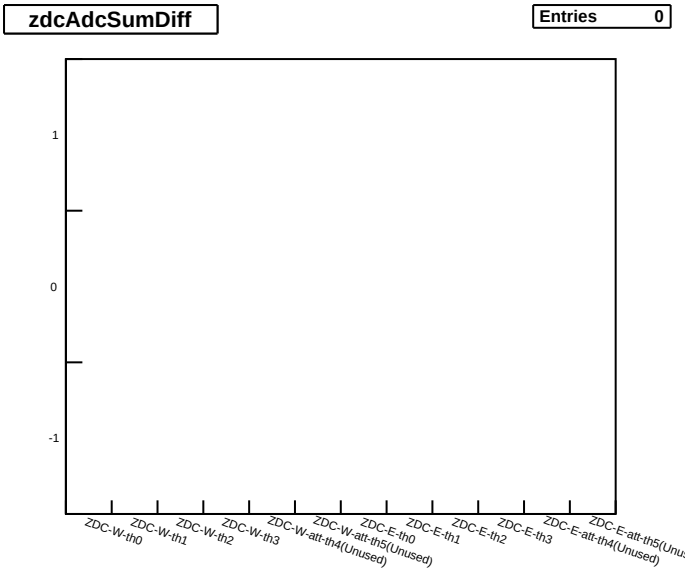

zdcEastTac
Entries 13214
10^4
10^3
10^2
10
0
200
400
600
800
1000

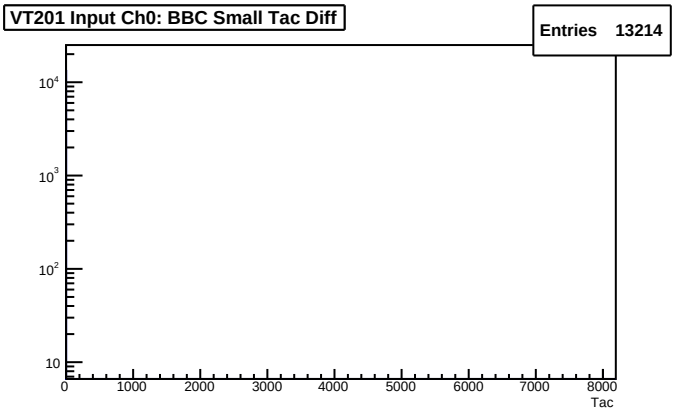

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

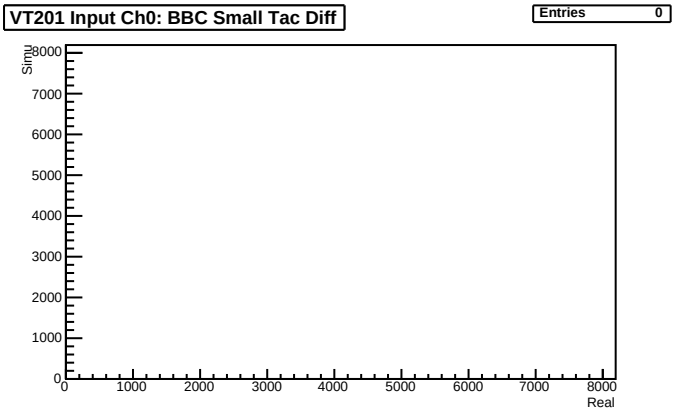

zdcWestTac
Entries 13214
10^4
10^3
10^2
10
0
200
400
600
800
1000

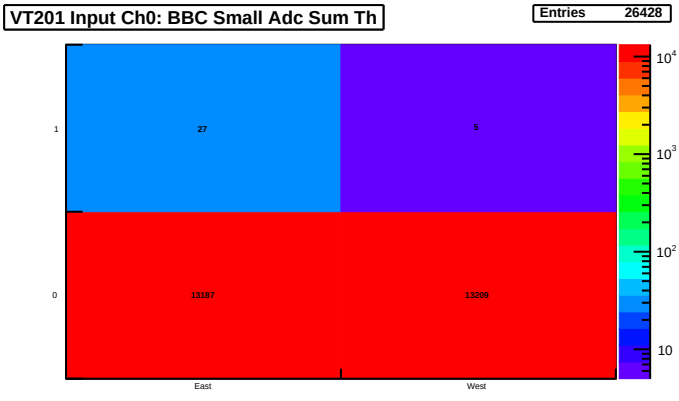

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

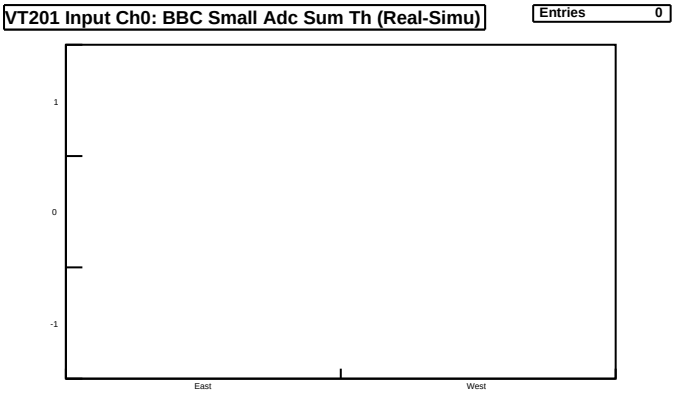

zdcAdcSum
Entries 158568
1
0
1
1476
13213
13214
11738
10^4
10^3
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

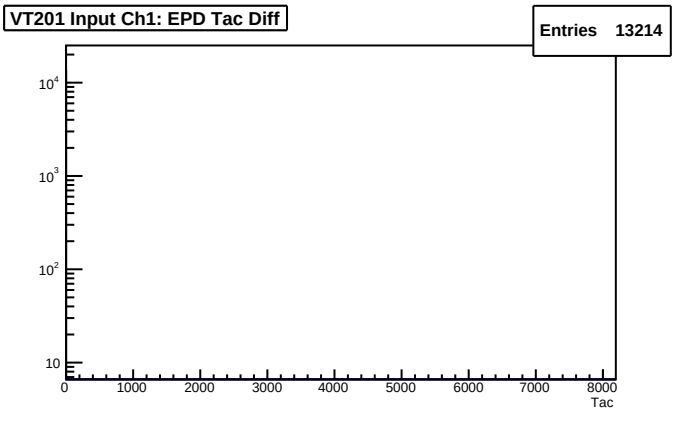

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

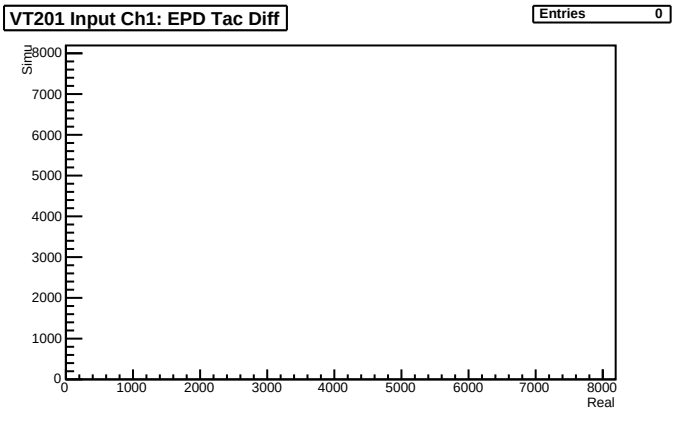

VT201 Input Ch0: BBC Small Tac Diff
Entries 13214
Tac

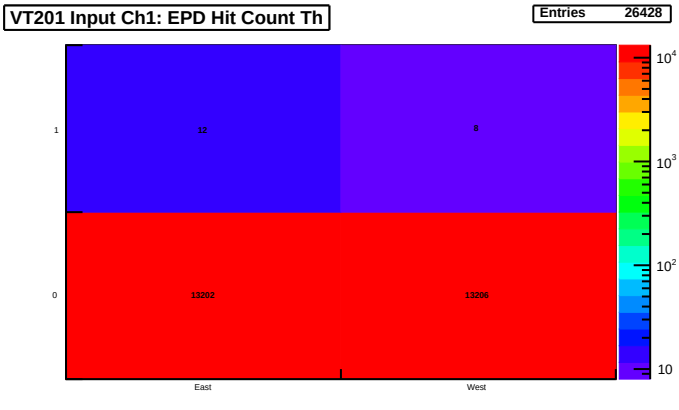

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

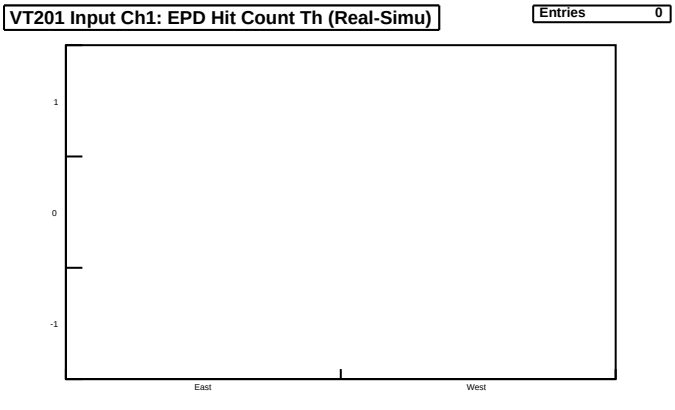

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 26428
27
5
13187
13209
East
West

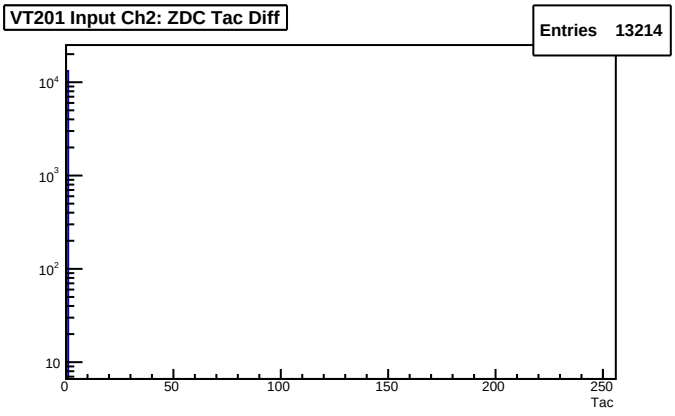

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

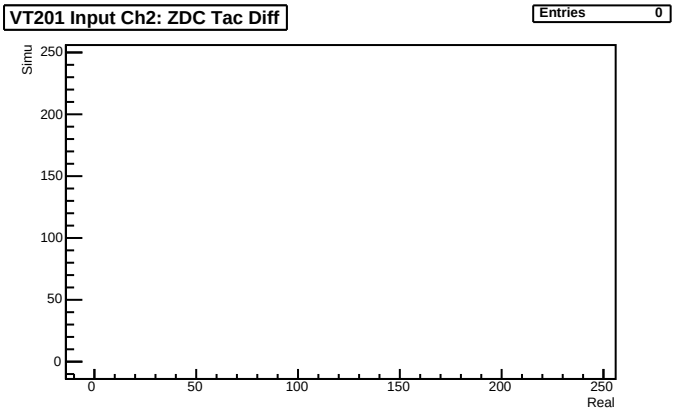

VT201 Input Ch1: EPD Tac Diff
Entries 13214
Tac

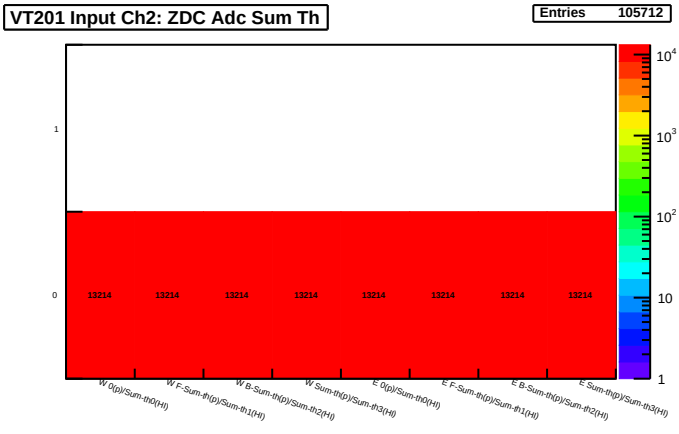

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

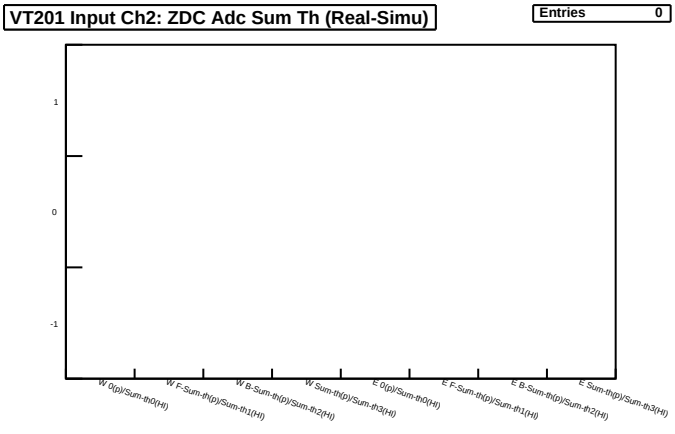

VT201 Input Ch1: EPD Hit Count Th
Entries 26428
12
8
13202
13206
East
West

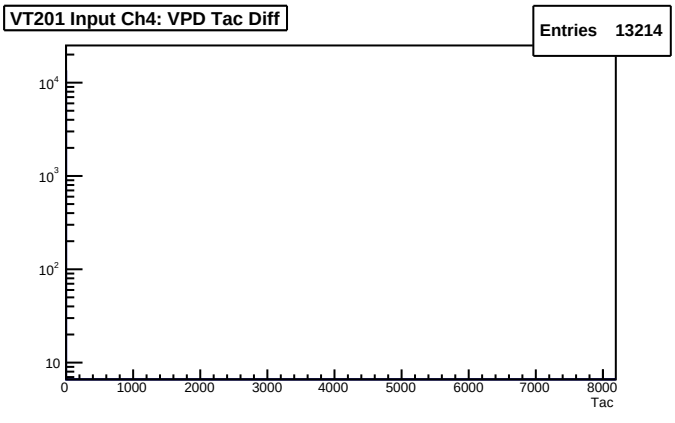

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

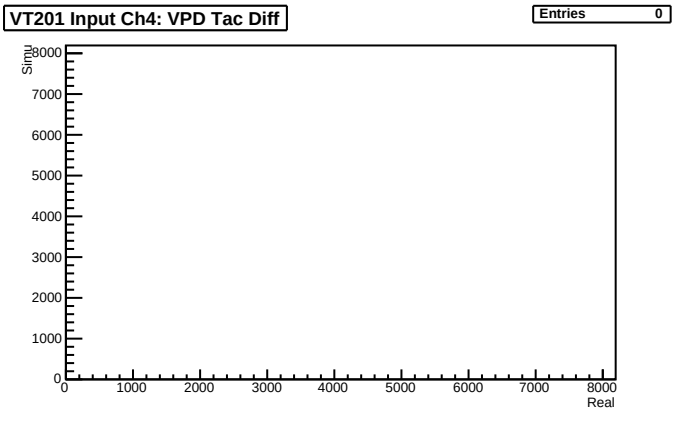

VT201 Input Ch2: ZDC Tac Diff
Entries 13214
Tac

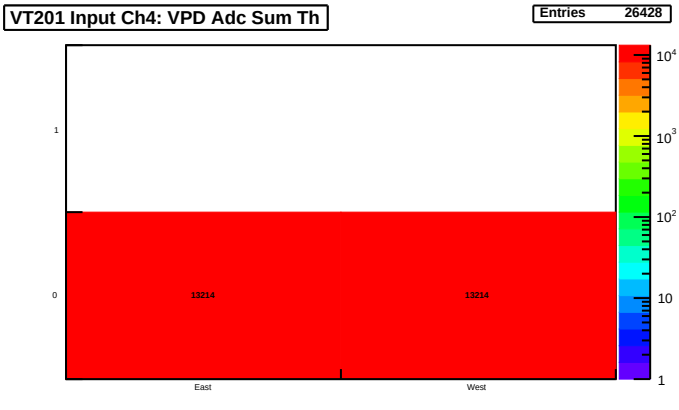

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

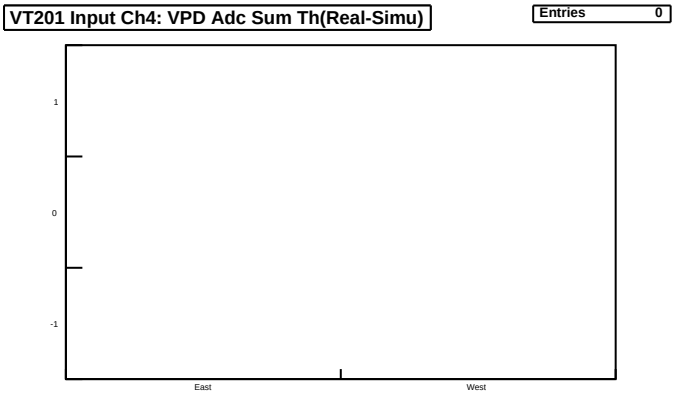

VT201 Input Ch2: ZDC Adc Sum Th
Entries 105712
13214
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

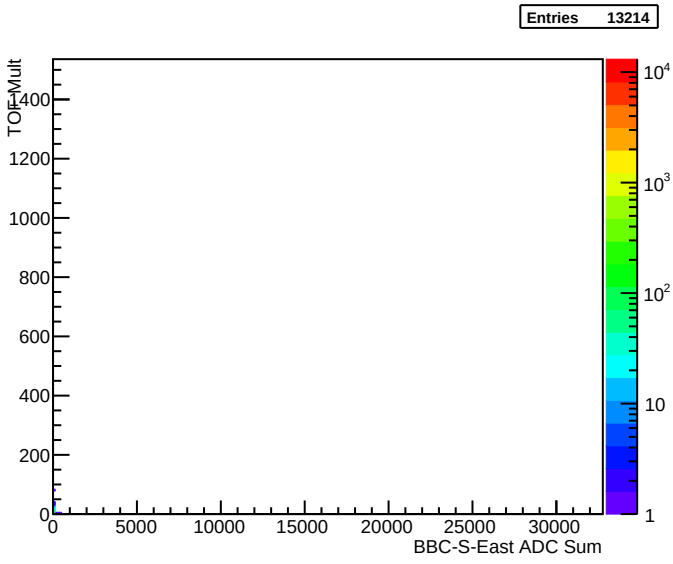

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(Hl)
W F-Sum-th(p)/Sum-th1(Hl)
W B-Sum-th(p)/Sum-th2(Hl)
W Sum-th(p)/Sum-th3(Hl)
E 0(p)/Sum-th0(Hl)
E F-Sum-th(p)/Sum-th1(Hl)
E B-Sum-th(p)/Sum-th2(Hl)
E Sum-th(p)/Sum-th3(Hl)

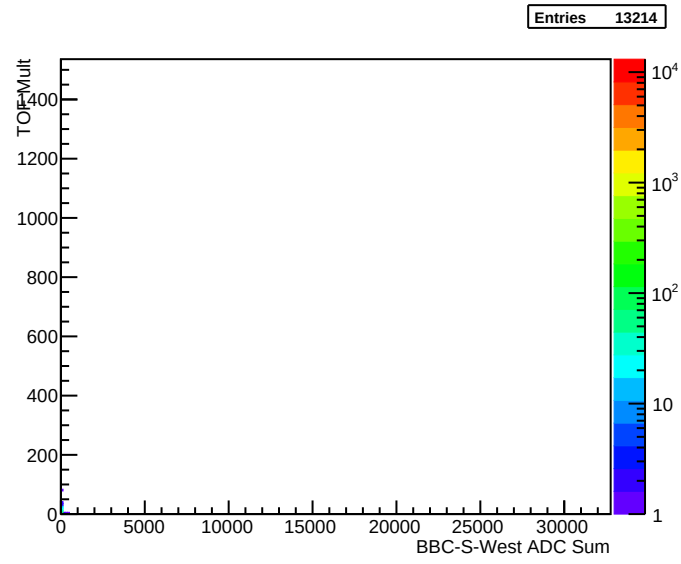

VT201 Input Ch4: VPD Tac Diff
Entries 13214
Tac

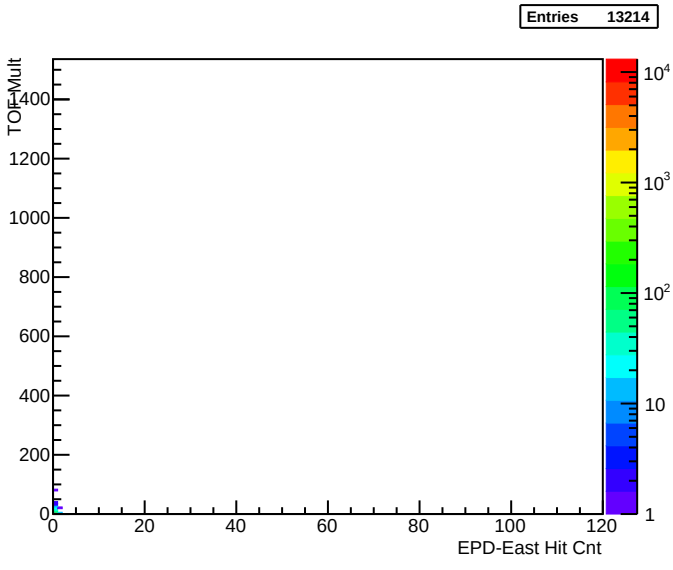

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

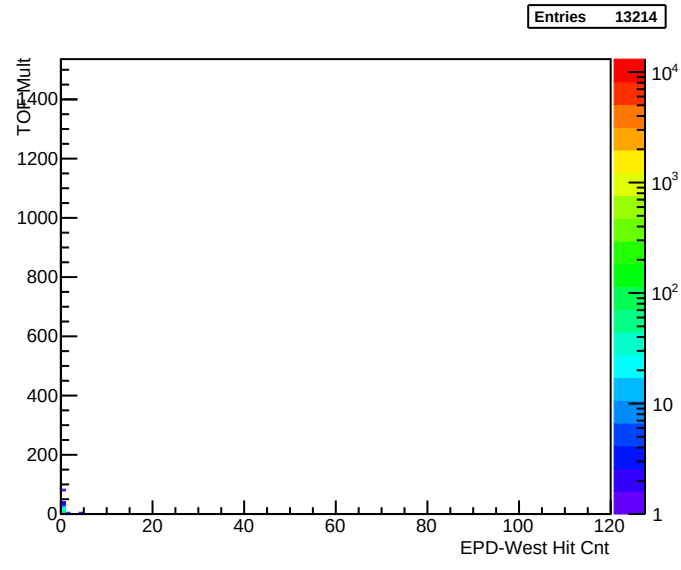

VT201 Input Ch4: VPD Adc Sum Th
Entries 26428
13214
13214
East
West

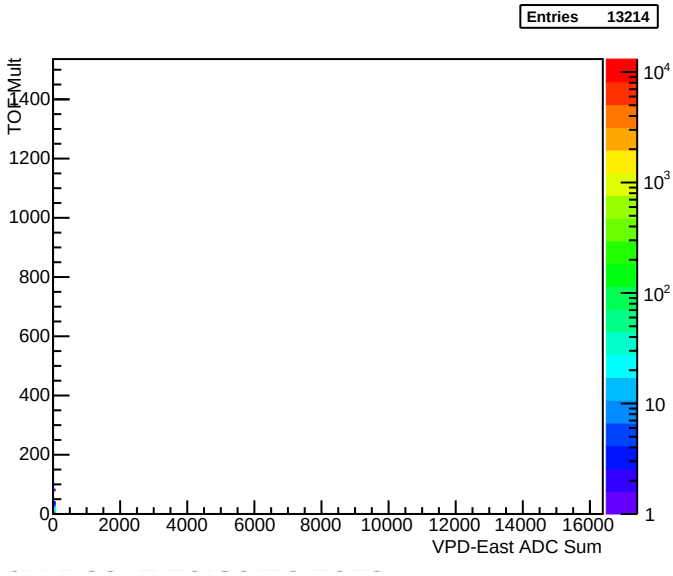

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

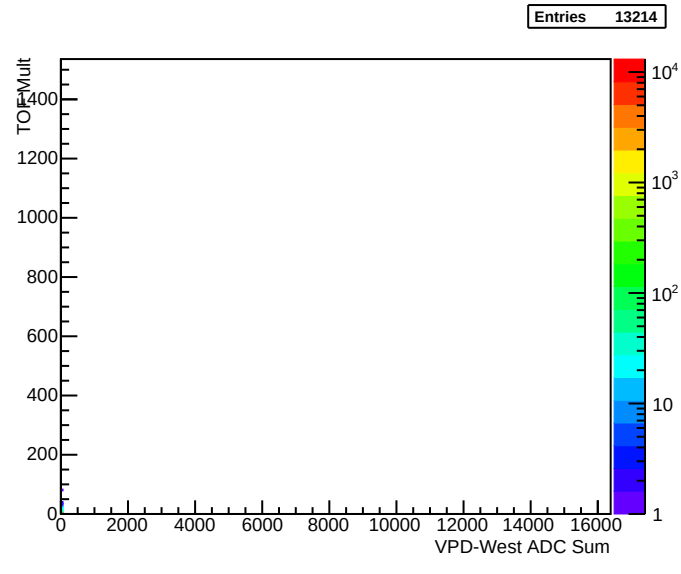

Entries 13214
TOF-Mult
BBC-S-East ADC Sum

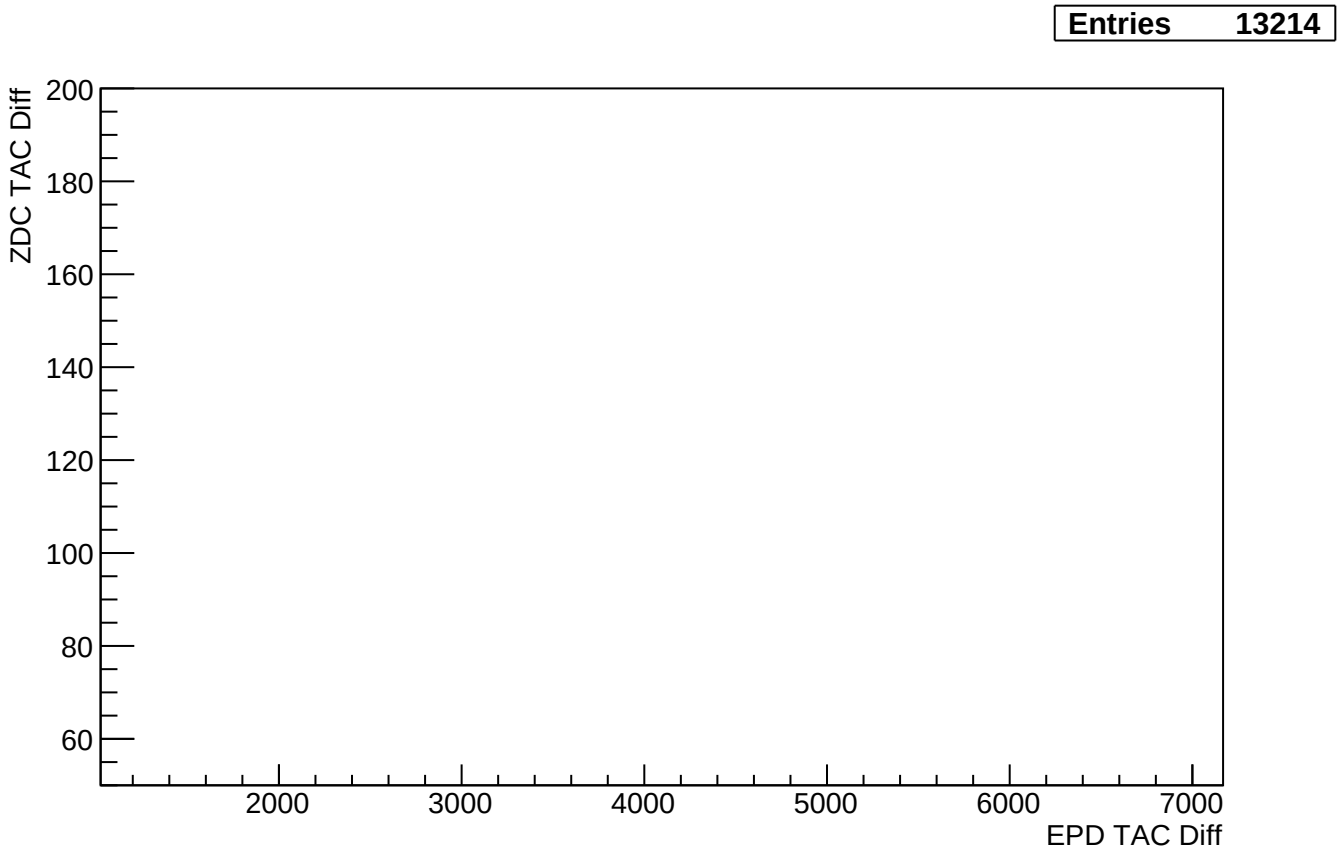

Entries 13214
TOF-Mult
BBC-S-West ADC Sum

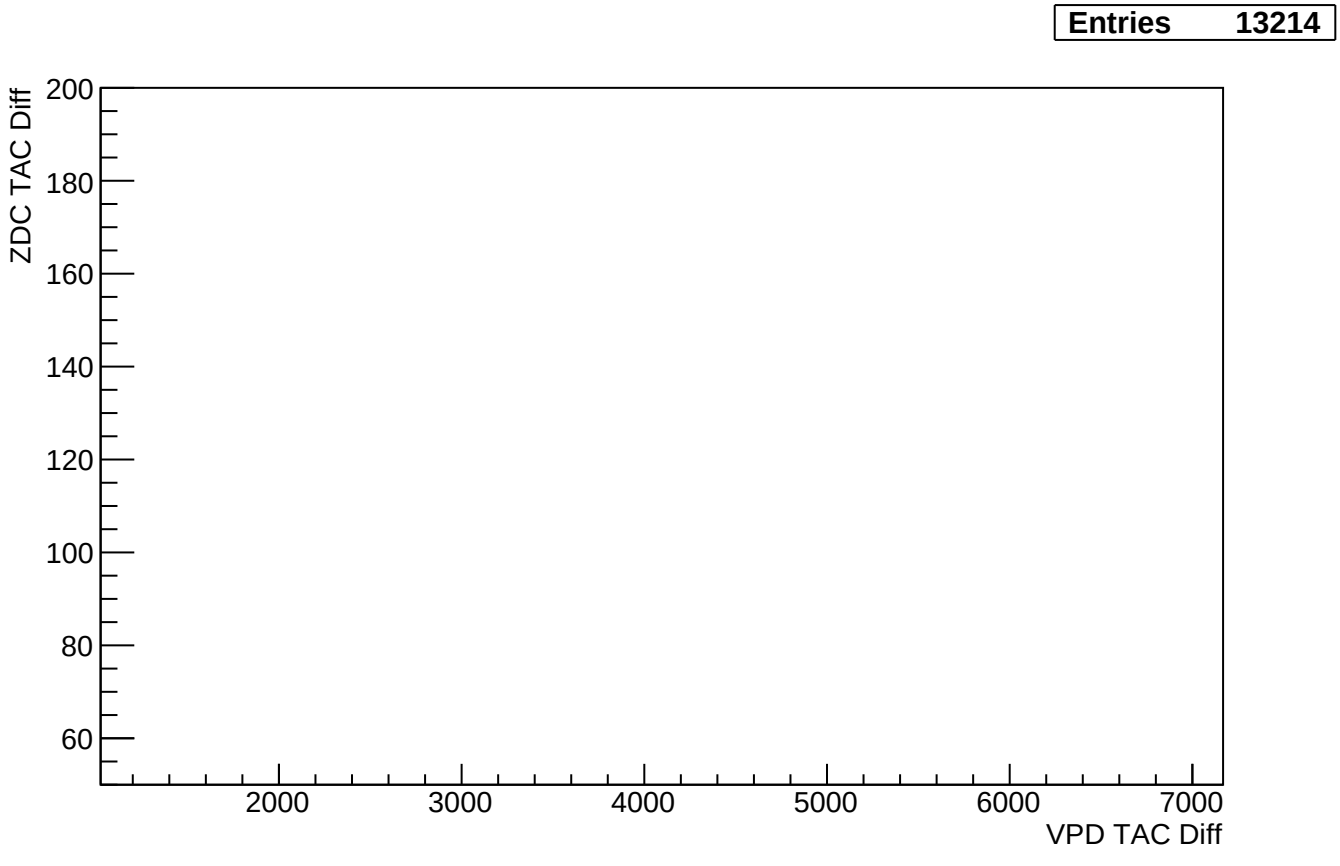

Entries 13214
TOF-Mult
EPD-East Hit Cnt

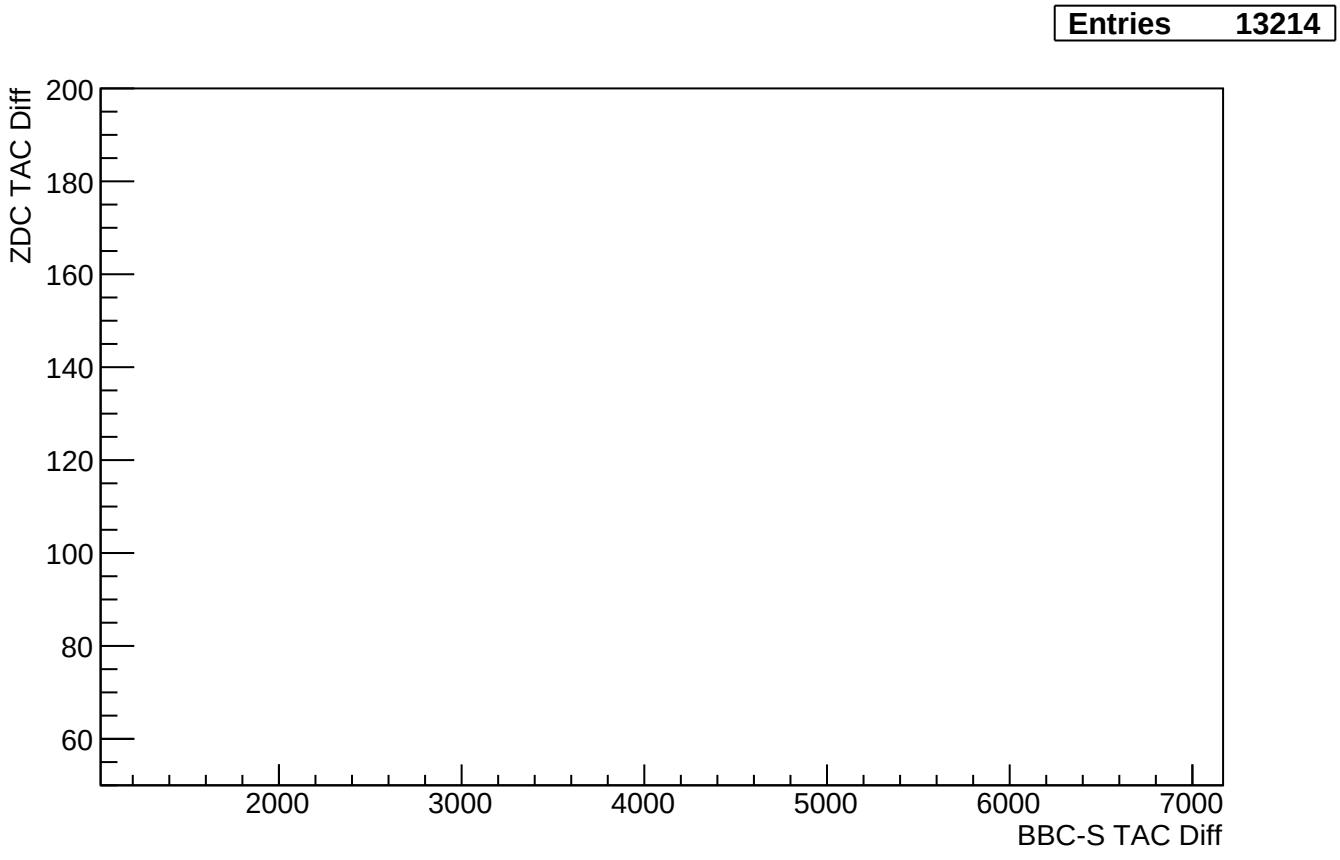

Entries 13214
TOF-Mult
EPD-West Hit Cnt

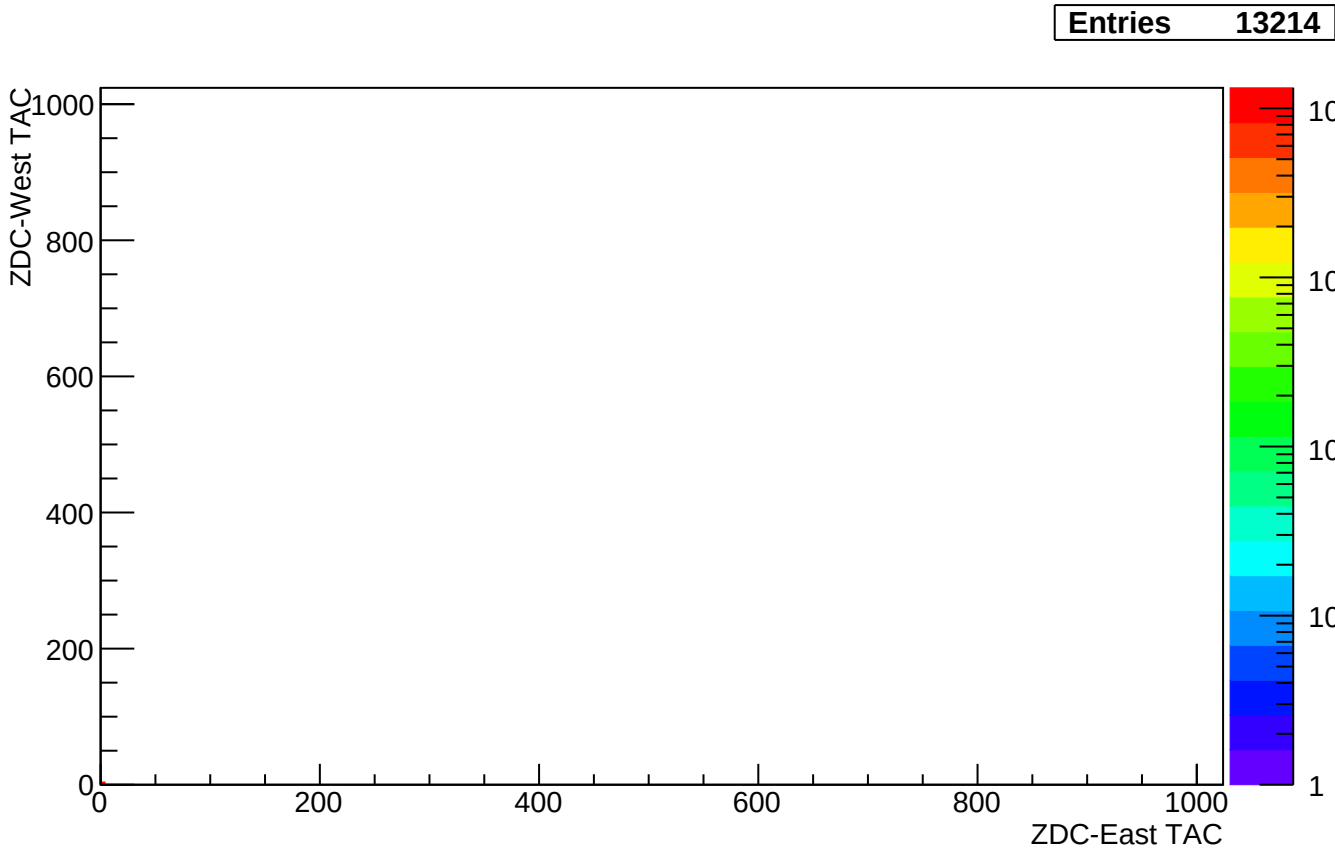

Entries 13214
TOF-Mult
VPD-East ADC Sum

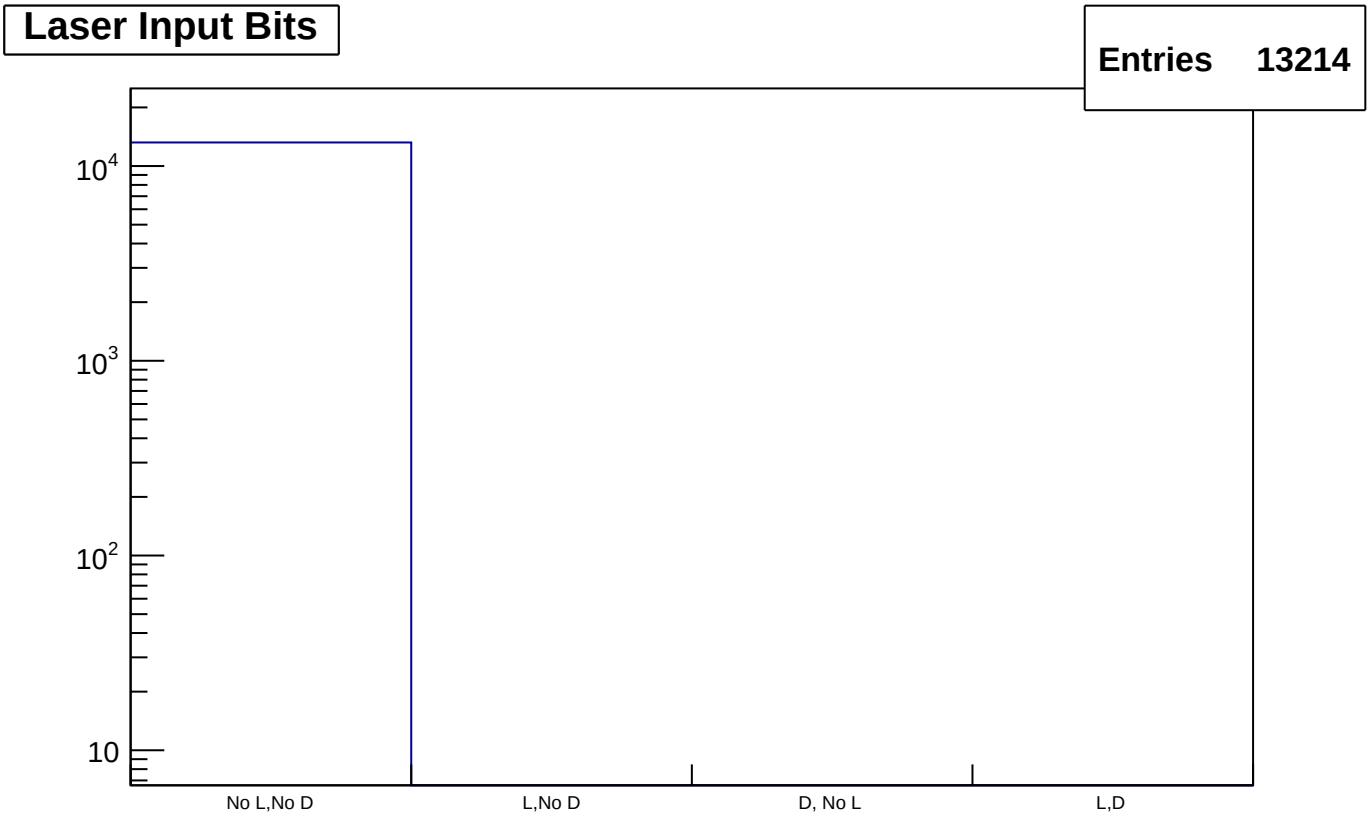

Entries 13214
TOF-Mult
VPD-West ADC Sum

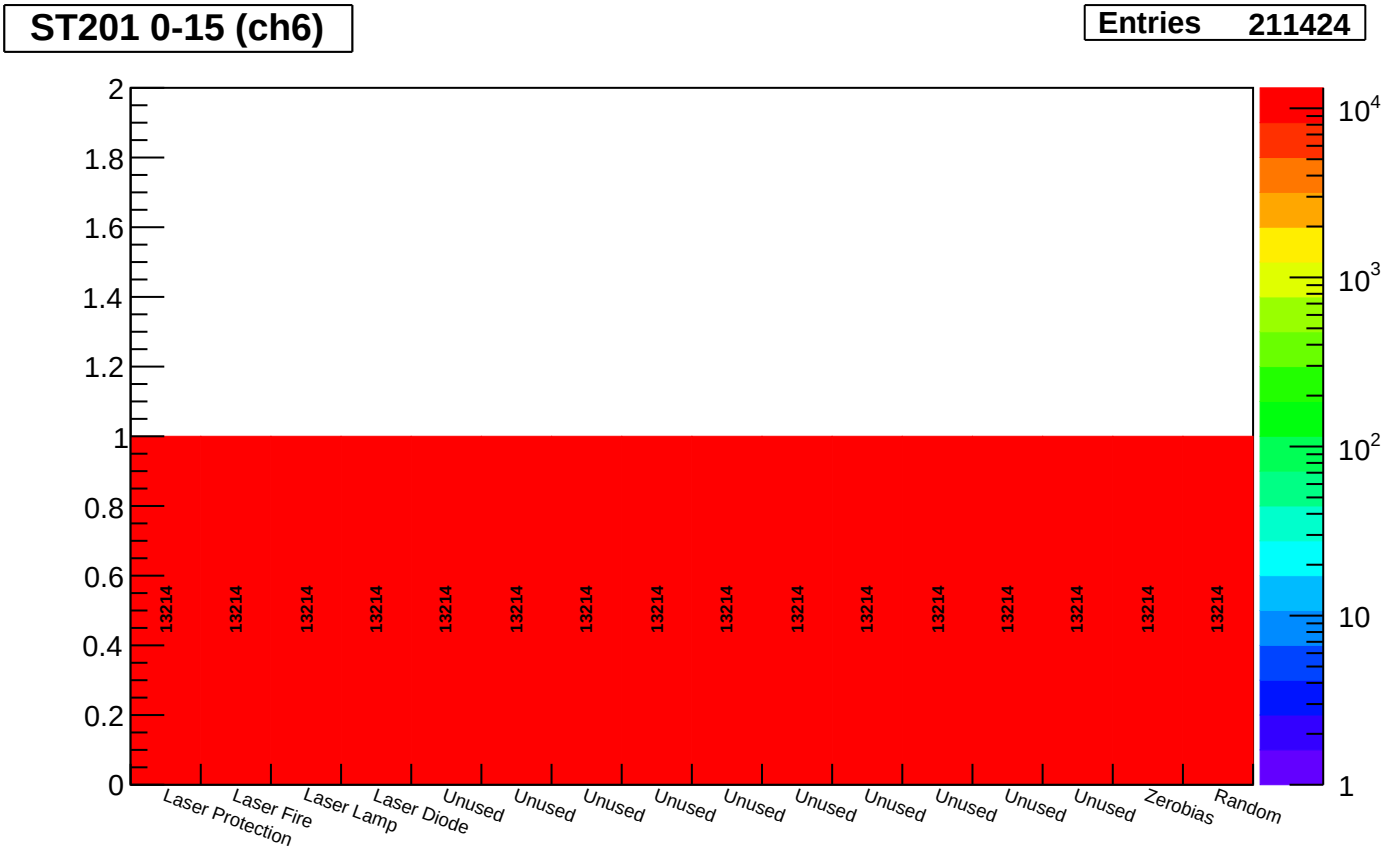

Entries 13214
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff

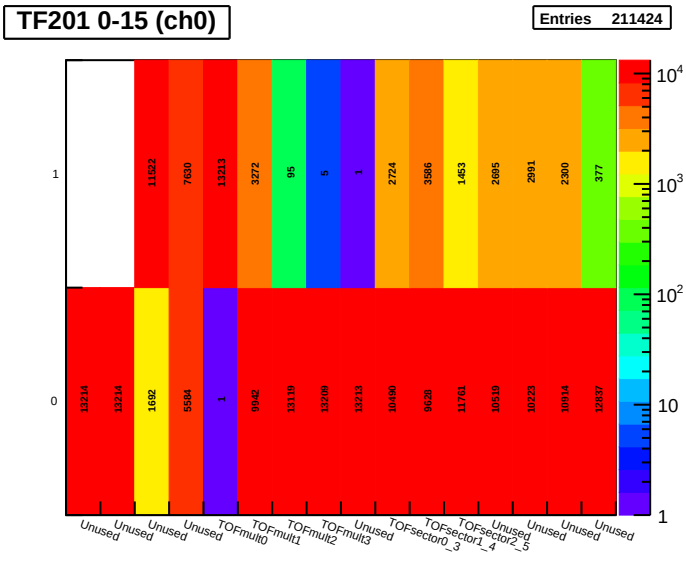

Entries 13214
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff

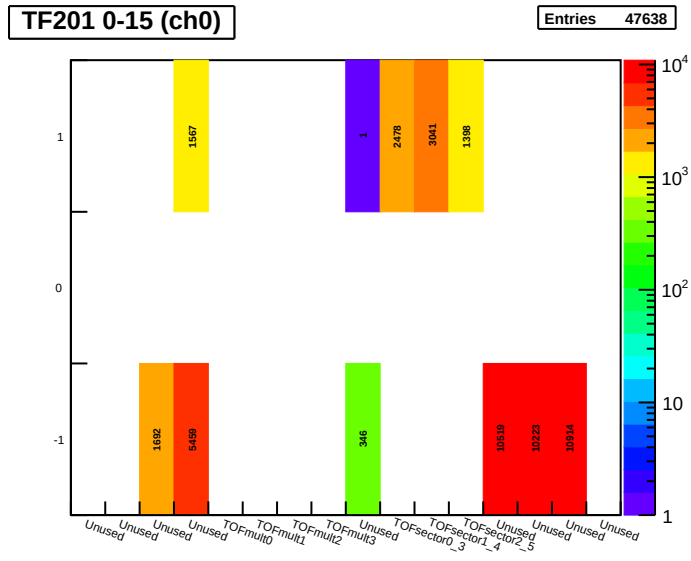

Entries 13214
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
BBC-S TAC Diff

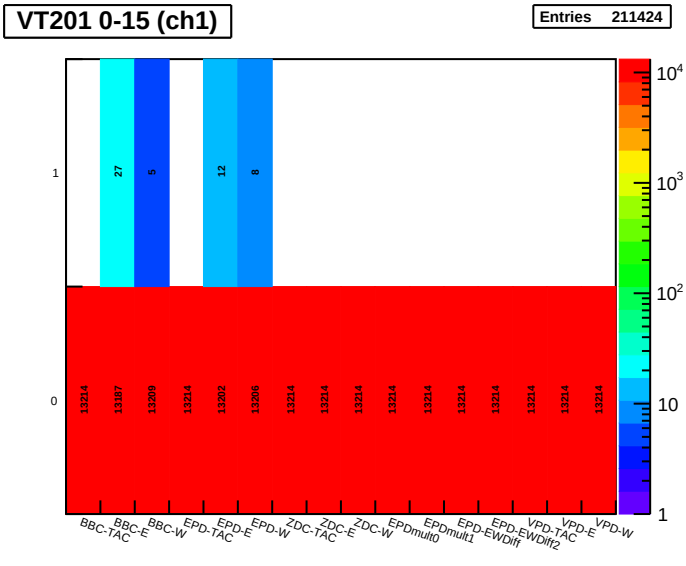

Entries 13214
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^4
10^3
10^2
10
1

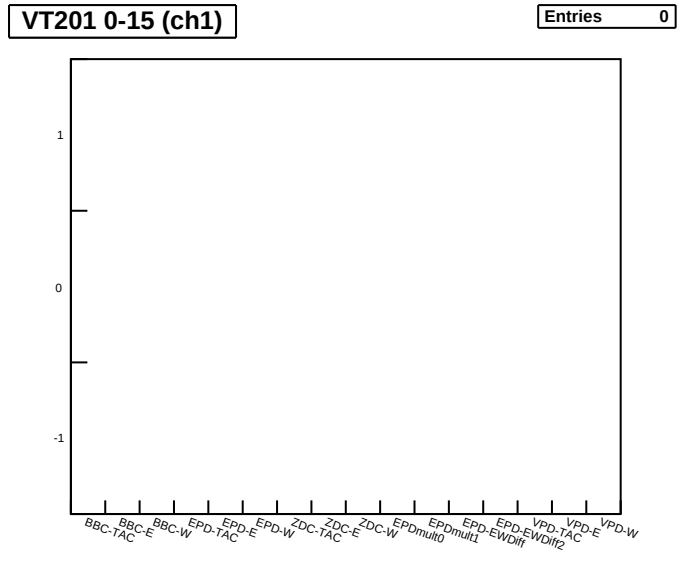

Laser Input Bits
Entries 13214
10^4
10^3
10^2
10
No L,No D
L,No D
D, No L
L,D

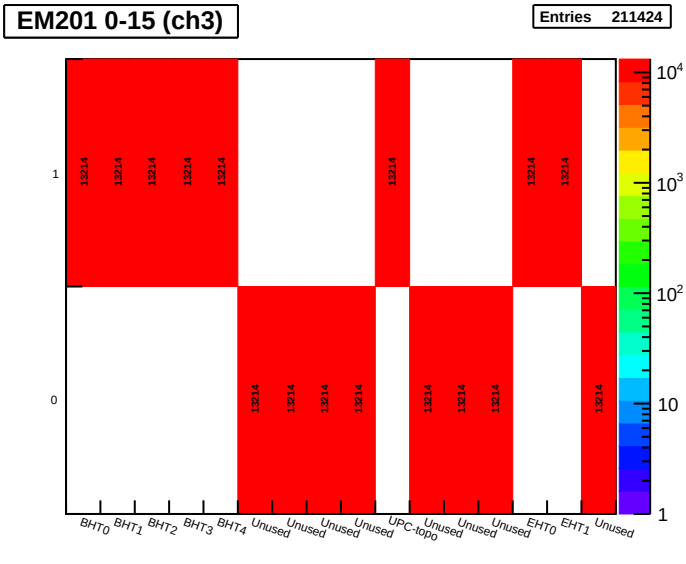

ST201 0-15 (ch6)
Entries 211424
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
13214
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random
10^4
10^3
10^2
10
1

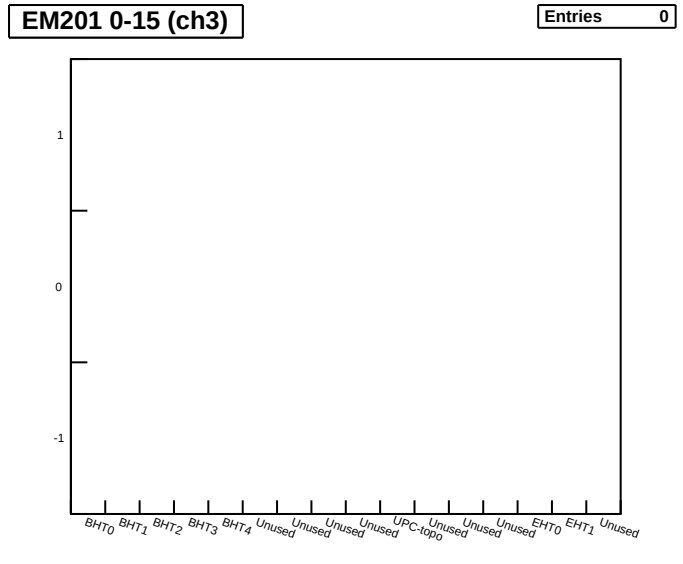

TF201 0-15 (ch0)
Entries 211424
1
0
11522 7630 13213 3272 95 5 1 2724 3586 1453 2695 2991 2300 377
13214 13214 1692 5584 1 9942 13119 13209 13213 10490 9628 11761 10519 10223 10914 12837
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused
10^4 10^3 10^2 10 1


TF201 0-15 (ch0)
Entries 47638
1
0
-1
1567 1 2478 3041 1398
1692 5459 346 10519 10223 10914
Unused Unused Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 Unused TOFsector0_3 TOFsector1_4 TOFsector2_5 Unused Unused Unused
10^4 10^3 10^2 10 1


VT201 0-15 (ch1)
Entries 211424
1
0
27 5 12 8
13214 13187 13209 13214 13202 13206 13214 13214 13214 13214 13214 13214 13214 13214 13214
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W
10^4 10^3 10^2 10 1


VT201 0-15 (ch1)
Entries 0
1
0
-1
BBC-TAC BBC-E BBC-W EPD-TAC EPD-E EPD-W ZDC-TAC ZDC-E ZDC-W EPDmult0 EPDmult1 EPD-EWDiff EPD-EWDiff2 VPD-TAC VPD-E VPD-W


EM201 0-15 (ch3)
Entries 211424
1
0
13214 13214 13214 13214 13214 13214 13214 13214 13214
13214 13214 13214 13214 13214 13214 13214 13214
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused
10^4 10^3 10^2 10 1


EM201 0-15 (ch3)
Entries 0
1
0
-1
BHT0 BHT1 BHT2 BHT3 BHT4 Unused Unused Unused Unused UPC-topo Unused Unused Unused EHT0 EHT1 Unused