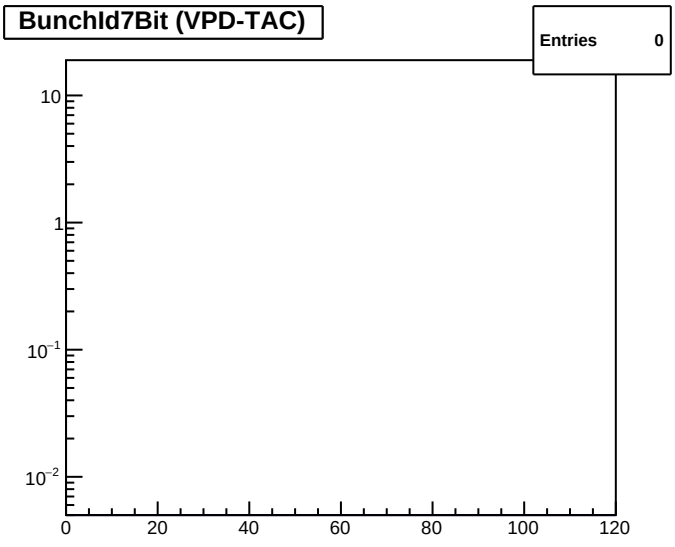
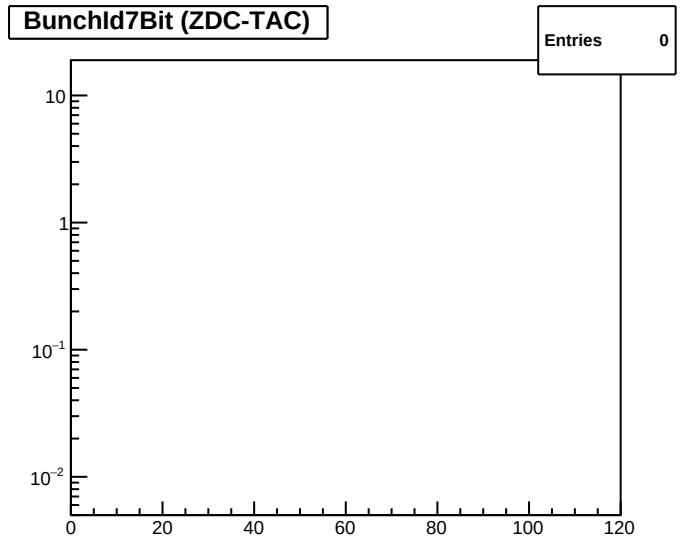
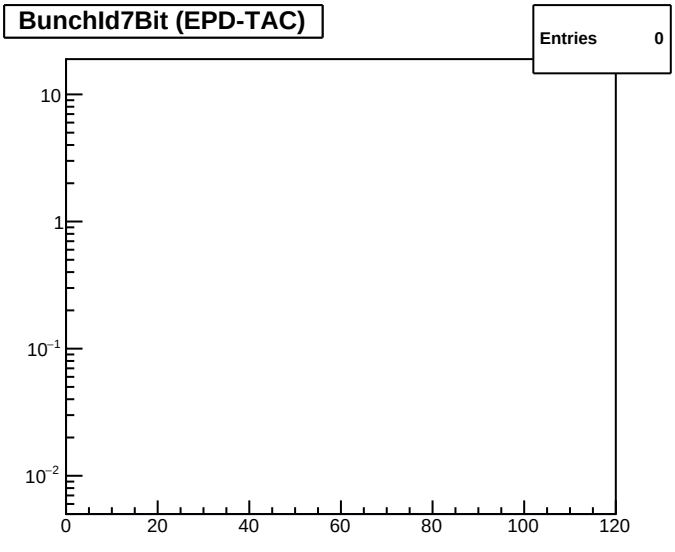
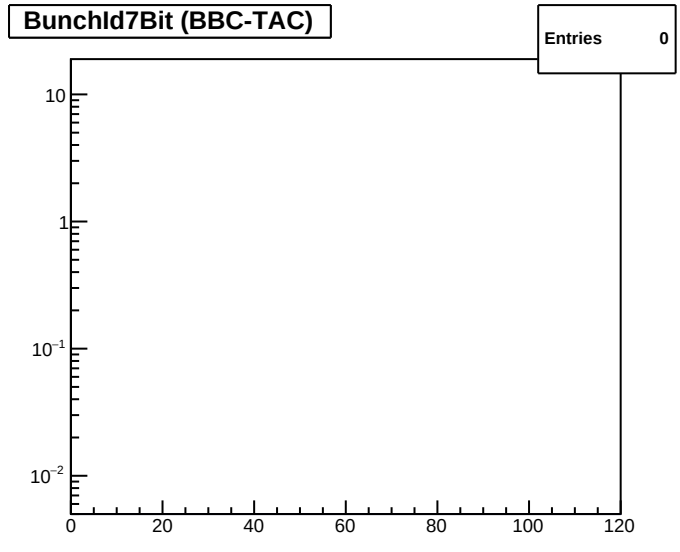
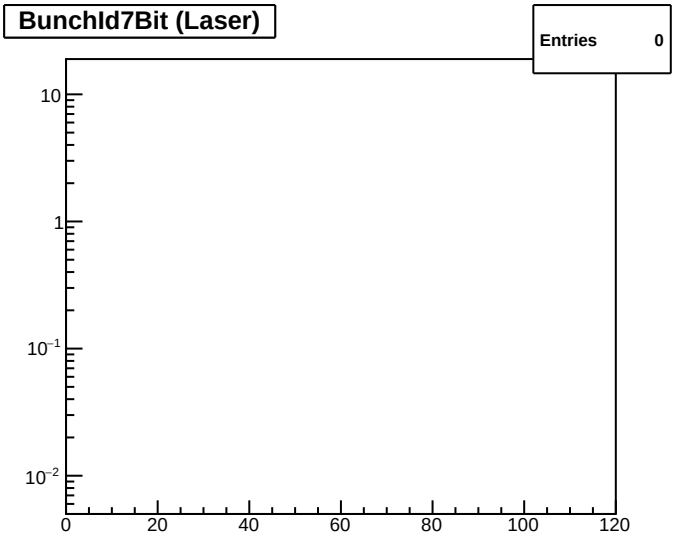
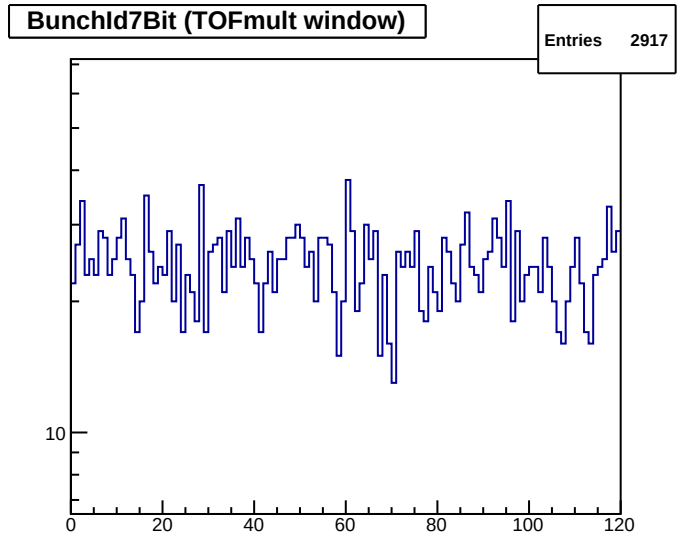

BunchId7Bit (VPD-TAC)
Entries 0

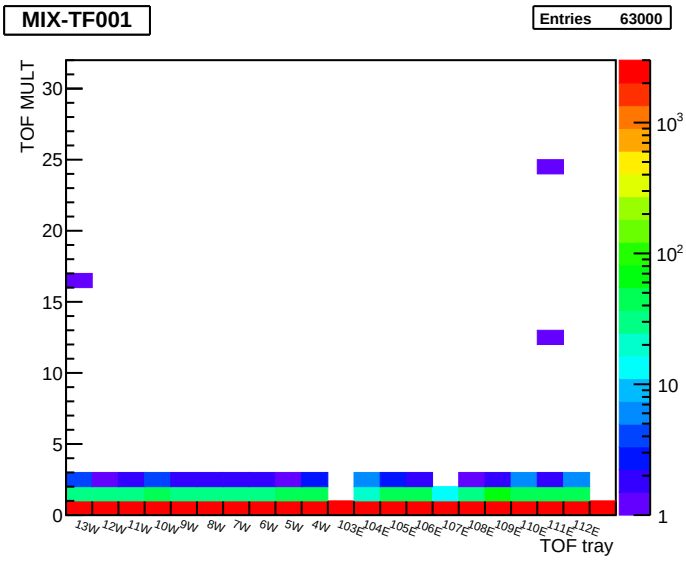

BunchId7Bit (ZDC-TAC)
Entries 0

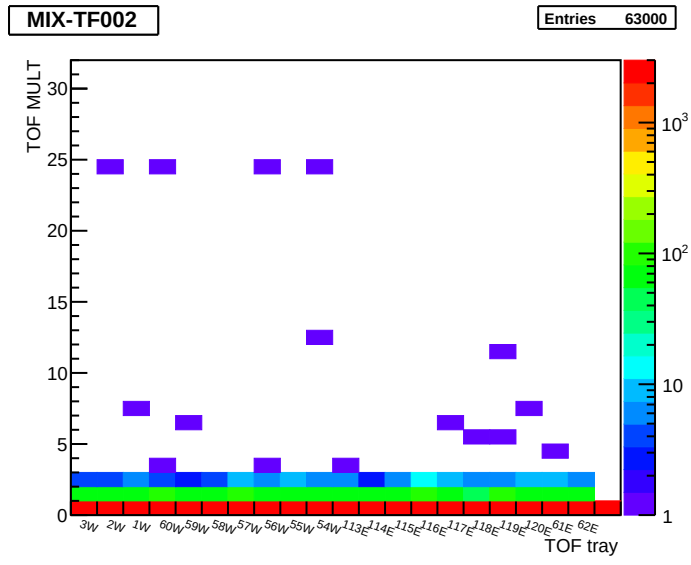

BunchId7Bit (EPD-TAC)
Entries 0

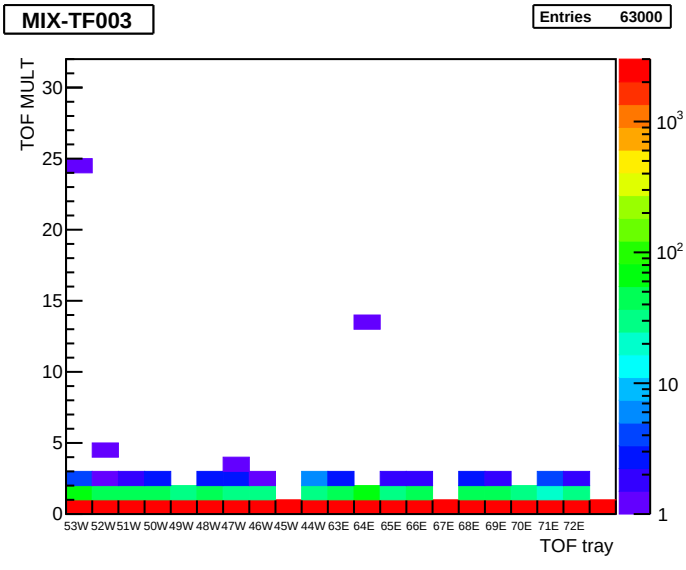

BunchId7Bit (BBC-TAC)
Entries 0

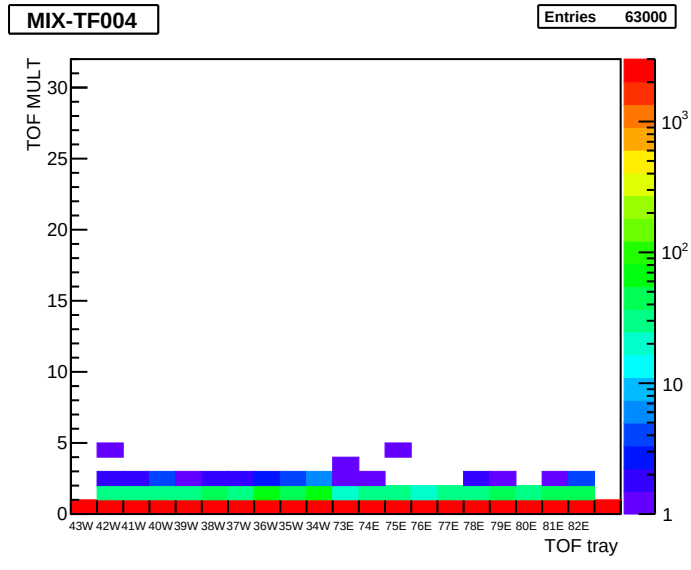

BunchId7Bit (Laser)
Entries 0

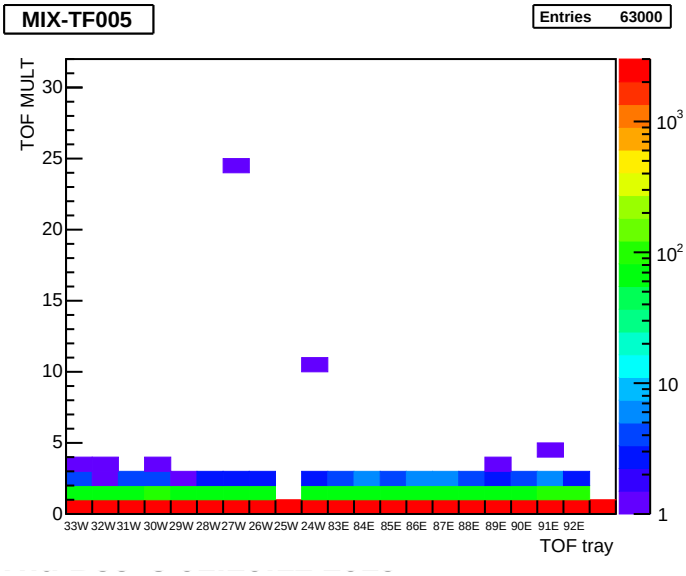

BunchId7Bit (TOFmult window)
Entries 2917

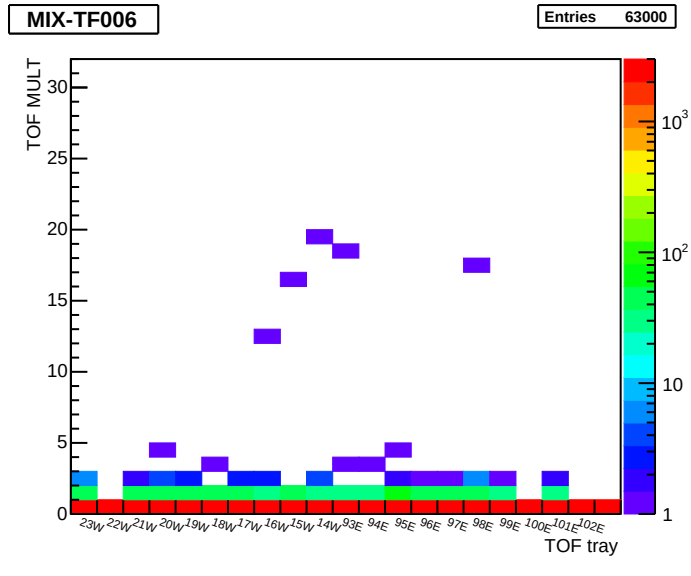

MIX-TF001
Entries 63000
TOF MULT
TOF tray

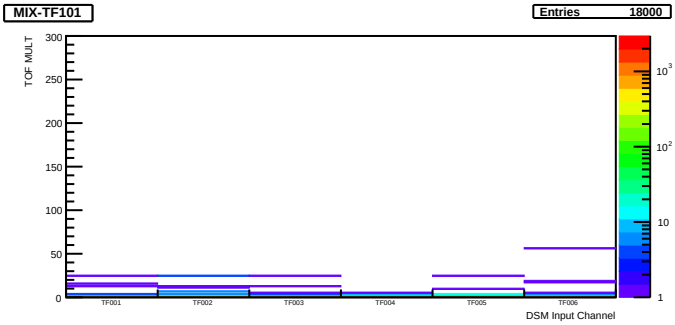

MIX-TF002
Entries 63000
TOF MULT
TOF tray

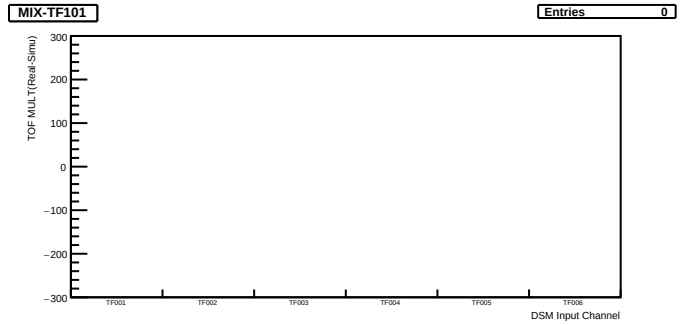

MIX-TF003
Entries 63000
TOF MULT
TOF tray

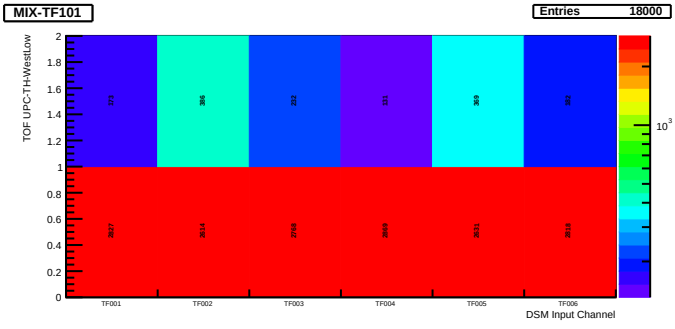

MIX-TF004
Entries 63000
TOF MULT
TOF tray

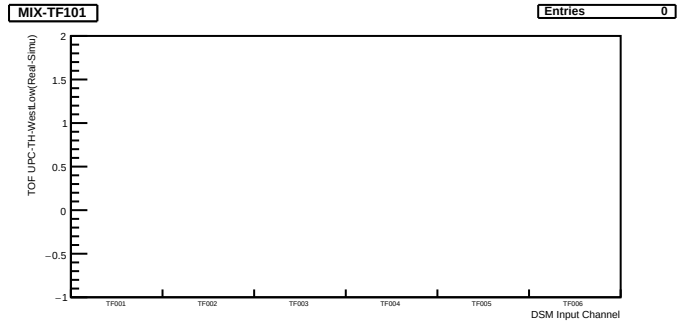

MIX-TF005
Entries 63000
TOF MULT
TOF tray

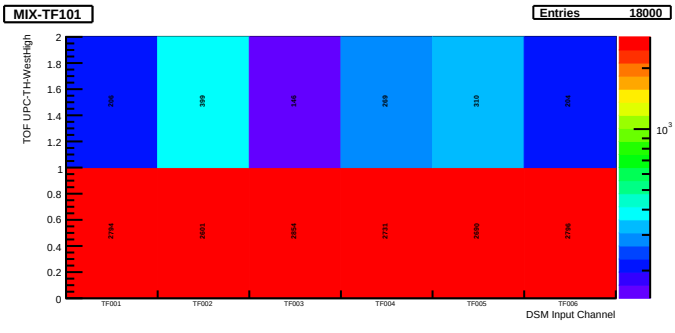

MIX-TF006
Entries 63000
TOF MULT
TOF tray

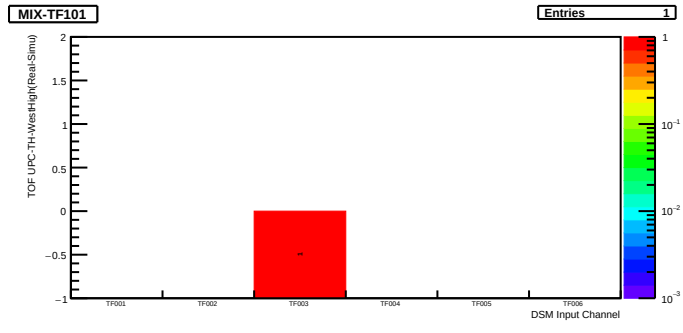

MIX-TF101
Entries 18000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

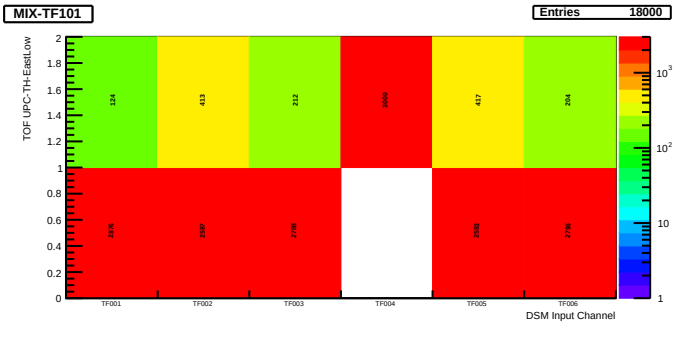

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

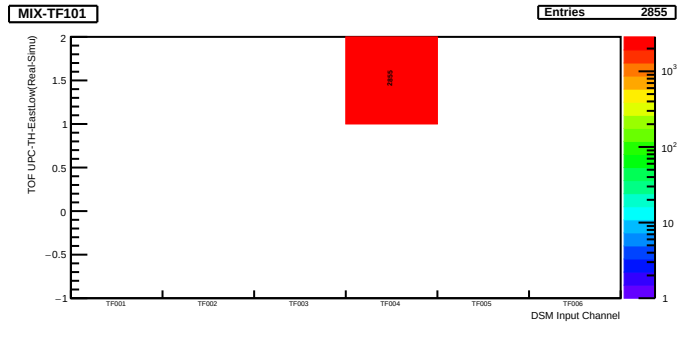

MIX-TF101
Entries 18000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

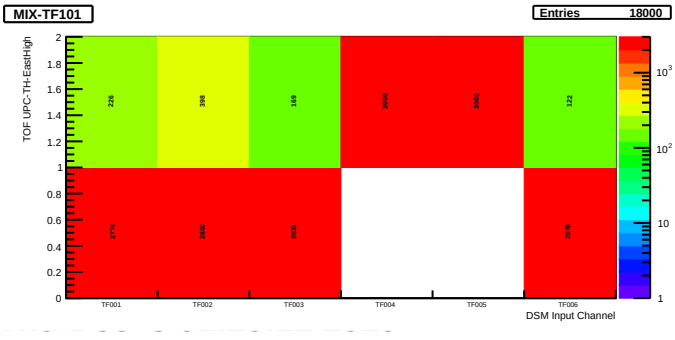

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

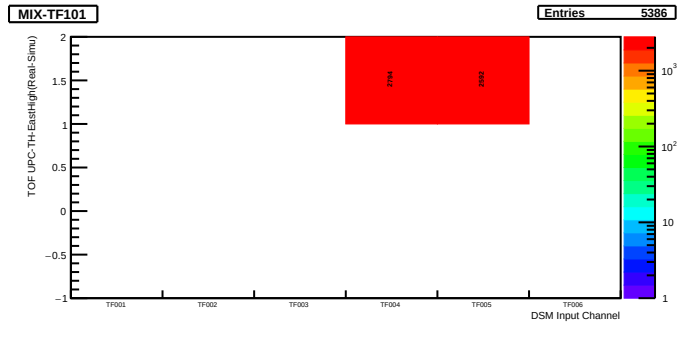

MIX-TF101
Entries 18000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

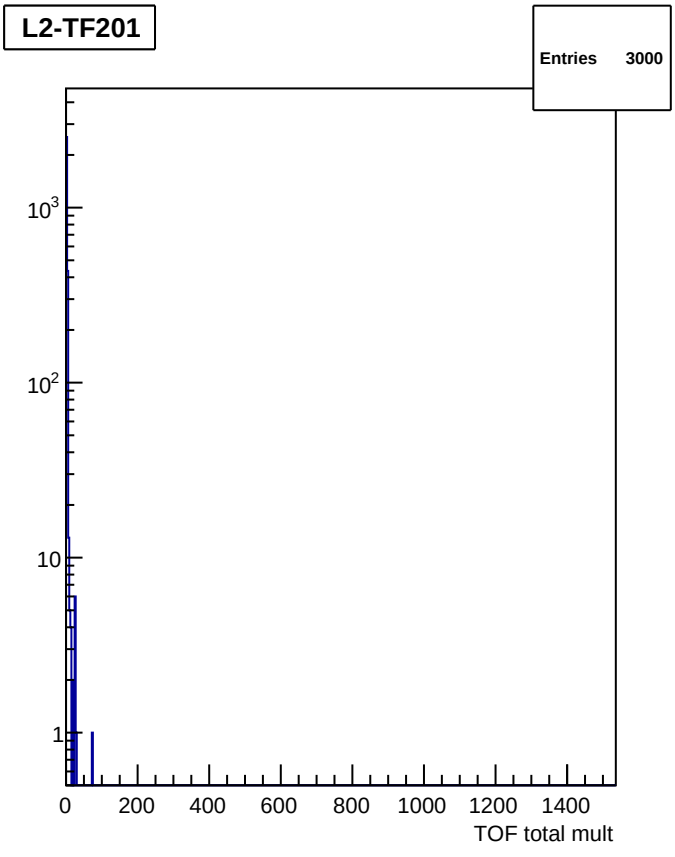

MIX-TF101
Entries 1
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

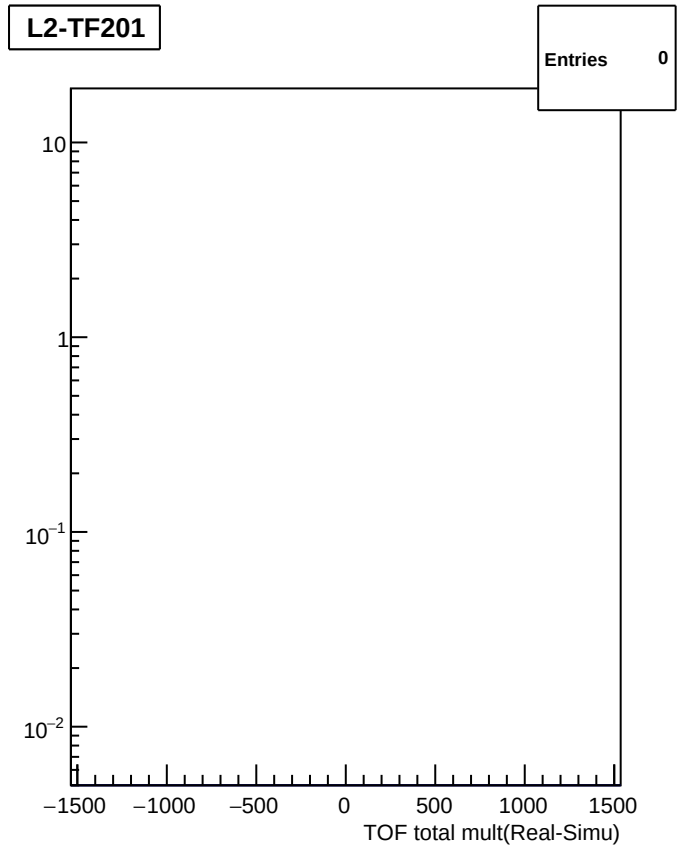

MIX-TF101
Entries 18000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

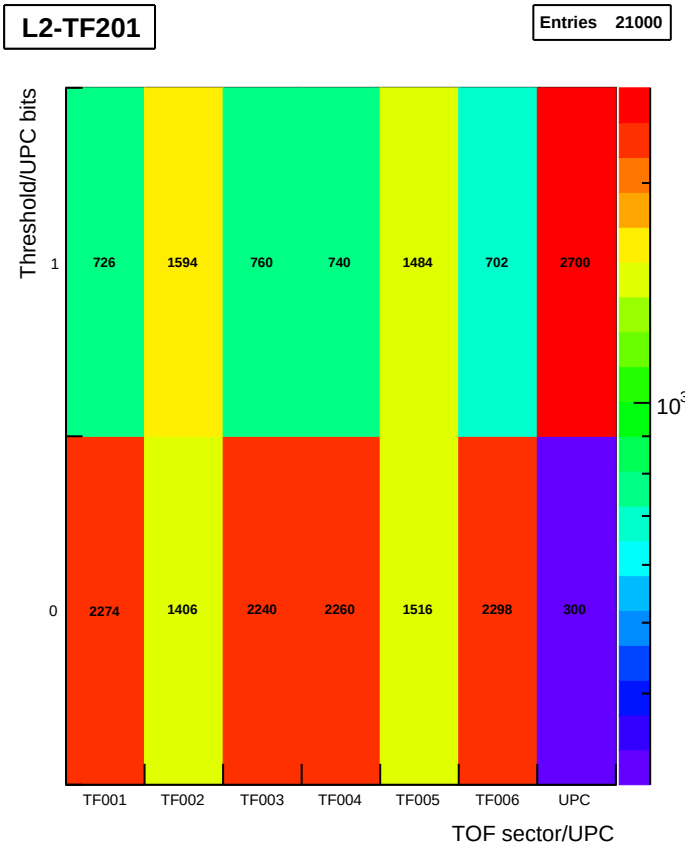

MIX-TF101
Entries 2855
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

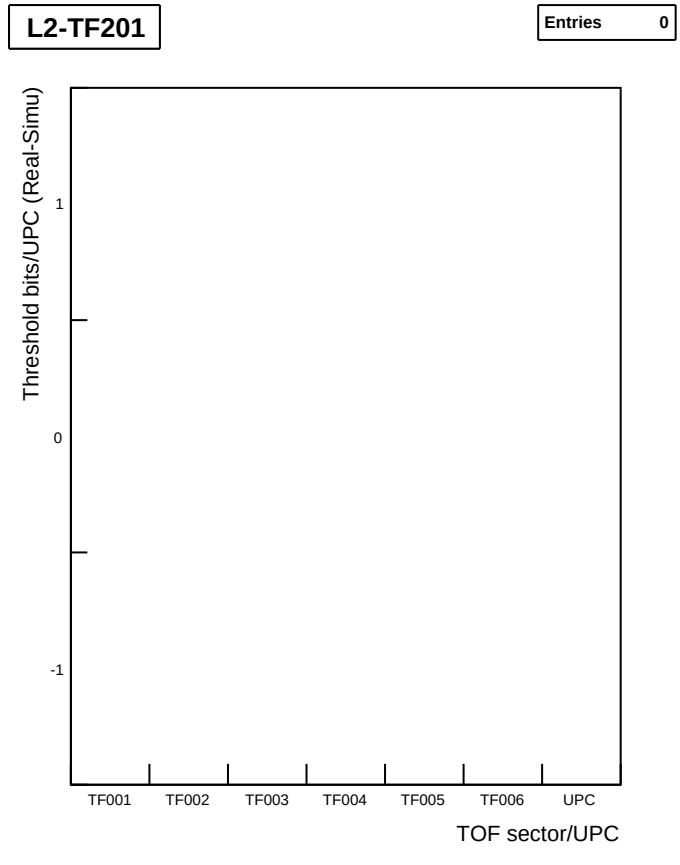

MIX-TF101
Entries 18000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

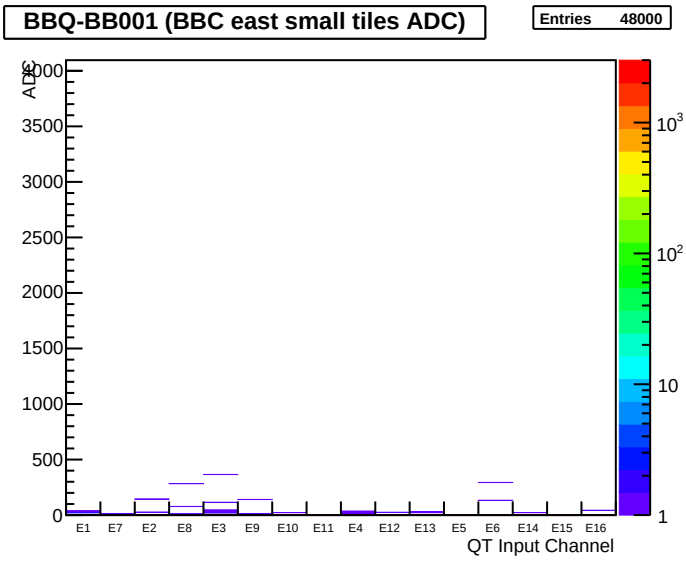

MIX-TF101
Entries 5386
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

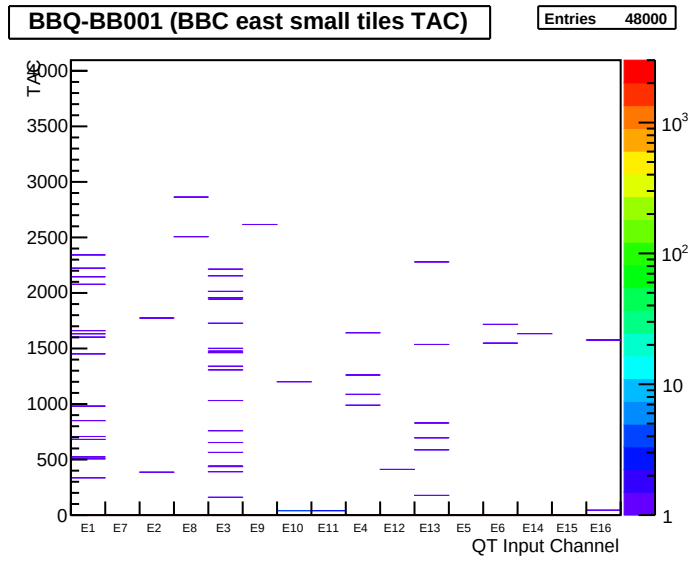

L2-TF201
Entries 3000
TOF total mult

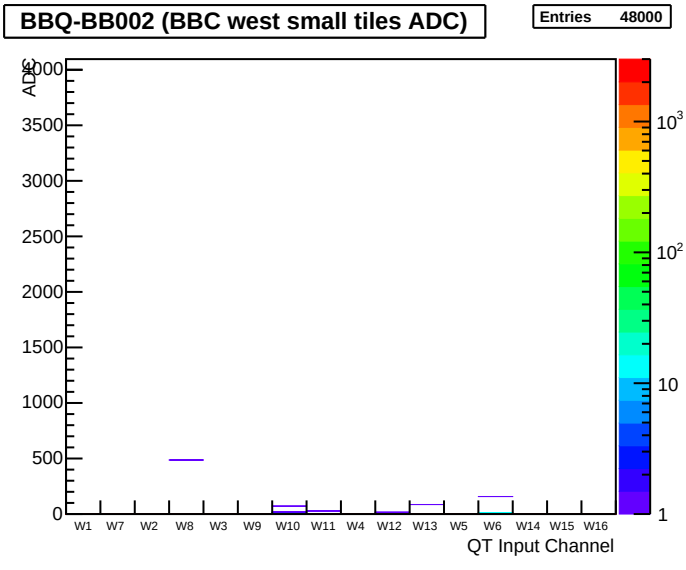

L2-TF201
Entries 0
TOF total mult(Real-Simu)

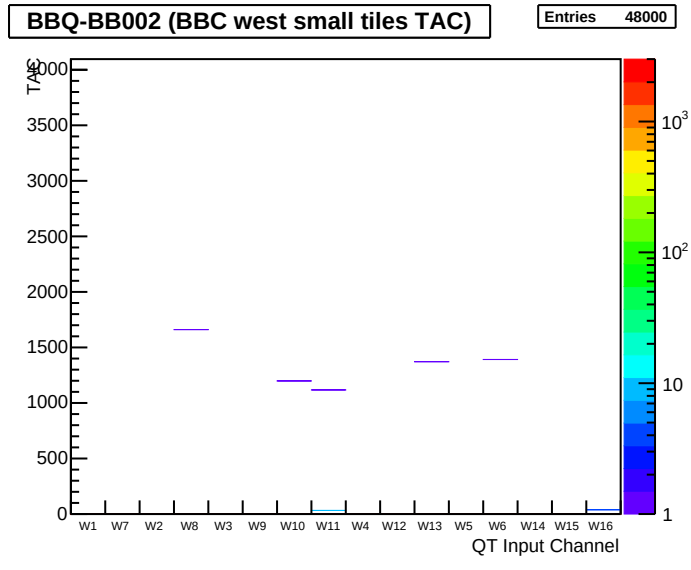

L2-TF201
Entries 21000
Threshold/UPC bits
726
1594
760
740
1484
702
2700
2274
1406
2240
2260
1516
2298
300
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

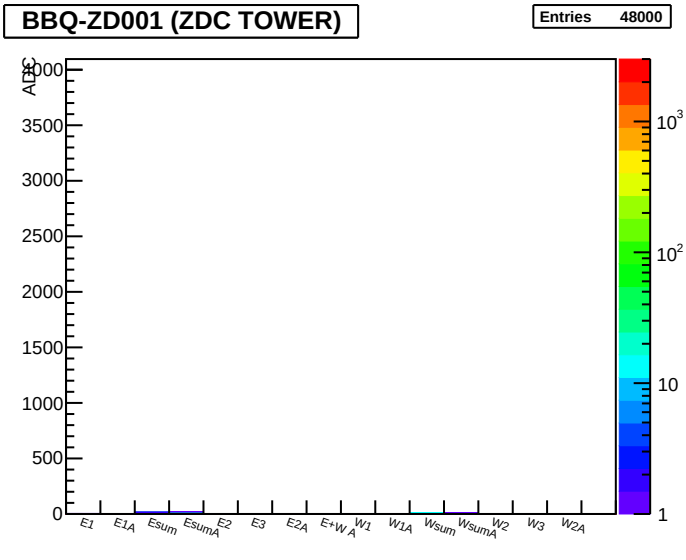

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

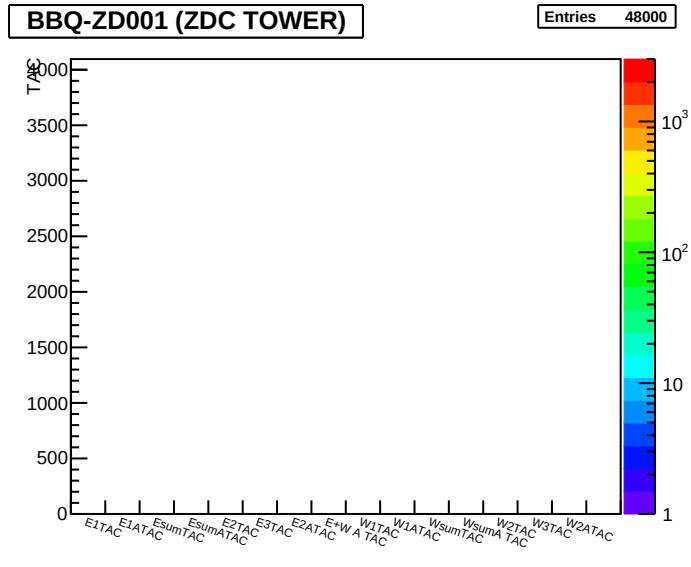

BBQ-BB001 (BBC east small tiles ADC)
Entries 48000
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

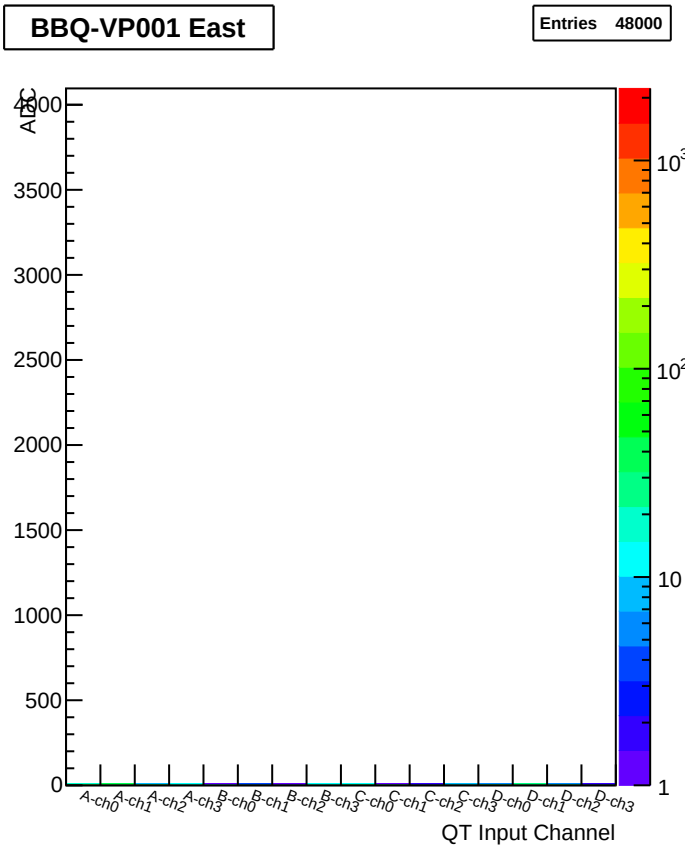

BBQ-BB001 (BBC east small tiles TAC)
Entries 48000
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

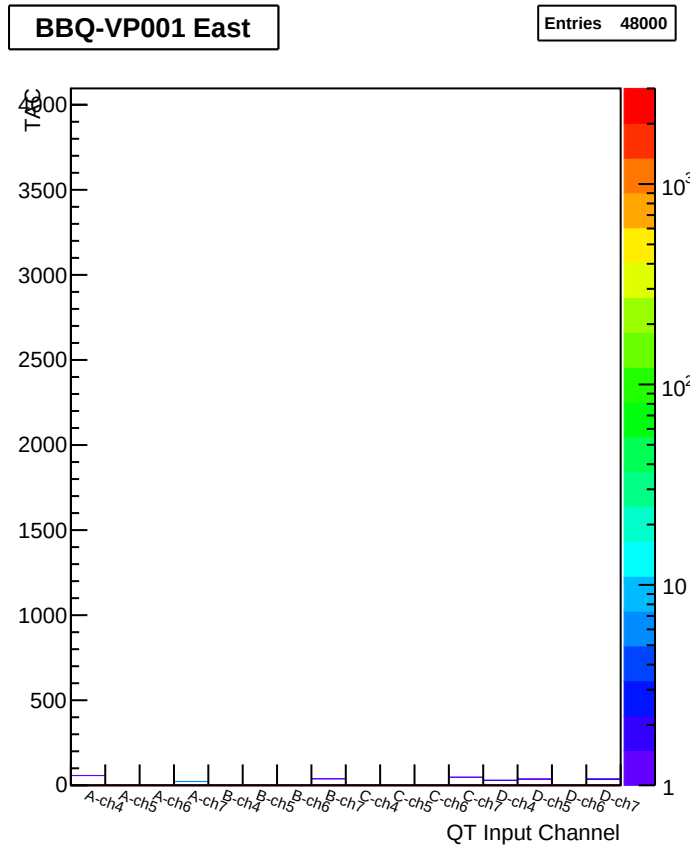

BBQ-BB002 (BBC west small tiles ADC)
Entries 48000
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

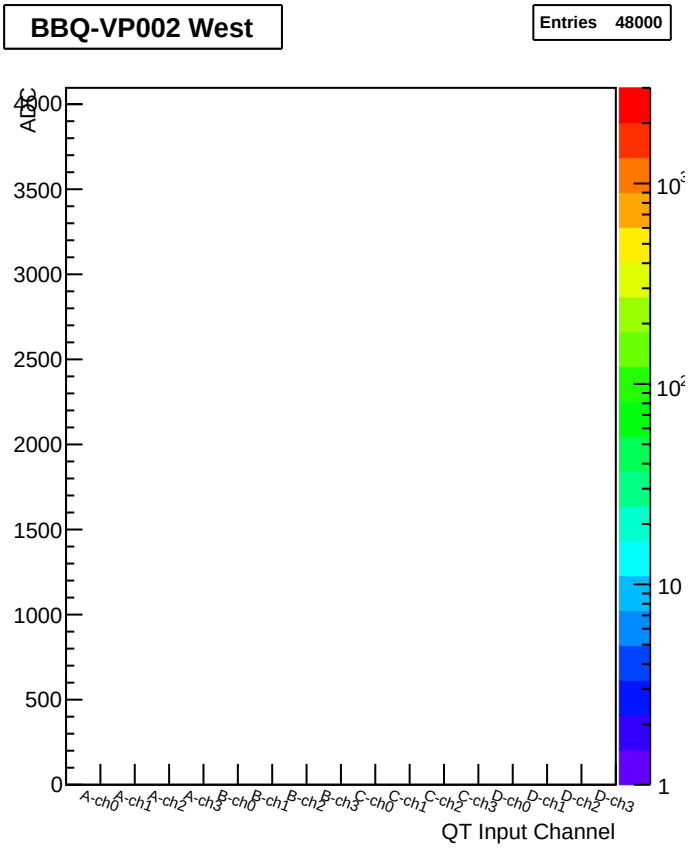

BBQ-BB002 (BBC west small tiles TAC)
Entries 48000
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

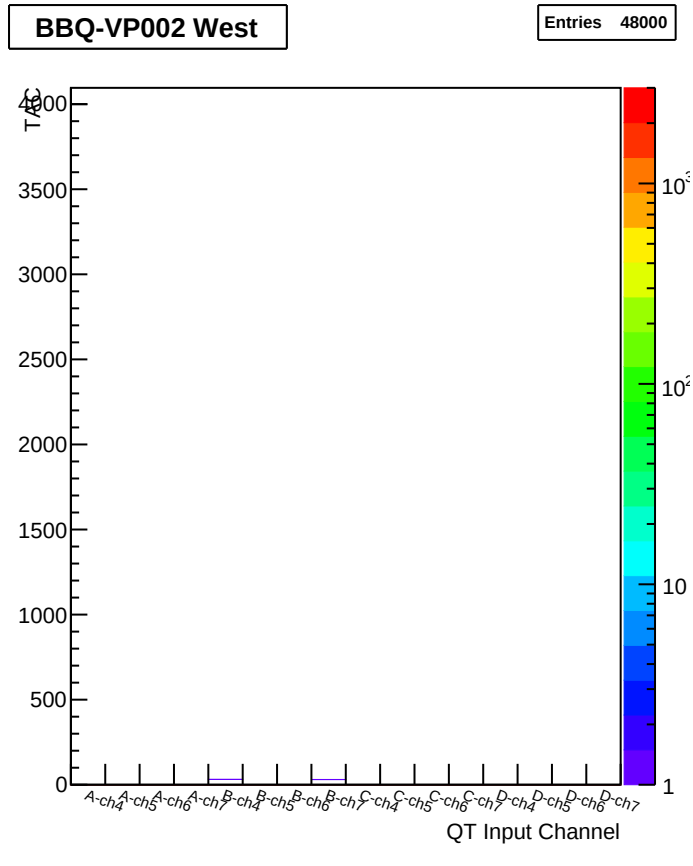

BBQ-ZD001 (ZDC TOWER)
Entries 48000
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

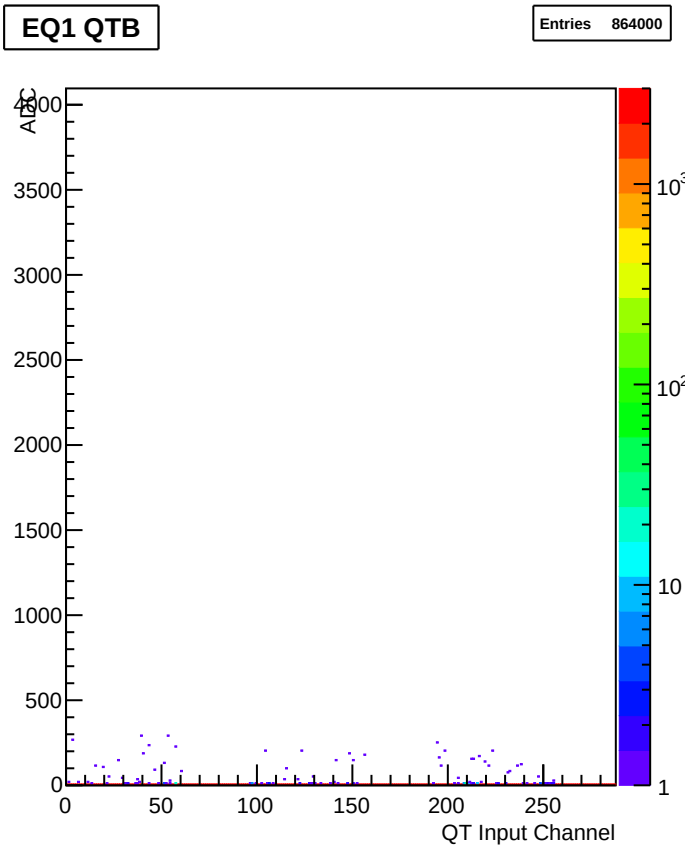

BBQ-ZD001 (ZDC TOWER)
Entries 48000
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

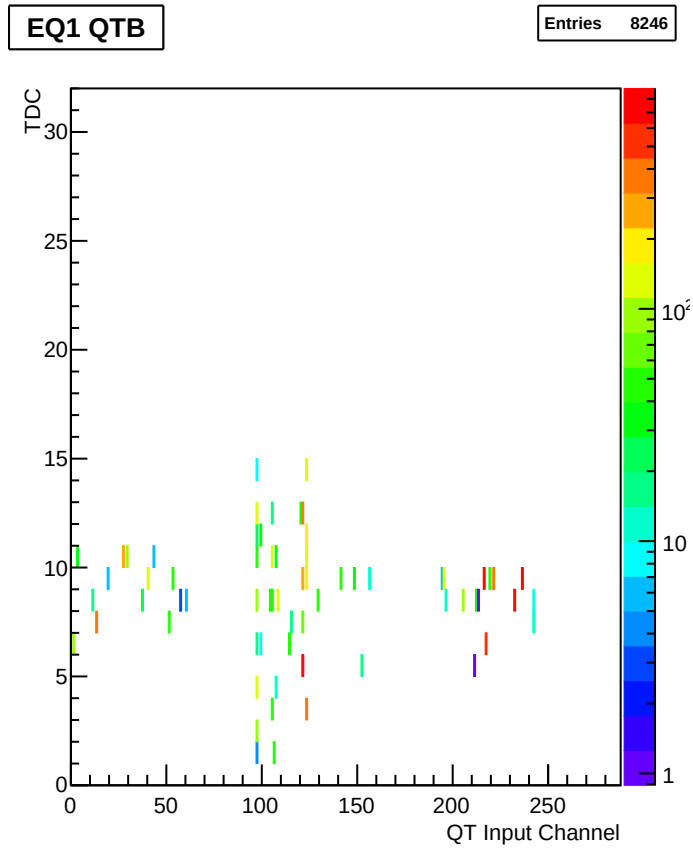

BBQ-VP001 East
Entries 48000
ADC
QT Input Channel

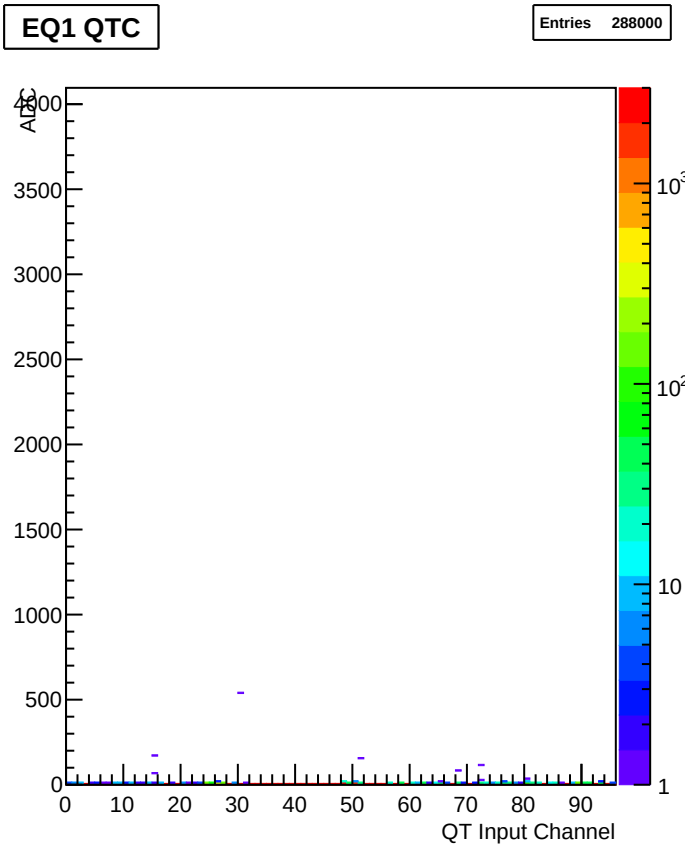

BBQ-VP001 East
Entries 48000
TADC
QT Input Channel

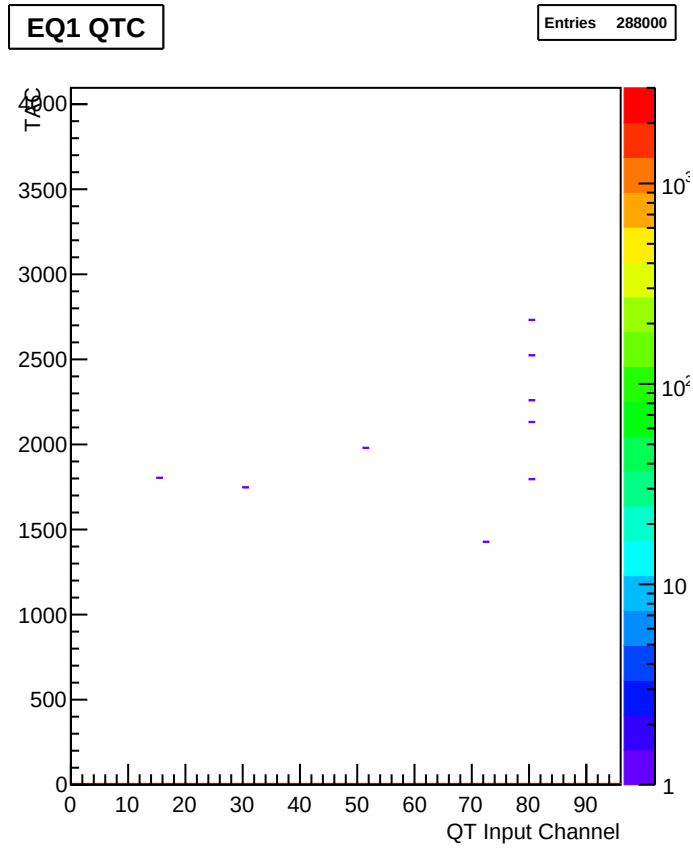

BBQ-VP002 West
Entries 48000
ADC
QT Input Channel

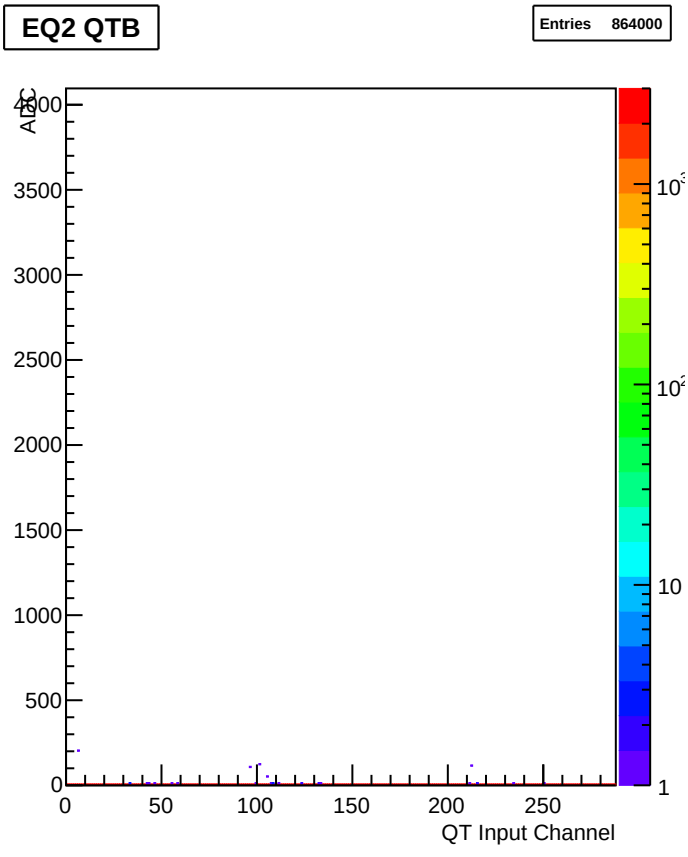

BBQ-VP002 West
Entries 48000
TADC
QT Input Channel

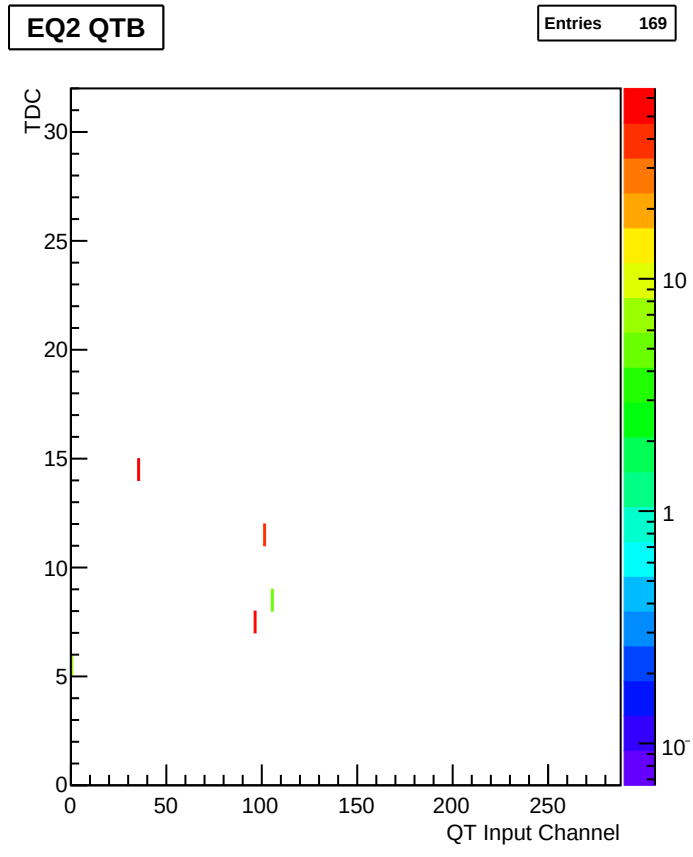

EQ1 QTB
Entries 864000
ADC
QT Input Channel

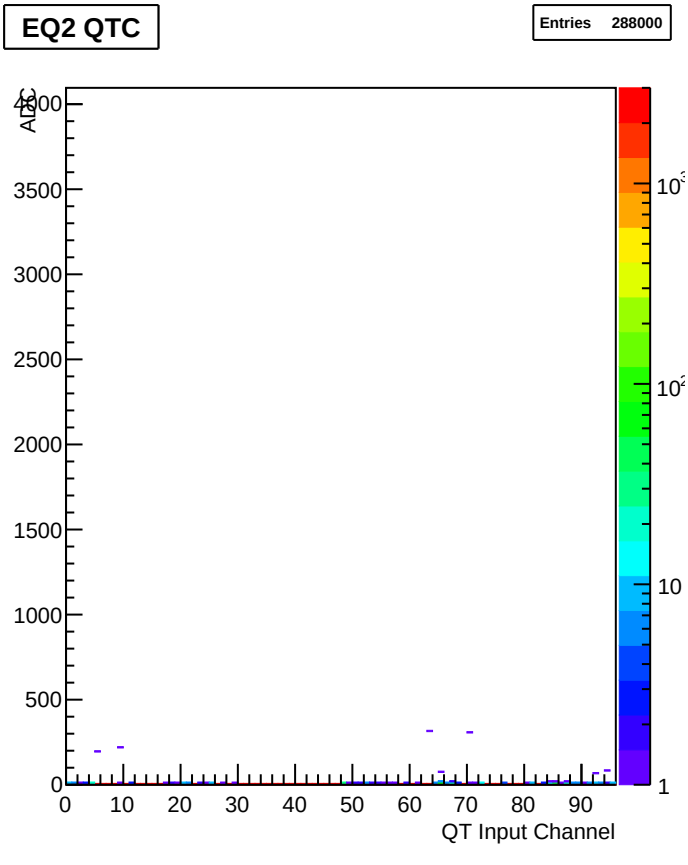

EQ1 QTB
Entries 8246
TDC
QT Input Channel

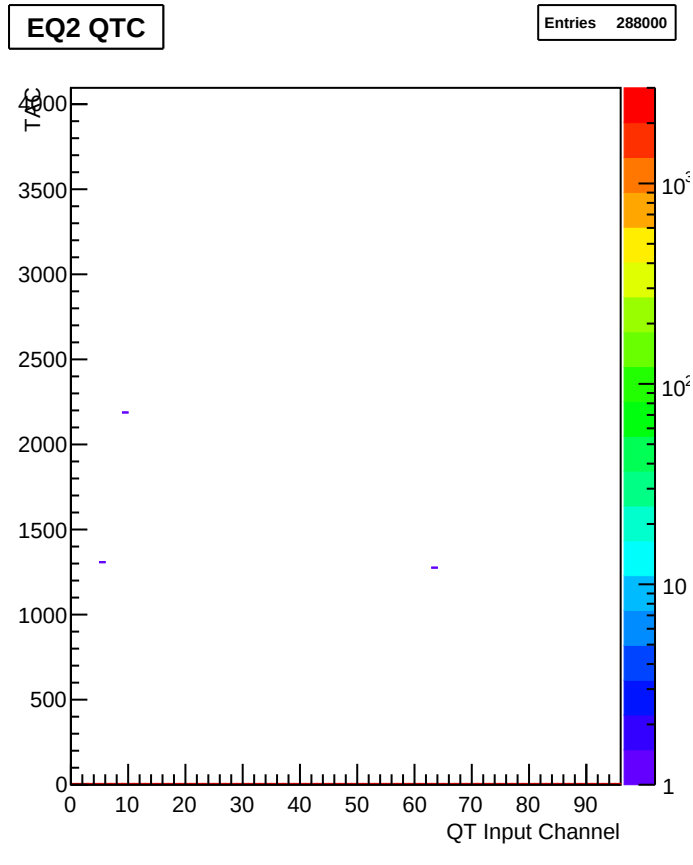

EQ1 QTC
Entries 288000
ADC
QT Input Channel

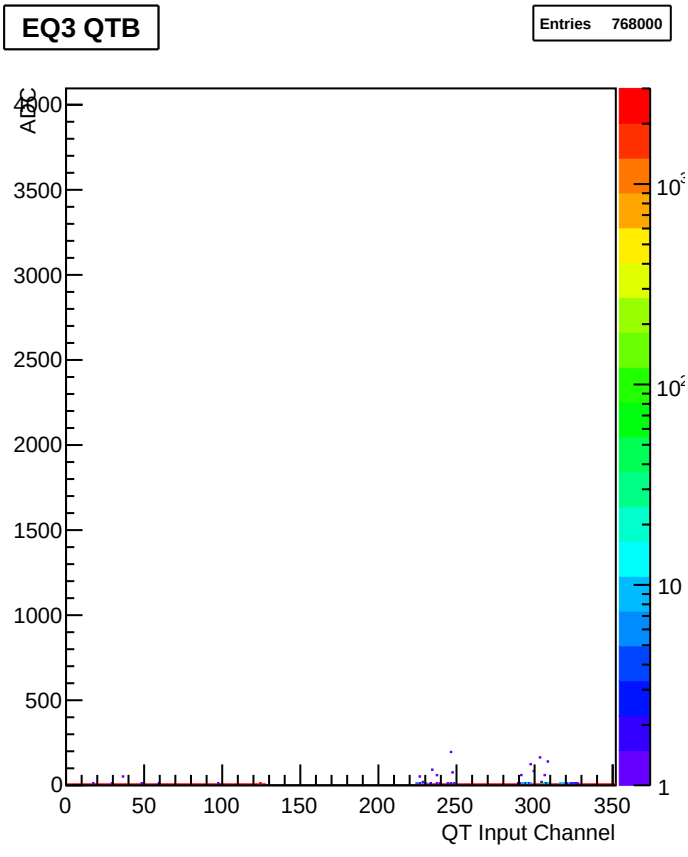

EQ1 QTC
Entries 288000
TADC
QT Input Channel

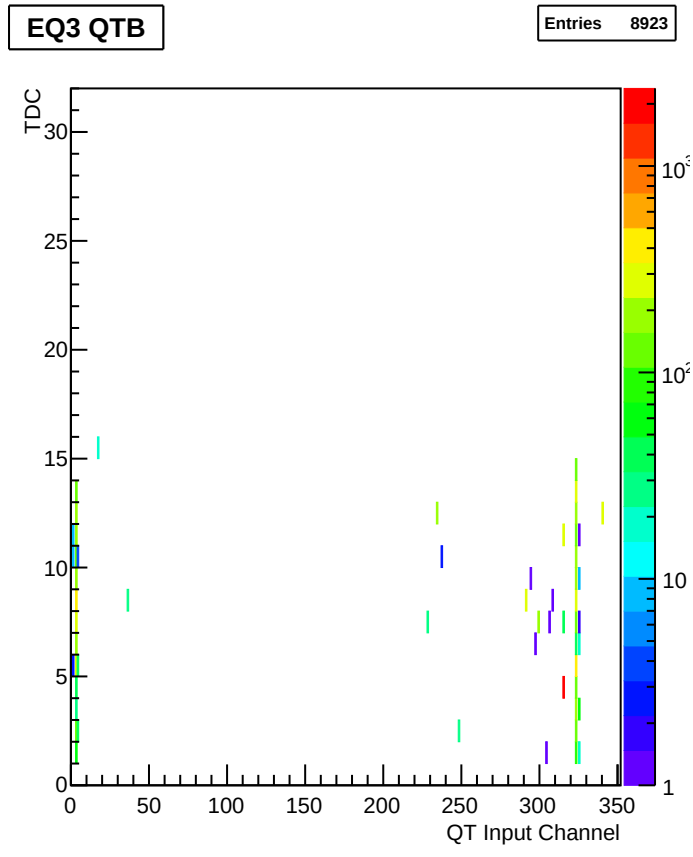

EQ2 QTB
Entries 864000
ADC
QT Input Channel

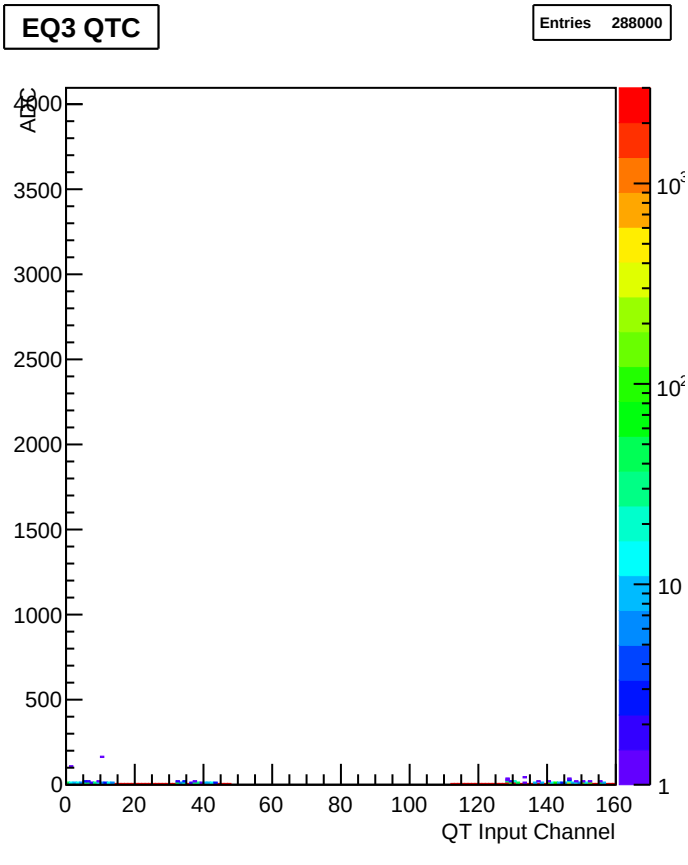

EQ2 QTB
Entries 169
TDC
QT Input Channel

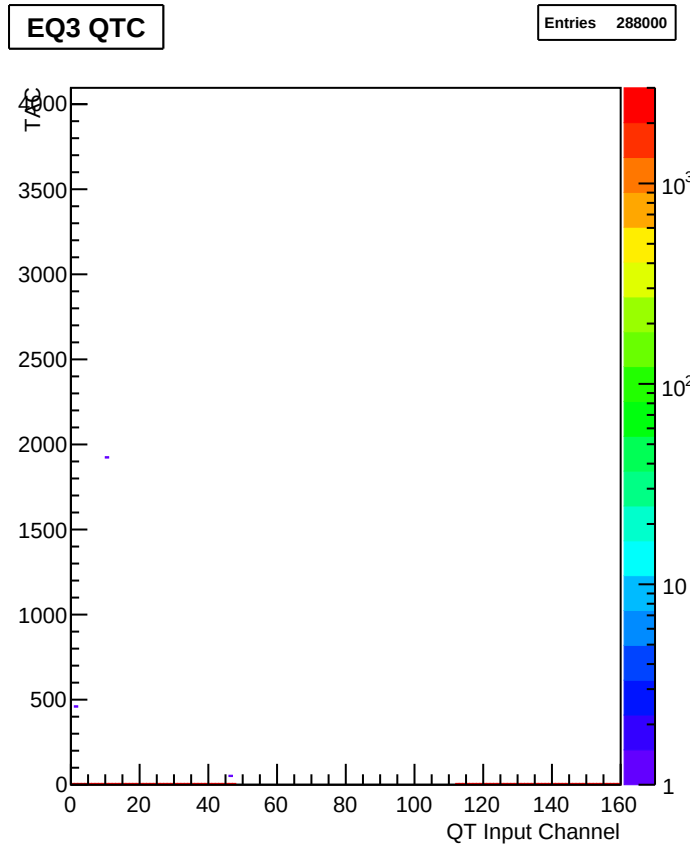

EQ2 QTC
Entries 288000
ADC
QT Input Channel

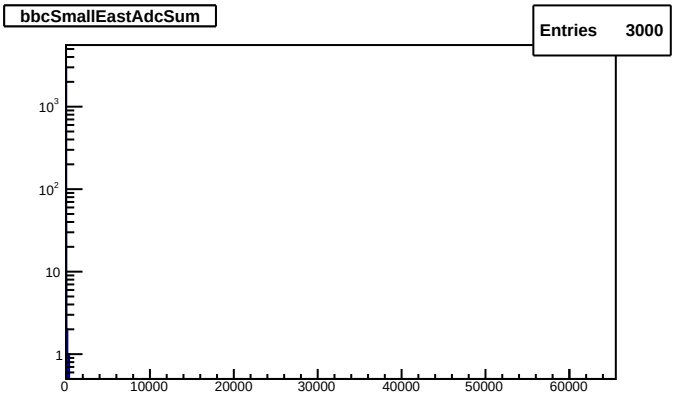

EQ2 QTC
Entries 288000
TADC
QT Input Channel

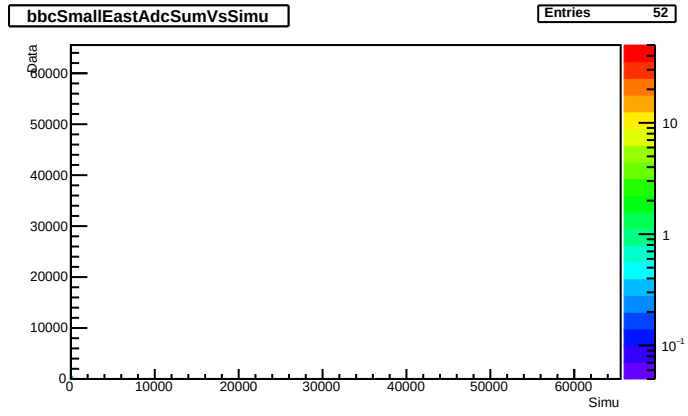

EQ3 QTB
Entries 768000
ADC
QT Input Channel

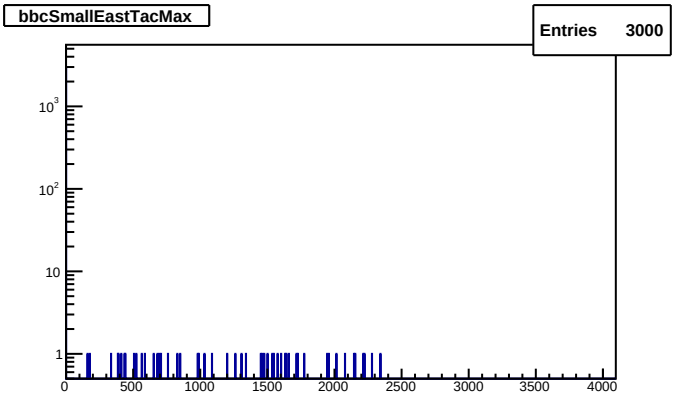

EQ3 QTB
Entries 8923
TDC
QT Input Channel

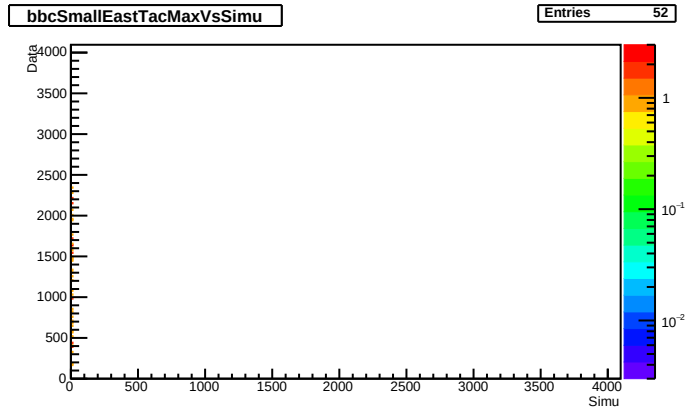

EQ3 QTC
Entries 288000
ADC
QT Input Channel

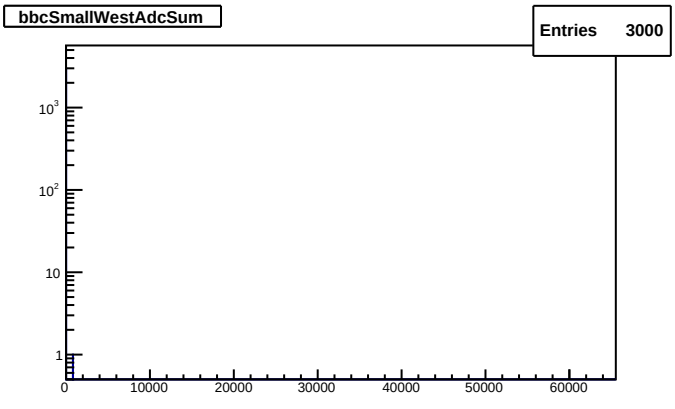

EQ3 QTC
Entries 288000
TADC
QT Input Channel

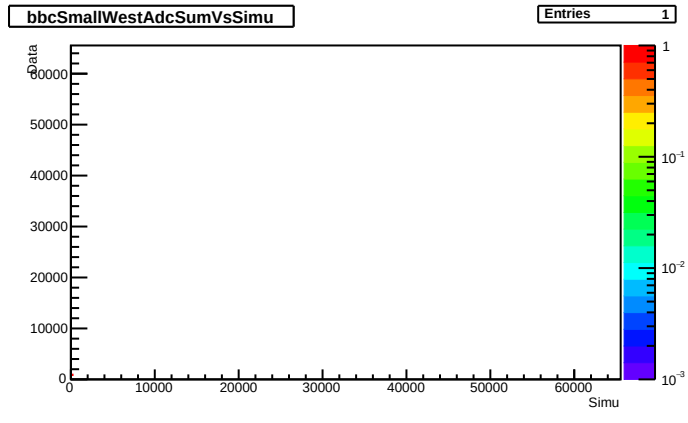

bbcSmallEastAdcSum
Entries 3000

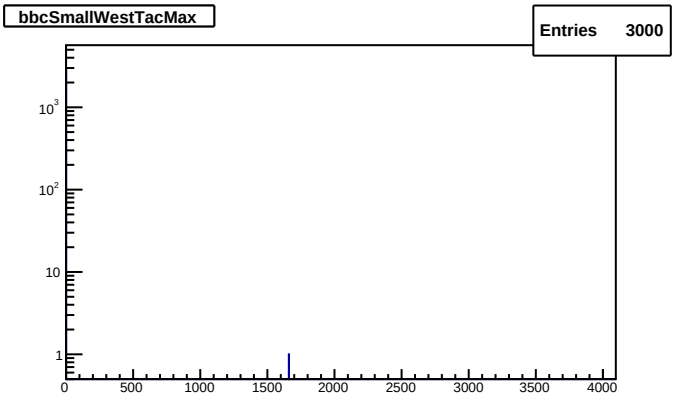

bbcSmallEastAdcSumVsSimu
Entries 52
Data
Simu

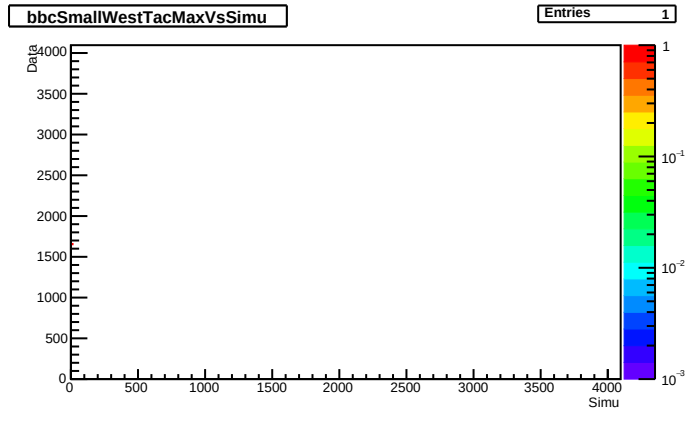

bbcSmallEastTacMax
Entries 3000

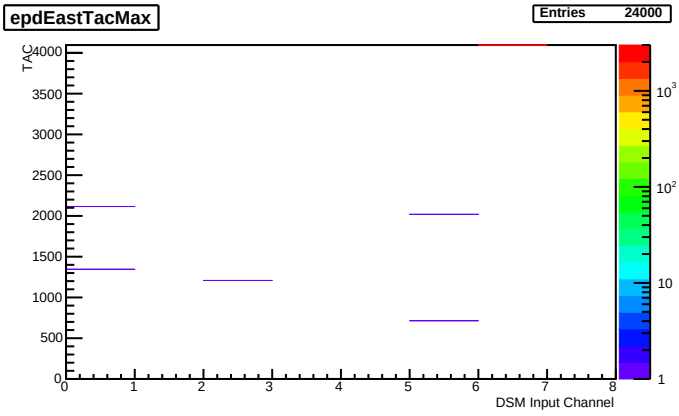

bbcSmallEastTacMaxVsSimu
Entries 52
Data
Simu

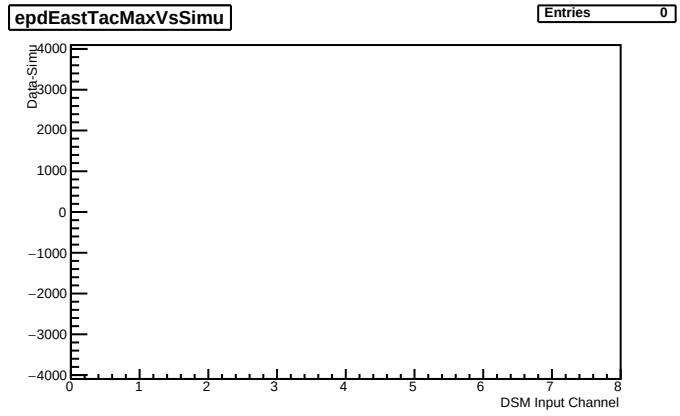

bbcSmallWestAdcSum
Entries 3000

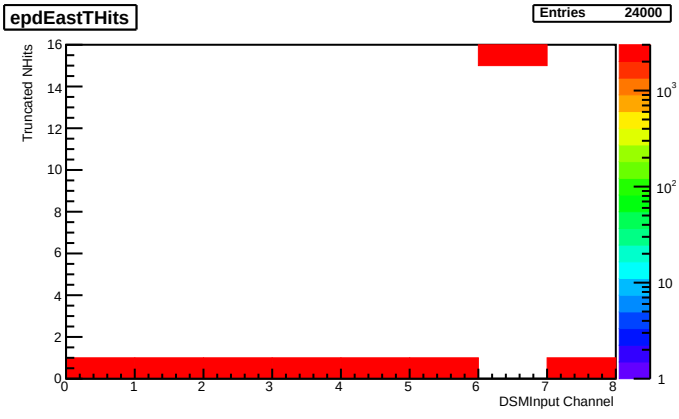

bbcSmallWestAdcSumVsSimu
Entries 1
Data
Simu

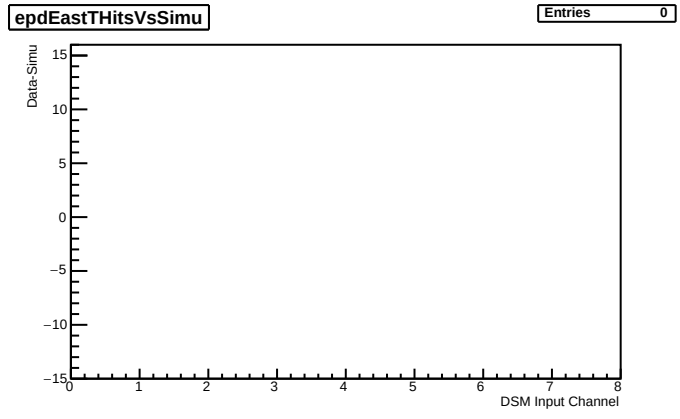

bbcSmallWestTacMax
Entries 3000

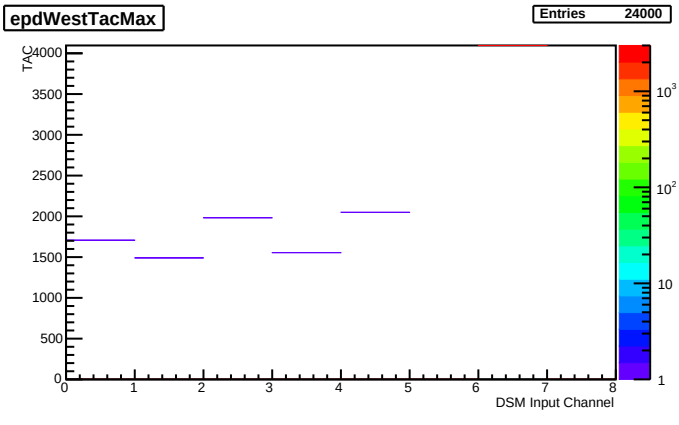

bbcSmallWestTacMaxVsSimu
Entries 1
Data
Simu

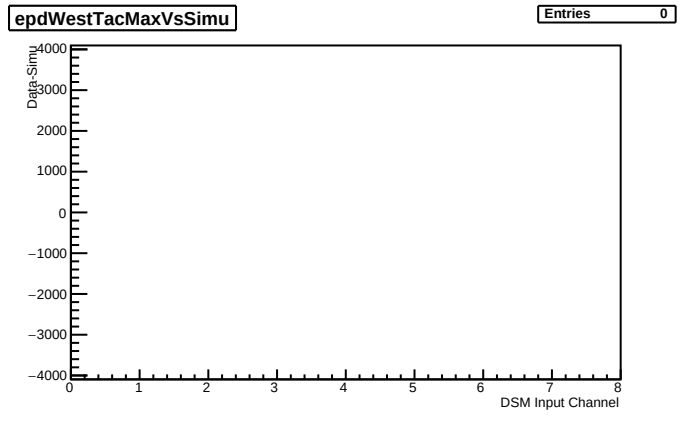

epdEastTacMax
Entries 24000
TAC
DSM Input Channel

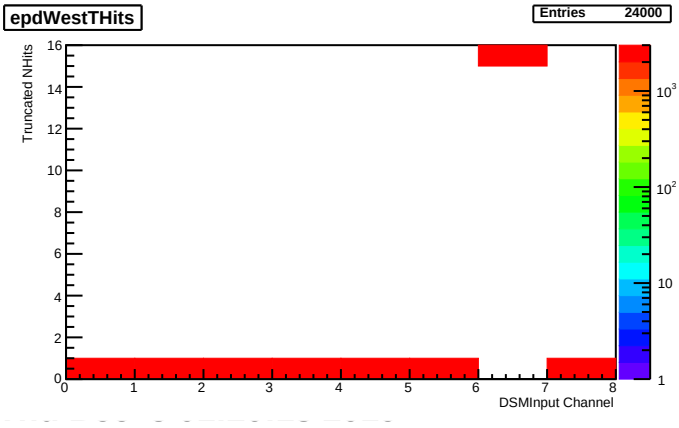

epdEastTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

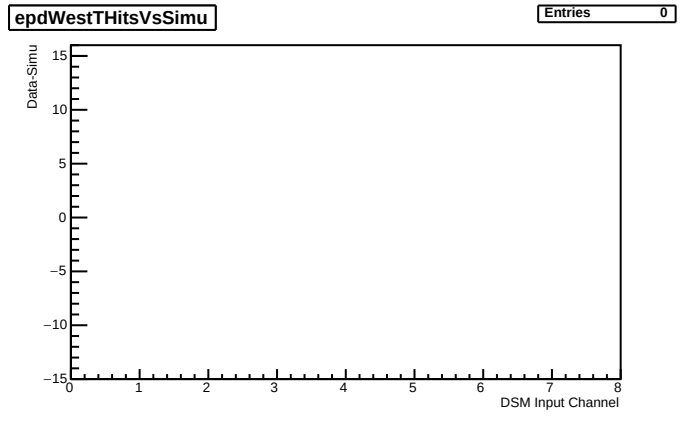

epdEastTHits
Entries 24000
Truncated NHits
DSMInput Channel

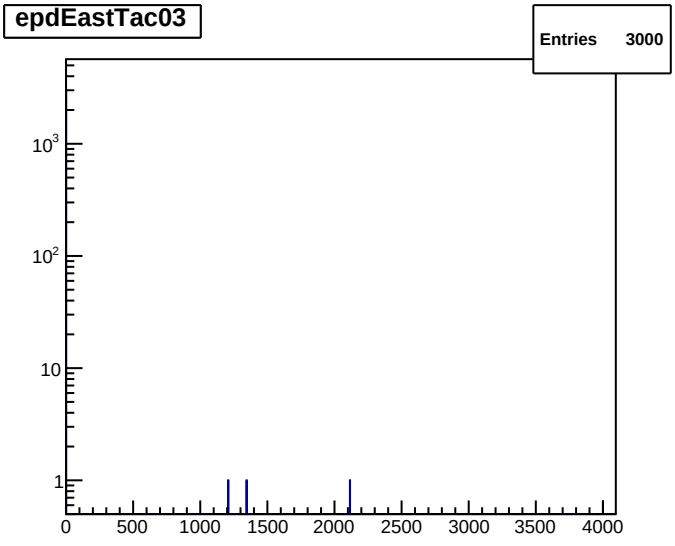

epdEastTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

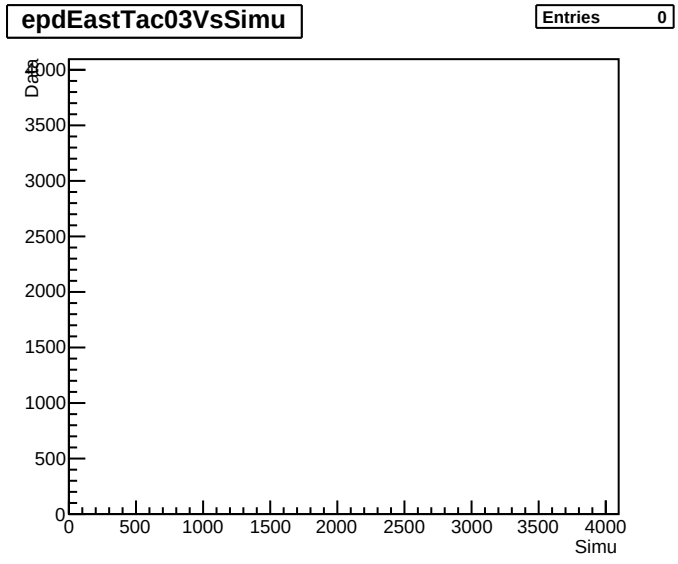

epdWestTacMax
Entries 24000
TAC
DSM Input Channel

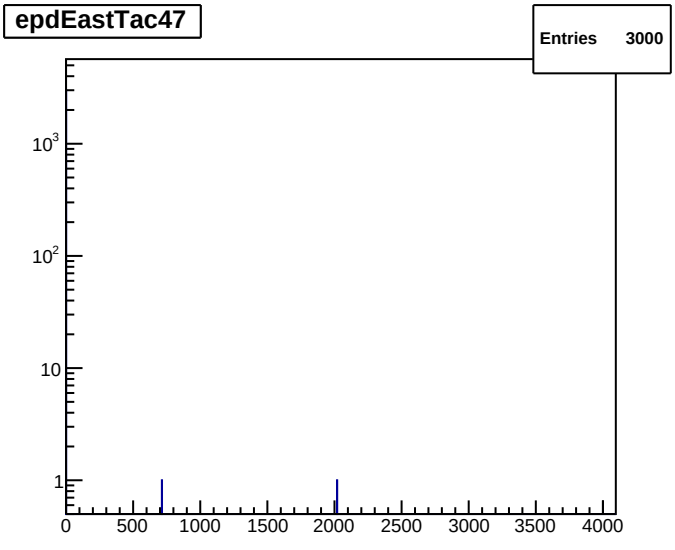

epdWestTacMaxVsSimu
Entries 0
Data-Simu
DSM Input Channel

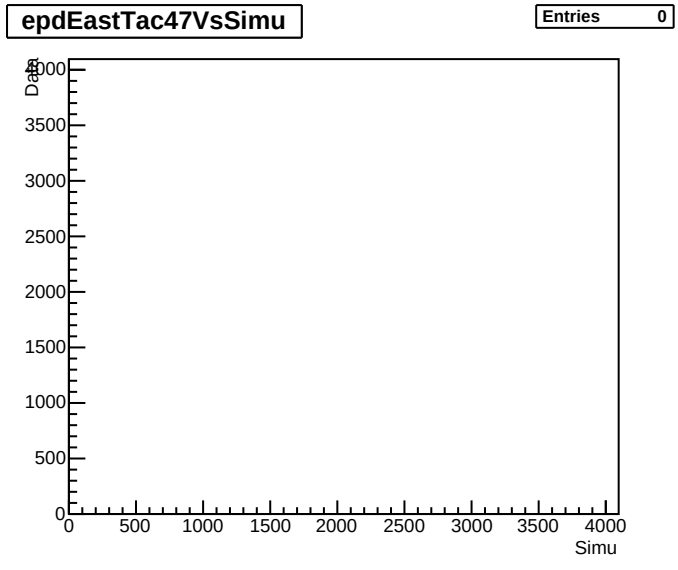

epdWestTHits
Entries 24000
Truncated NHits
DSMInput Channel

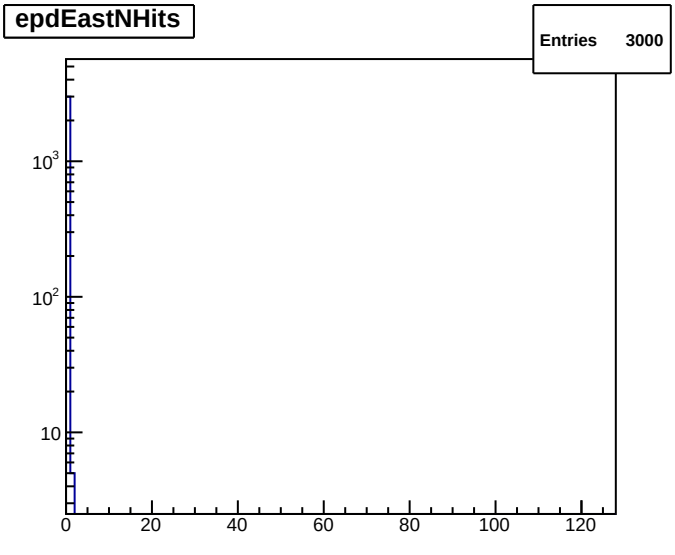

epdWestTHitsVsSimu
Entries 0
Data-Simu
DSM Input Channel

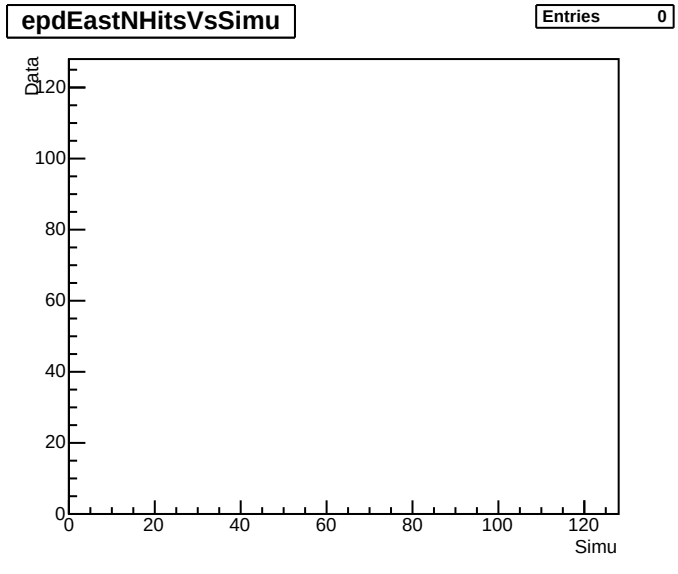

epdEastTac03
Entries 3000
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

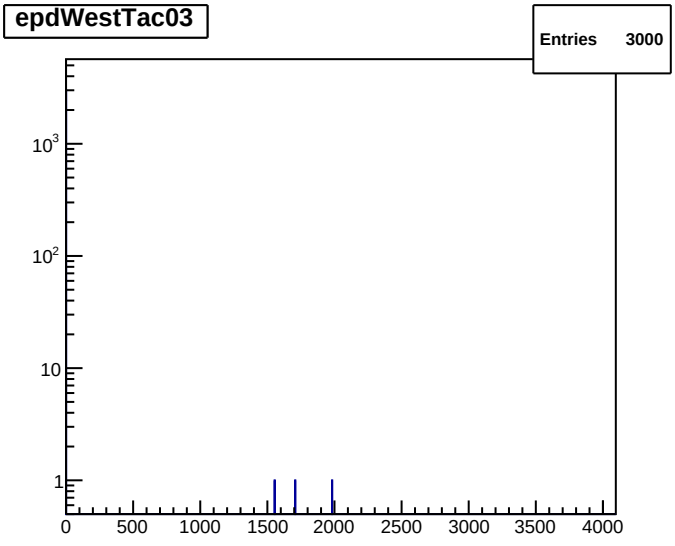

epdEastTac03VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

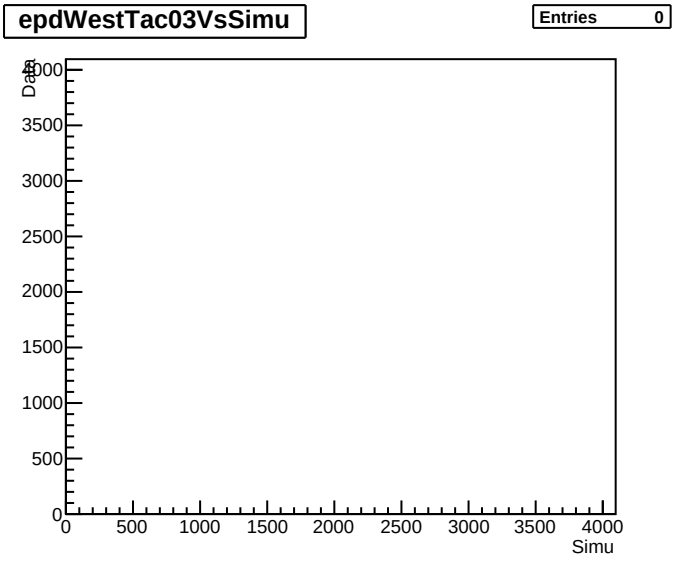

epdEastTac47
Entries 3000
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

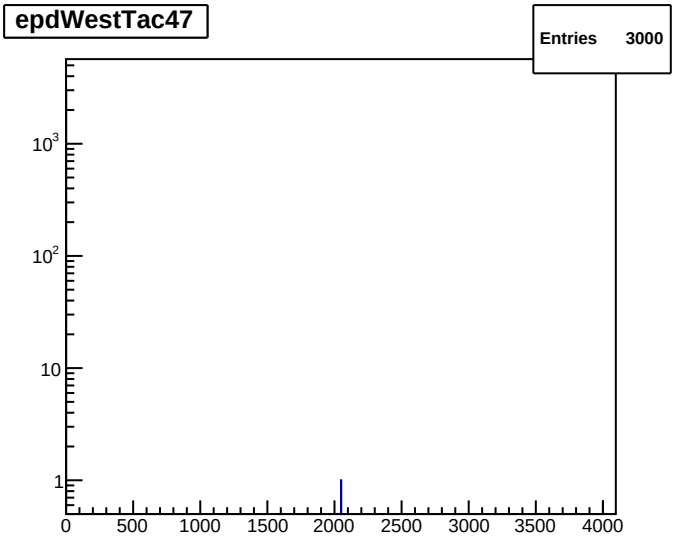

epdEastTac47VsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

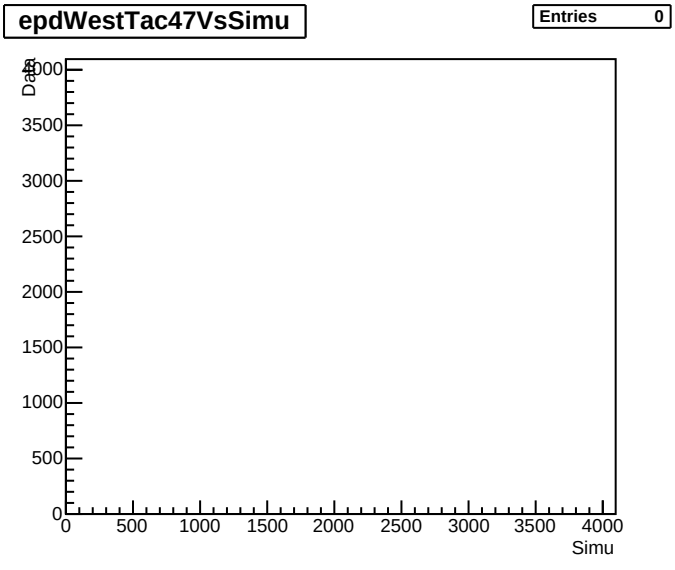

epdEastNHits
Entries 3000
10^3
10^2
10
0 20 40 60 80 100 120

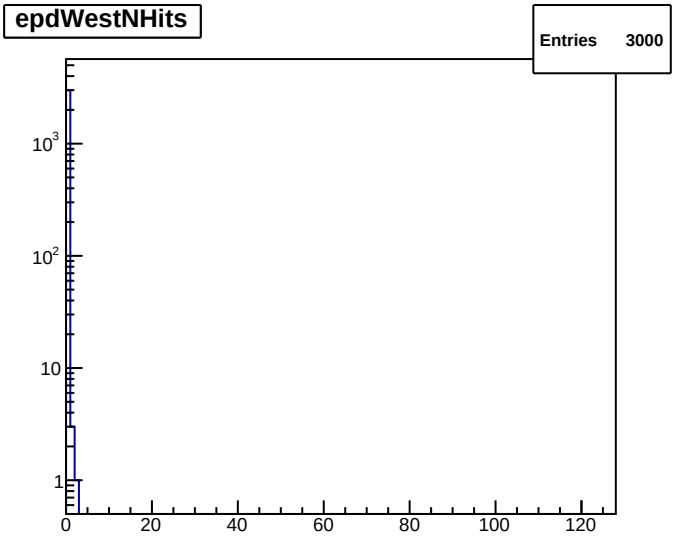

epdEastNHitsVsSimu
Entries 0
Data
120 100 80 60 40 20 0
0 20 40 60 80 100 120
Simu

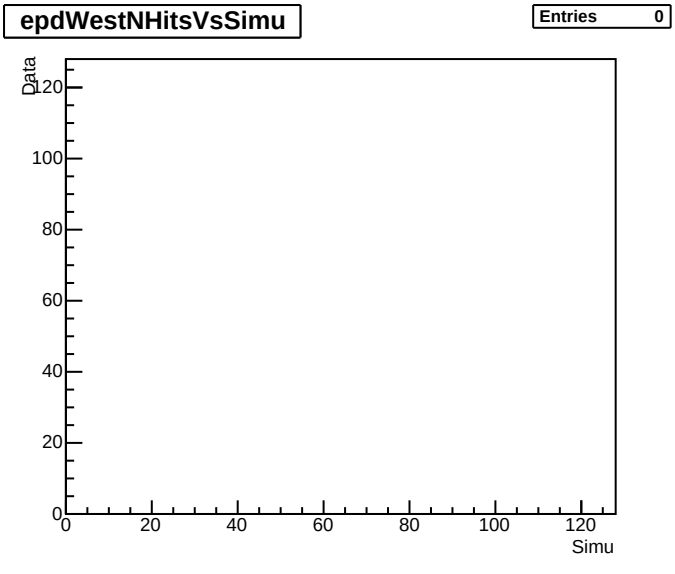

epdWestTac03
Entries 3000
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

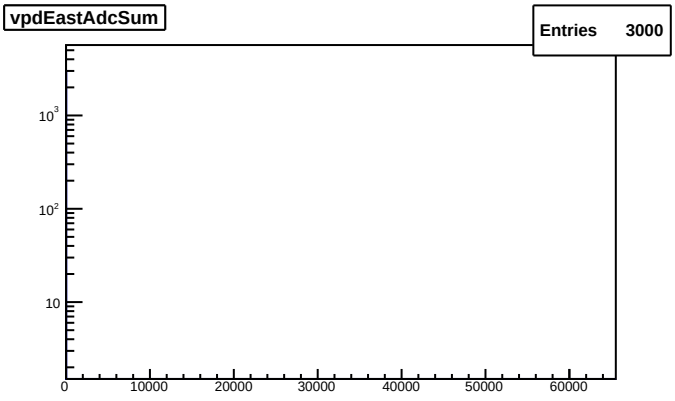

epdWestTac03VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

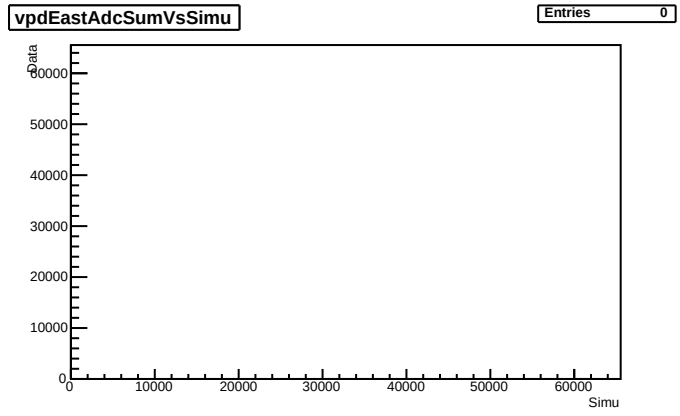

epdWestTac47
Entries 3000
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

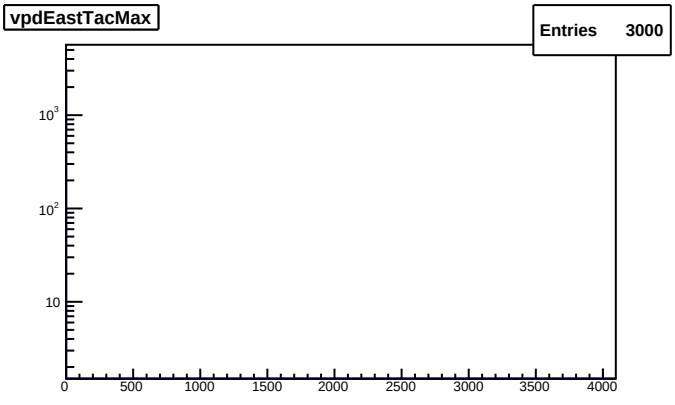

epdWestTac47VsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

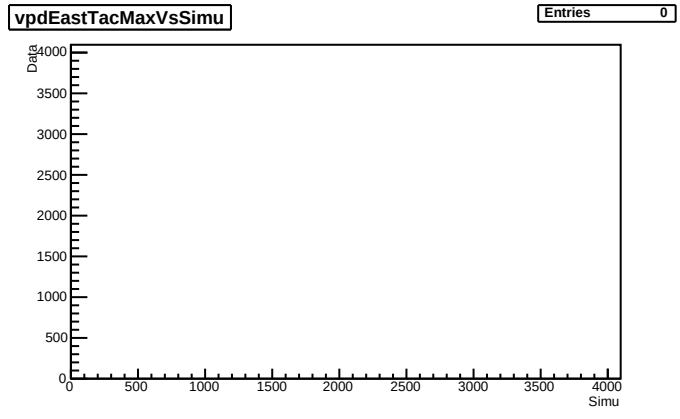

epdWestNHits
Entries 3000
10^3
10^2
10
1
0 20 40 60 80 100 120

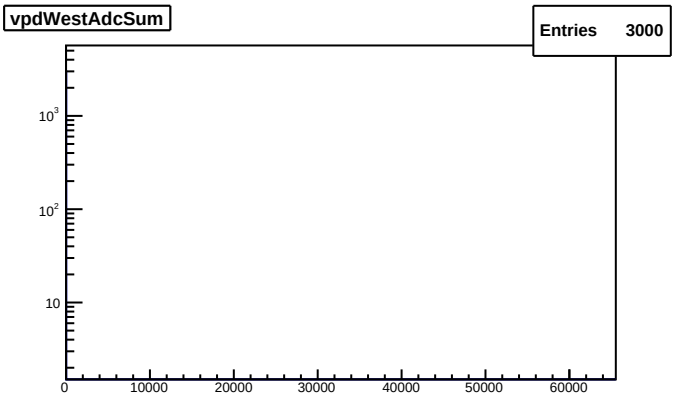

epdWestNHitsVsSimu
Entries 0
Data
120
100
80
60
40
20
0
0 20 40 60 80 100 120
Simu

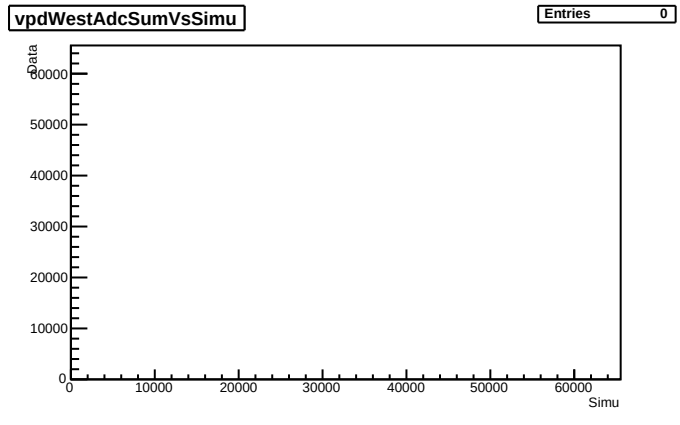

vpdEastAdcSum
Entries 3000
10^3
10^2
10
0
10000
20000
30000
40000
50000
60000

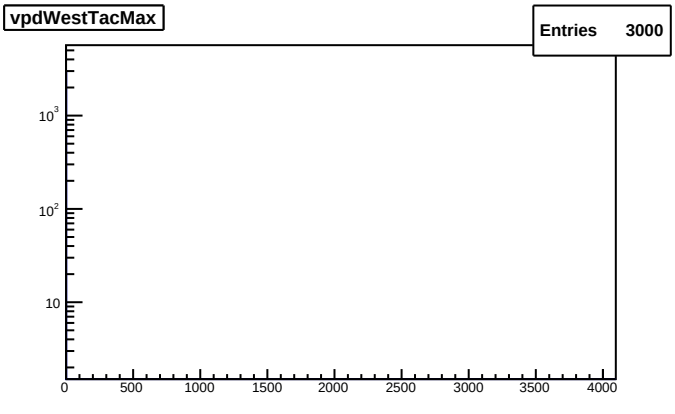

vpdEastAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0
10000
20000
30000
40000
50000
60000
Simu

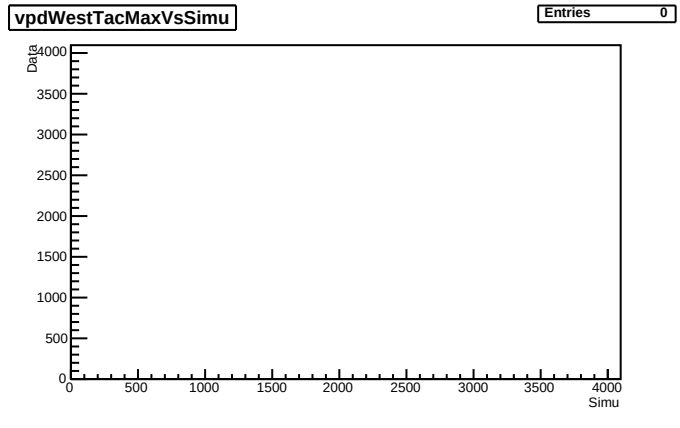

vpdEastTacMax
Entries 3000
10^3
10^2
10
0
500
1000
1500
2000
2500
3000
3500
4000

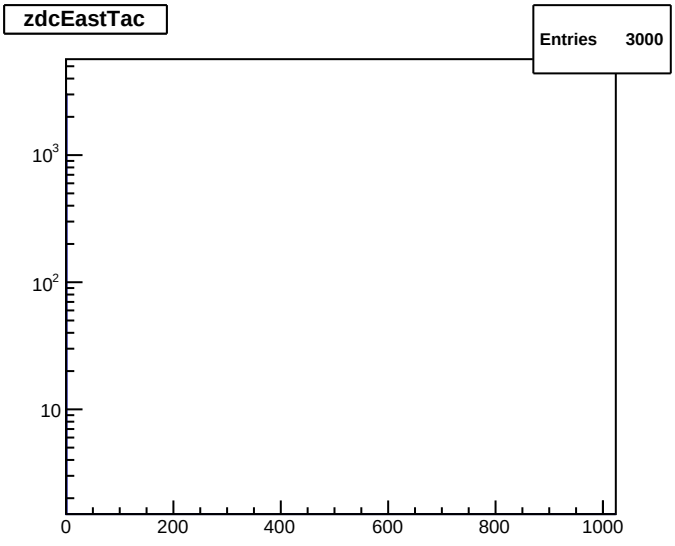

vpdEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

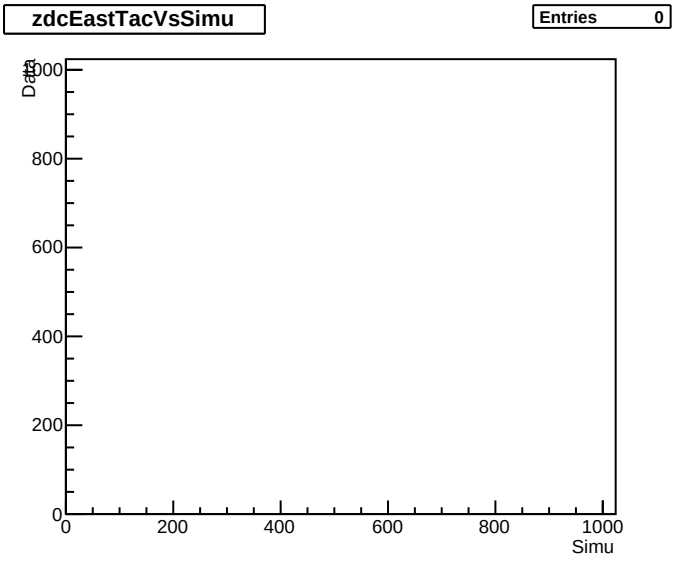

vpdWestAdcSum
Entries 3000
10^3
10^2
10
0
10000
20000
30000
40000
50000
60000

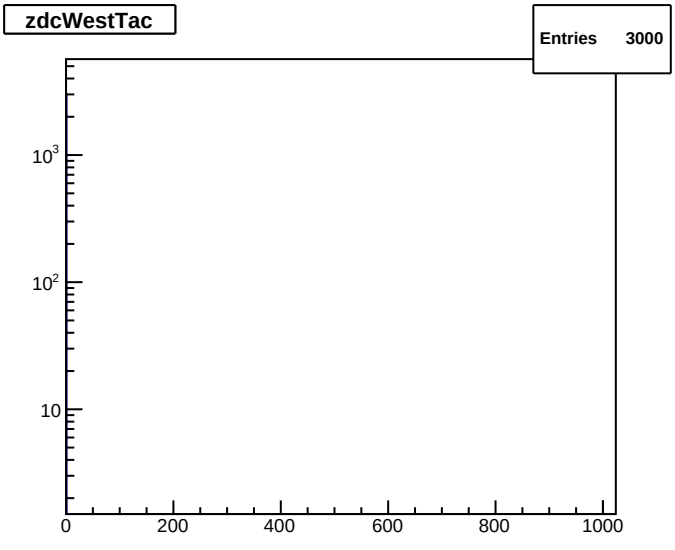

vpdWestAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0
10000
20000
30000
40000
50000
60000
Simu

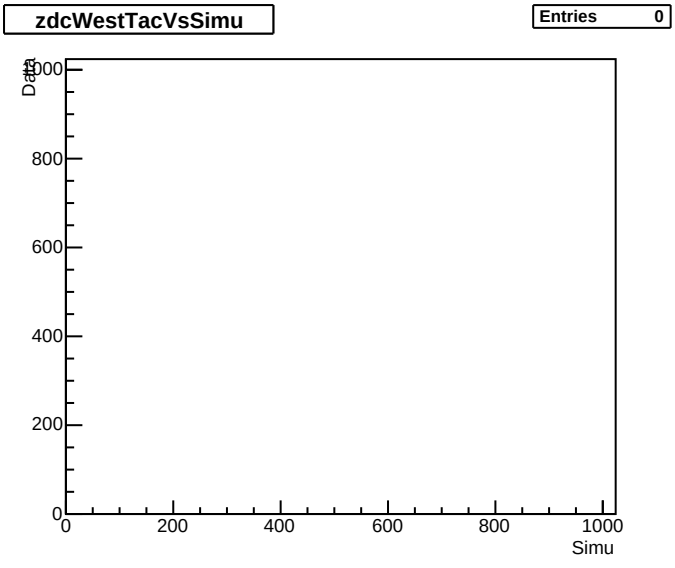

vpdWestTacMax
Entries 3000
10^3
10^2
10
0
500
1000
1500
2000
2500
3000
3500
4000

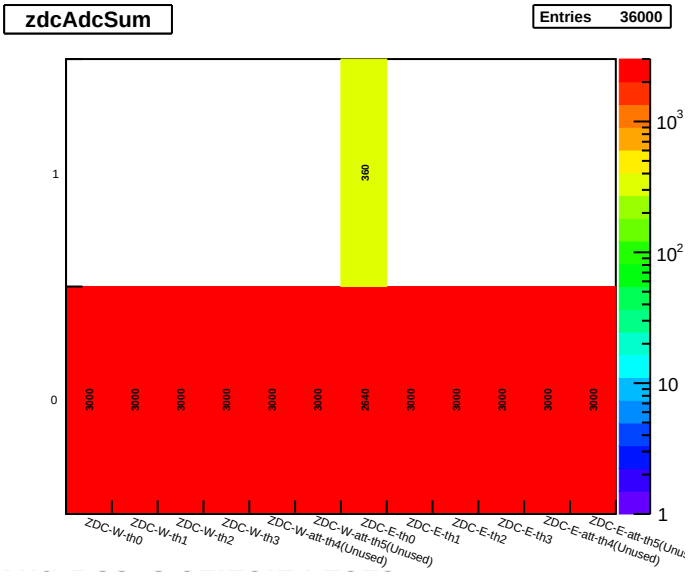

vpdWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

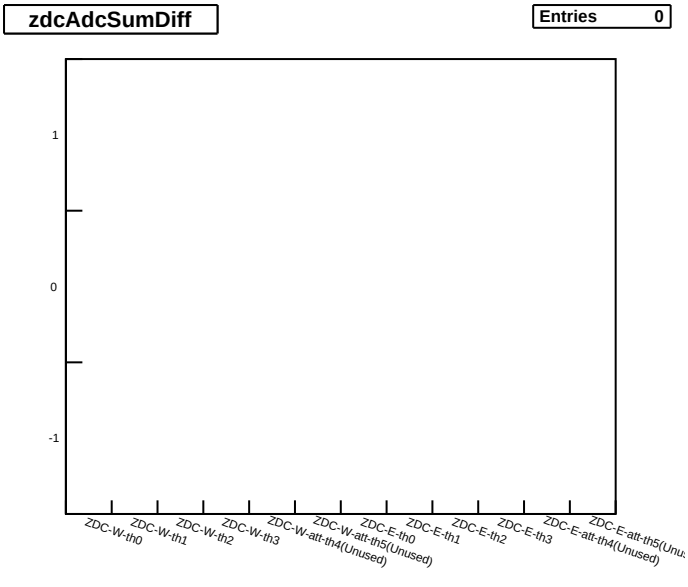

zdcEastTac
Entries 3000
10^3
10^2
10
0
200
400
600
800
1000

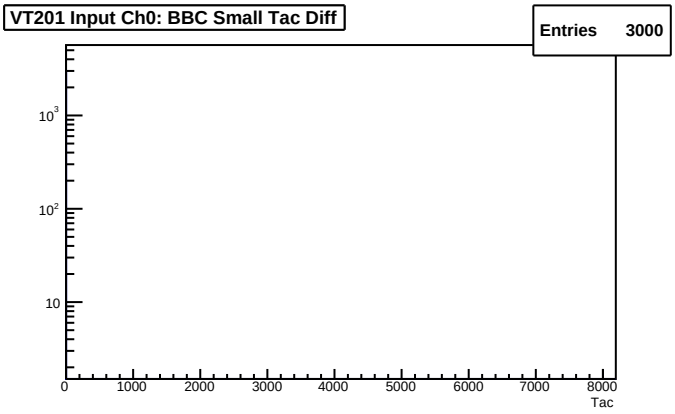

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

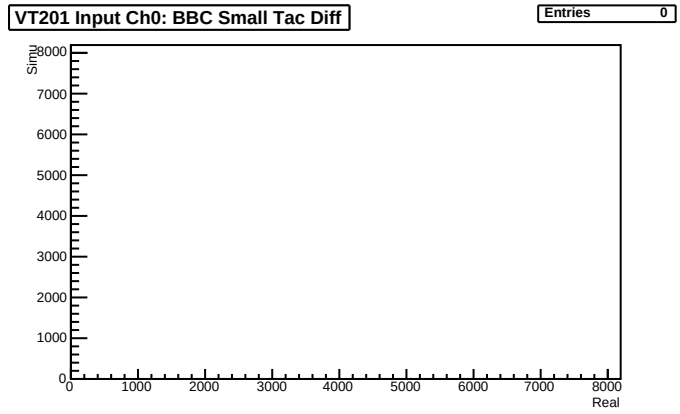

zdcWestTac
Entries 3000
10^3
10^2
10
0
200
400
600
800
1000

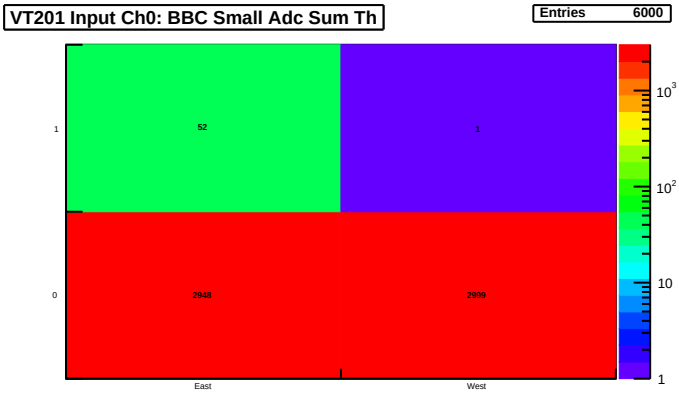

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

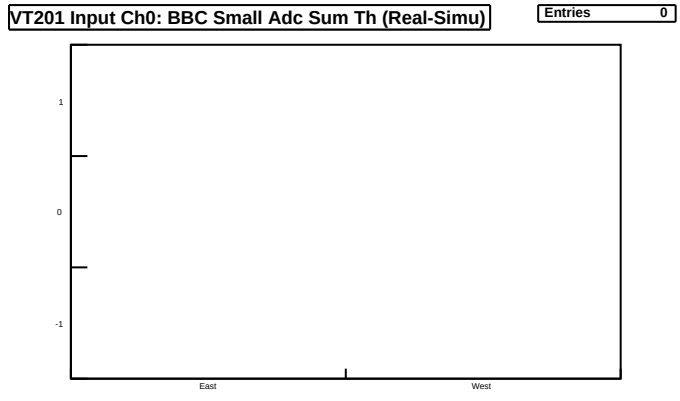

zdcAdcSum
Entries 36000
1
0
360
3000
3000
3000
3000
3000
3000
2640
3000
3000
3000
3000
3000
10^3
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

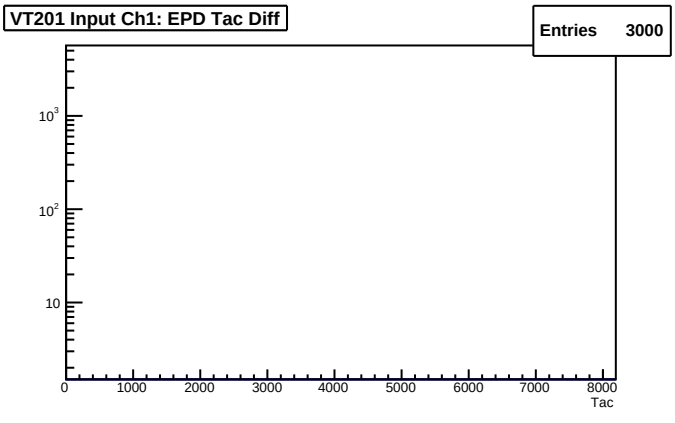

zdcAdcSumDiff
Entries 0
1
0
-1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

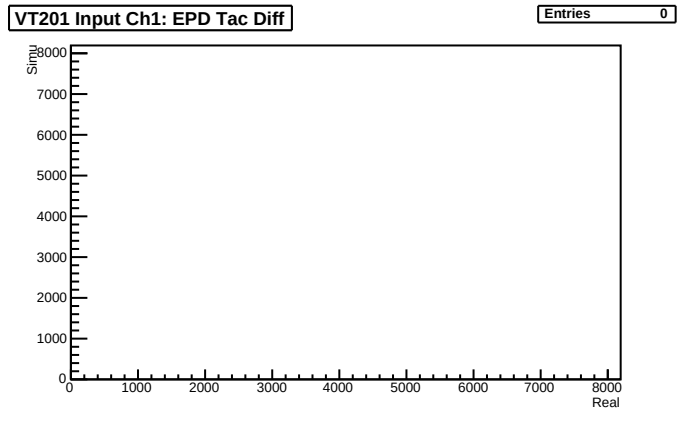

VT201 Input Ch0: BBC Small Tac Diff
Entries 3000
Tac

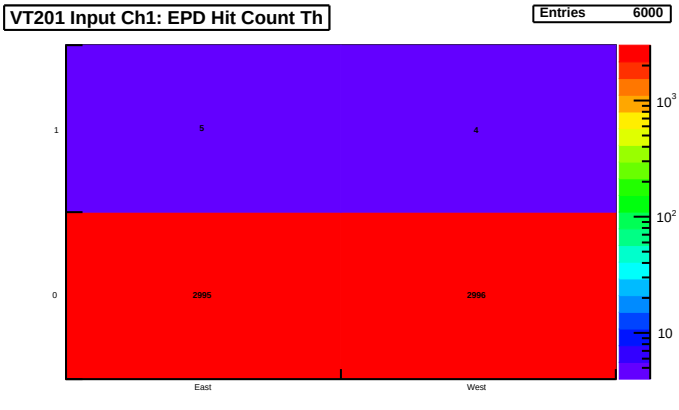

VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

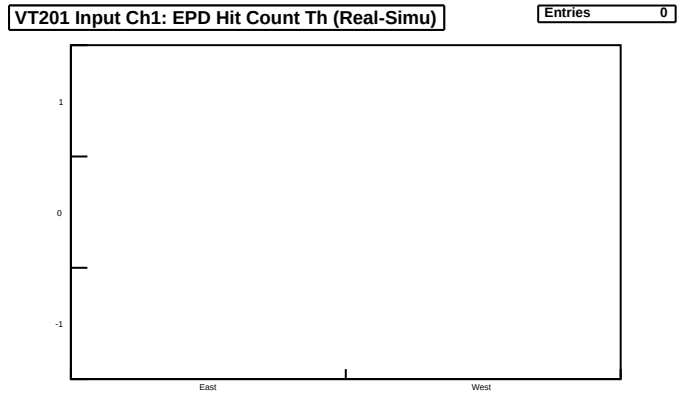

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 6000
52
1
2948
2999
East
West

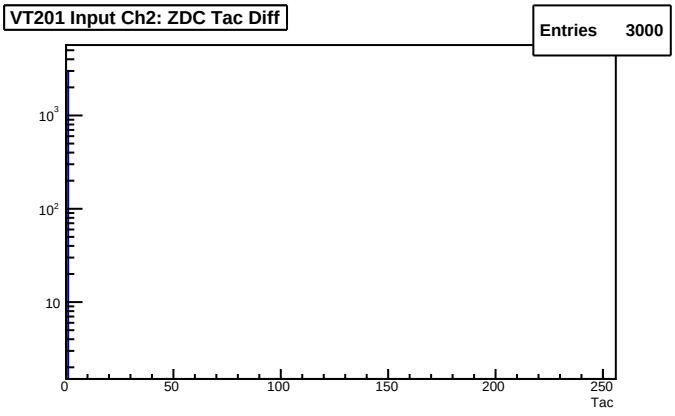

VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
East
West

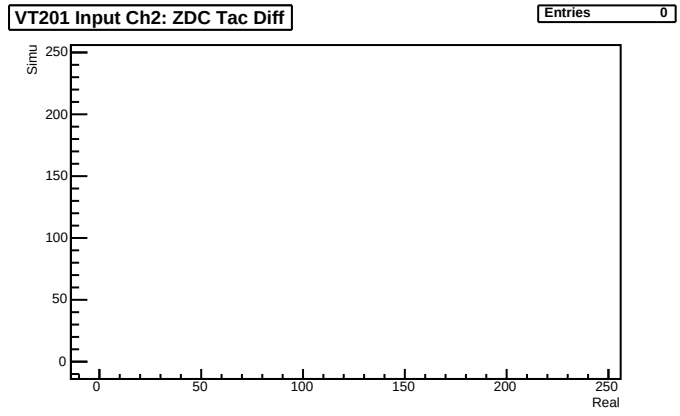

VT201 Input Ch1: EPD Tac Diff
Entries 3000
Tac

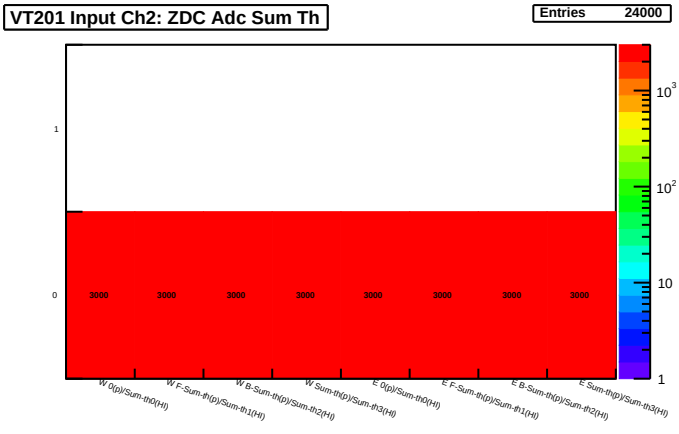

VT201 Input Ch1: EPD Tac Diff
Entries 0
Simu
Real

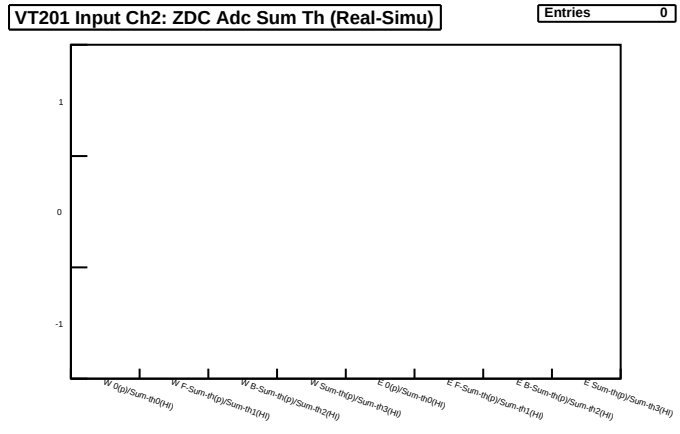

VT201 Input Ch1: EPD Hit Count Th
Entries 6000
5
4
2995
2996
East
West

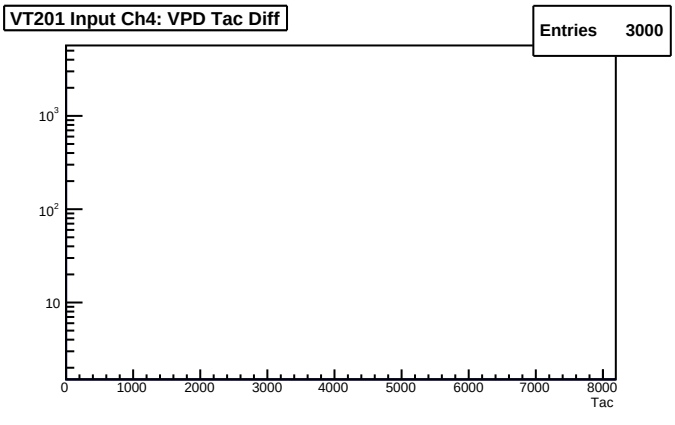

VT201 Input Ch1: EPD Hit Count Th (Real-Simu)
Entries 0
East
West

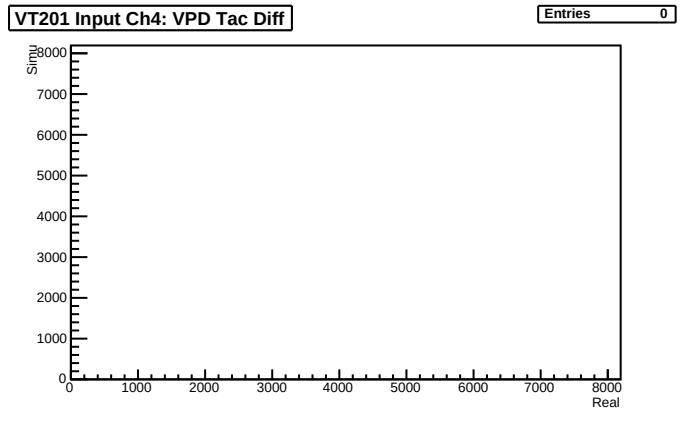

VT201 Input Ch2: ZDC Tac Diff
Entries 3000
Tac

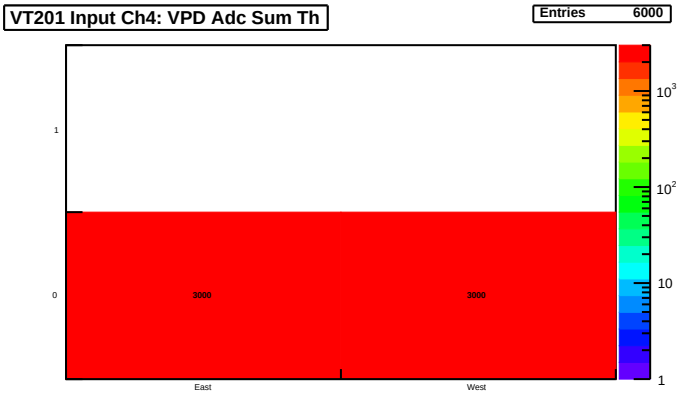

VT201 Input Ch2: ZDC Tac Diff
Entries 0
Simu
Real

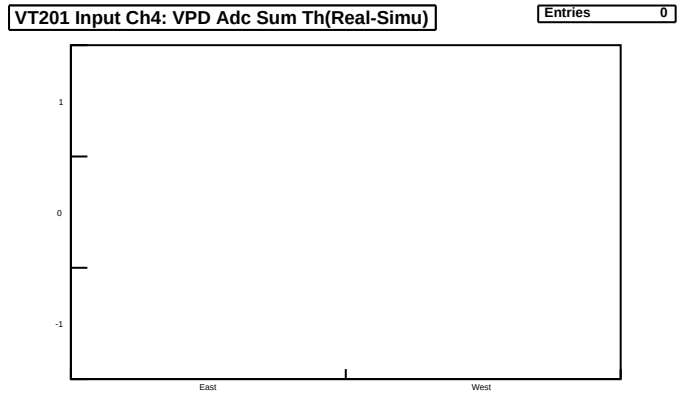

VT201 Input Ch2: ZDC Adc Sum Th
Entries 24000
3000
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

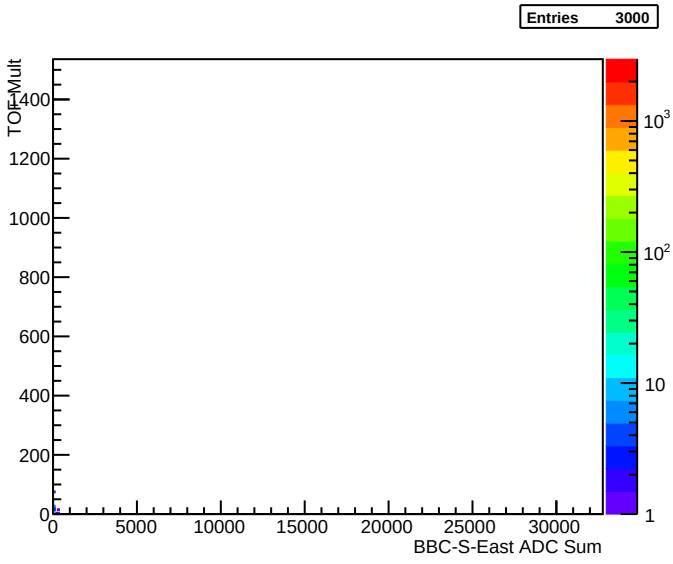

VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)
Entries 0
W 0(p)/Sum-th0(H)
W F-Sum-th(p)/Sum-th1(H)
W B-Sum-th(p)/Sum-th2(H)
W Sum-th(p)/Sum-th3(H)
E 0(p)/Sum-th0(H)
E F-Sum-th(p)/Sum-th1(H)
E B-Sum-th(p)/Sum-th2(H)
E Sum-th(p)/Sum-th3(H)

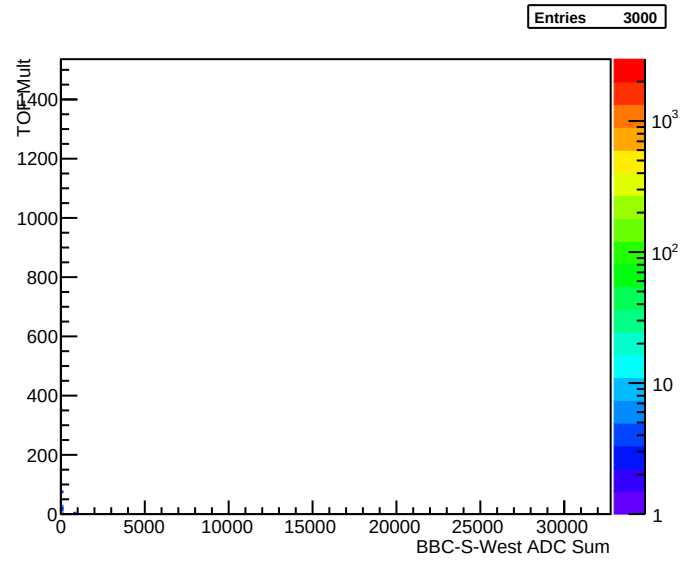

VT201 Input Ch4: VPD Tac Diff
Entries 3000
Tac

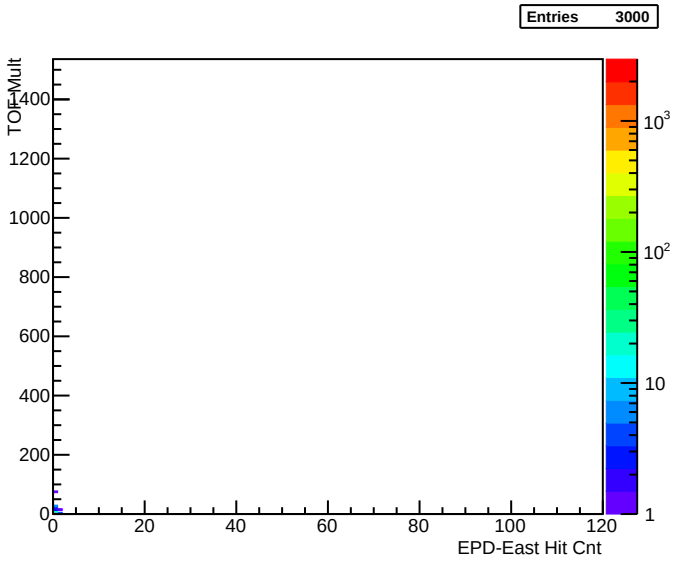

VT201 Input Ch4: VPD Tac Diff
Entries 0
Simu
Real

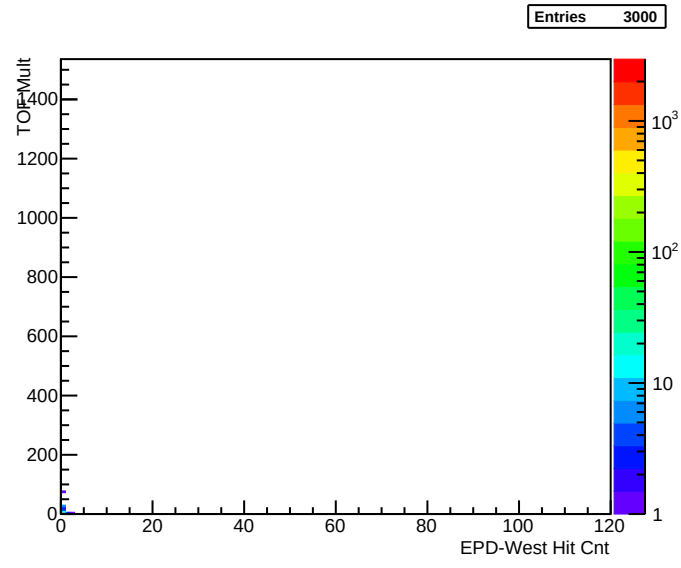

VT201 Input Ch4: VPD Adc Sum Th
Entries 6000
3000
3000
East
West

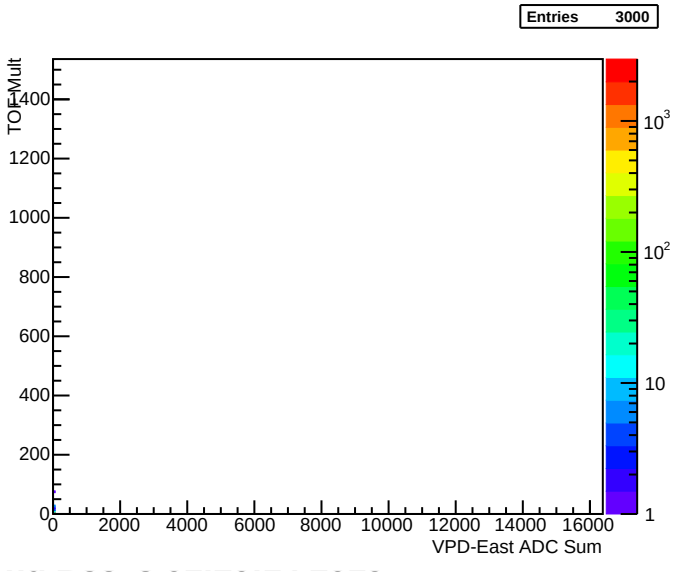

VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)
Entries 0
East
West

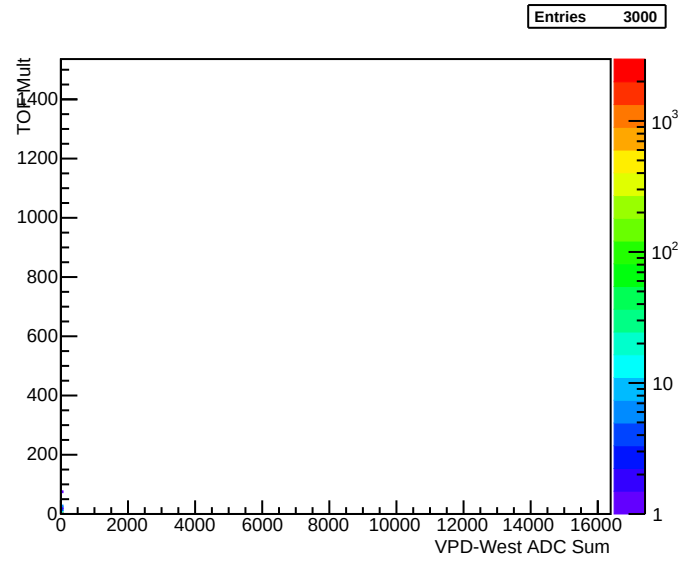

Entries 3000
TOF-Mult
BBC-S-East ADC Sum

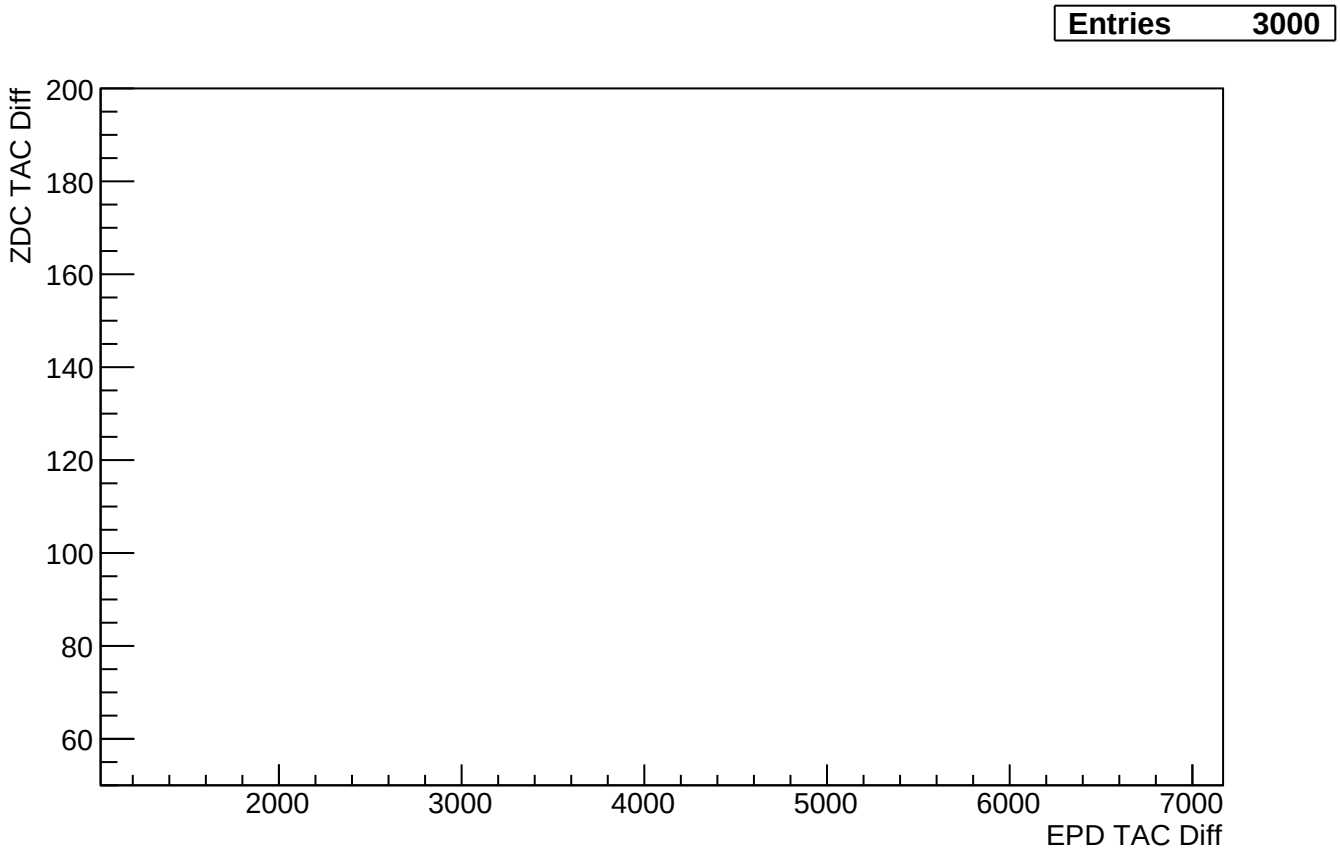

Entries 3000
TOF-Mult
BBC-S-West ADC Sum

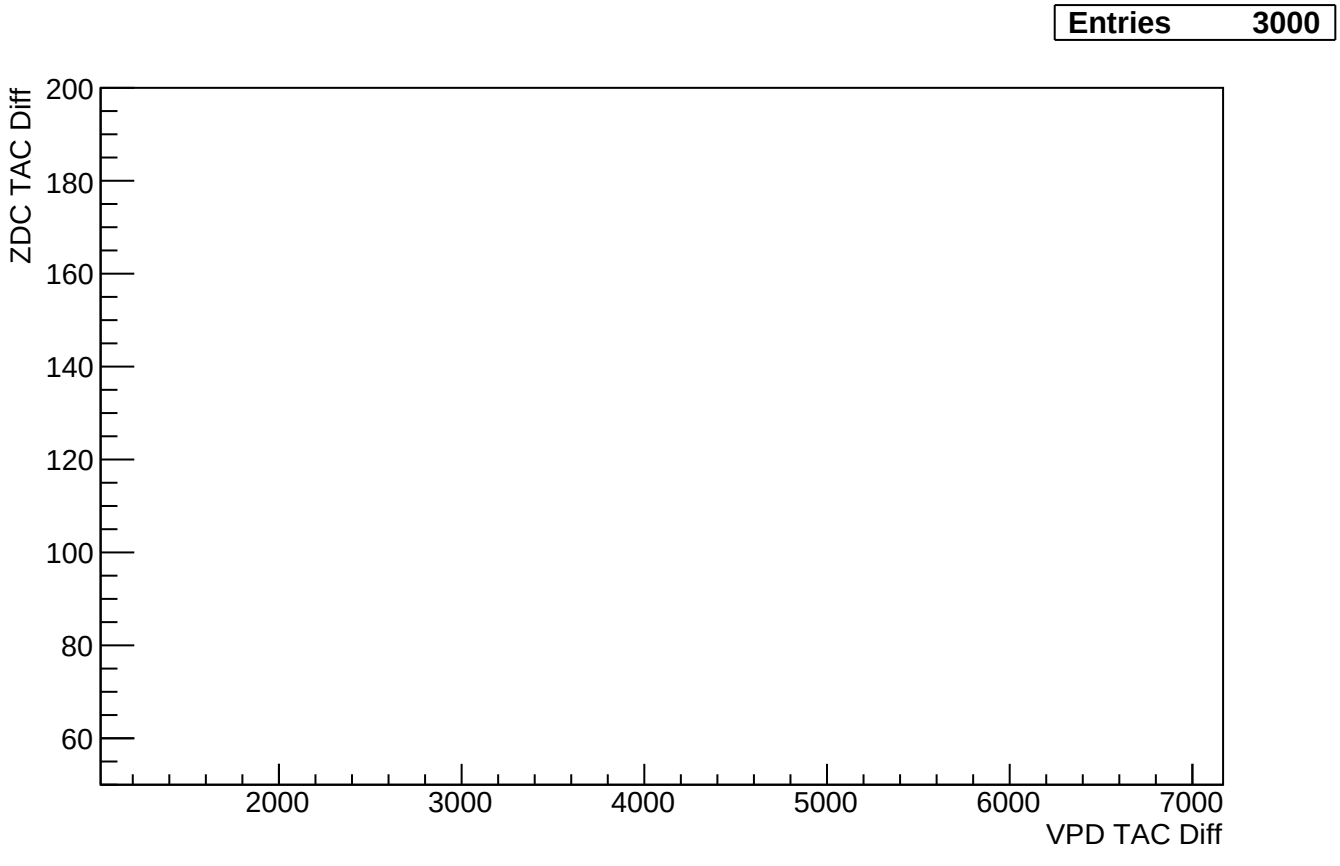

Entries 3000
TOF-Mult
EPD-East Hit Cnt

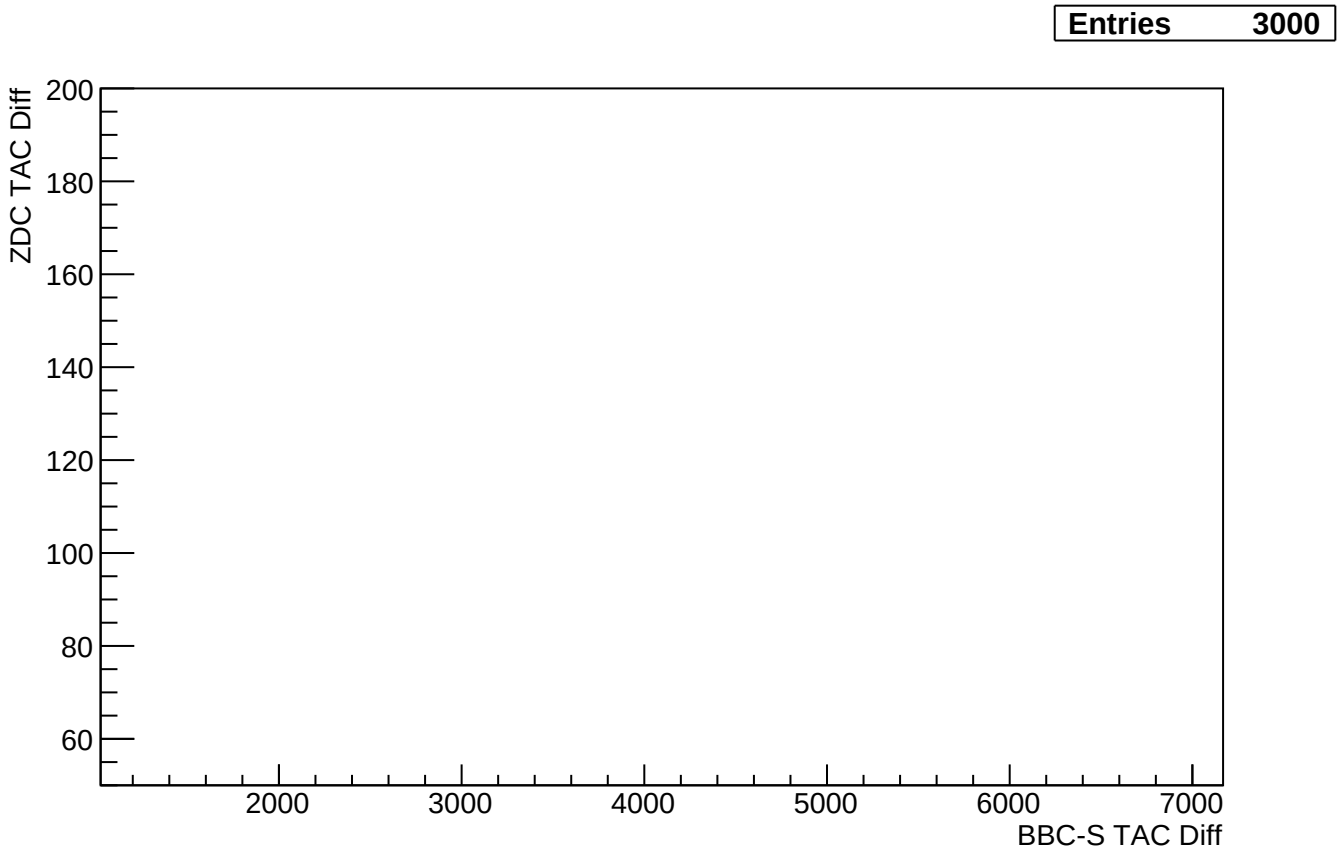

Entries 3000
TOF-Mult
EPD-West Hit Cnt

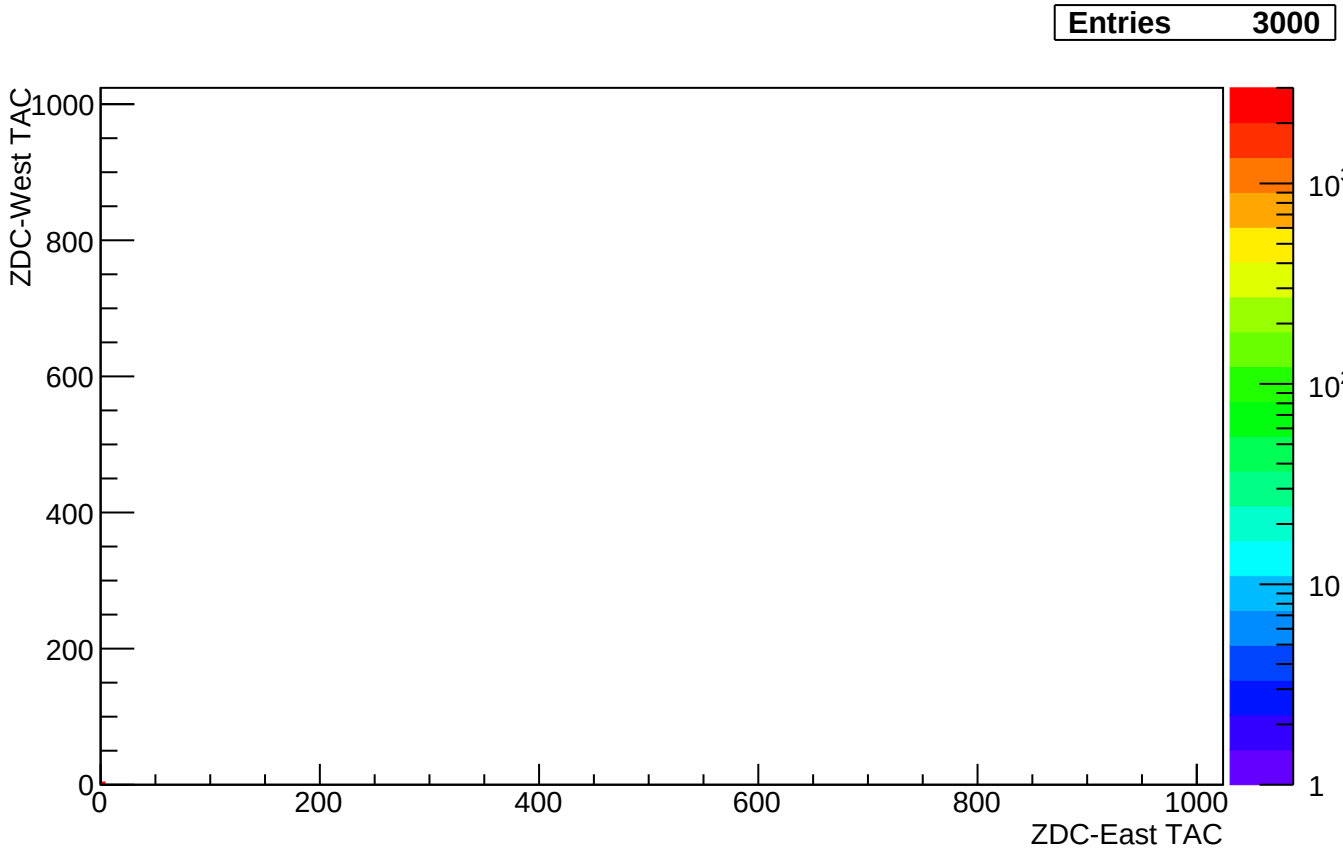

Entries 3000
TOF-Mult
VPD-East ADC Sum

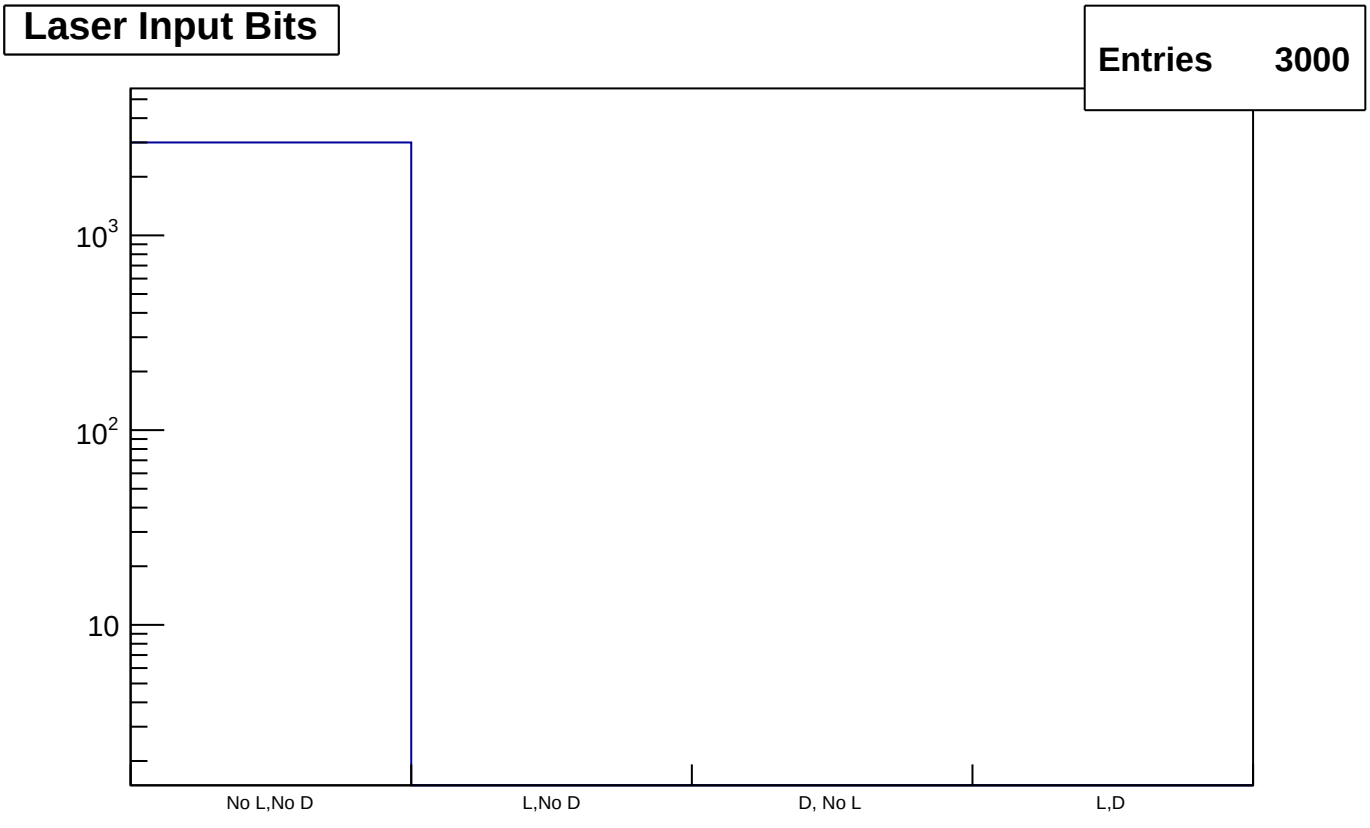

Entries 3000
TOF-Mult
VPD-West ADC Sum

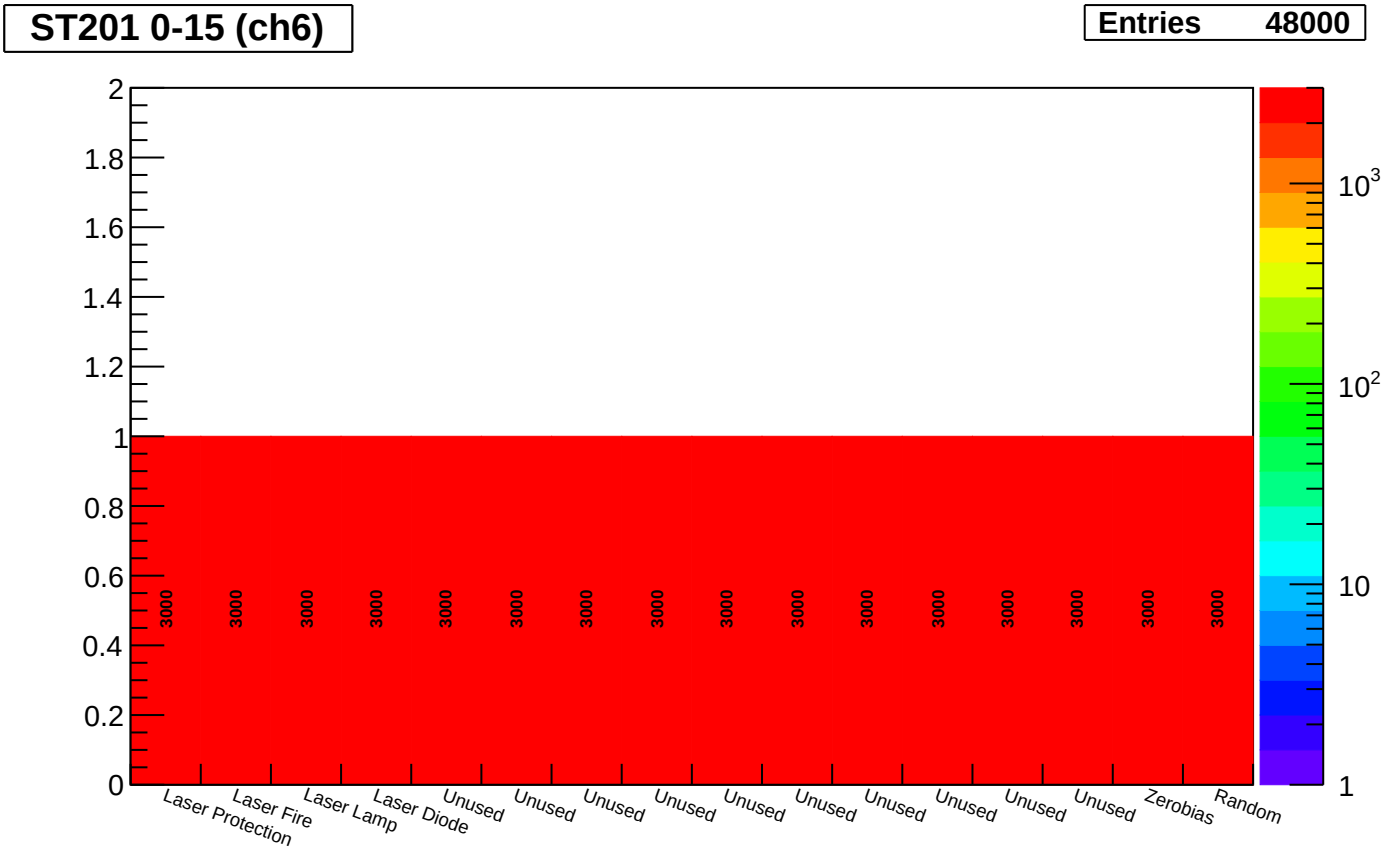

Entries 3000
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
EPD TAC Diff

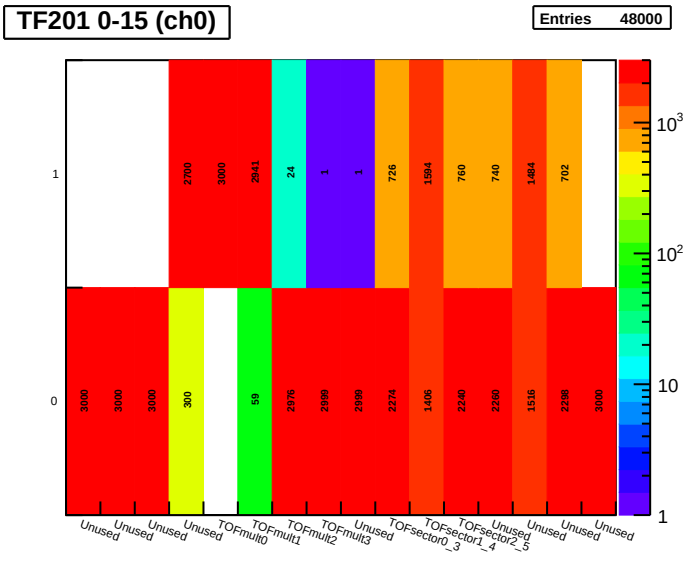

Entries 3000
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
VPD TAC Diff

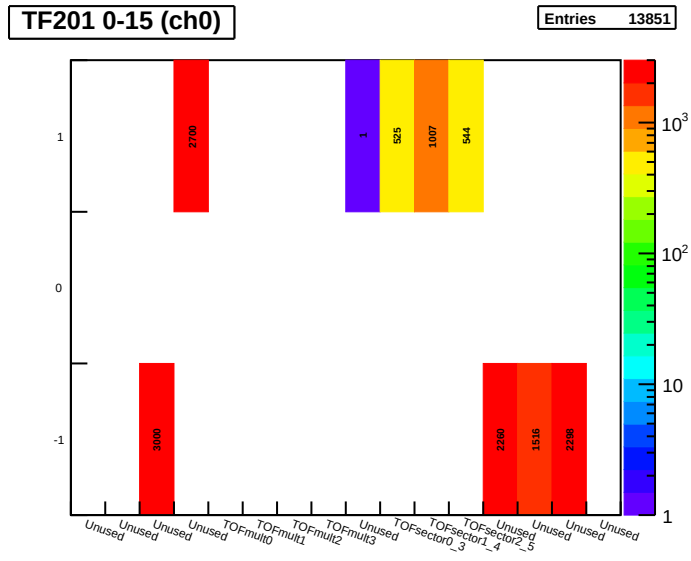

Entries 3000
ZDC TAC Diff
200
180
160
140
120
100
80
60
2000
3000
4000
5000
6000
7000
BBC-S TAC Diff

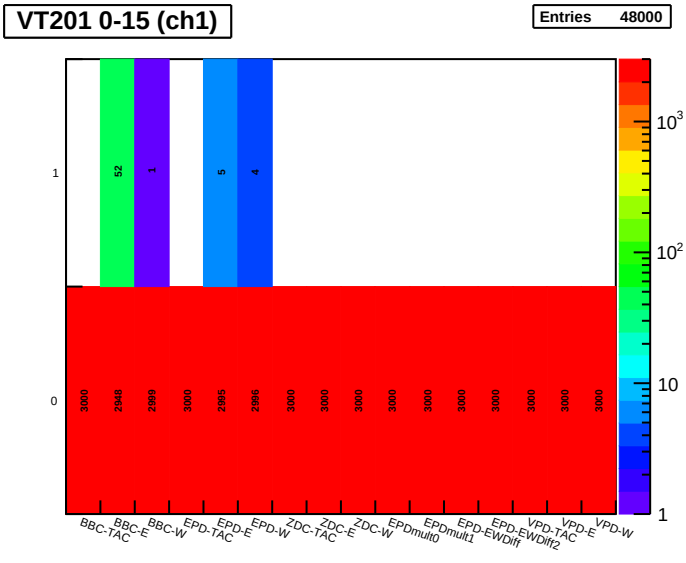

Entries 3000
ZDC-West TAC
1000
800
600
400
200
0
0
200
400
600
800
1000
ZDC-East TAC
10^3
10^2
10
1

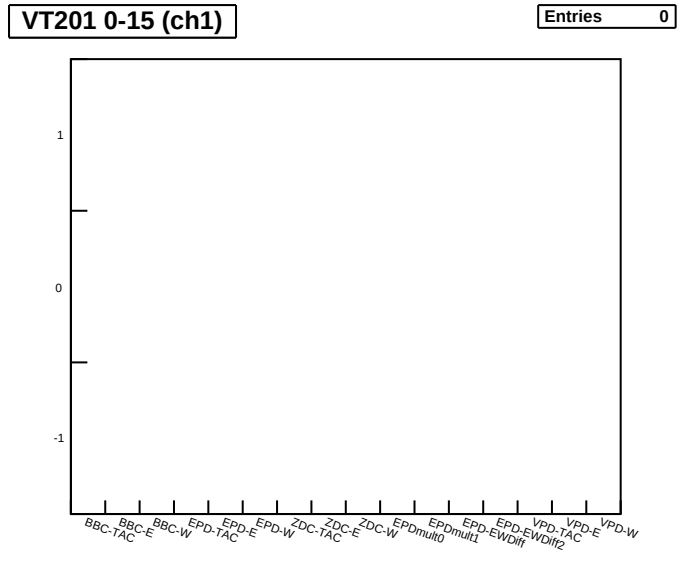

Laser Input Bits
Entries 3000
10^3
10^2
10
No L,No D
L,No D
D, No L
L,D

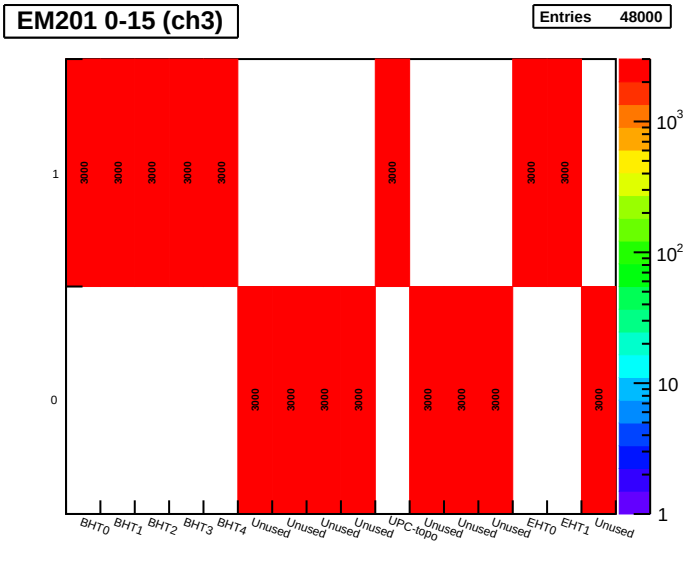

ST201 0-15 (ch6)
Entries 48000
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3000
Laser Protection
Laser Fire
Laser Lamp
Laser Diode
Unused
Zerobias
Random
10^3
10^2
10
1

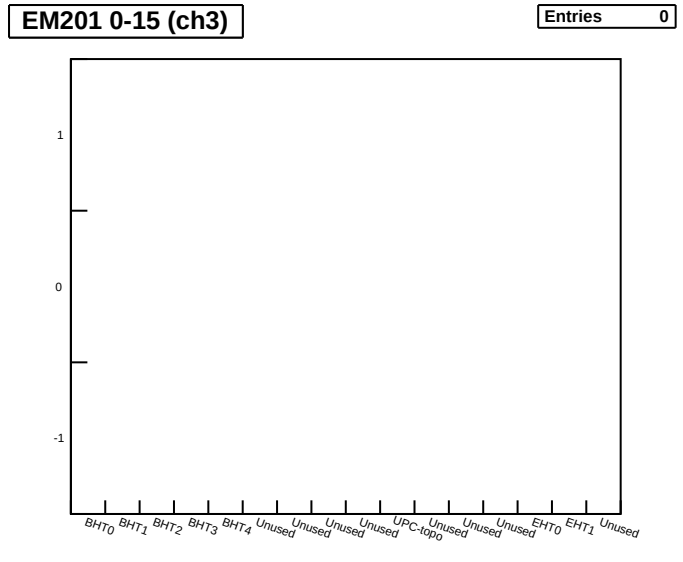

TF201 0-15 (ch0)
Entries 48000
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFsector0_3
TOFsector1_4
TOFsector2_5


TF201 0-15 (ch0)
Entries 13851
Unused
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFsector0_3
TOFsector1_4
TOFsector2_5


VT201 0-15 (ch1)
Entries 48000
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC
BBC-E
BBC-W
EPD-TAC
EPD-E
EPD-W
ZDC-TAC
ZDC-E
ZDC-W
EPDmult0
EPDmult1
EPD-EWDiff
EPD-EWDiff2
VPD-TAC
VPD-E
VPD-W


EM201 0-15 (ch3)
Entries 48000
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1


EM201 0-15 (ch3)
Entries 0
BHT0
BHT1
BHT2
BHT3
BHT4
Unused
UPC-topo
EHT0
EHT1